

TAOS 2.0

MV Build - Final

05/09/2006


	EE	DATE	POWER	DATE					
DRAWER					TITLE TAOS20				
DESIGN									
CHECK									
RESPONSIBLE									
SIZE = 3					VER :				
FILE NAME : XXXX-XXXXXX-XX					SIZE A3 CODE CS DOC NUMBER 1310A20305 REV A02				
P/N : XXXXXXXXXXXX					SHEET 1 OF 69				

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INVENTEC

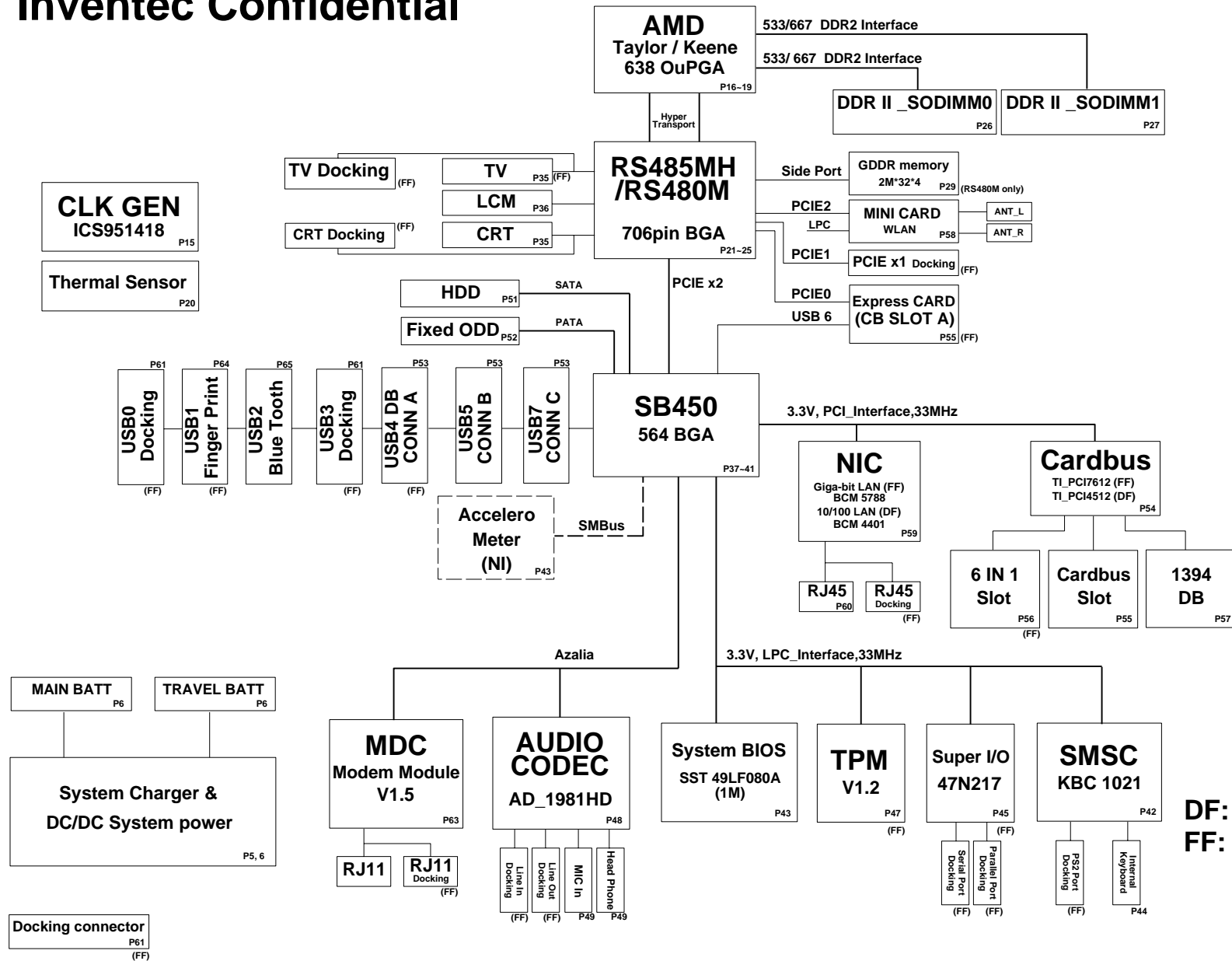
TITLE TAOS20

SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

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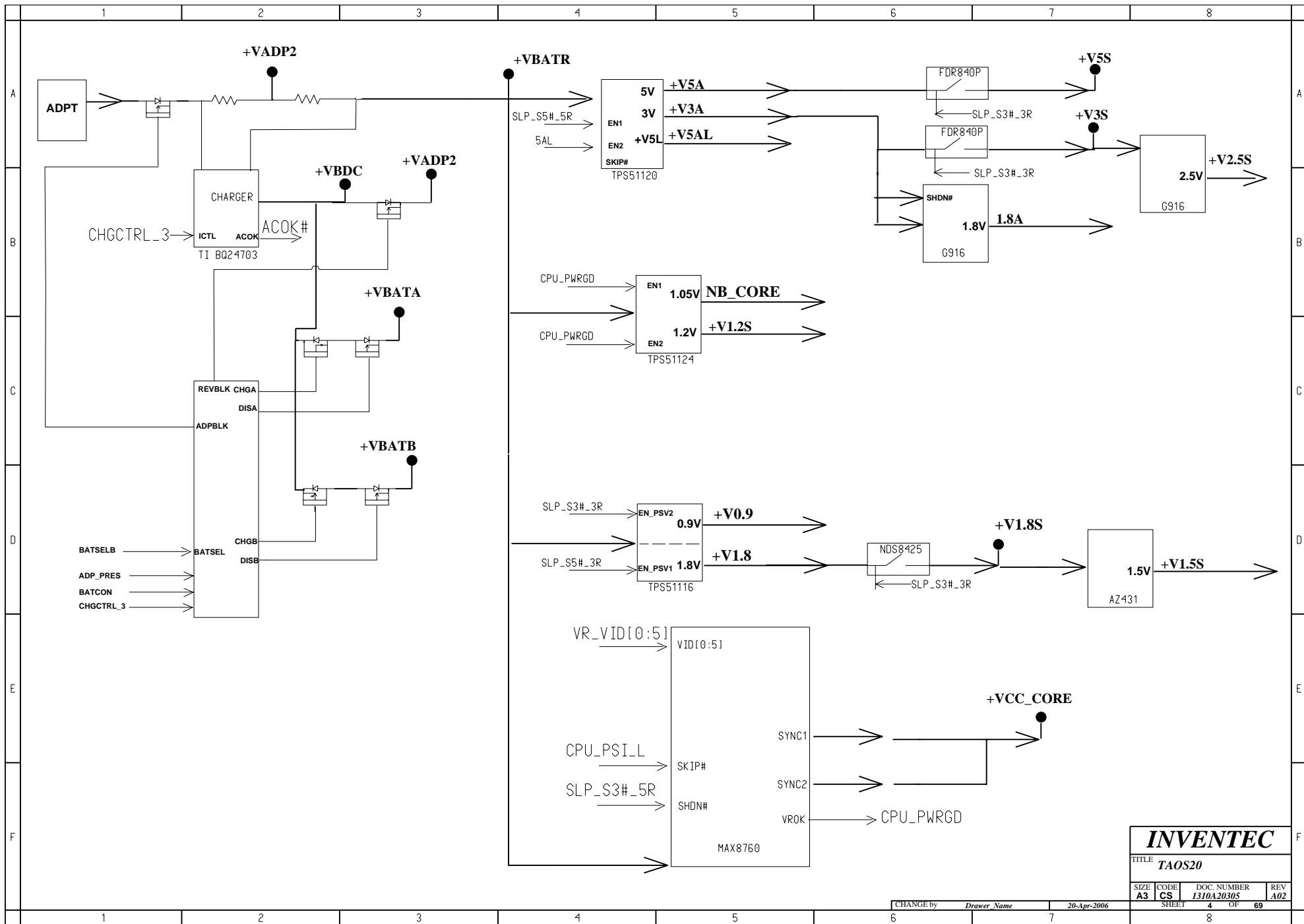
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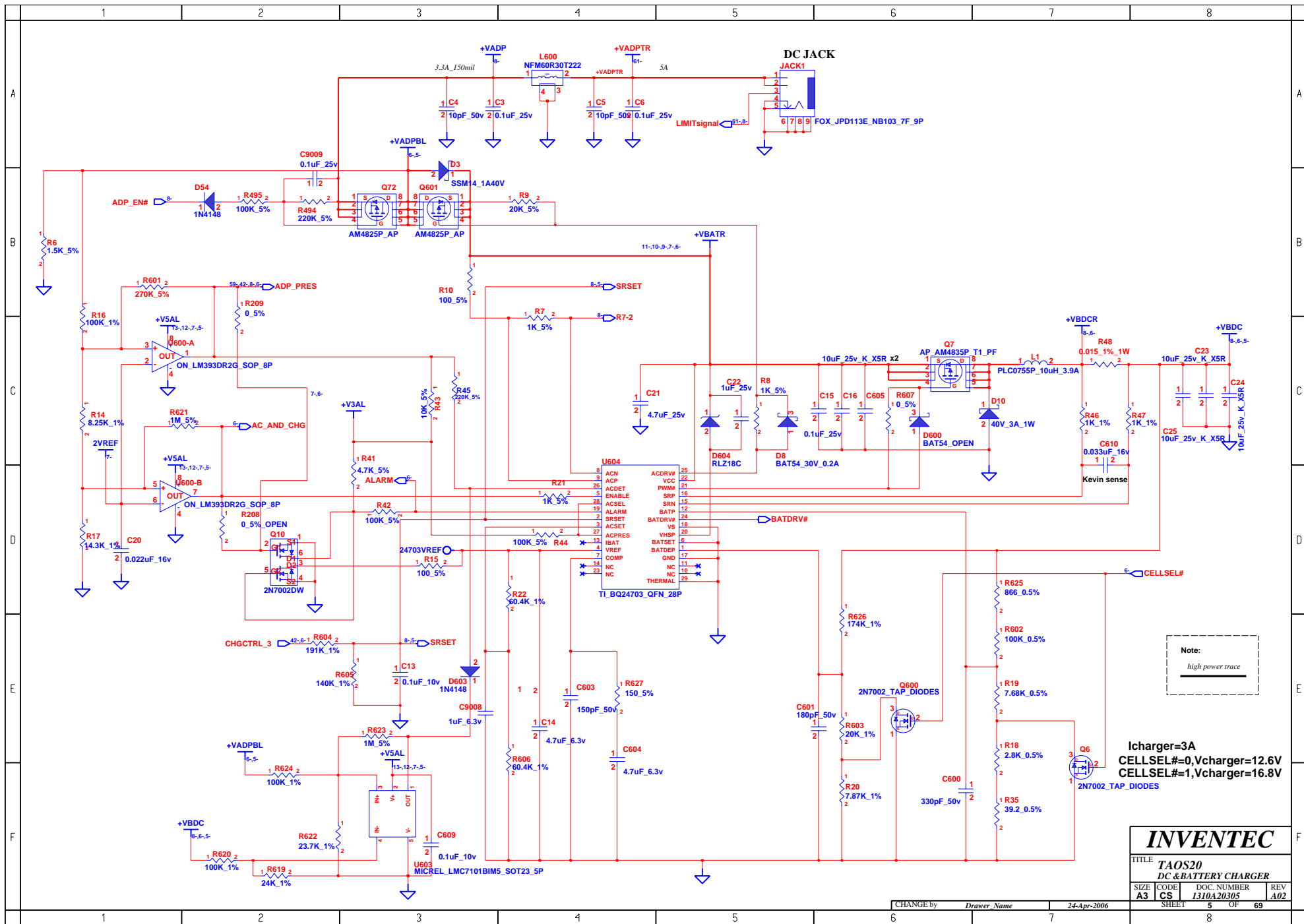


DF: De-Featured
FF: Full-Featured

INVENTEC

TITLE TAOS20 Block Diagram			
SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
CHANGE by Drawer Name		20-Apr-2006	
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Note:
high power trace

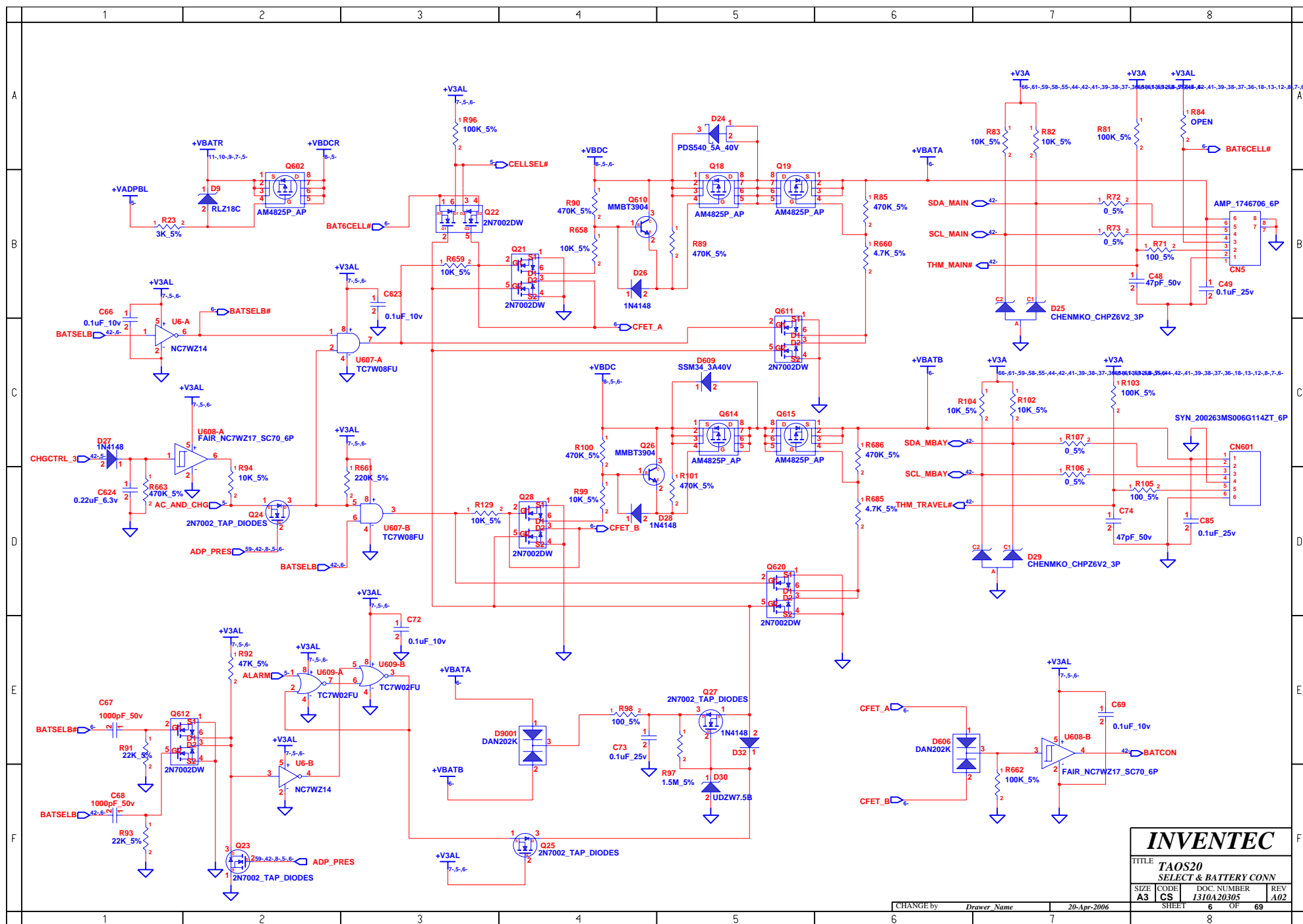
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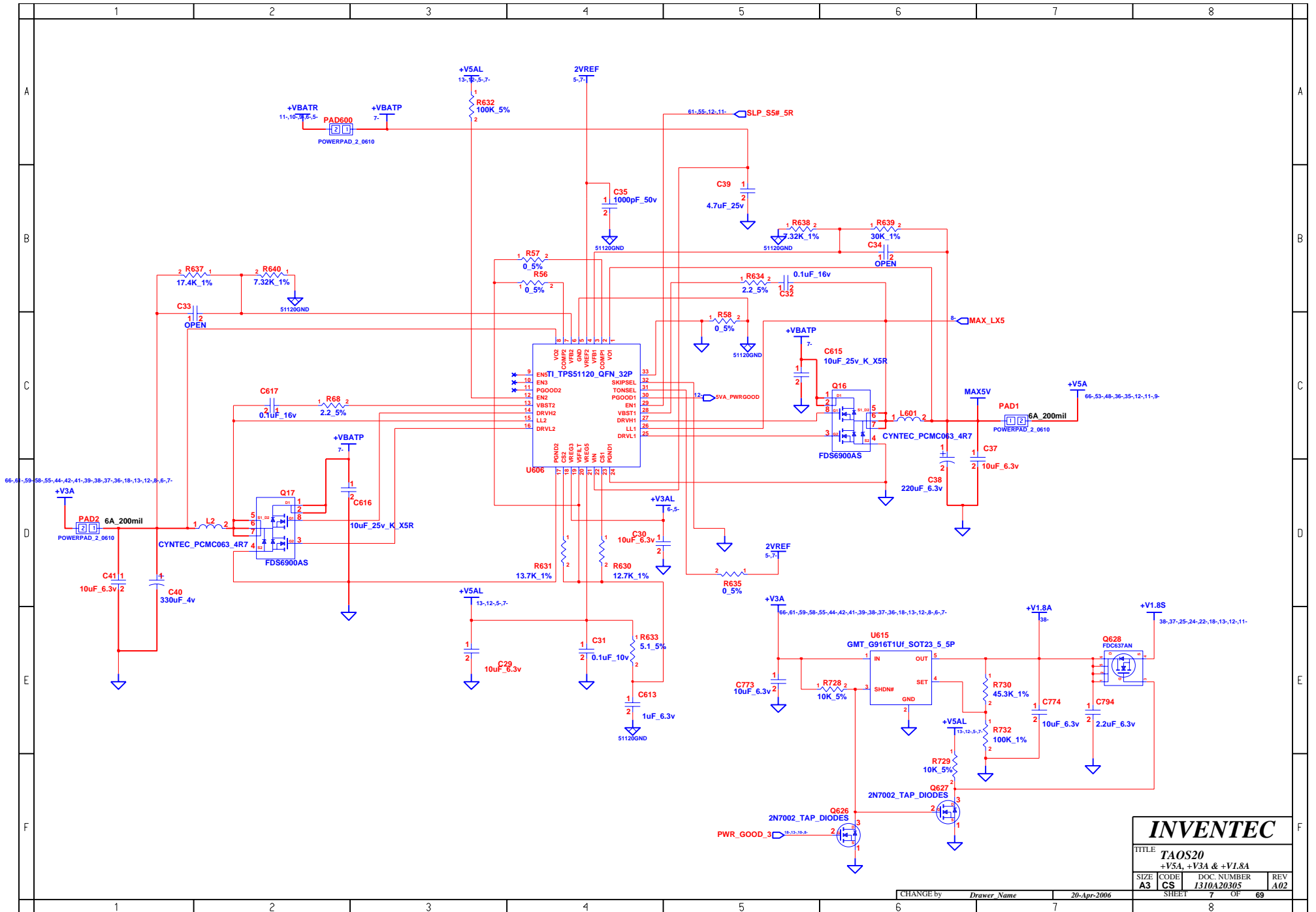
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TITLE TAOS20 DC & BATTERY CHARGER			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02

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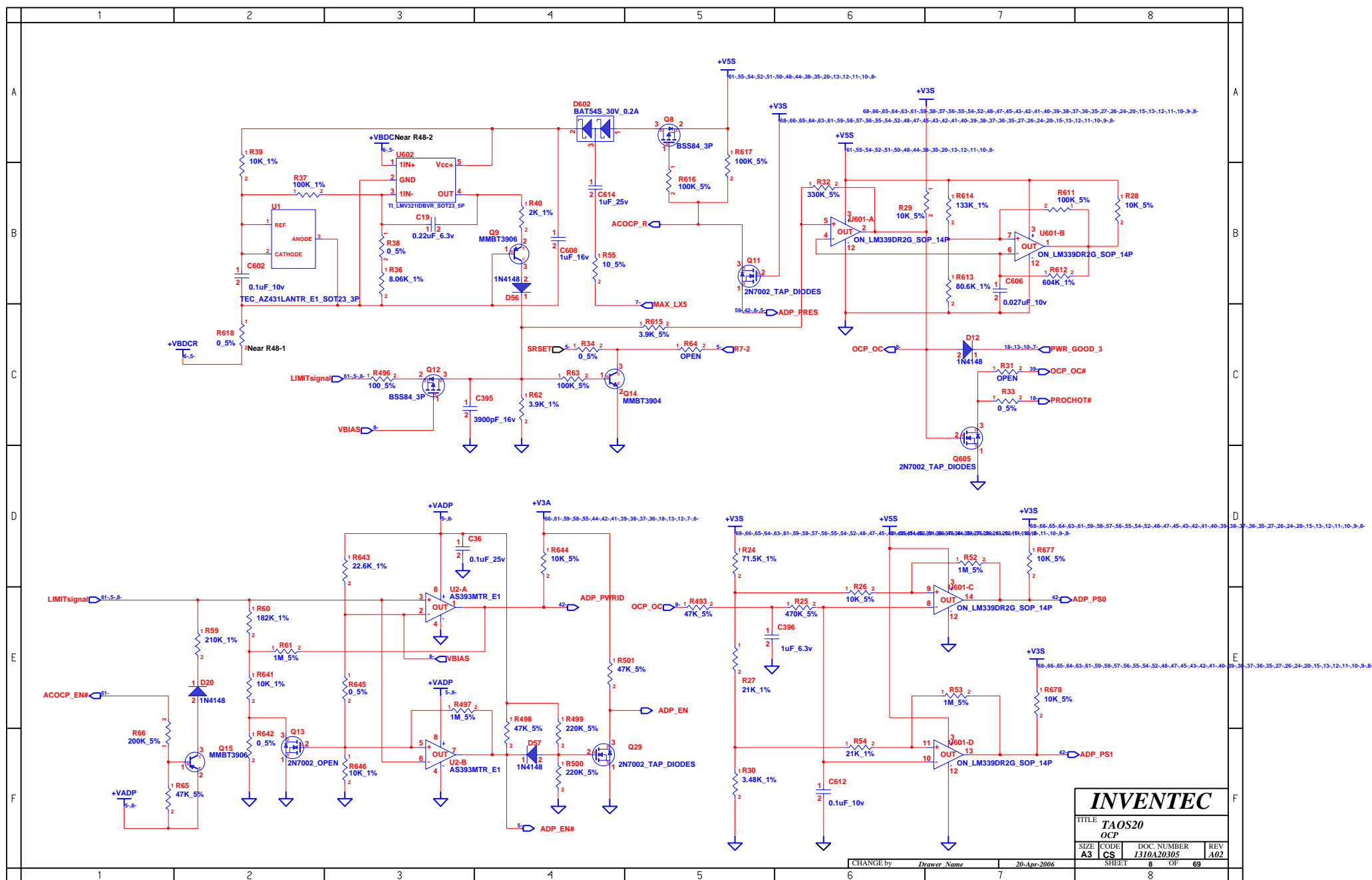


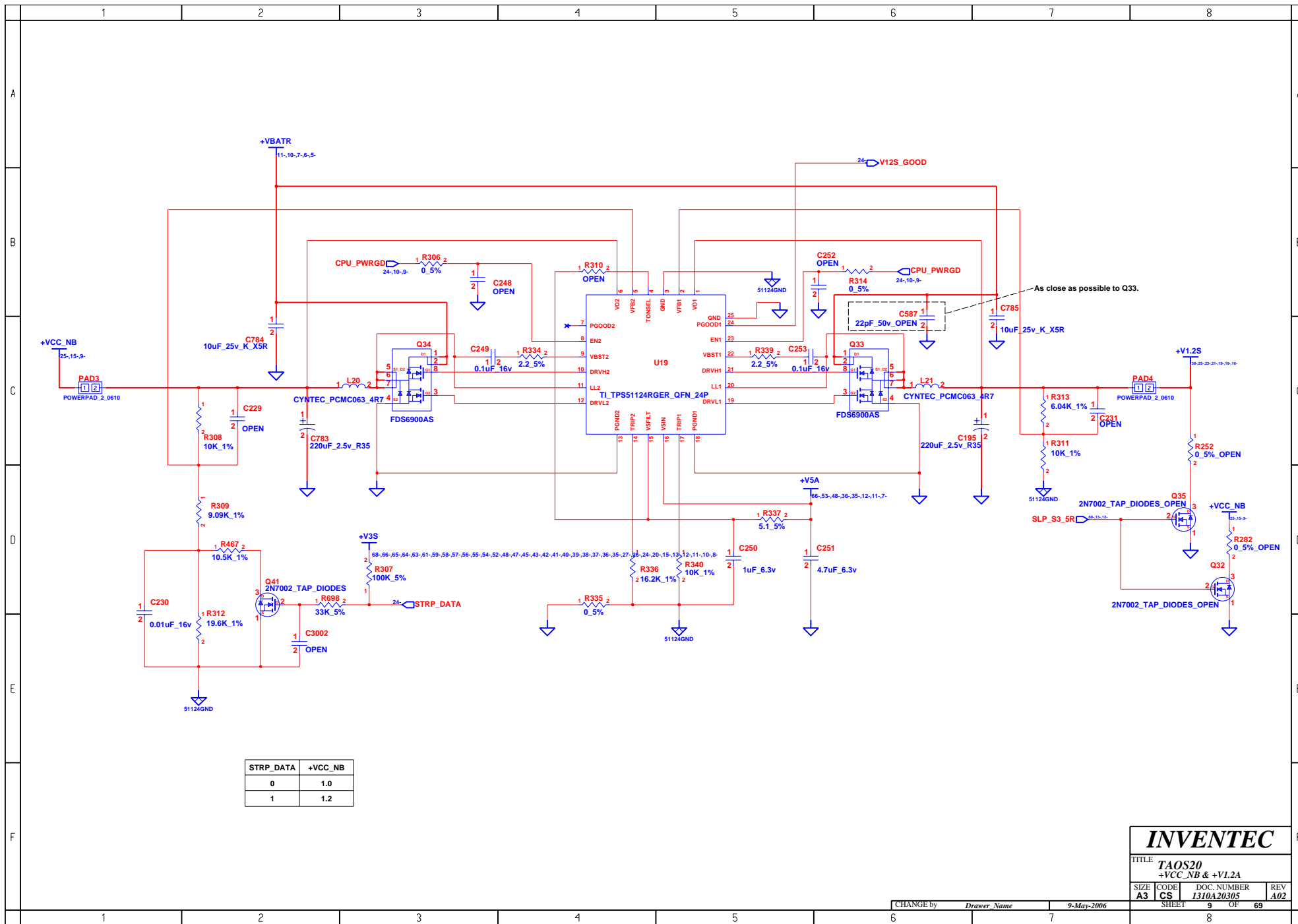


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TITLE			
TAOS20			
+V5A, +V3A & +V1.8A			
SIZE	CODE	DOC. NUMBER	REV
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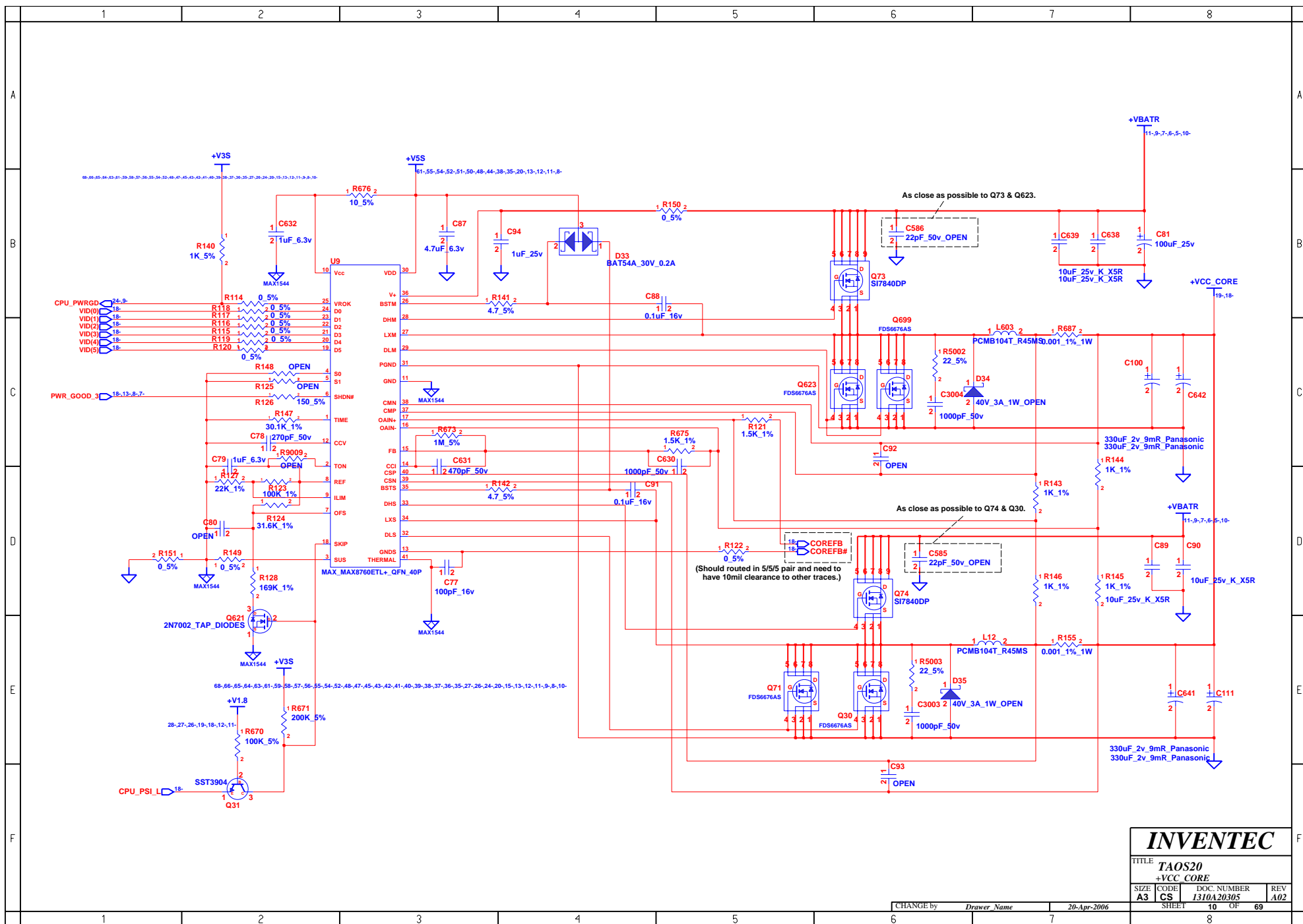
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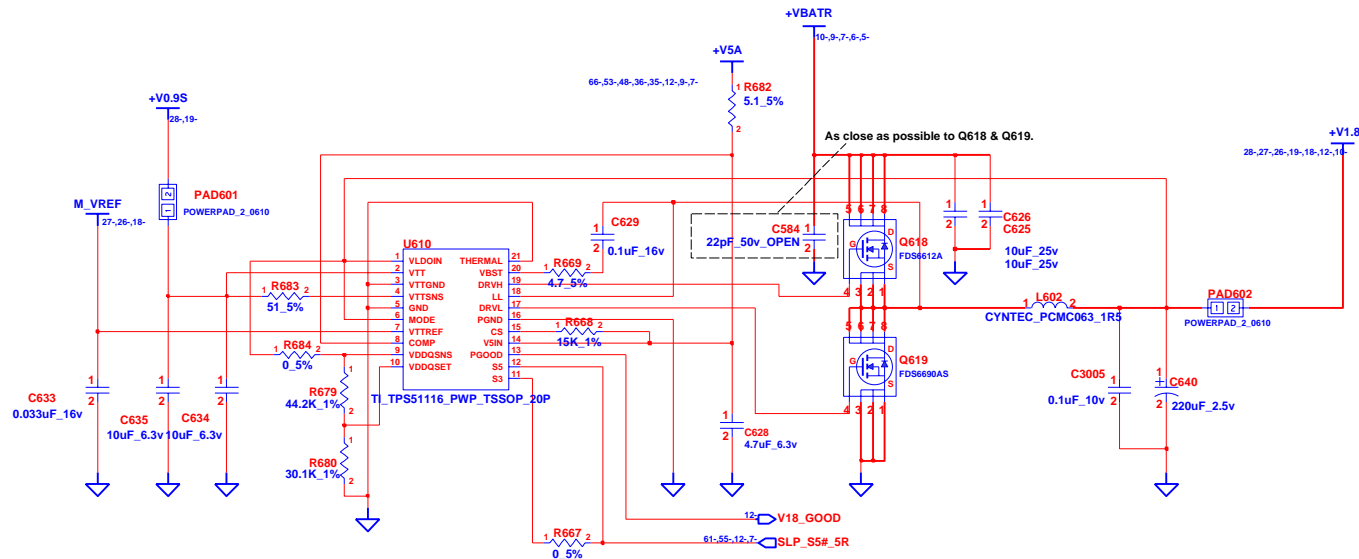
TITLE
TAOS20
+VCC_NB & +V1.2A

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A20305	A02

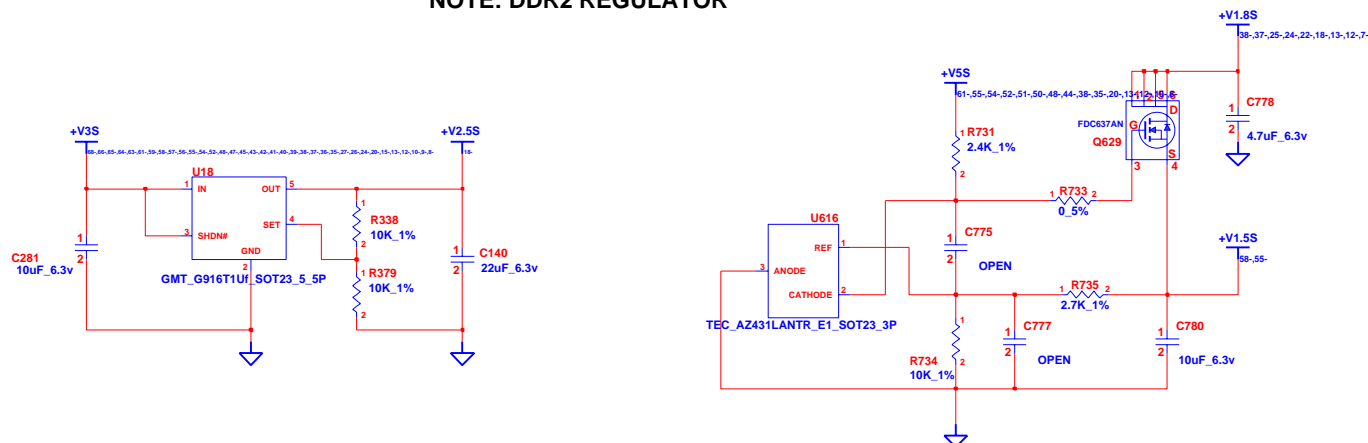
CHANGE by *Drawer Name* 9-May-2006

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NOTE: DDR2 REGULATOR

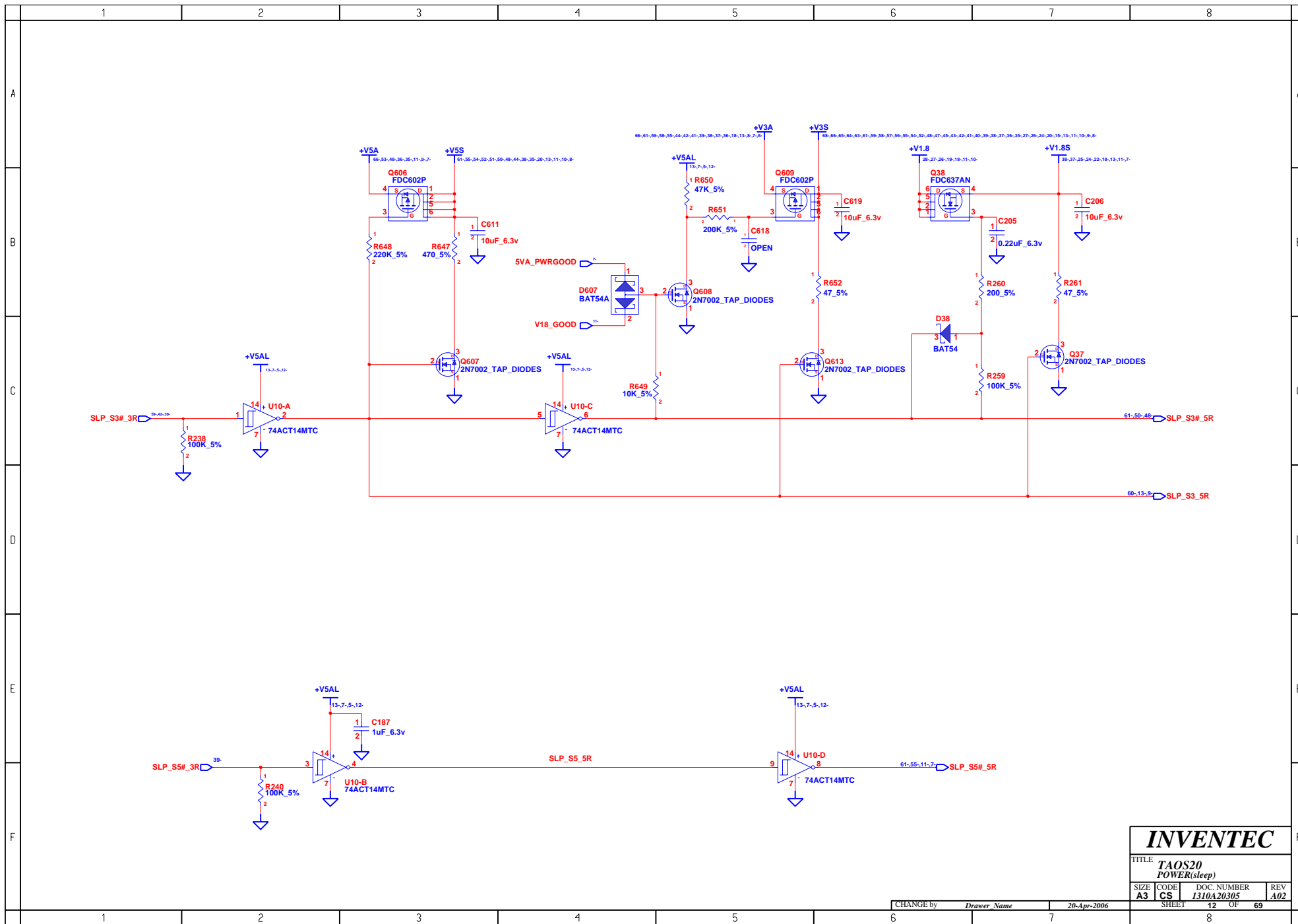


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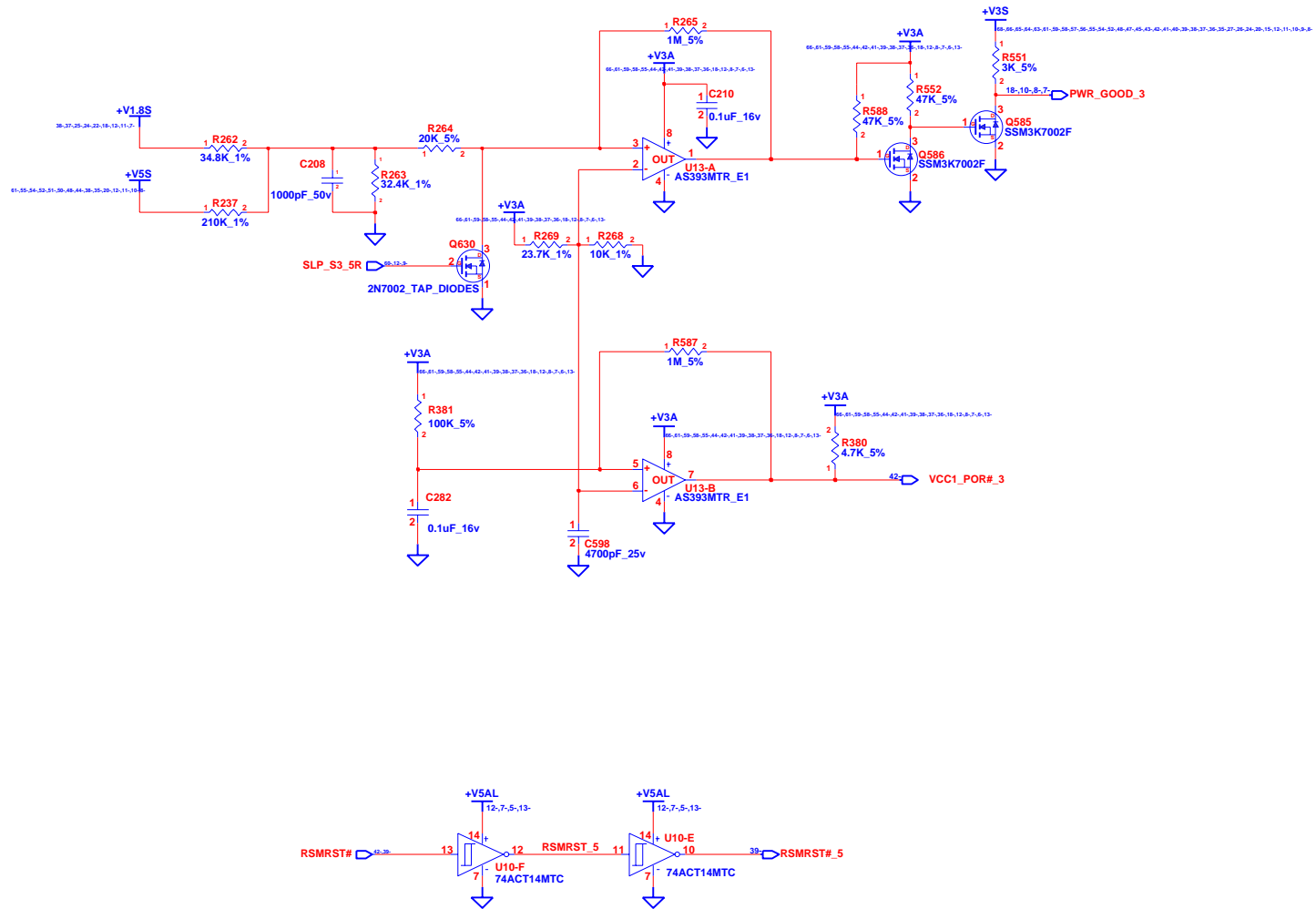
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TAOS20			
DDR2 POWER +V2.5S & +V1.5S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A20305	A02

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INVENTEC			
TITLE TAOS20 POWER(sleep)			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
CHANGE by Drawer Name		20-Apr-2006	
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INVENTEC

TITLE
TAOS20
POWER(Sequence)

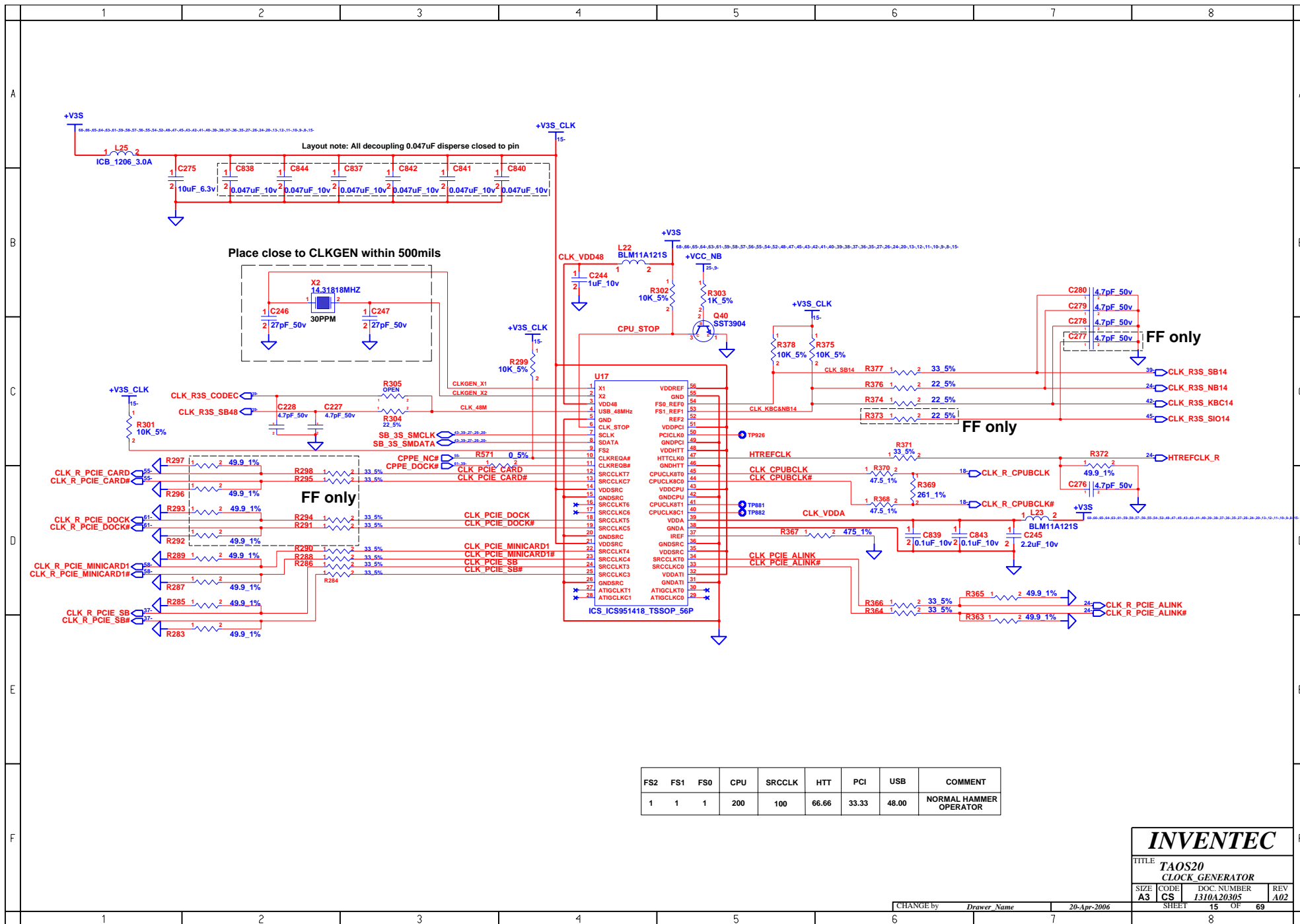
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A20305	A02

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B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

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SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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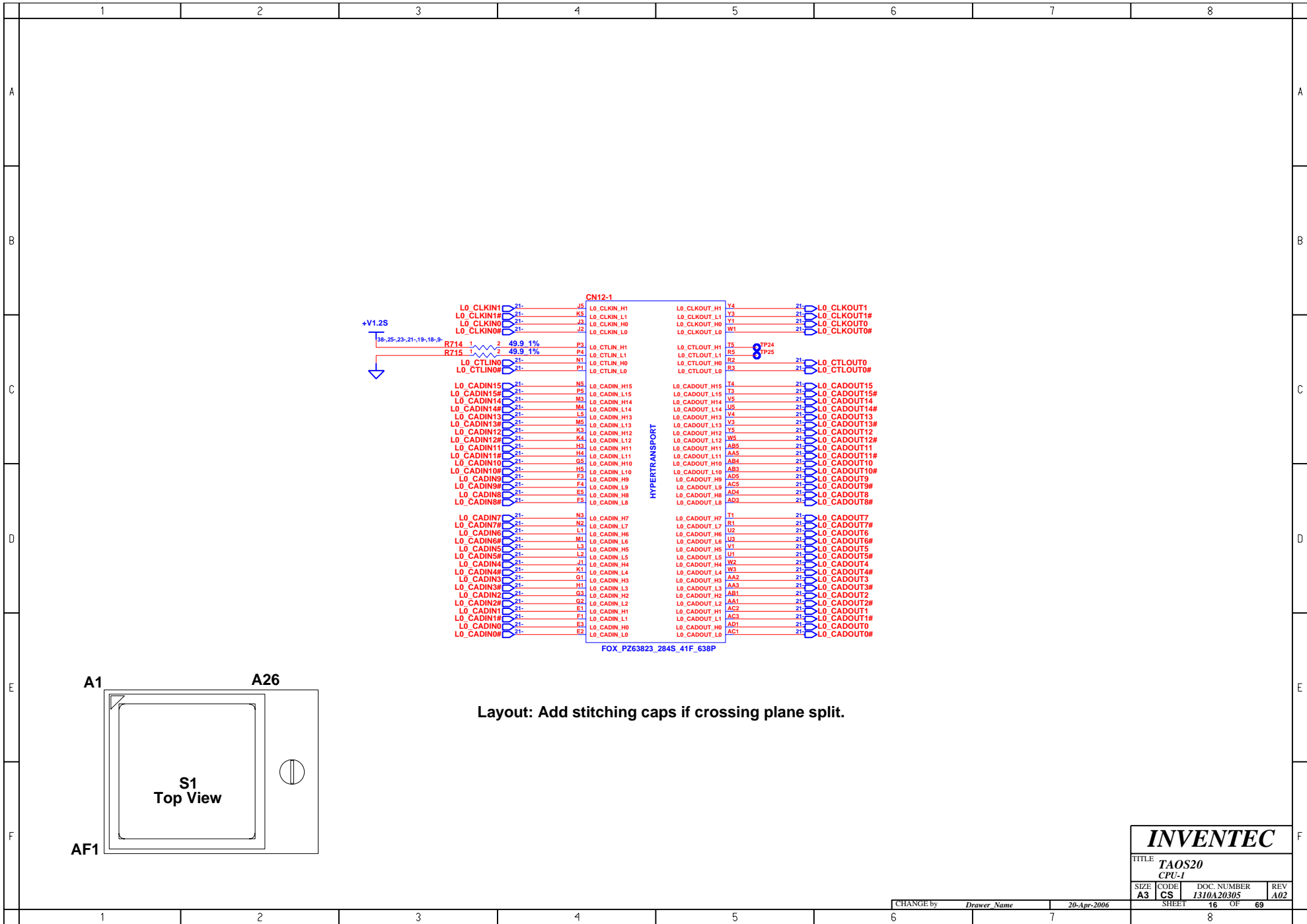
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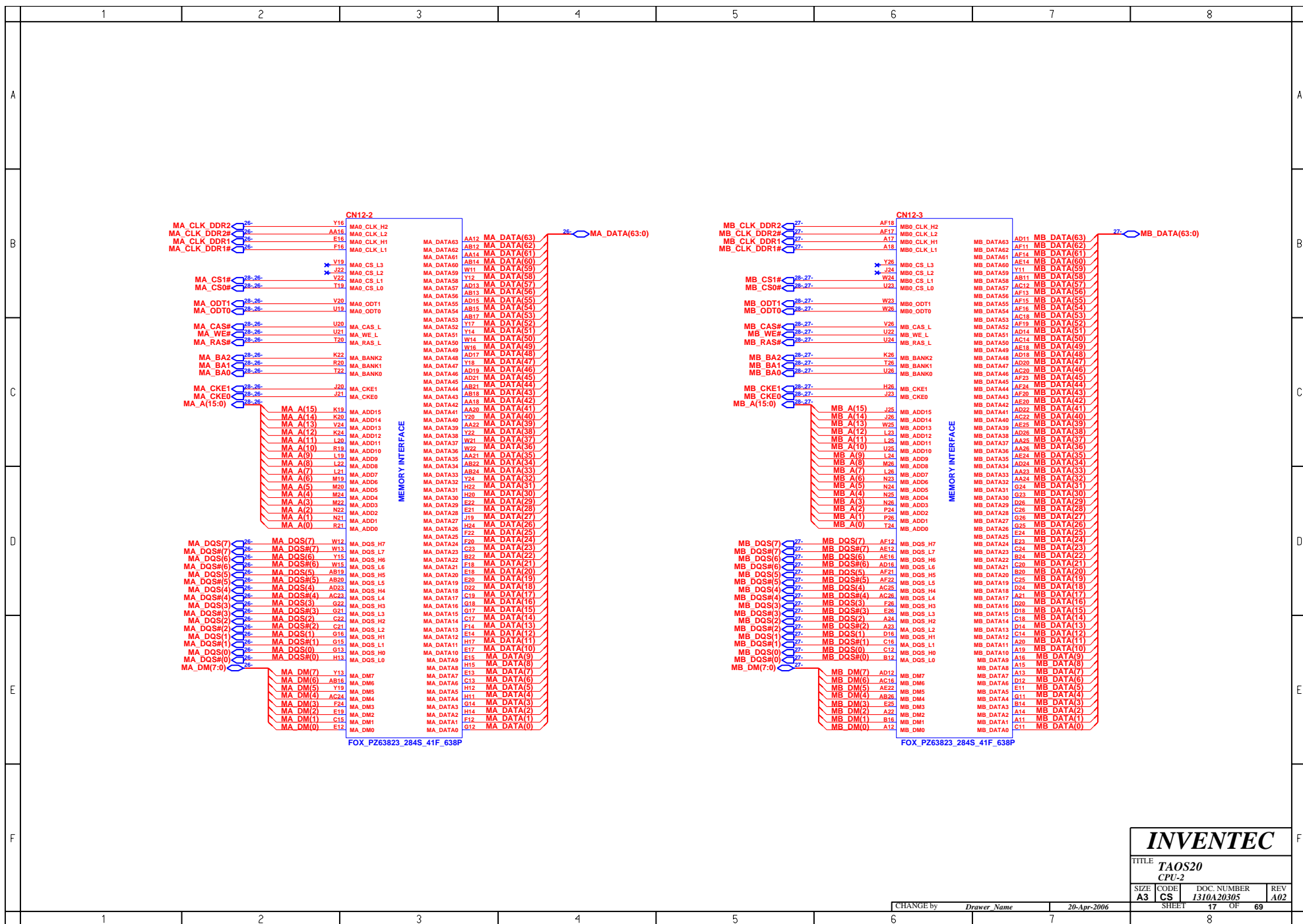
TITLE
TAOS20
CLOCK GENERATOR

SIZE CODE DOC. NUMBER REV
A3 CS 1310A20305 A02

CHANGE by Drawer Name 20-Apr-2006

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Keep trace to resistor less than 600mils from CPU pin and trace to AC caps less than 1250mils.

Do not use AMD serial I/F to thermal monitoring circuit in the future

Routing DIFF pair type

Required for compatibility with future processors.

Keep trace to resistors less than 1" from CPU pin.

Route as 80 ohm differential impedance. Keep trace to resistor less than 1" FROM CPU PIN.

FOR debug (220ohm)

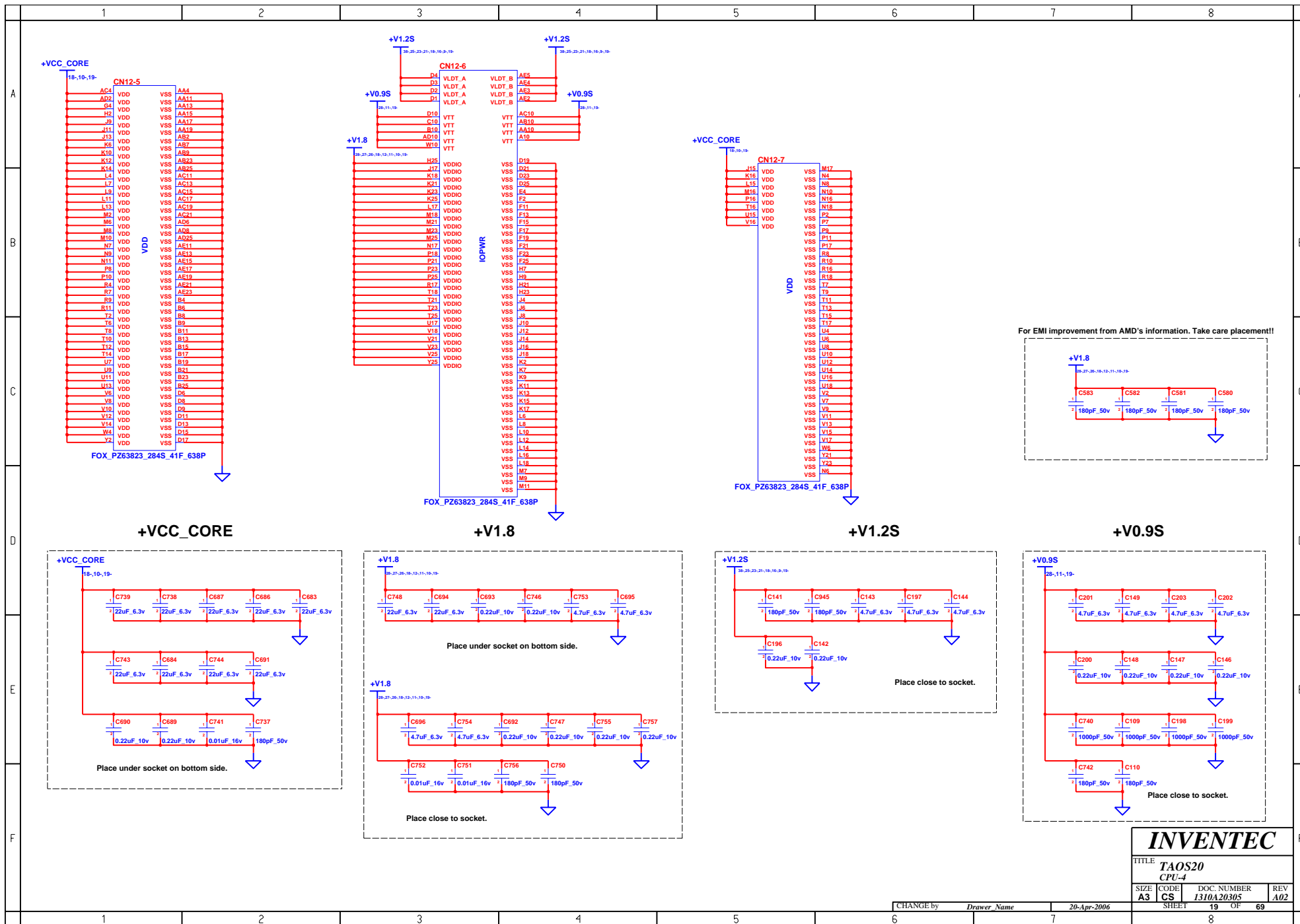
HDT Header

INVENTEC

TITLE TAOS20 CPU-3			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02

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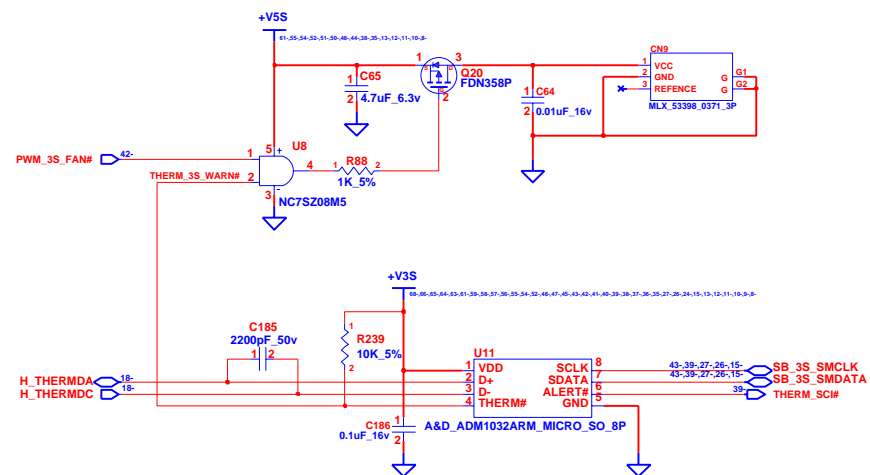
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TITLE
TAOS20
CPU-4

SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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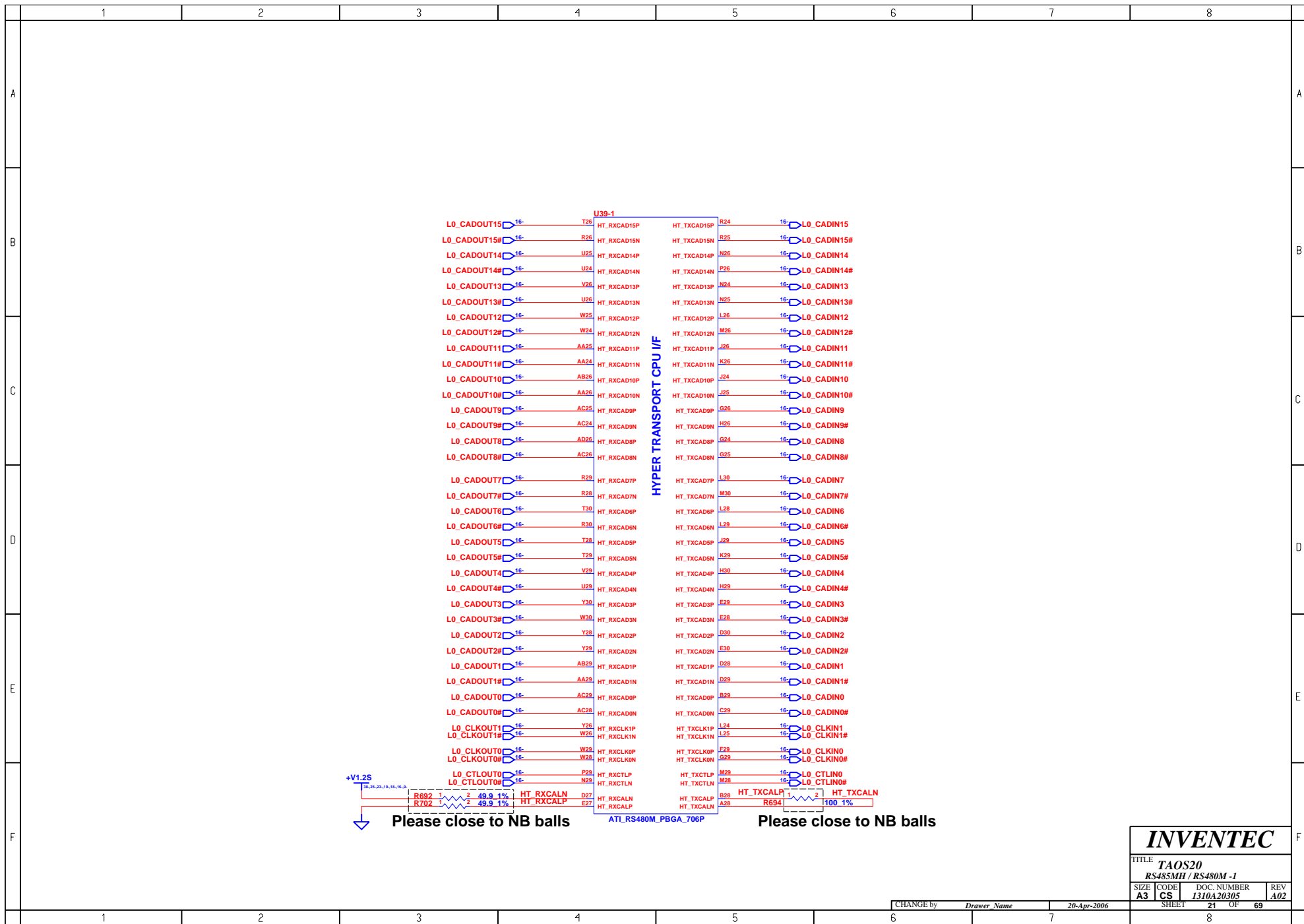
CHANGE by *Drawer Name* 20-Apr-2006

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LAYOUT Note: Put the thermal sensor close to CPU.

INVENTEC			
TITLE TAOS20 THERMAL & FAN CONTROLLER			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A01
CHANGE by Drawer Name		20-Apr-2006	
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INVENTEC

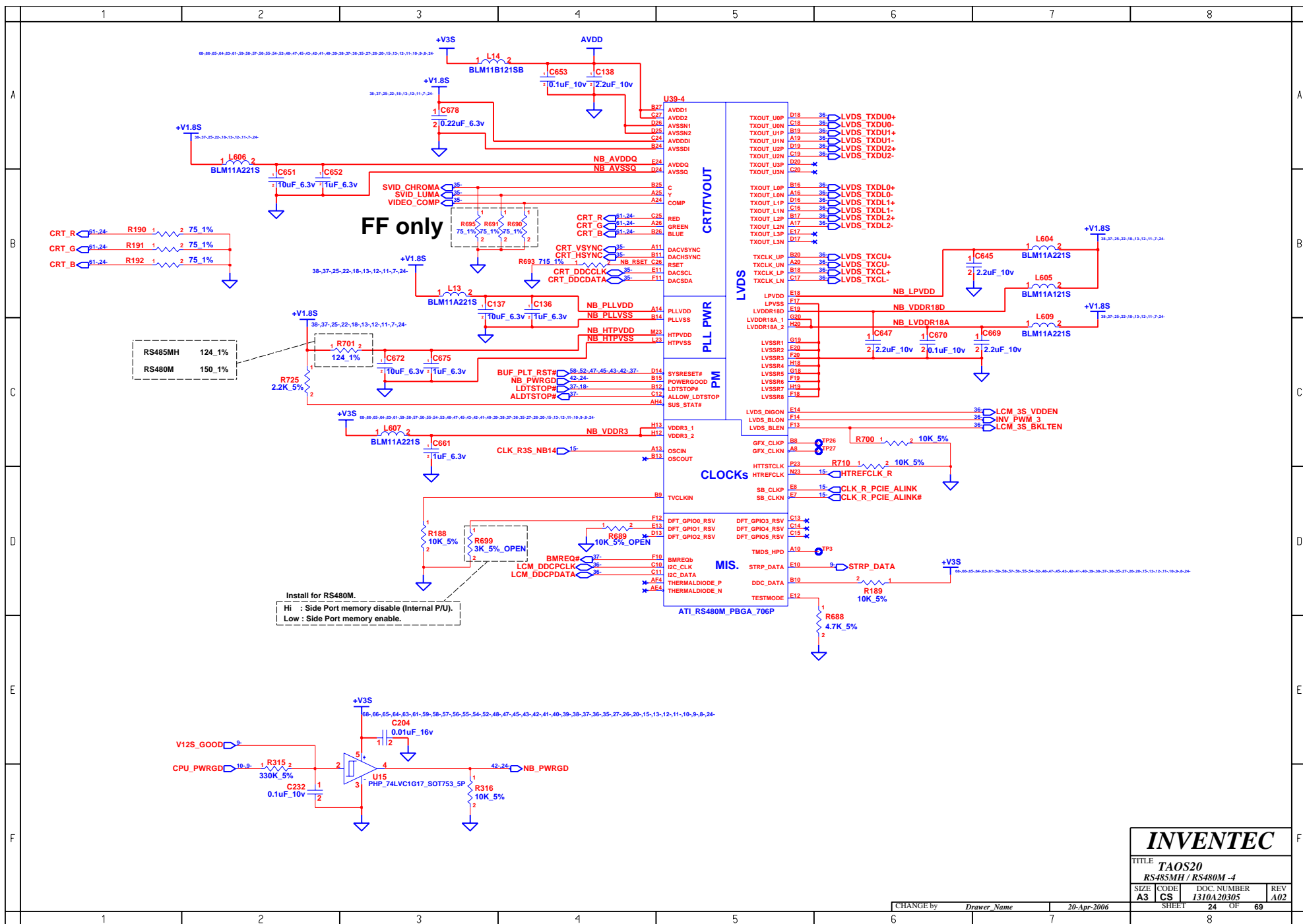
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TAOS20
RS485MH / RS480M -1

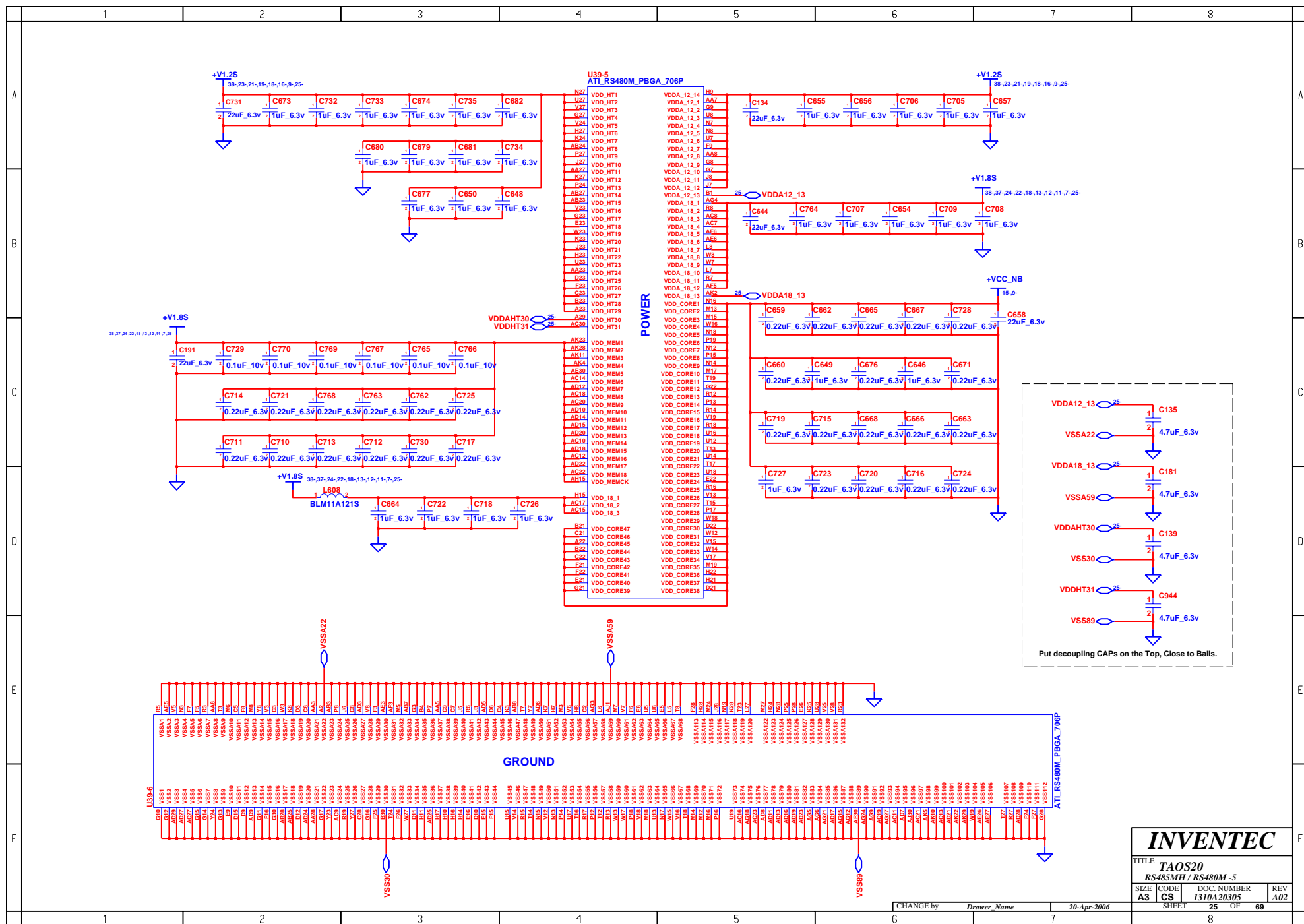
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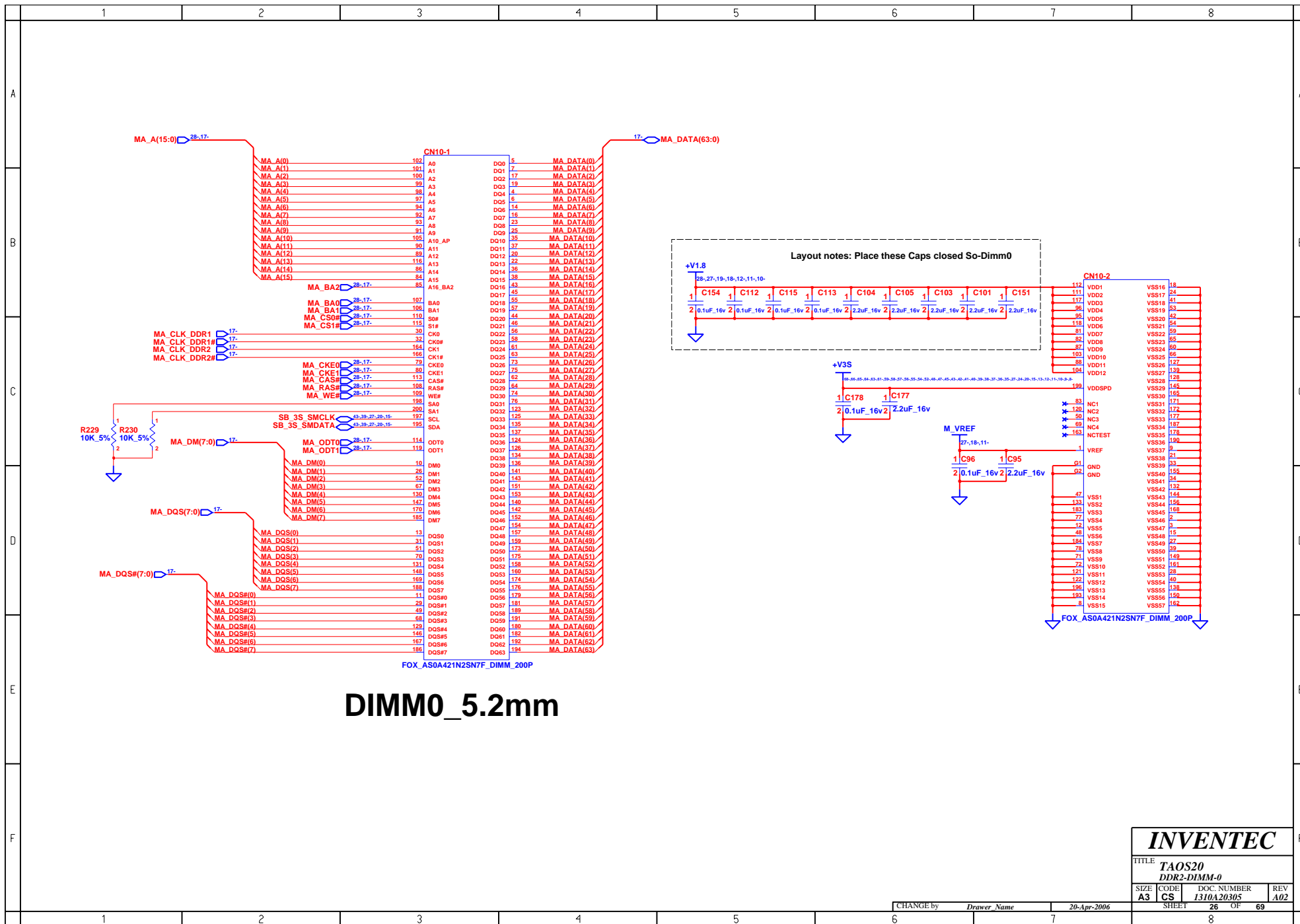
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DIMM0_5.2mm

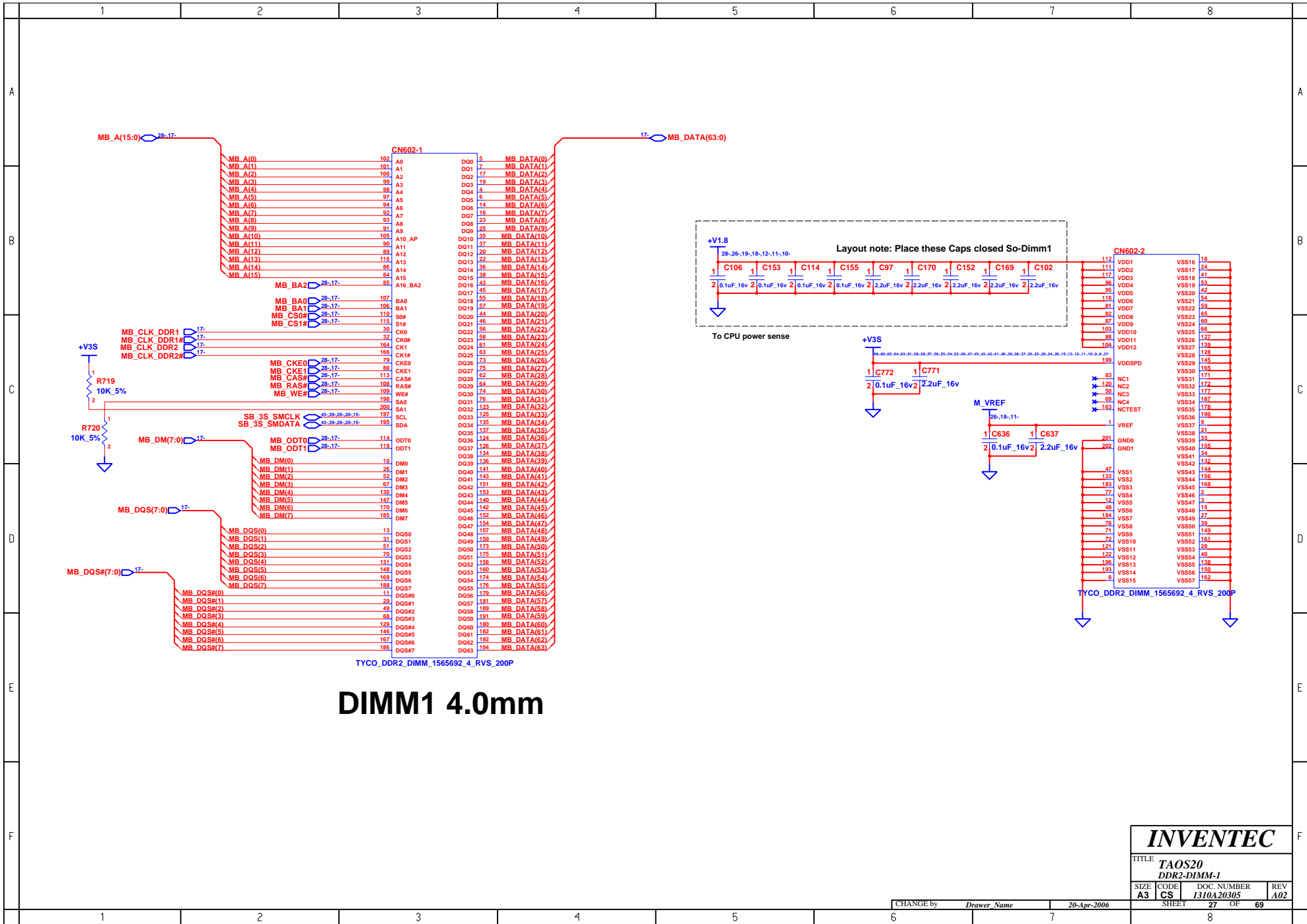
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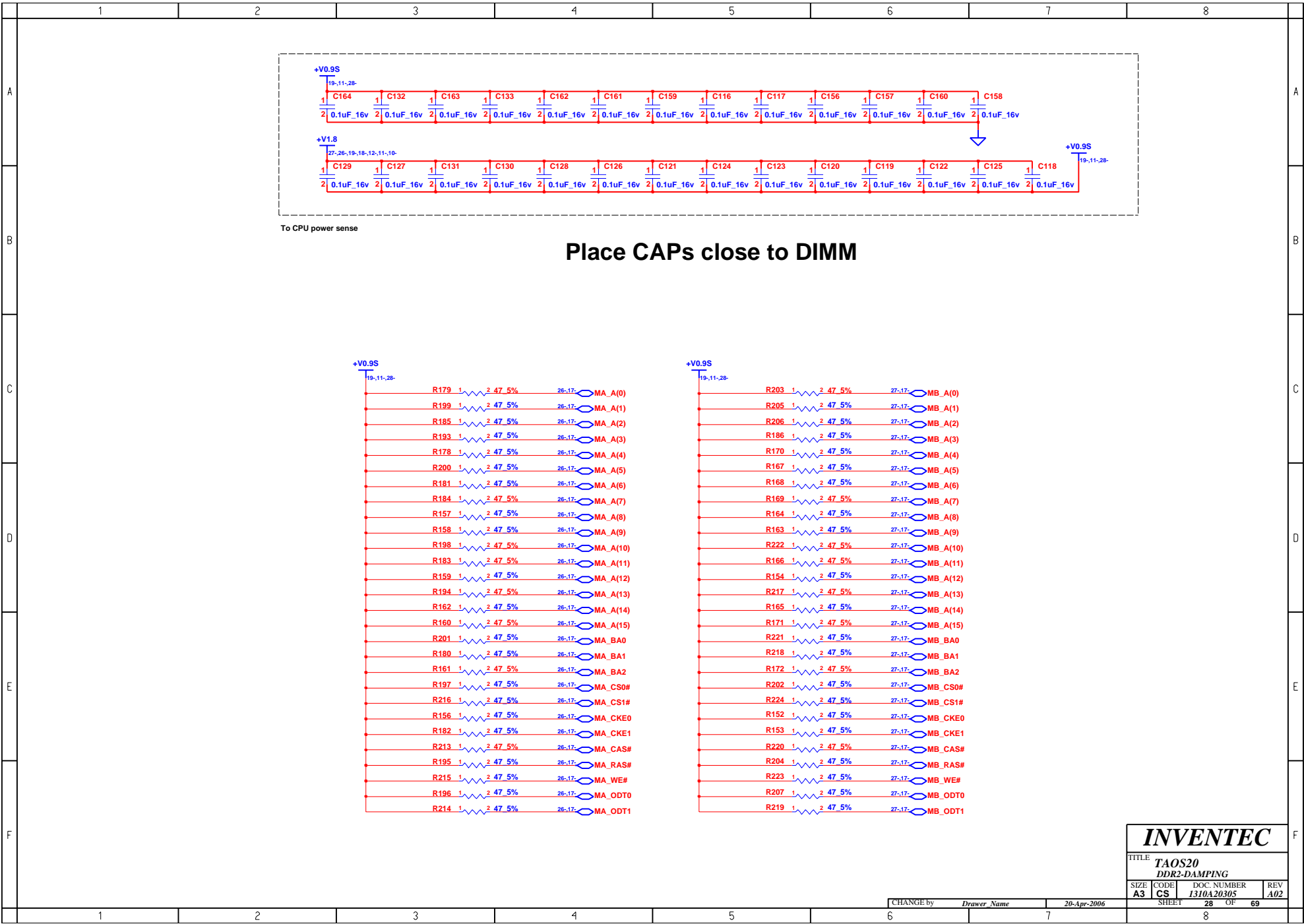
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TAOS20
DDR2-DIMM-0

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A20305	A02

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C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

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TITLE TAOS20 BLANK			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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CHANGE by	Drawer Name	20-Apr-2006
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C									C
D									D
E									E
F									F
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SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

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C									C
D									D
E									E
F									F
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SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

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E									E
F									F
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INVENTEC

TITLE TAOS20 BLANK			
SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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CHANGE by *Drawer Name* 27-Sep-2003

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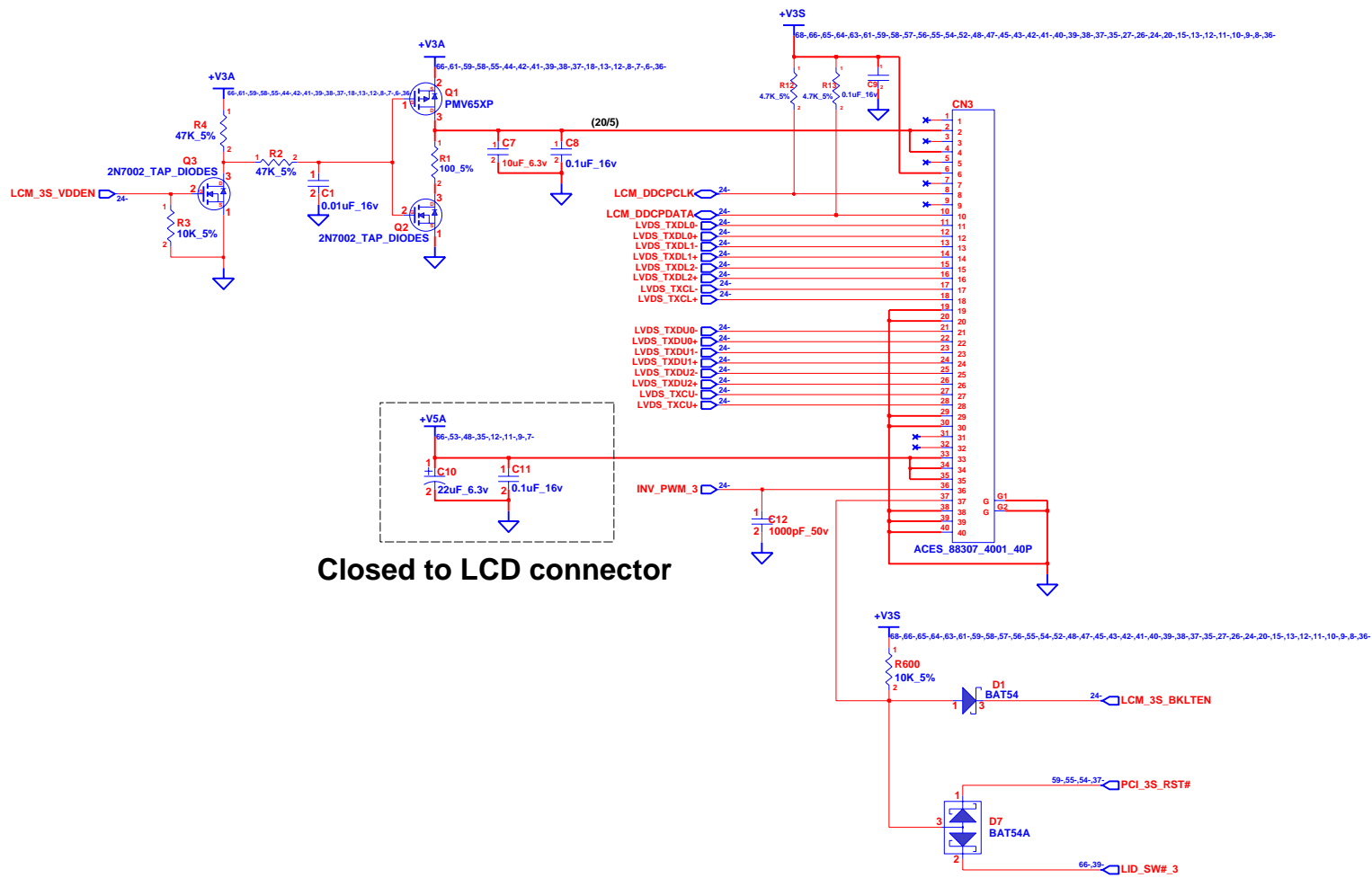
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SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

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B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

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CHANGE by *Drawer Name* 20-Apr-2006

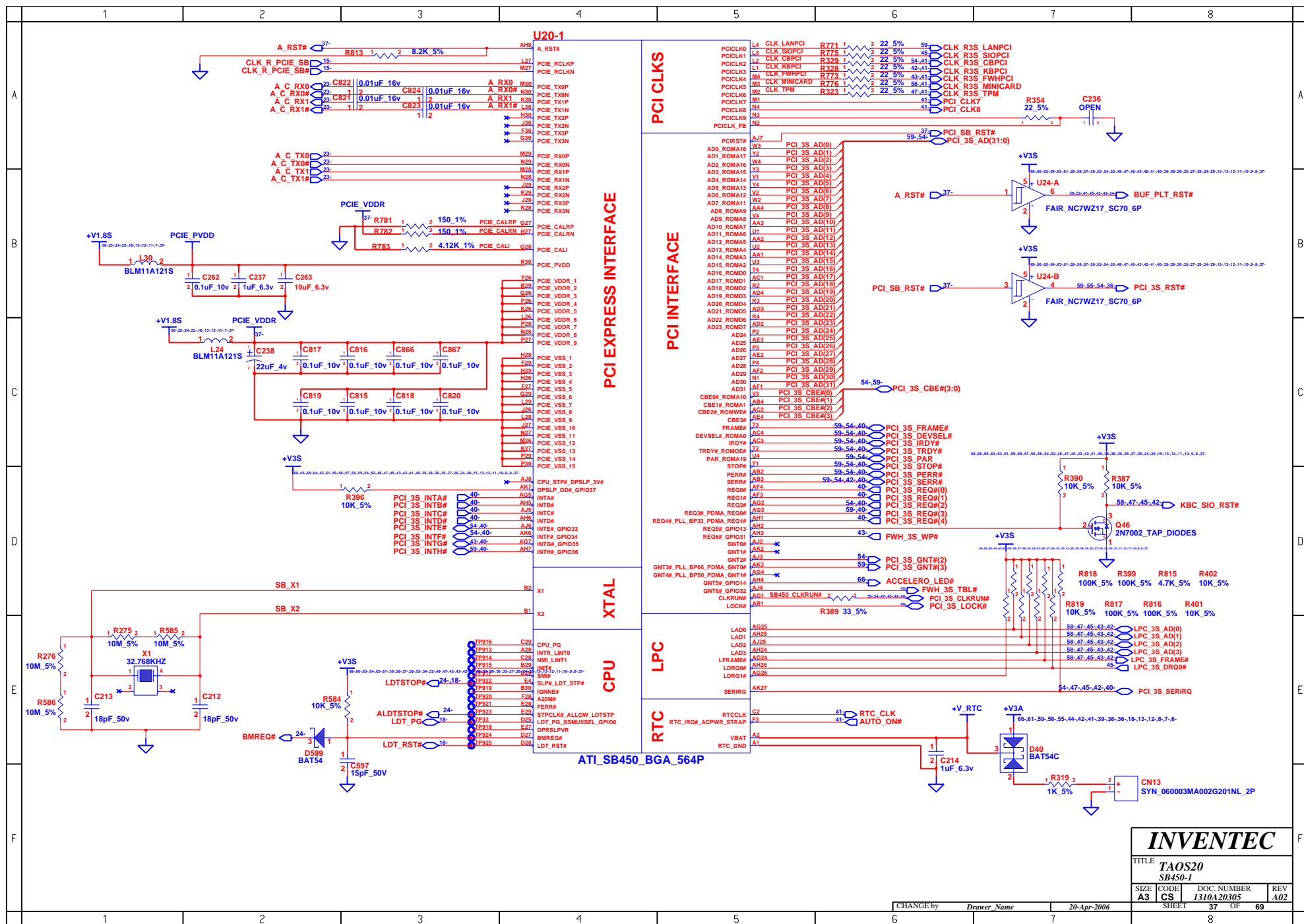


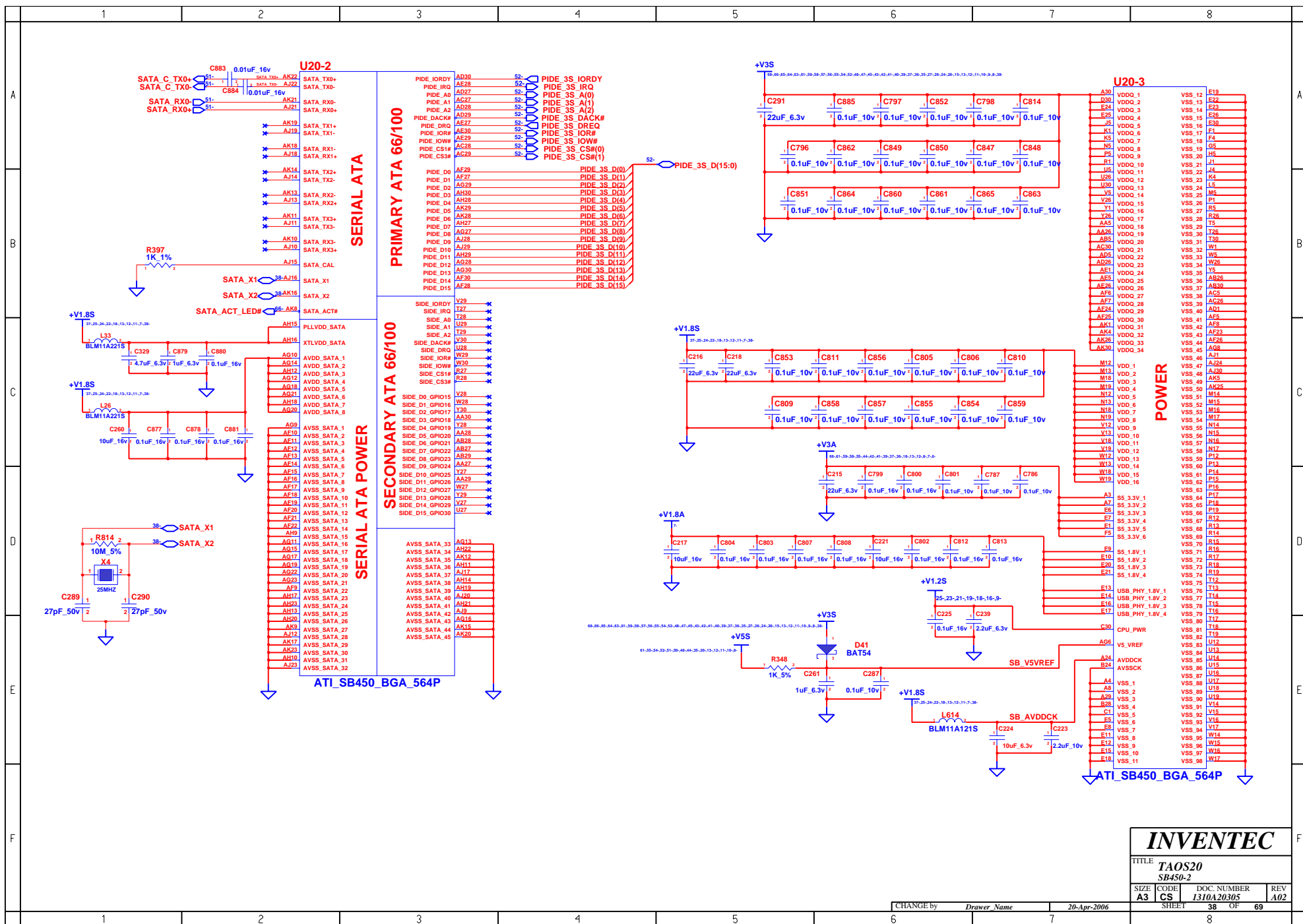
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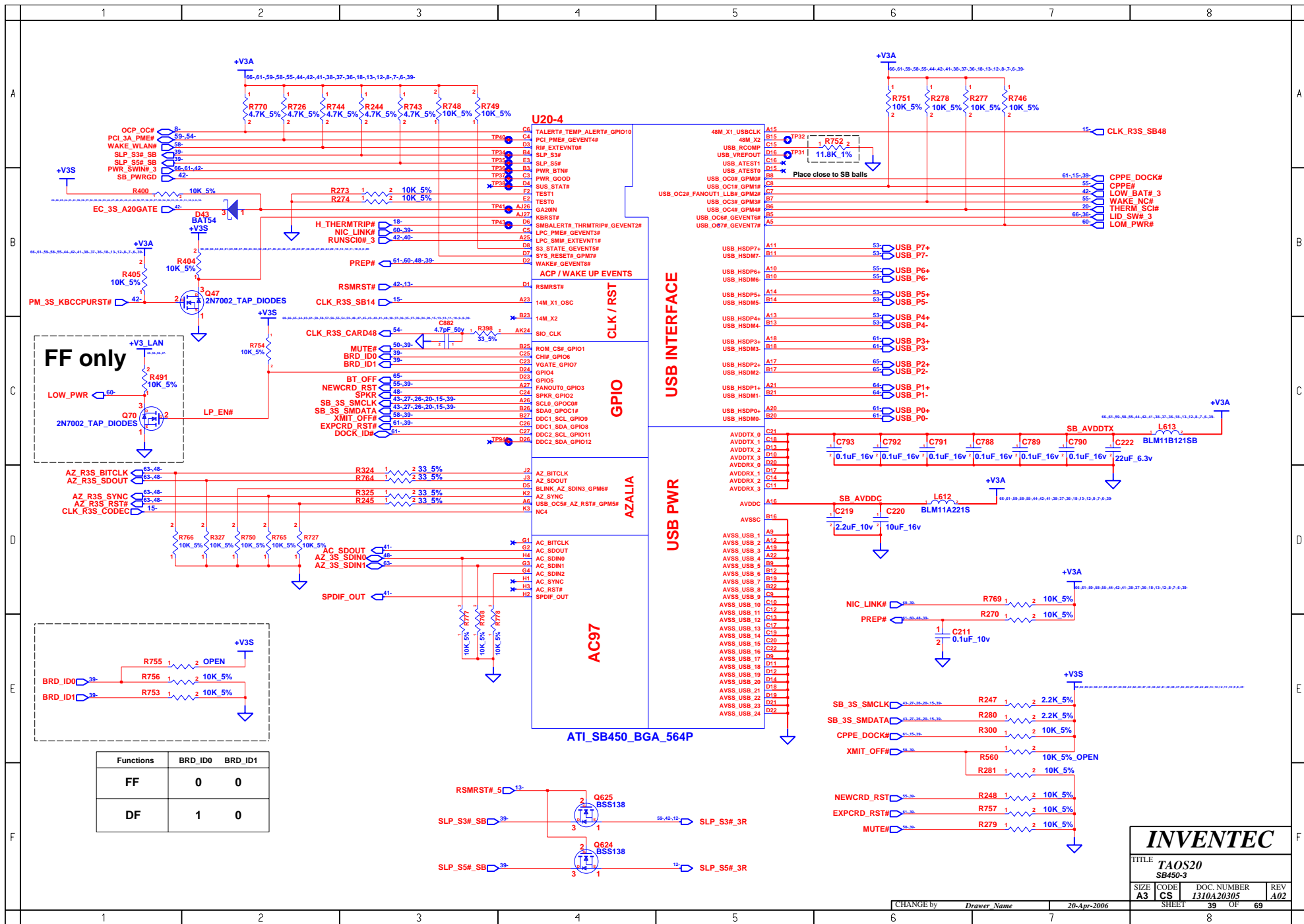
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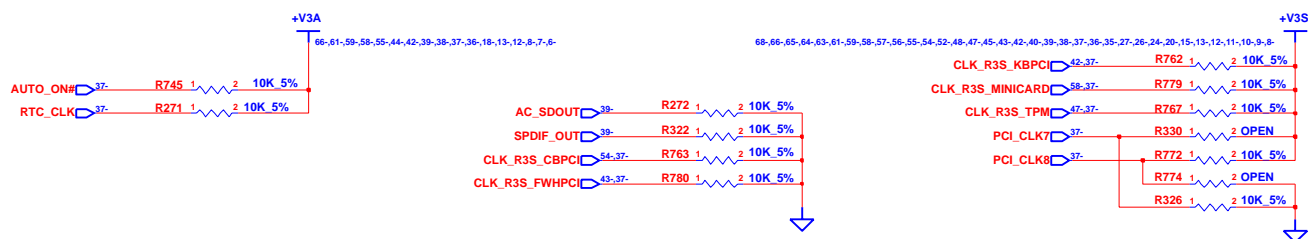


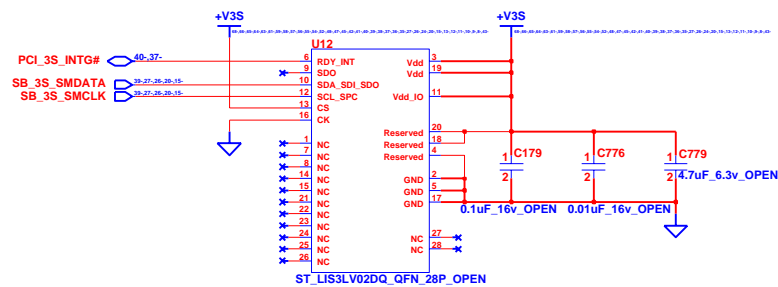
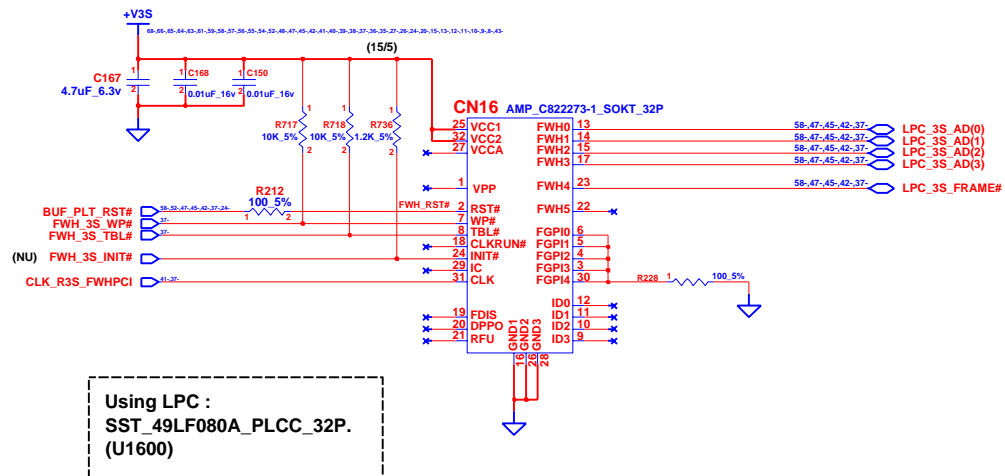
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TITLE TAOS20 SB450-3			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

	ACPWRON	AC_SDOUT	RTC_CLK	SPDIF_OUT	CLK_R3S_CBPCI	CLK_R3S_KBPCI	CLK_R3S_FWHPCI	CLK_R3S_MINICARD	PCI_CLK6	PCI_CLK7 PCI_CLK8
PULL HIGH	MANUAL POWER ON ■	USE DEBUG STRAPS	INTERNAL RTC ■	SIO 24MHz	48MHz XTAL	PHY PWRDOWN DISABLE ■	USE INT. PLL48	PCIE CM_SET LOW ■	CPU K8 ■	HH: PCI ROM HL: LPC 1 ROM
PULL LOW	AUTO POWER ON	IGNORE DEBUG STRAPS ■	EXTERNAL RTC	SIO 48MHz ■	48MHz OSC ■	PHY PWRDOWN ENABLE	USE EXT. 48M CLK ■	PCIE CM_SET HIGH	CPU P4	LH: LPC 2 ROM ■ LL: FWH ROM





Accelerometer (NI)

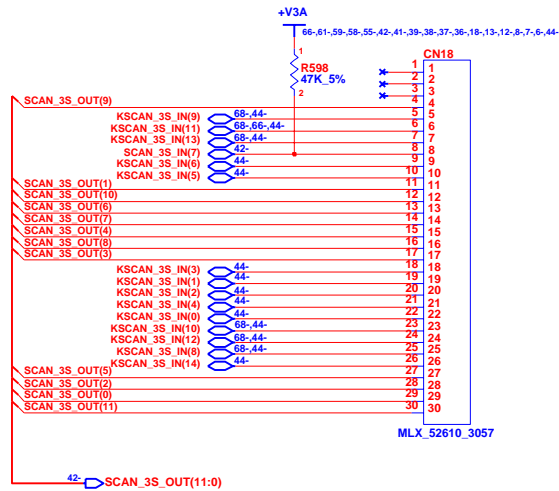
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TITLE			
TAOS20			
LPC & Accelerometer(NI)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A20305	A02

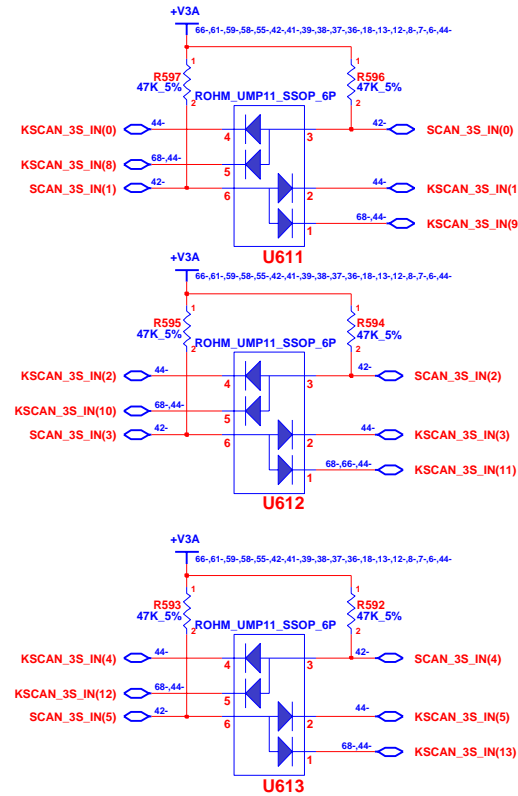
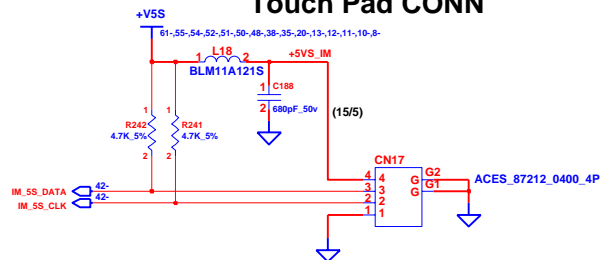
CHANGE by *Drawer Name* 20-Apr-2006

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Keyboard CONN



Touch Pad CONN

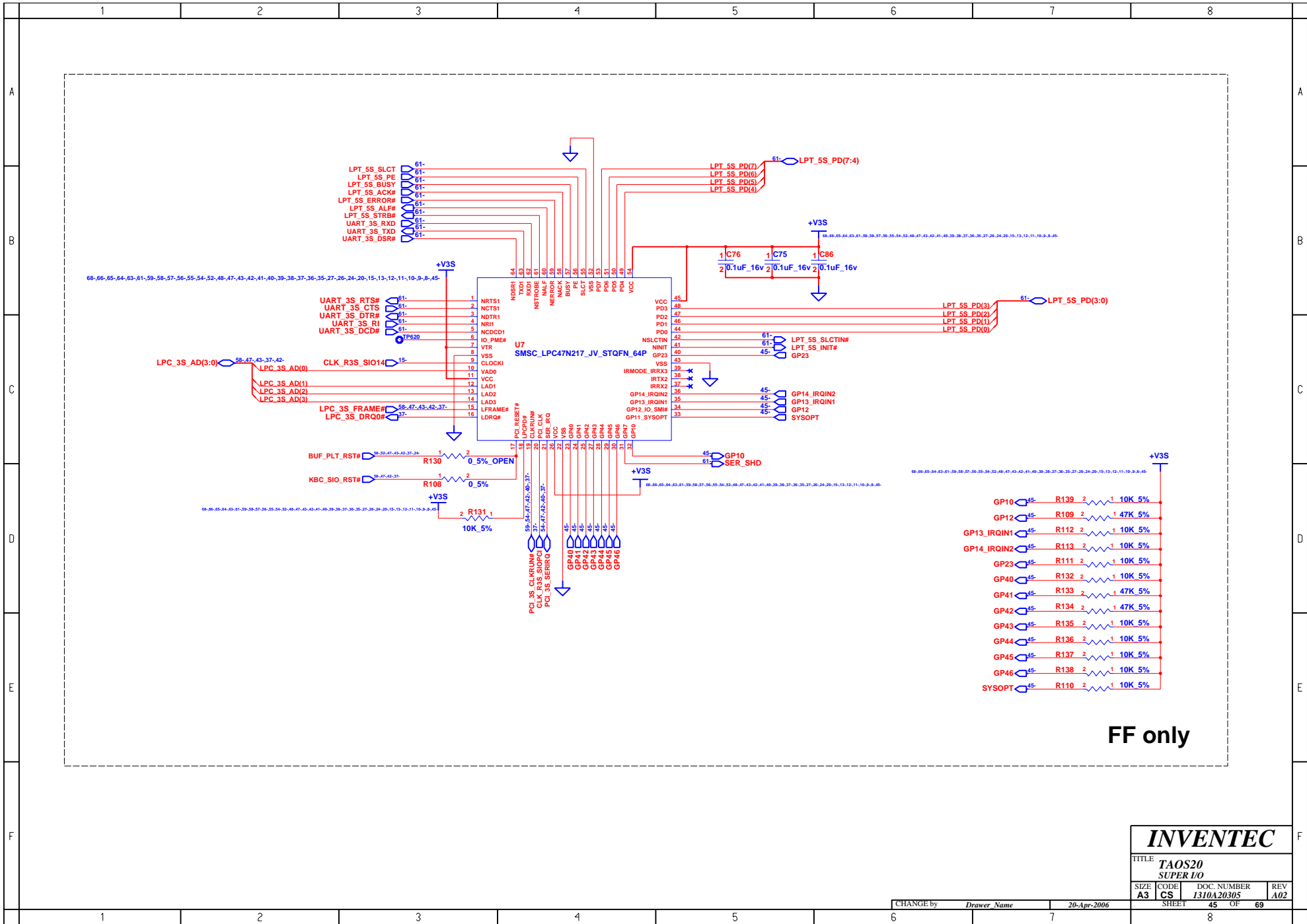


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TITLE			
TAOS20			
INT.KBC/POINT DEVICES			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A20305	A02

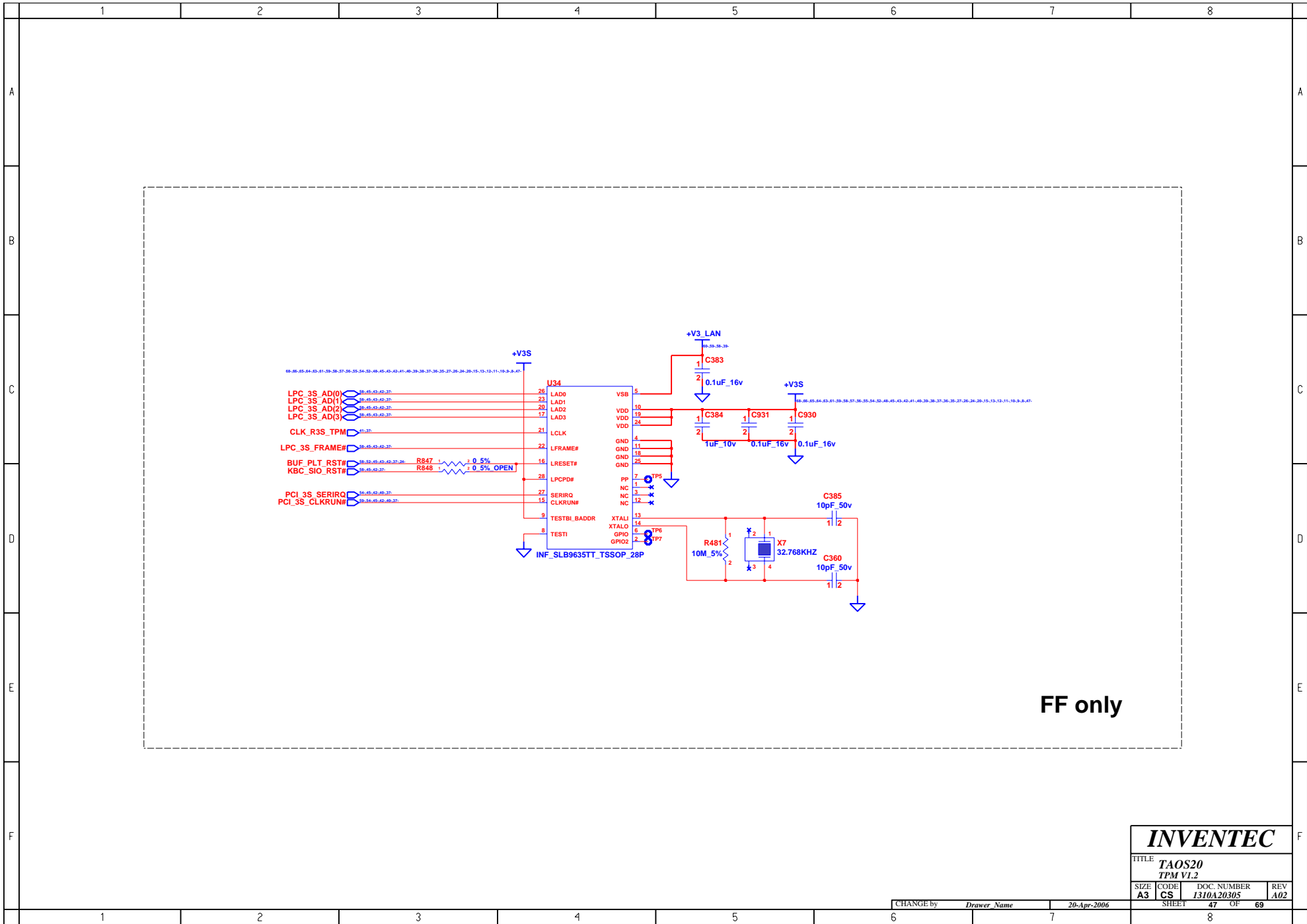
CHANGE by *Drawer Name* 20-Apr-2006

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C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

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TITLE TAOS20 Blank			
SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
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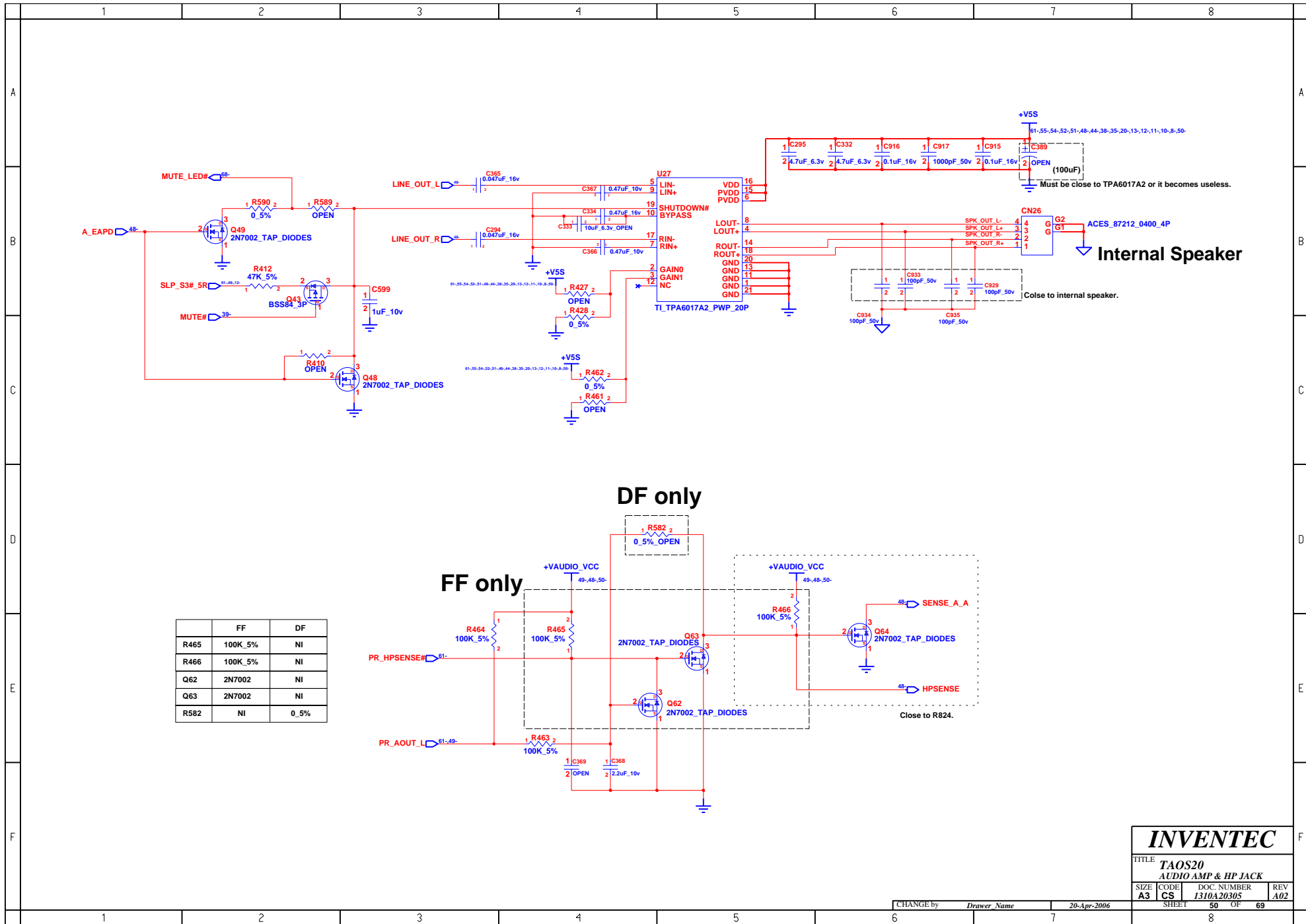
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TITLE
TAOS20
TPM V1.2

SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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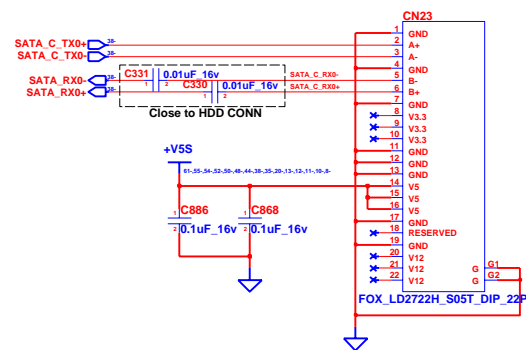
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TITLE
TAOS20
AUDIO AMP & HP JACK

SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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CHANGE by *Drawer Name* 20-Apr-2006

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HDD CONN

INVENTEC

TITLE
TAOS20
SATA HDD CONN

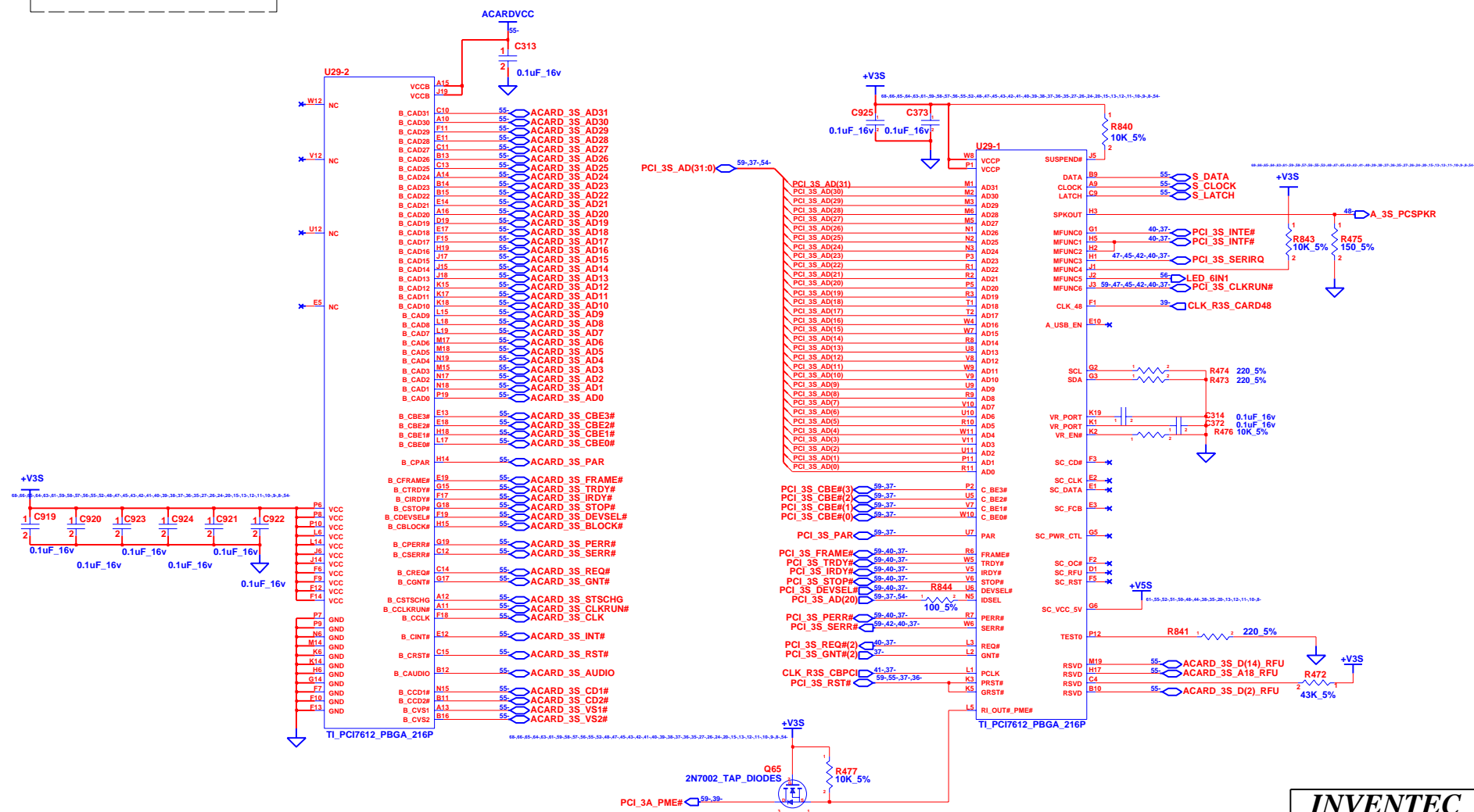
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A3 CS 1310A20305 A02

CHANGE by Drawer Name 20-Apr-2006

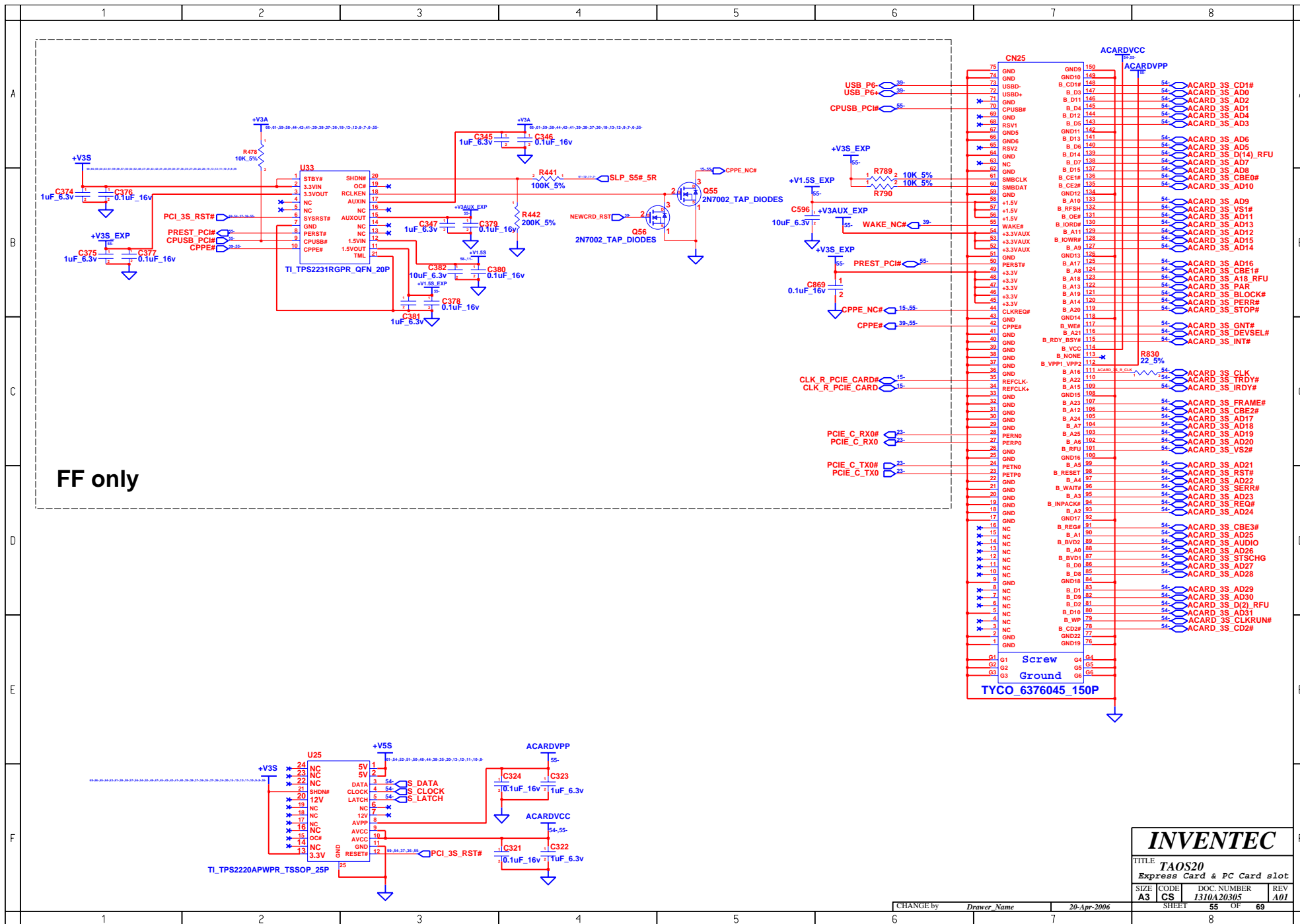
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DF: TI_PCI4512

**INVENTEC**

TITLE			
TAOS20 CARDBUS CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
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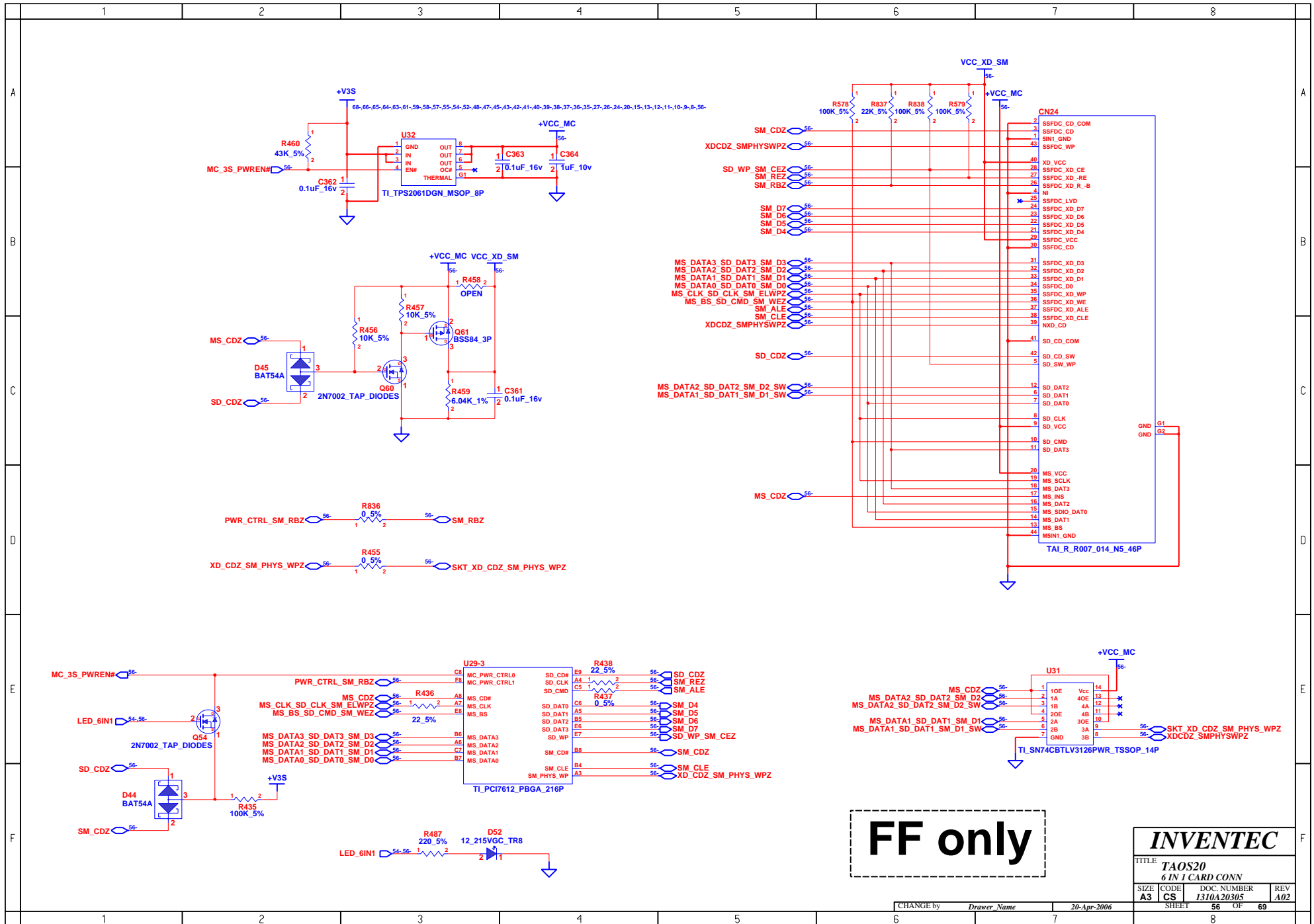
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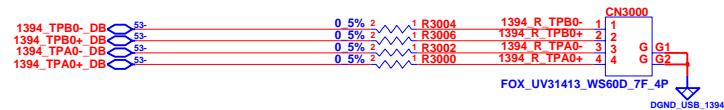
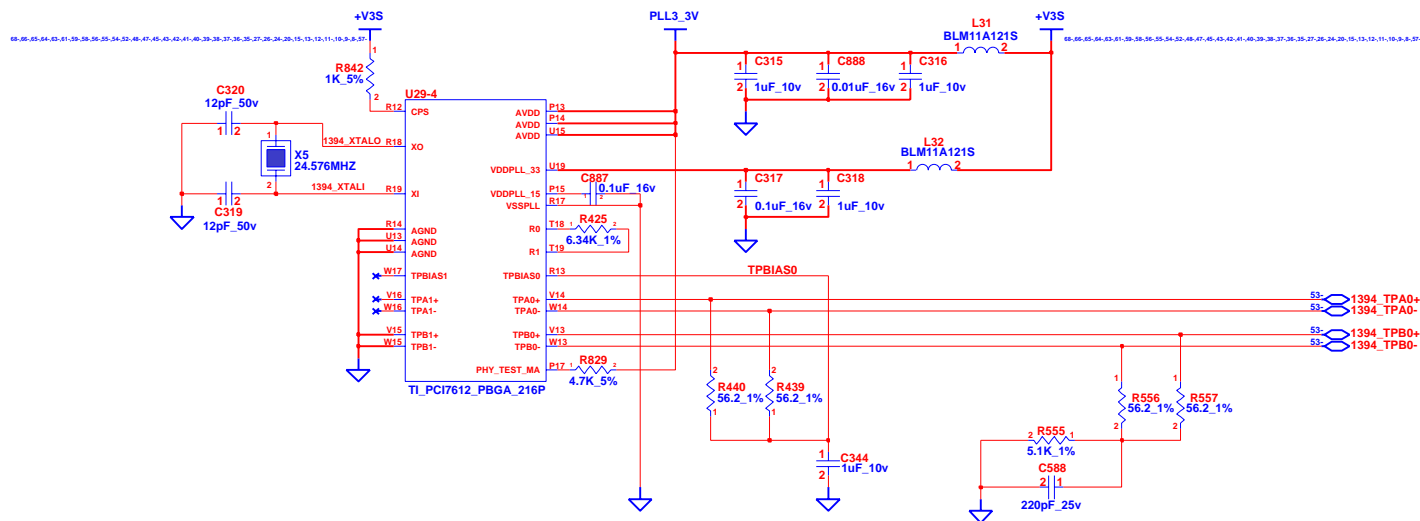
TITLE: TAOS20
Express Card & PC Card slot

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A20305	A01

CHANGE by: Drawer Name 20-Apr-2006

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USB_1394 DB
(See page53)

INVENTEC

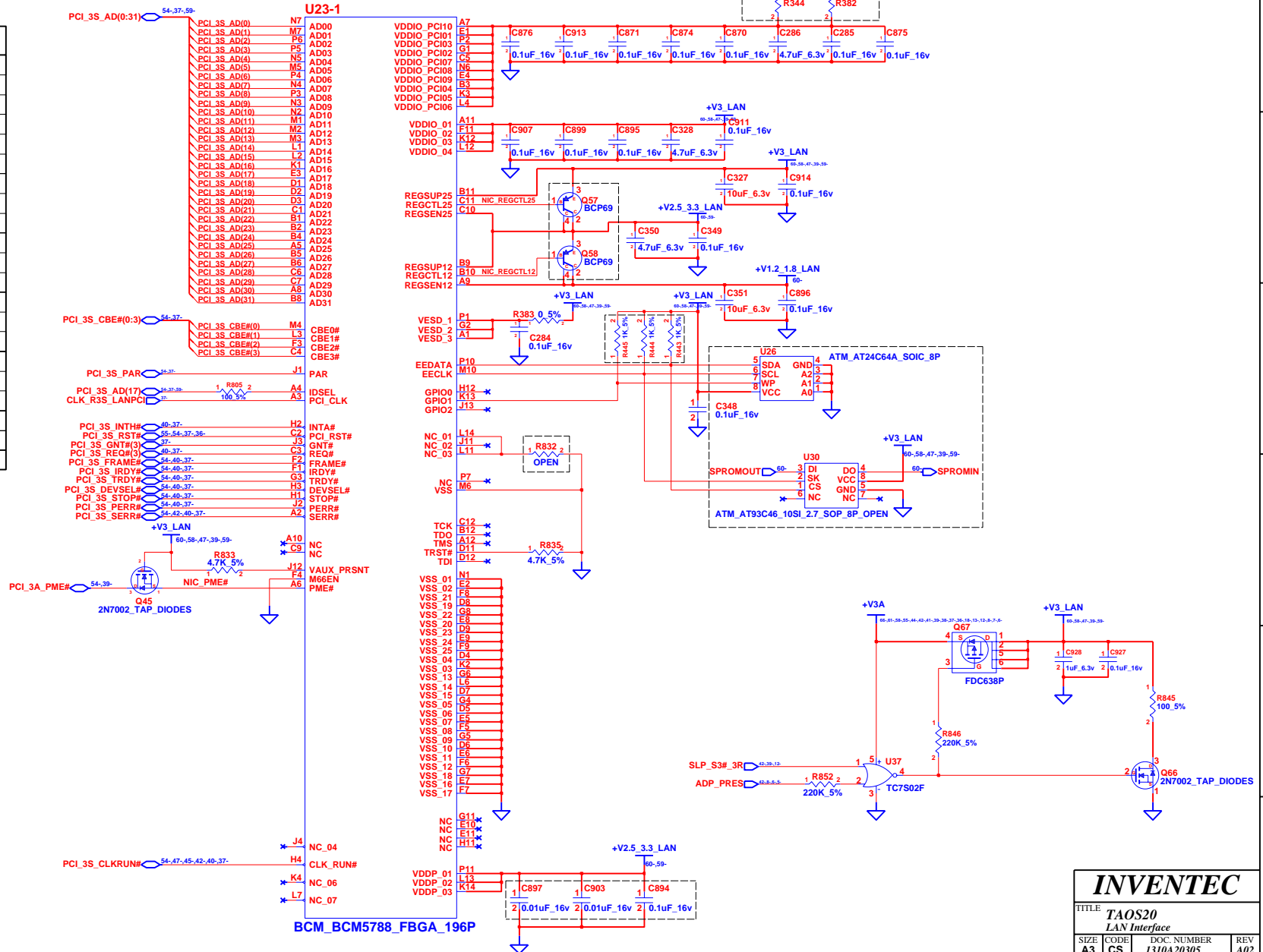
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1394 TAOS20 CONN & USB_1394 DB			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A20305	A02

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REF (U23)	(FF) BCM5788	(DF) BCM4401
R344	NI	0.5%
R382	0.5%	NI
Q57	BCP69	NI
Q58	BCP69	NI
R443	1K_5%	NI
R444	1K_5%	NI
R445	1K_5%	NI
U26	AT24C64A	NI
U30	NI	AT93C46A
R832	NI	0.5%
C897	0.01uF_16V	NI
C894	0.01uF_16V	NI
C903	0.01uF_16V	NI

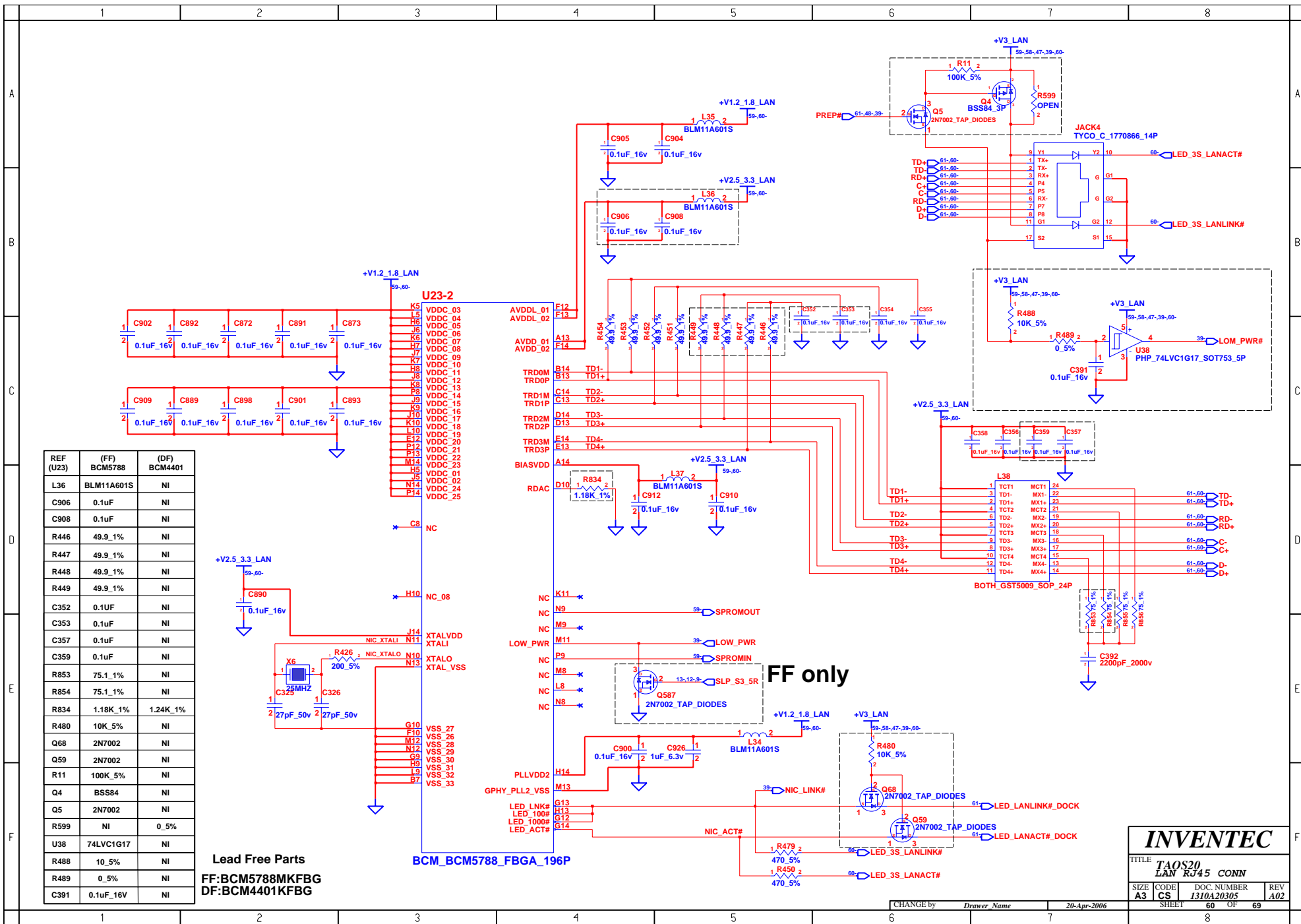
Lead Free Parts
FF:BCM5788MKFBG
DF:BCM4401KFBG

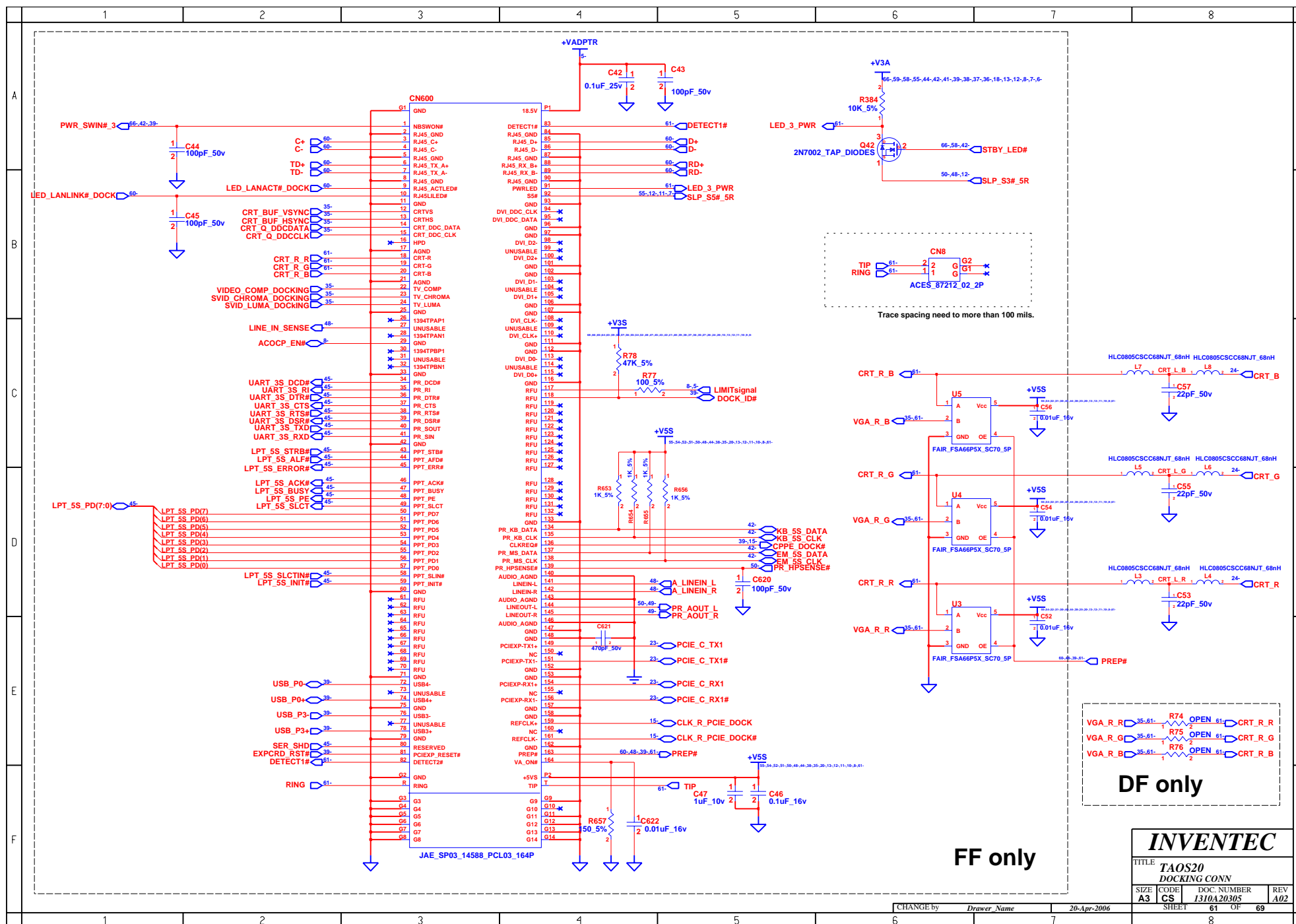


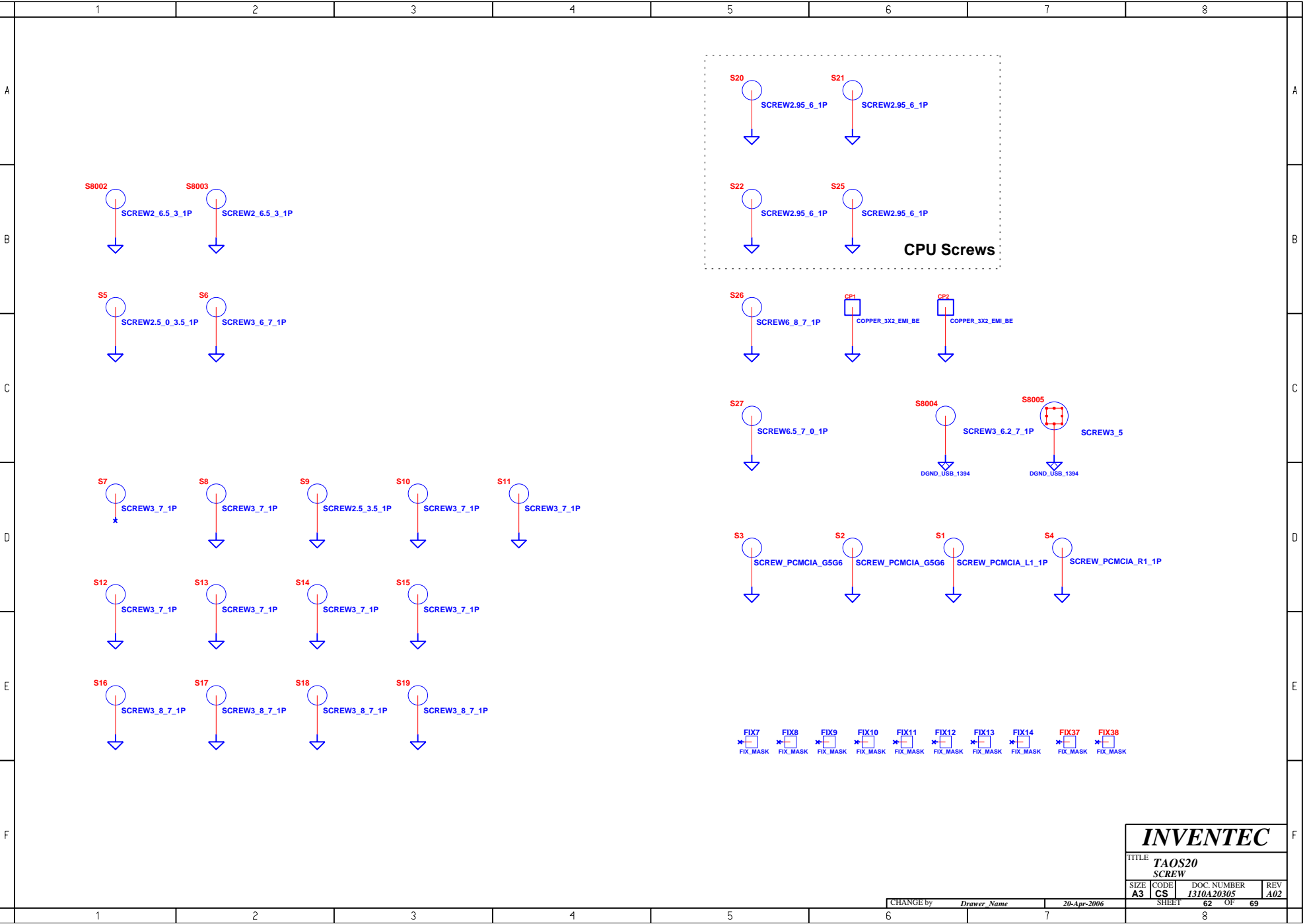
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SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02
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CHANGE by Drawer Name 20-Apr-2006

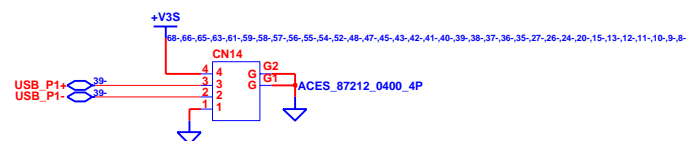






Finger Print DB

FF only



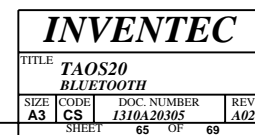
INVENTEC

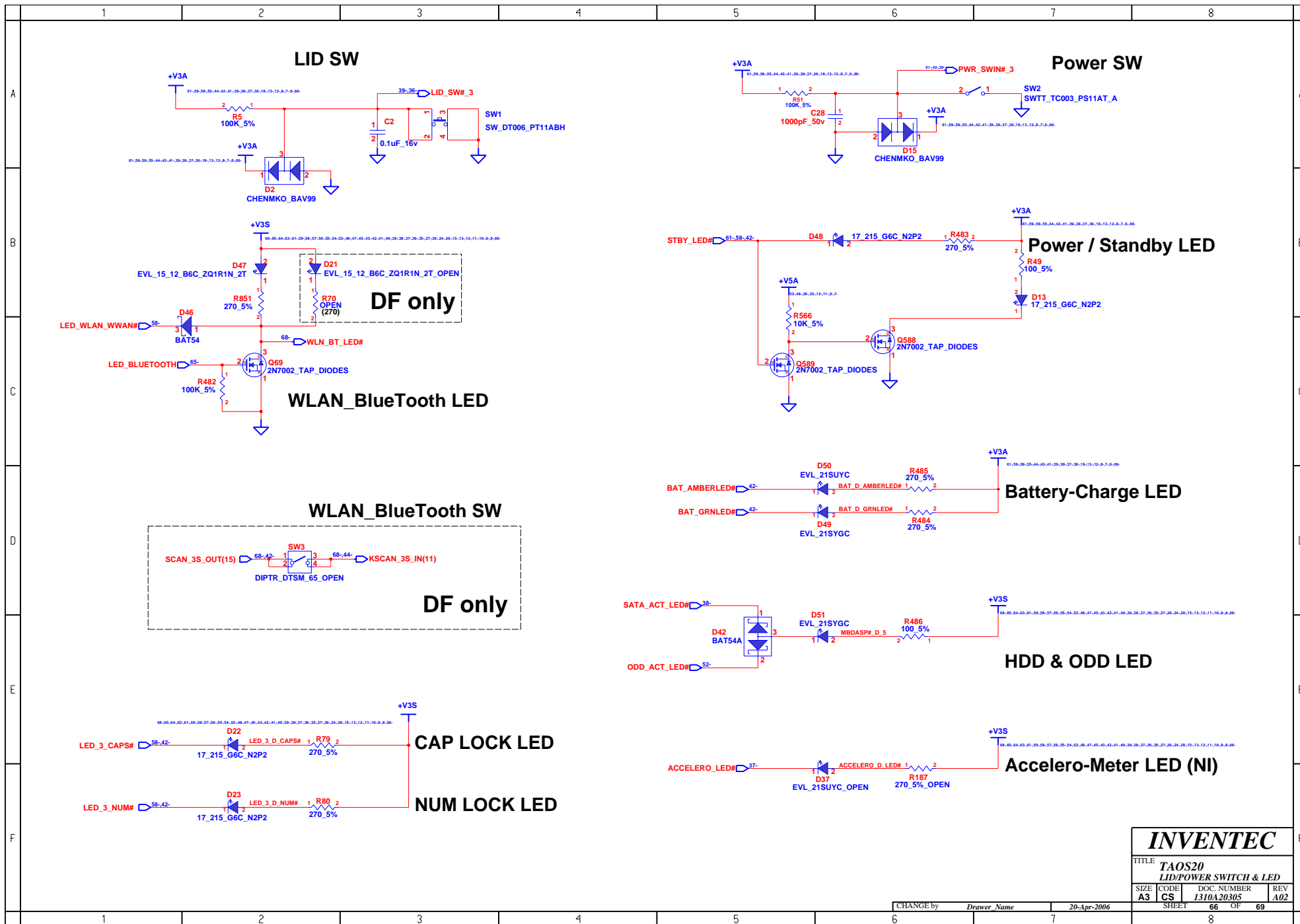
TITLE
TAOS20
Fingerprint

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A20522	A01

CHANGE by *Drawer Name* 20-Apr-2006

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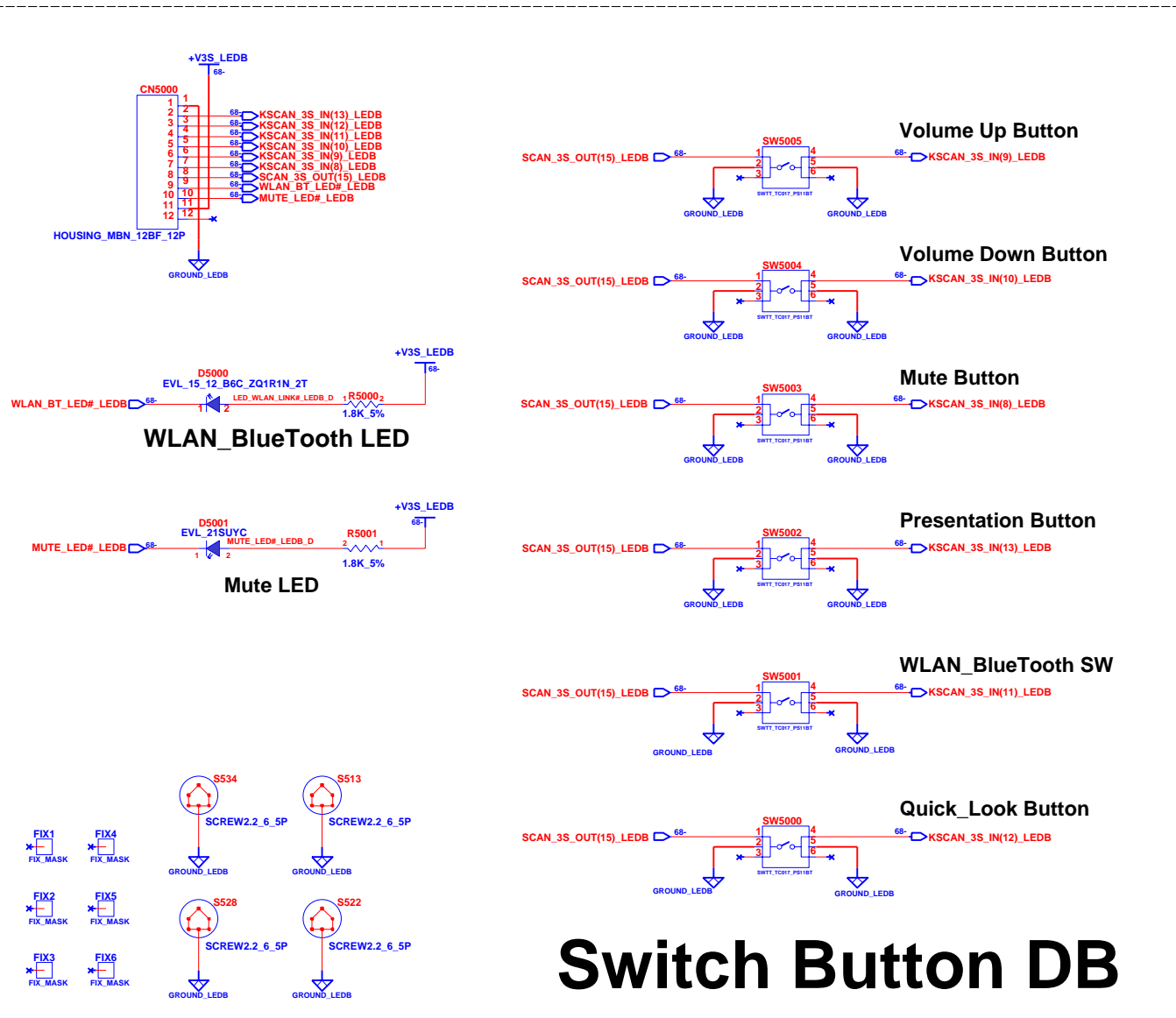




	1	2	3	4	5	6	7	8	
A	<div>BLANK</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE TAOS20 BLANK			
SIZE A3	CODE CS	DOC. NUMBER 1310A20305	REV A02
SHEET		67	OF 69

FF only



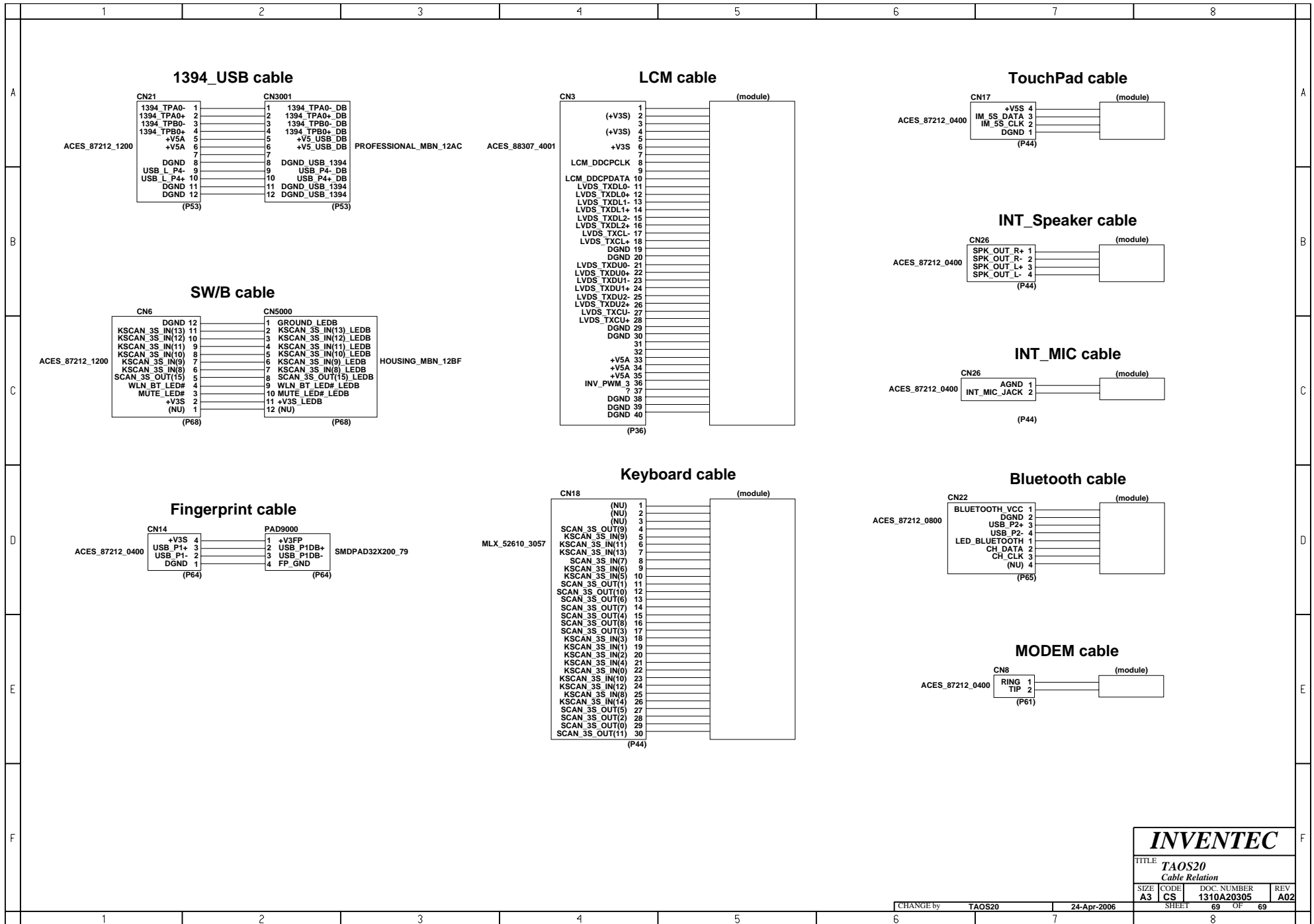
Switch Button DB

INVENTEC

TITLE TAOS20 LED/SWITCH BOARD			
SIZE A3	CODE CS	DOC. NUMBER I310A20305	REV A02

CHANGE by *Drawer Name* 3-May-2006

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INVENTEC

TITLE			
TAOS20			
Cable Relation			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A20305	A02

CHANGE by TAOS20 24-Apr-2006 SHEET 69 OF 69