DAVOS-DF

PV-1-BUILD

2004.11.04

DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE
DRAWER				
DESIGN				
CHECK				
RESPONSIBLE				

SIZE = 3 VER :

FILE NAME : XXXX-XXXXXX-XX

P/N XXXXXXXXXXX

INVENTEC

TITLE ***DAVOS-DF***

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 1 OF 65

TABLE OF CONTENTS

PAGE

5- DC& BATTERY CHARGER
6- SELECT & BATTERY CONN
7- SYSTEM POWER(3V/5V)
8- D/B SYSTEM POWER(+VCCP)
9- SYSTEM POWER(+V2.5/+V1.5A)
10- D/B SYSTEM POWER(+V1.25S)
11- CPU POWER(VCC_CORE)
12- BLANK
13- POWER(SLEEP)
14- POWER(SEQUENCE)
15- CLOCK_GENERATOR
16- DOTHAN-1
17- DOTHAN-2
18- DOTHAN-3
19- DOTHAN-4
20- THERMAL&FAN CONTROLLER
21- ALVISO-1
22- ALVISO-2
23- ALVISO-3
24- ALVISO-4
25- ALVISO-5
26- DDR-DIMM0

PAGE

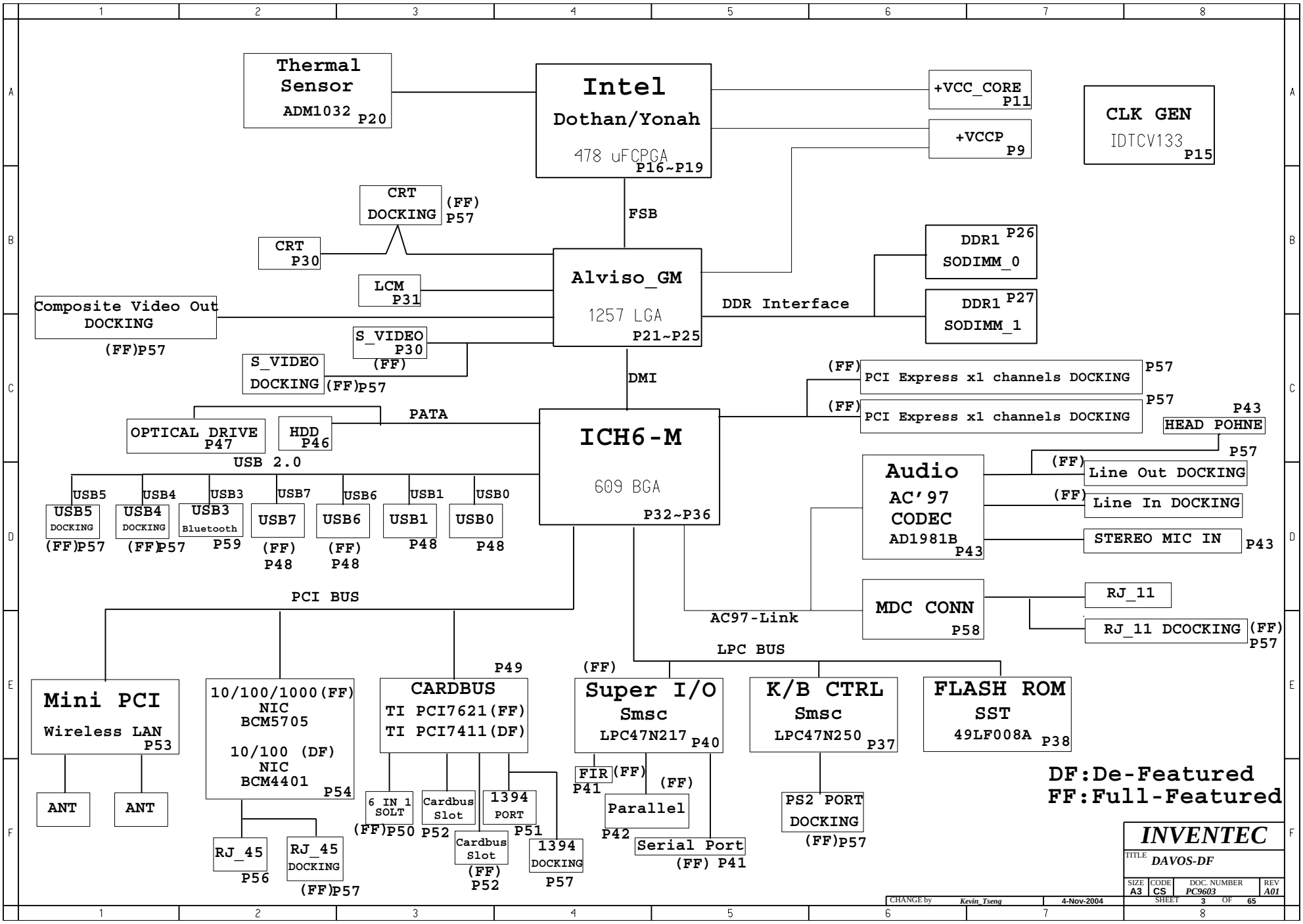
27- DDR-DIMM1
28- DDR-DAMPING RESISTORS
29- DDR-TERMINAL RESISTORS
30- CRT& MINI DIN
31- LCD CONN
32- ICH6-1
33- ICH6-2
34- ICH6-3
35- ICH6-4
36- ICH6-5
37- KBC
38- FWH
39- INT. KB CONN & TOUCHPAD
40- SUPER I/O
41- SERIAL PORT & IR
42- PARALLEL PORT
43- AC97 CODEC
44- EQ, MIC & HP JACK
45- AUDIO AMP
46- HDD CONN
47- OPTICAL DRIVE CONN
48- USB CONN

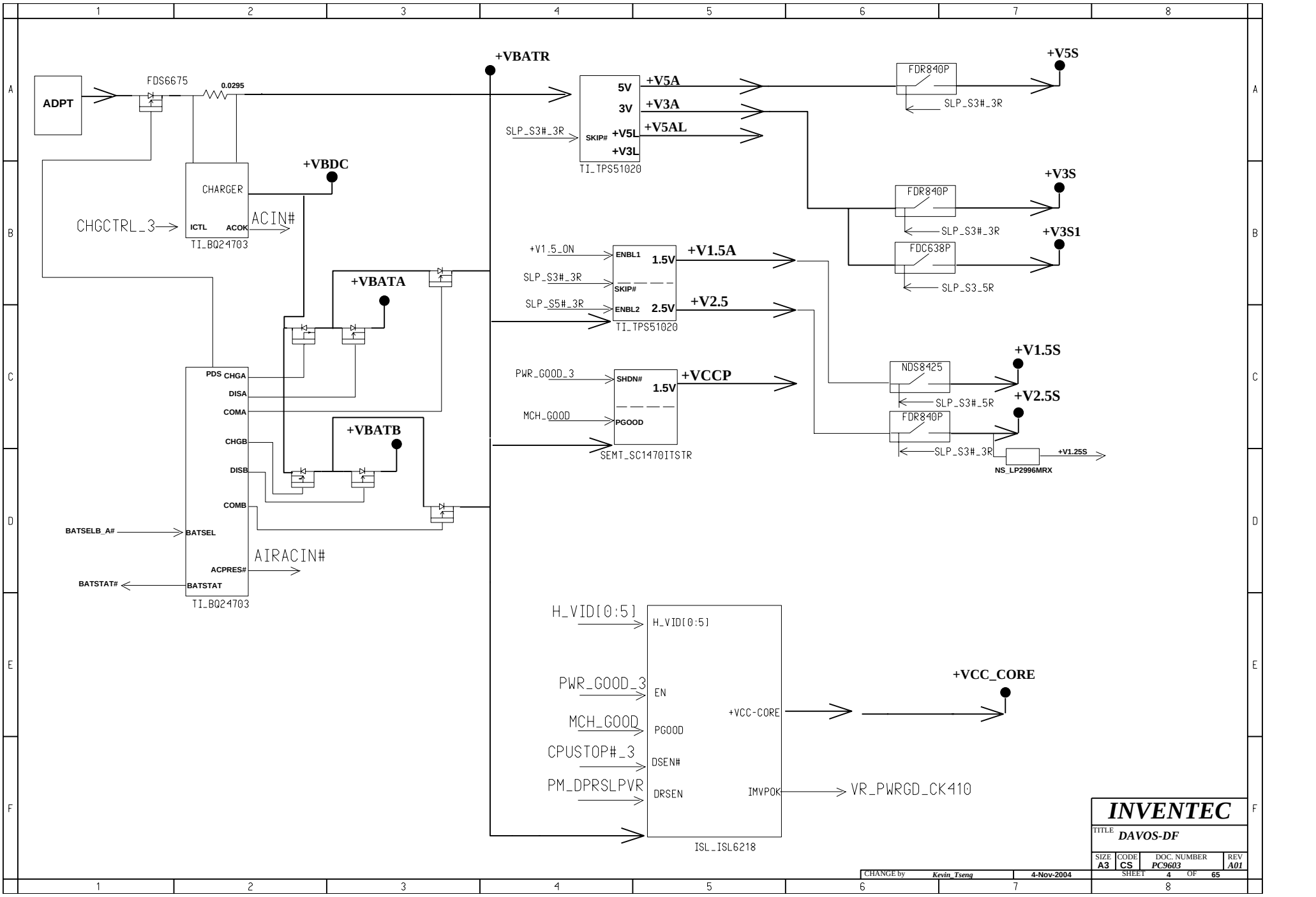
PAGE

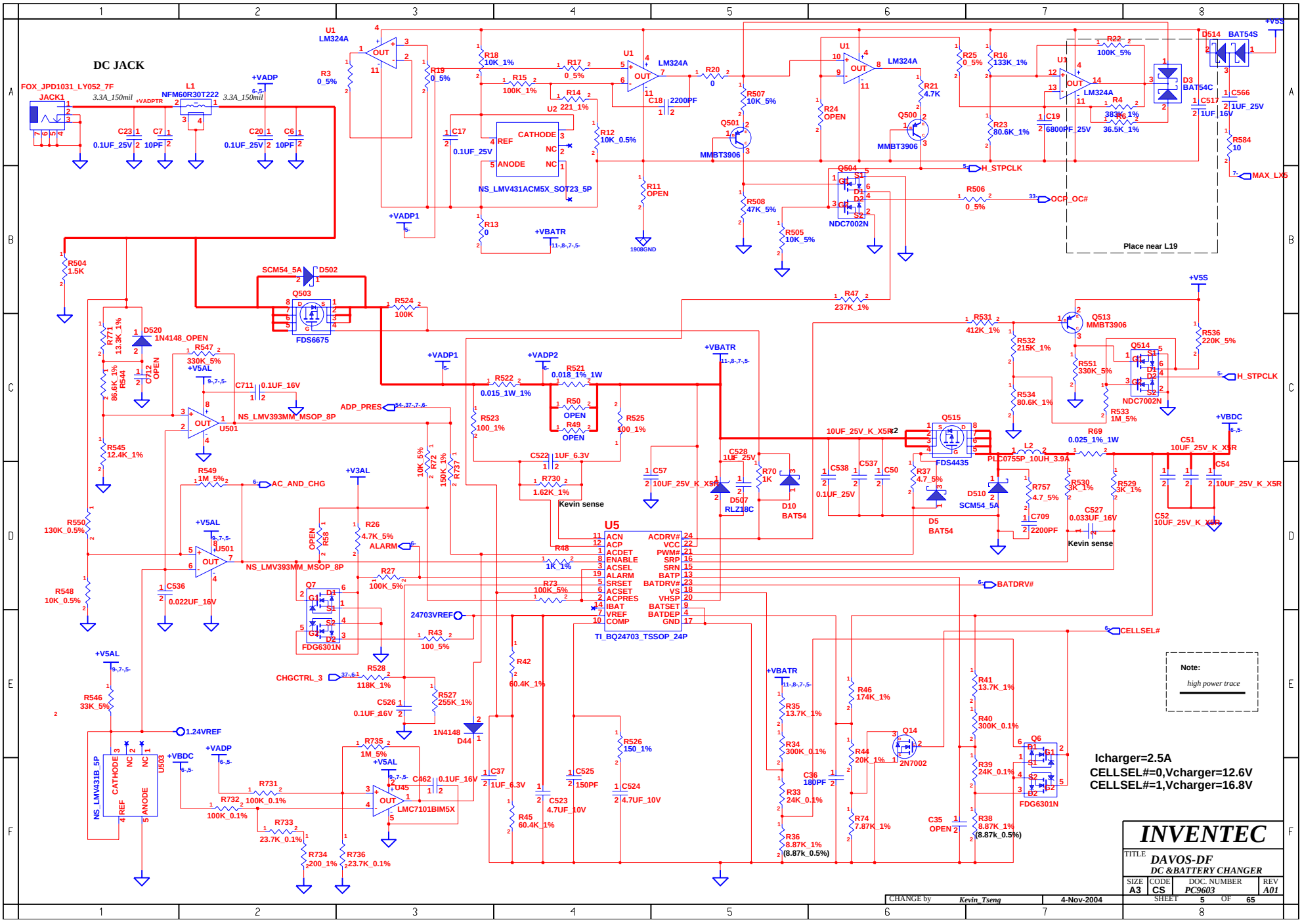
49- CARDBUS CONTROLL
50- 6 IN 1 SLOT DAUGHTOR BOARD
51- 1394 JACK
52- PC CARD SLOT
53- MINIPCI CONN
54- 10/100/1000 NIC
55- BLANK
56- NIC-TRANSFORMER
57- DOCKING CONN
58-MDC CONN
59- BLUETOOTH CONN
60- LEDS
61- SWITCH & BUTTON
62- SCREW
63- BUTTON & LED DAUGHTER BOARD
64- CABLES
65- CHANGE LIST

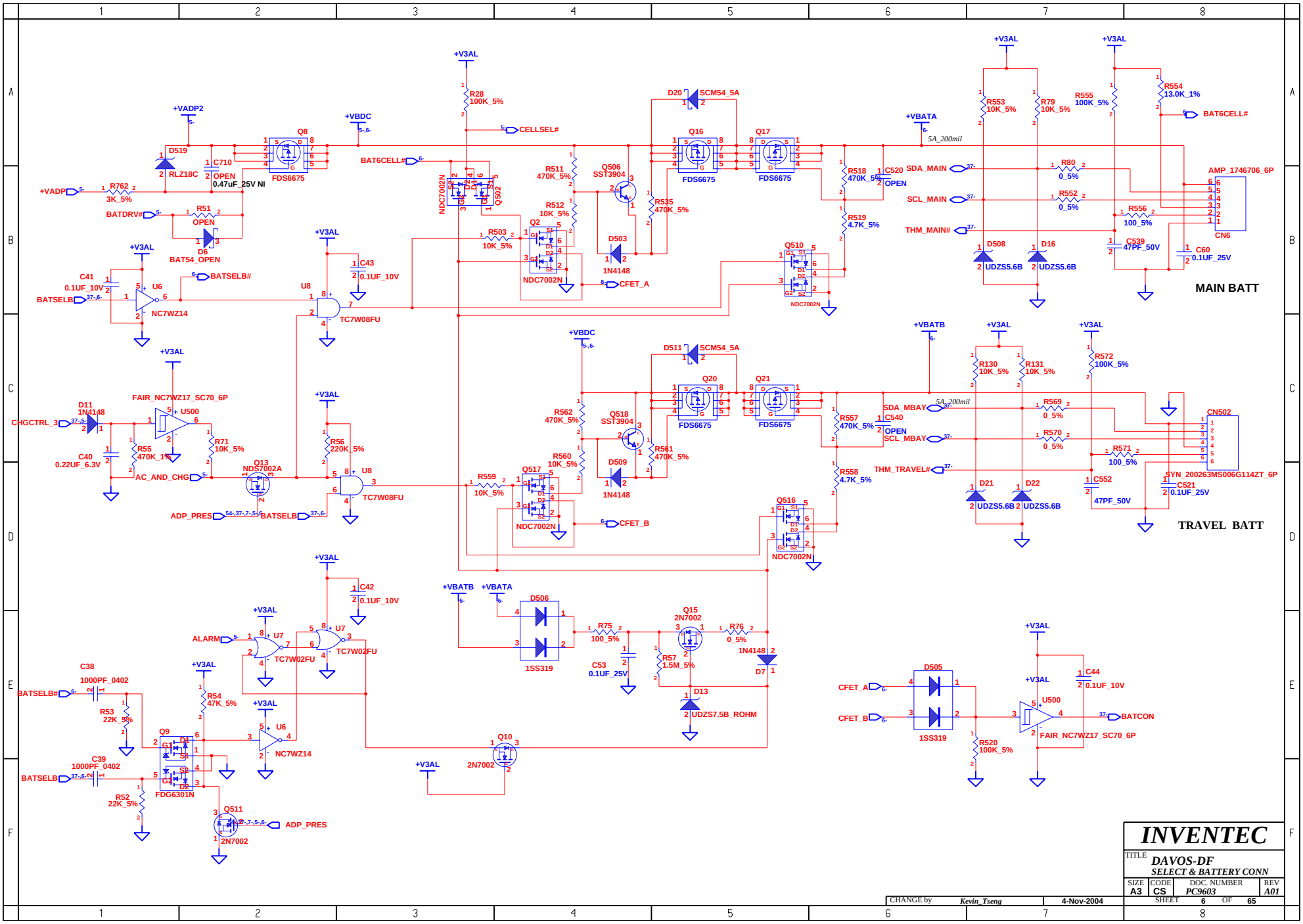
CHANGE by	Kevin_Tseng	4-Nov-2004

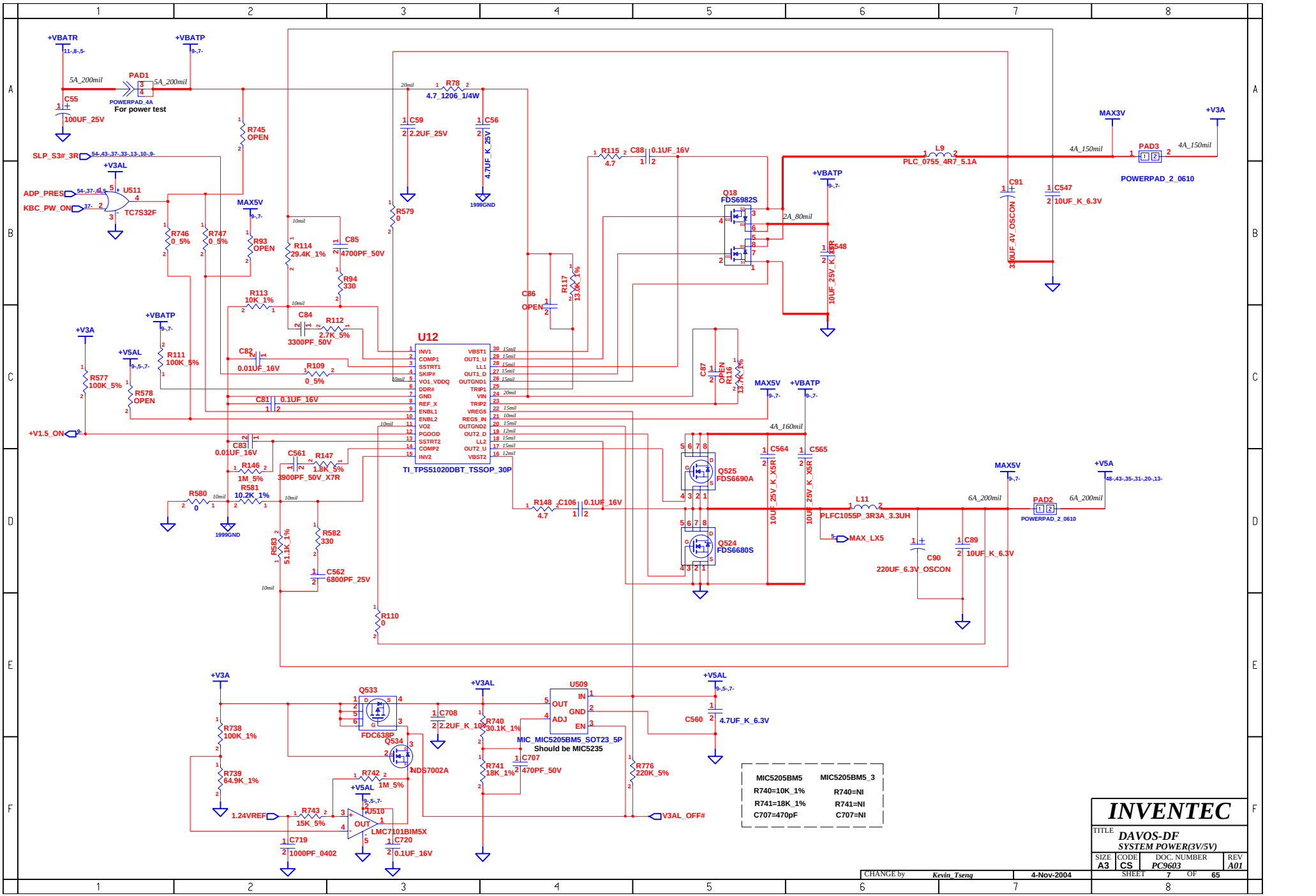
INVENTEC			
TITLE: DAVOS-DF			
SIZE: A3	CODE: CS	DOC. NUMBER: PC9603	REV: A01
SHEET 2 OF 65			

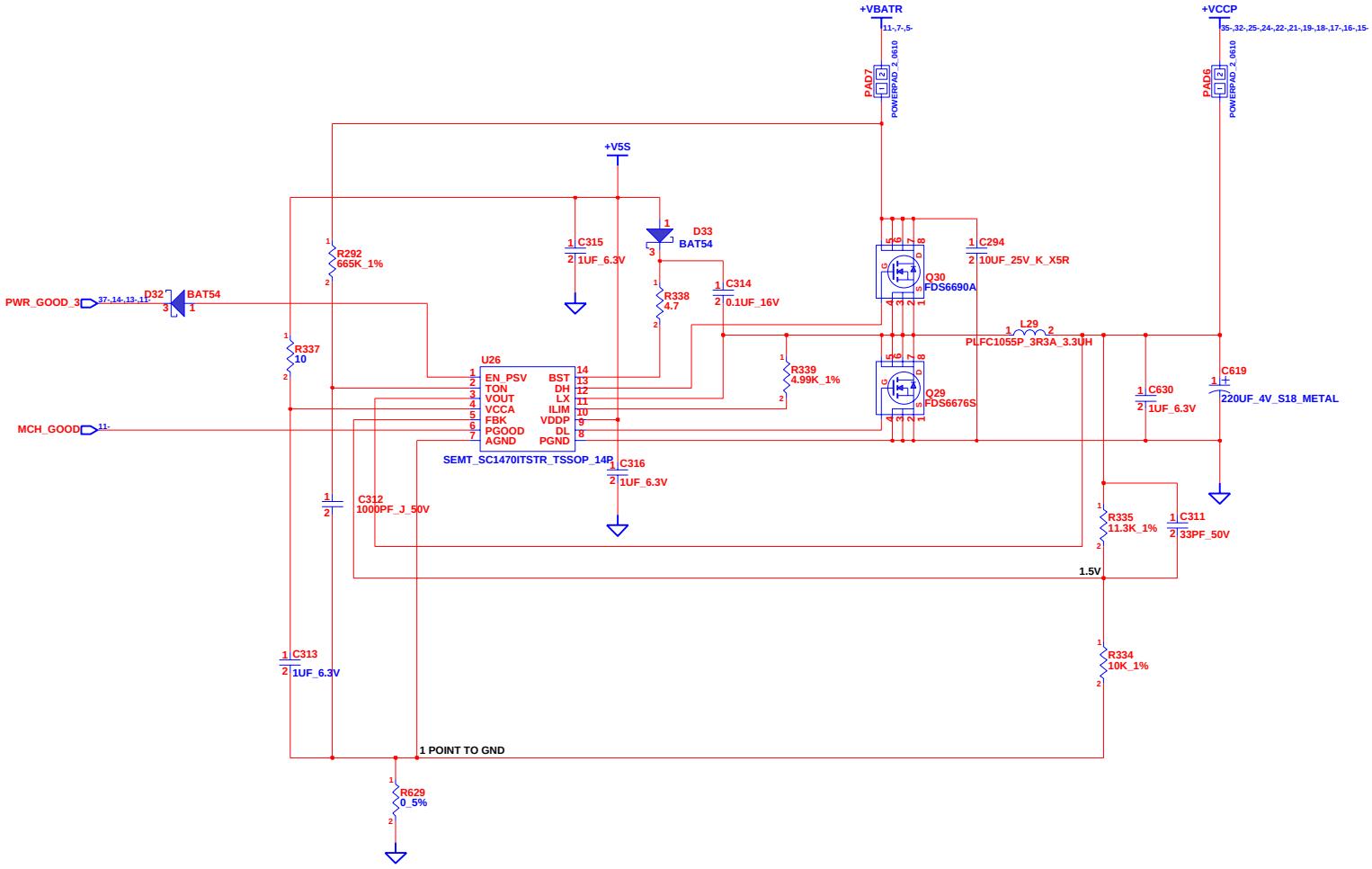

Thermal Sensor
ADM1032 P20
Intel
Dothan/Yonah
478 uFCPGA
P16~P19
+VCC_CORE P11
+VCCP P9
CLK GEN
IDTCV133 P15
CRT DOCKING (FF) P57
CRT P30
FSB
LCM P31
Alviso_GM
1257 LGA
P21~P25
DDR Interface
DDR1 SODIMM_0 P26
DDR1 SODIMM_1 P27
Composite Video Out DOCKING
(FF)P57
S_VIDEO P30 (FF)
S_VIDEO DOCKING (FF)P57
DMI
PATA
OPTICAL DRIVE P47
HDD P46
USB 2.0
ICH6-M
609 BGA
P32~P36
(FF) PCI Express x1 channels DOCKING P57
(FF) PCI Express x1 channels DOCKING P57
HEAD POHNE P43
Audio
AC'97
CODEC
AD1981B P43
(FF) Line Out DOCKING P57
(FF) Line In DOCKING
STEREO MIC IN P43
USB5 USB4 USB3 USB7 USB6 USB1 USB0
USB5 DOCKING (FF)P57
USB4 DOCKING (FF)P57
USB3 Bluetooth P59
USB7 (FF) P48
USB6 (FF) P48
USB1 P48
USB0 P48
PCI BUS
AC97-Link
MDC CONN P58
RJ_11
RJ_11 DCOCKING (FF) P57
LPC BUS
Mini PCI
Wireless LAN P53
ANT
ANT
10/100/1000(FF) NIC BCM5705
10/100 (DF) NIC BCM4401 P54
RJ_45 P56
RJ_45 DOCKING (FF)P57
P49
CARDBUS
TI PCI7621(FF)
TI PCI7411(DF)
6 IN 1 SOLT (FF)P50
Cardbus Slot P52
1394 PORT P51
Cardbus Slot (FF) P52
1394 DOCKING P57
(FF)
Super I/O
Smsc
LPC47N217 P40
FIR (FF) P41
Parallel (FF) P42
Serial Port (FF) P41
K/B CTRL
Smsc
LPC47N250 P37
PS2 PORT DOCKING (FF)P57
FLASH ROM
SST
49LF008A P38
DF:De-Featured
FF:Full-Featured
INVENTEC
TITLE DAVOS-DF
SIZE A3 CODE CS DOC. NUMBER PC9603 REV A01
SHEET 3 OF 65
CHANGE by Kevin_Tseng 4-Nov-2004


ADPT
FDS6675
0.0295
+VBATR
5V
3V
SKIP#
+V5L
+V3L
+V5A
+V3A
+V5AL
SLP_S3#_3R
TI_TPS51020
FDR840P
+V5S
SLP_S3#_3R
+V3S
+V3S1
FDC638P
SLP_S3_5R
+VBDC
CHARGER
CHGCTRL_3
ICTL
ACOK
ACIN#
TI_BQ24703
+VBATA
+VBATB
PDS
CHGA
DISA
COMA
CHGB
DISB
COMB
BATSELB_A#
BATSEL
ACPRES#
AIRACIN#
BATSTAT#
BATSTAT
TI_BQ24703
+V1.5_ON
ENBL1
1.5V
SKIP#
SLP_S5#_3R
ENBL2
2.5V
+V1.5A
+V2.5
TI_TPS51020
PWR_GOOD_3
SHDN#
1.5V
MCH_GOOD
PGOOD
+VCCP
SEMT_SC1470ITSTR
NDS8425
+V1.5S
SLP_S3#_5R
+V2.5S
+V1.25S
NS_LP2996MRX
H_VID[0:5]
PWR_GOOD_3
MCH_GOOD
CPUSTOP#_3
PM_DPRSLPVR
EN
PGOOD
DSEN#
DRSEN
+VCC-CORE
IMVPOK
+VCC_CORE
VR_PWRGD_CK410
ISL_ISL6218
INVENTEC
TITLE
DAVOS-DF
SIZE
A3
CODE
CS
DOC. NUMBER
PC9603
REV
A01
CHANGE by
Kevin_Tseng
4-Nov-2004
SHEET
4
OF
65


DC JACK
FOX_JPD1031_LY052_7F
JACK1
3.3A_150mil
L1
NFM60R30T222
+VADPTR
+VADP
C23 0.1UF_25V
C7 10PF
C20 0.1UF_25V
C6 10PF
U1 LM324A
R3 0_5%
R19 0_5%
R18 10K_1%
R15 100K_1%
R17 0_5%
R14 221_1%
U2
R12 10K_0.5%
R11 OPEN
C17 0.1UF_25V
REF
CATHODE
ANODE
NC
NS_LMV431ACM5X_SOT23_5P
R13 0
+VADP1
+VBATR
1908GND
U1 LM324A
C18 2200PF
R20 0
Q501 MMBT3906
R507 10K_5%
R508 47K_5%
R505 10K_5%
Q500 MMBT3906
R21 4.7K
R24 OPEN
R25 0_5%
R16 133K_1%
R23 80.6K_1%
C19 6800PF_25V
R22 100K_5%
R4 383K_1%
36.5K_1%
D3 BAT54C
D514 BAT54S
C566 1UF_25V
C517 1UF_16V
R584 10
MAX_LX6
+V5S
Q504
NDC7002N
R506 0_5%
OCP_OC#
H_STPCLK
Place near L19
R504 1.5K
SCM54_5A
D502
Q503
FDS6675
R524 100K
R47 237K_1%
R771 13.3K_1%
D520 1N4148_OPEN
R544 86.6K_1%
C712 OPEN
R547 330K_5%
+V5AL
C711 0.1UF_16V
U501
NS_LMV393MM_MSOP_8P
ADP_PRES
+VADP1
+VADP2
R522 0.015_1W_1%
R521 0.018_1%_1W
R50 OPEN
R49 OPEN
R523 100_1%
R525 100_1%
+VBATR
C522 1UF_6.3V
R730 1.62K_1%
Kevin sense
C57 10UF_25V_K_X5R
C528 1UF_25V
D507 RLZ18C
R70 1K
D10 BAT54
10UF_25V_K_X5R x2
C538 0.1UF_25V
C537
C50
Q515
FDS4435
R37 4.7_5%
D5 BAT54
D510 SCM54_5A
R757 4.7_5%
C709 2200PF
L2 PLC0755P_10UH_3.9A
R69 0.025_1%_1W
R530 3K_1%
R529 3K_1%
C527 0.033UF_16V
Kevin sense
C51 10UF_25V_K_X5R
C54 10UF_25V_K_X5R
C52 10UF_25V_K_X5R
+VBDC
R531 412K_1%
R532 215K_1%
R534 80.6K_1%
R551 330K_5%
R533 1M_5%
Q513 MMBT3906
Q514
NDC7002N
R536 220K_5%
H_STPCLK
R549 1M_5%
AC_AND_CHG
+V3AL
R550 130K_0.5%
R548 10K_0.5%
C536 0.022UF_16V
R26 4.7K_5%
ALARM
R58 OPEN
R48 1K_1%
R27 100K_5%
R73 100K_5%
24703VREF
R43 100_5%
Q7
FDG6301N
U5
TI_BQ24703_TSSOP_24P
ACN
ACP
ACDET
ENABLE
ACSEL
ALARM
SRSET
ACSET
ACPRES
IBAT
VREF
COMP
ACDRV#
VCC
PWM#
SRP
SRN
BATP
BATDRV#
VS
VHSP
BATSET
BATDEP
GND
BATDRV#
CELLSEL#
R546 33K_5%
1.24VREF
CHGCTRL_3
R528 118K_1%
C526 0.1UF_16V
R527 255K_1%
1N4148
D44
R42 60.4K_1%
R45 60.4K_1%
C37 1UF_6.3V
C525 150PF
C523 4.7UF_10V
R526 150_1%
C524 4.7UF_10V
R735 1M_5%
C462 0.1UF_16V
U45 LMC7101BIM5X
R731 100K_0.1%
R732 100K_0.1%
R733 23.7K_0.1%
R734 200_1%
R736 23.7K_0.1%
U503 NS_LMV431B_5P
+VBDC
+VADP
R35 13.7K_1%
R34 300K_0.1%
C36 180PF
R33 24K_0.1%
R36 8.87K_1% (8.87K_0.5%)
R46 174K_1%
R44 20K_1%
Q14 2N7002
R74 7.87K_1%
C35 OPEN
R41 13.7K_1%
R40 300K_0.1%
R39 24K_0.1%
R38 8.87K_1% (8.87K_0.5%)
Q6
FDG6301N
Note:
high power trace
Icharger=2.5A
CELLSEL#=0,Vcharger=12.6V
CELLSEL#=1,Vcharger=16.8V
INVENTEC
TITLE
DAVOS-DF
DC &BATTERY CHANGER
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 5 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004


+V3AL
+VADP2
+VBDC
+VBATA
+VBATB
CELLSEL#
BAT6CELL#
BATDRV#
BATSELB#
CFET_A
CFET_B
ADP_PRES
ALARM
SDA_MAIN
SCL_MAIN
THM_MAIN#
SDA_MBAY
SCL_MBAY
THM_TRAVEL#
BATCON
MAIN BATT
TRAVEL BATT
AMP_1746706_6P
CN6
CN502
SYN_200263MS006G114ZT_6P
FAIR_NC7WZ17_SC70_6P
NC7WZ14
TC7W08FU
TC7W02FU
FDS6675
NDC7002N
FDG6301N
1SS319
2N7002
NDS7002A
SCM54_5A
UDZS5.6B
UDZS7.5B_ROHM
1N4148
SST3904
5A_200mil
INVENTEC
DAVOS-DF
SELECT & BATTERY CONN
CHANGE by
Kevin_Tseng
4-Nov-2004
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 6 OF 65


+VBATR
+VBATP
PAD1
POWERPAD_4A
For power test
C55
100UF_25V
SLP_S3#_3R
+V3AL
ADP_PRES
KBC_PW_ON
U511
TC7S32F
MAX5V
R745
OPEN
R746
0_5%
R747
0_5%
R93
OPEN
R114
29.4K_1%
C85
4700PF_50V
R94
330
R113
10K_1%
C84
3300PF_50V
R112
2.7K_5%
C82
0.01UF_16V
R109
0_5%
C81
0.1UF_16V
+V3A
+V5AL
R577
100K_5%
R578
OPEN
+VBATP
R111
100K_5%
+V1.5_ON
C83
0.01UF_16V
R146
1M_5%
C561
3900PF_50V_X7R
R147
1.8K_5%
R580
0
R581
10.2K_1%
1999GND
R582
330
R583
51.1K_1%
C562
6800PF_25V
R110
0
U12
TI_TPS51020DBT_TSSOP_30P
INV1
COMP1
SSTRT1
SKIP#
VO1_VDDQ
DDR#
GND
REF_X
ENBL1
ENBL2
VO2
PGOOD
SSTRT2
COMP2
INV2
VBST1
OUT1_U
LL1
OUT1_D
OUTGND1
TRIP1
VIN
TRIP2
VREG5
REGS_IN
OUTGND2
OUT2_D
LL2
OUT2_U
VBST2
R579
0
C59
2.2UF_25V
C56
4.7UF_K_25V
1999GND
R78
4.7_1206_1/4W
R115
4.7
C88
0.1UF_16V
C86
OPEN
R117
13.0K_1%
R148
4.7
C106
0.1UF_16V
C87
OPEN
R116
13.7K_1%
Q18
FDS6982S
2A_80mil
L9
PLC_0755_4R7_5.1A
4A_150mil
MAX3V
PAD3
POWERPAD_2_0610
+V3A
C91
330UF_4V_OSCON
C547
10UF_K_6.3V
C548
10UF_25V_K_X5R
+VBATP
MAX5V
+VBATP
4A_160mil
Q525
FDS6690A
Q524
FDS6680S
C564
10UF_25V_K_X5R
C565
10UF_25V_K_X5R
L11
PLFC1055P_3R3A_3.3UH
MAX_LX5
6A_200mil
MAX5V
PAD2
POWERPAD_2_0610
+V5A
C90
220UF_6.3V_OSCON
C89
10UF_K_6.3V
+V3A
Q533
FDC638P
Q534
NDS7002A
R738
100K_1%
R739
64.9K_1%
R742
1M_5%
R743
15K_5%
1.24VREF
U510
LMC7101BIM5X
C719
1000PF_0402
C720
0.1UF_16V
+V3AL
C708
2.2UF_K_10V
R740
30.1K_1%
R741
18K_1%
C707
470PF_50V
U509
MIC_MIC5205BM5_SOT23_5P
Should be MIC5235
IN
GND
EN
OUT
ADJ
+V5AL
C560
4.7UF_K_6.3V
R776
220K_5%
V3AL_OFF#
MIC5205BM5
R740=10K_1%
R741=18K_1%
C707=470pF
MIC5205BM5_3
R740=NI
R741=NI
C707=NI
INVENTEC
TITLE
DAVOS-DF
SYSTEM POWER(3V/5V)
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 7 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004



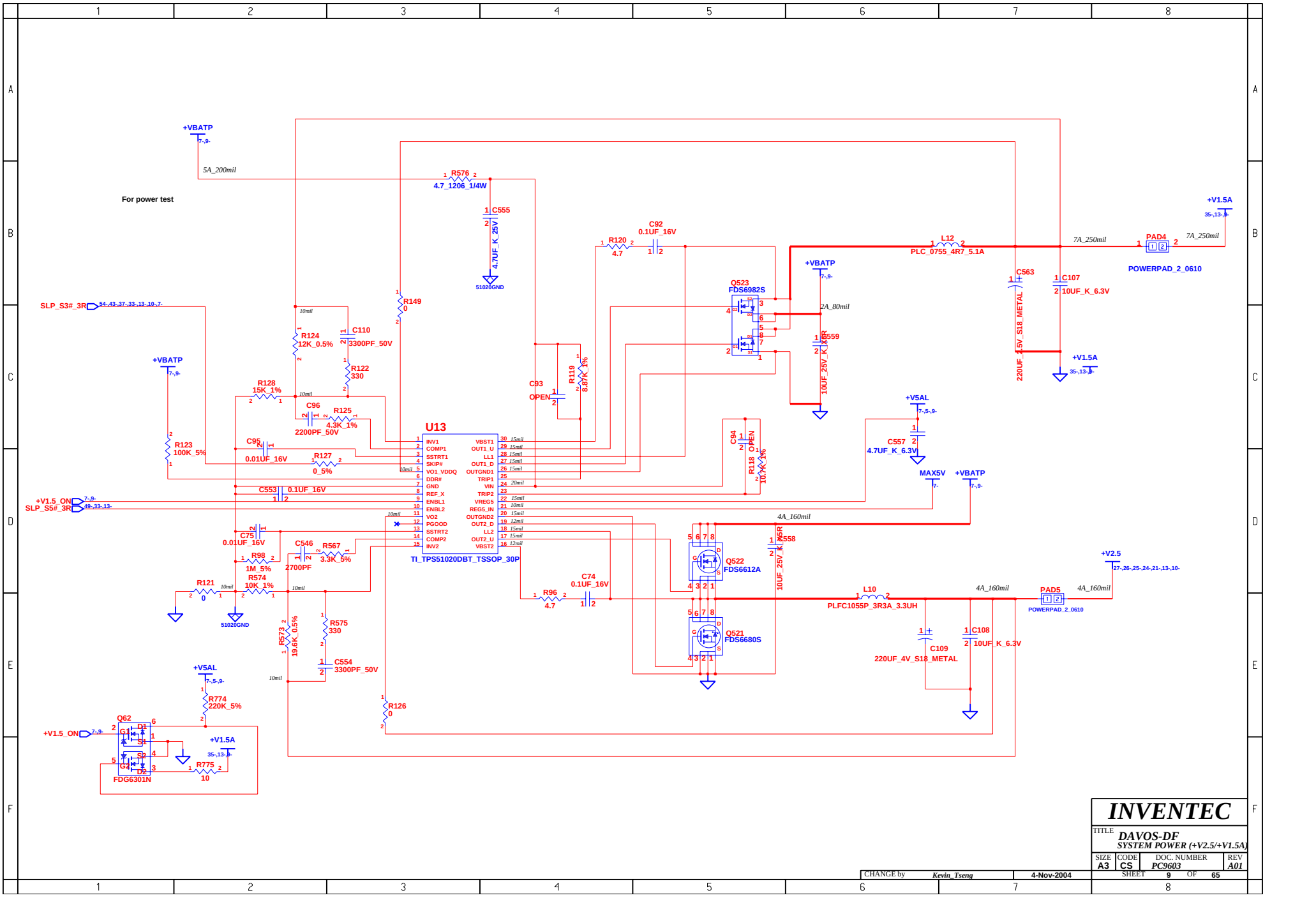
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
SYSTEM POWER(+VCCP)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 8 OF 65



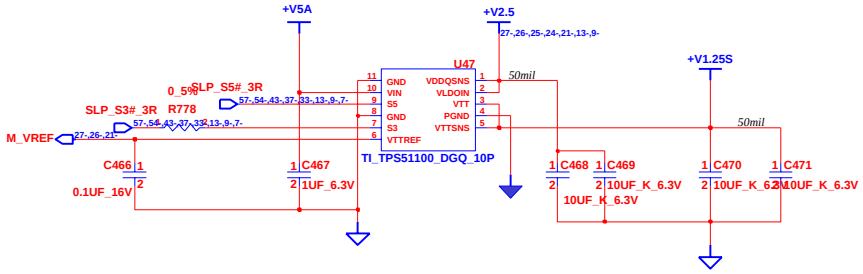
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
SYSTEM POWER (+V2.5/+V1.5A)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 9 OF 65



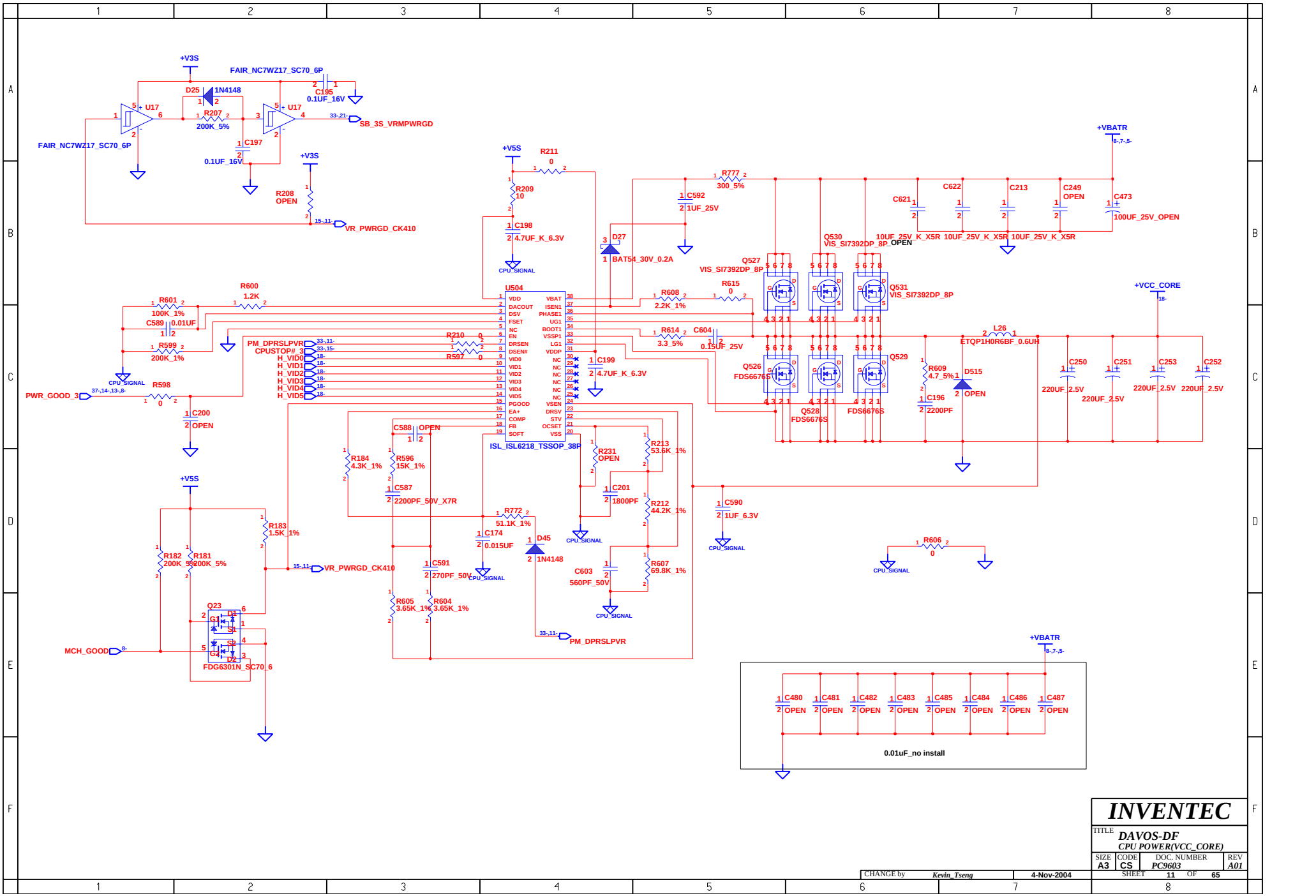
CHANGE by	Kevin_Tseng	4-Nov-2004

INVENTEC

TITLE: ***DAVOS-DF***
SYSTEM POWER(+V1.25S)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 10 OF 65



INVENTEC

TITLE: *DAVOS-DF*
CPU POWER(VCC_CORE)

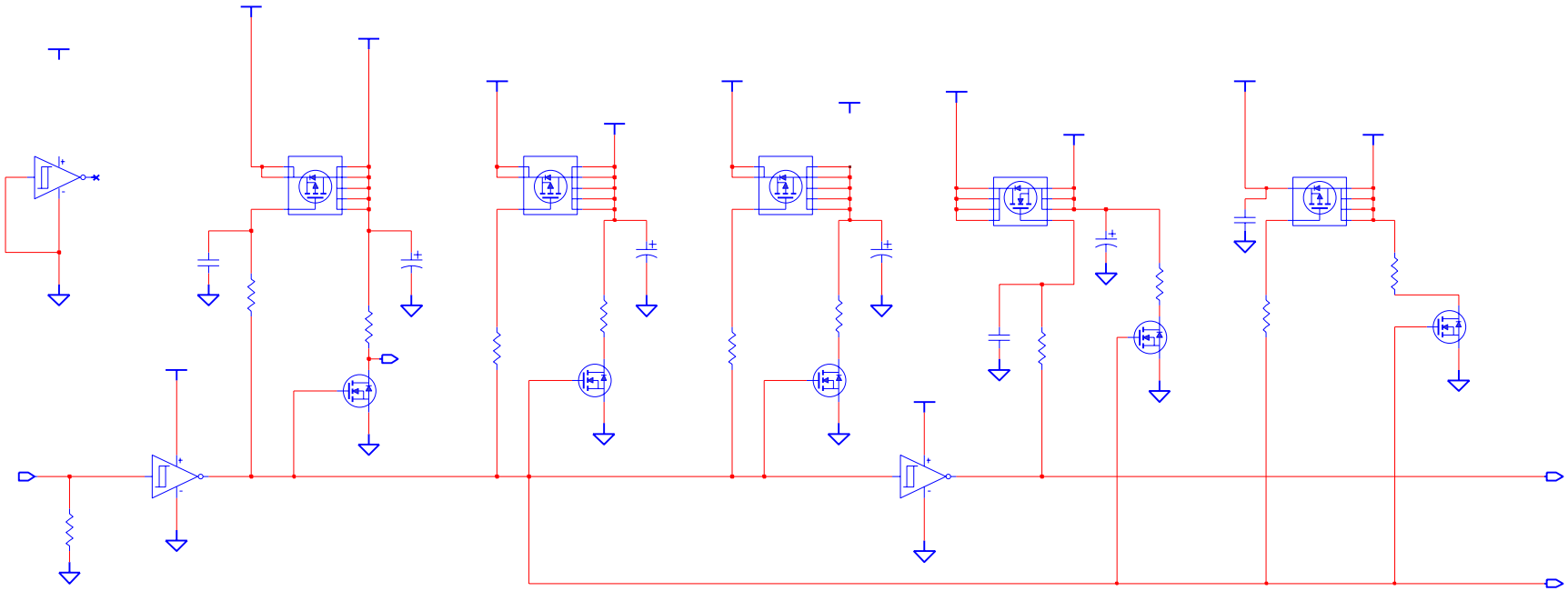
SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

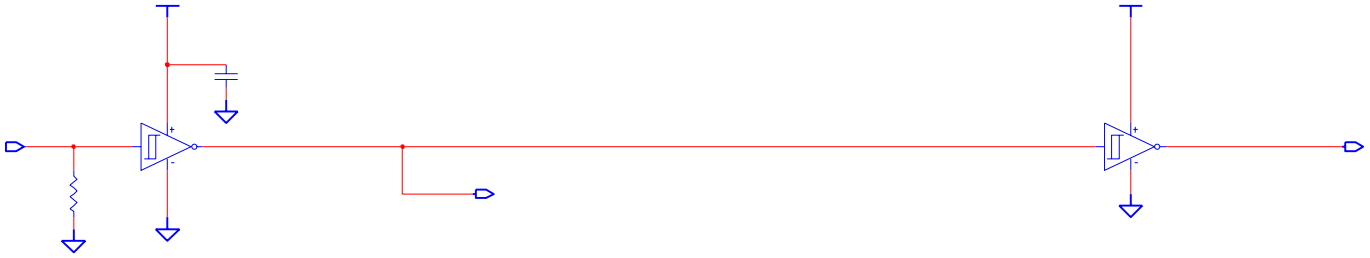
CHANGE by: *Kevin_Tseng* 4-Nov-2004

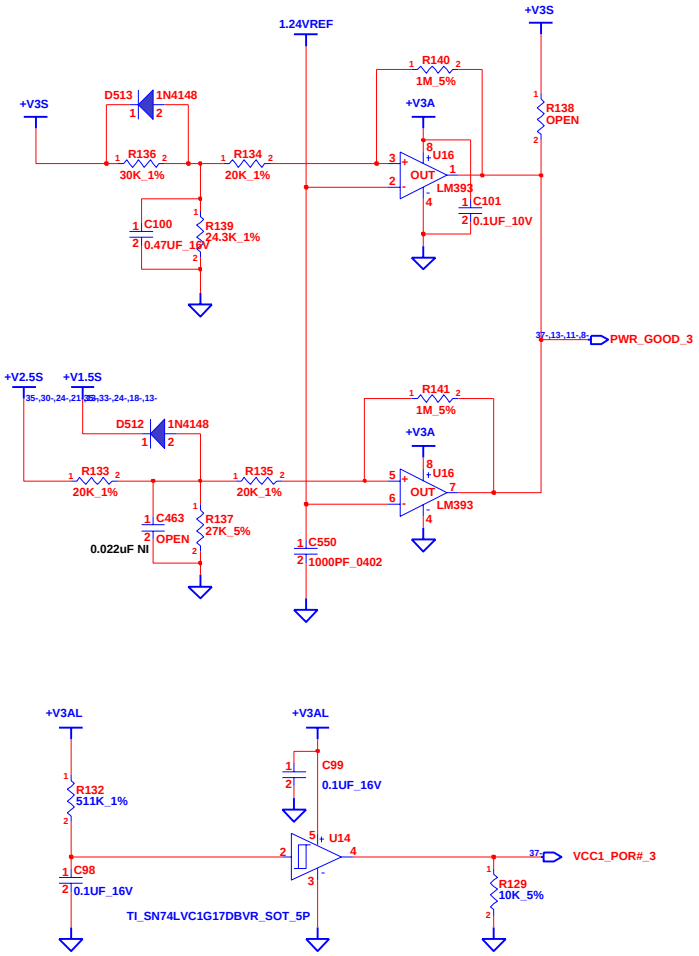
SHEET 11 OF 65

BLANK

INVENTEC			
TITLE	DAVOS-DF		
SIZE A3	CODE CS	DOC. NUMBER PC9603	REV A01
CHANGE by Kevin_Tseng	4-Nov-2004	SHEET 12 OF 65	







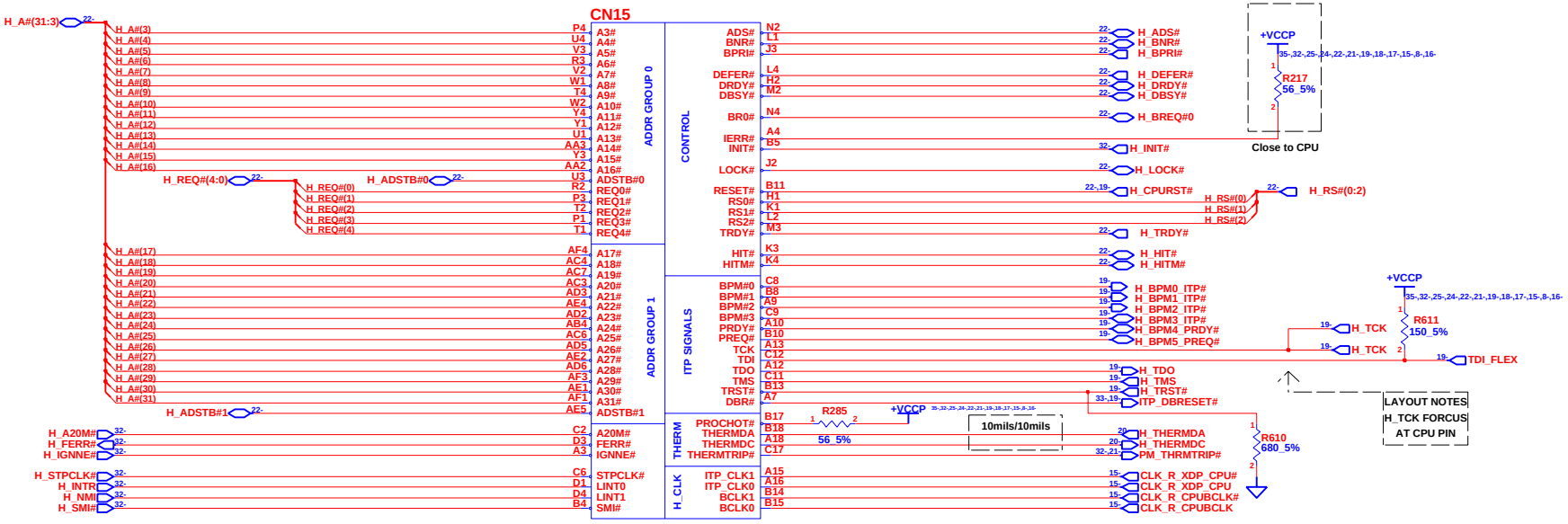
CHANGE by	*Kevin_Tseng*	4-Nov-2004	

INVENTEC

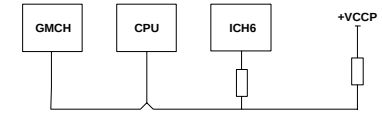
TITLE: ***DAVOS-DF***
POWER(SEQUENCE)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 14 OF 65



AMP_uFCPGA478M_478P_Dothan



PM_THRMTRIP# should be T at CPU

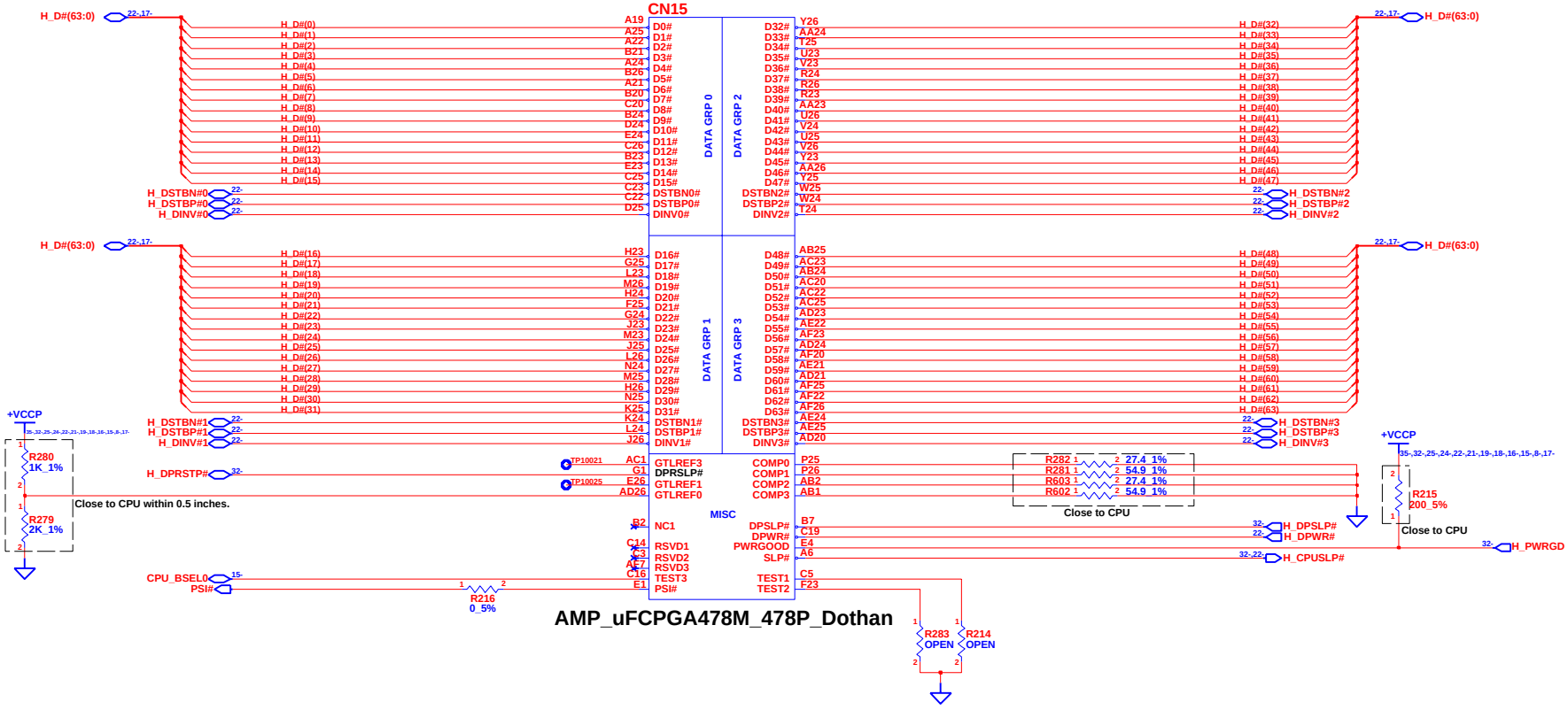
CHANGE by	*Kevin_Tseng*	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
DOTHAN-1

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 16 OF 65



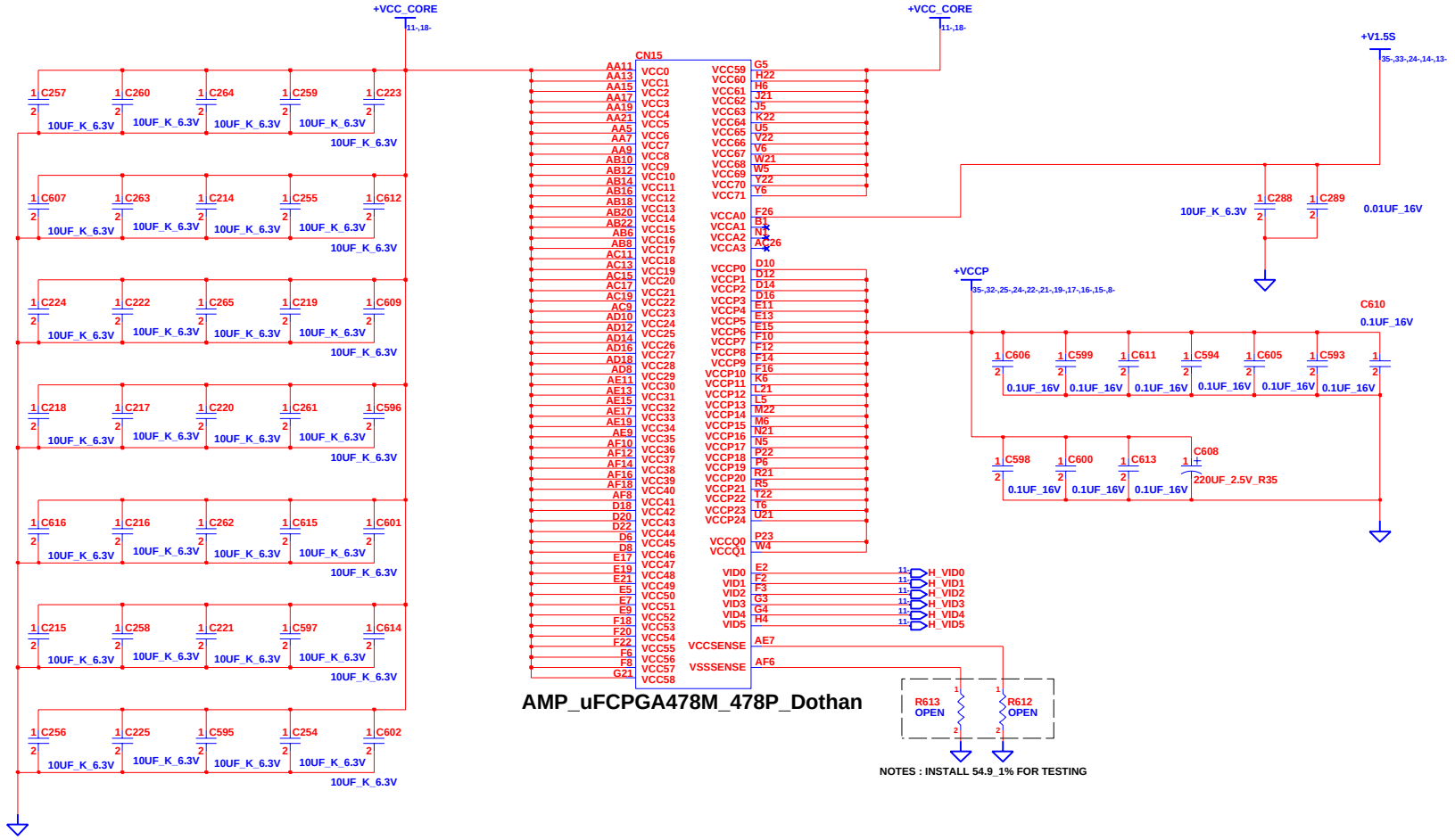
CHANGE by	Kevin_Tseng
	4-Nov-2004

INVENTEC

TITLE: *DAVOS-DF*
DOTHAN-2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 17 OF 65



CHANGE by	Kevin_Tseng	4-Nov-2004		

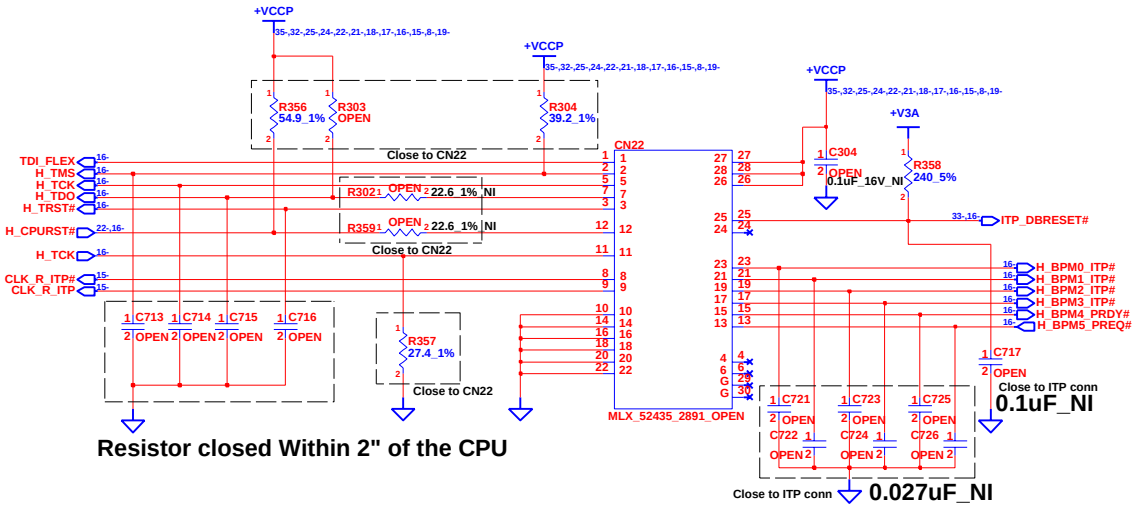
INVENTEC

TITLE: *DAVOS-DF*
DOTHAN-3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 18 OF 65

In-Target Probe



Resistor closed Within 2" of the CPU

LAYOUT NOTES : H_CPURST# PIN OF MCH_M FORK OUT INTO TWO BRANCHES ON CPU AND ITP CONNECTOR

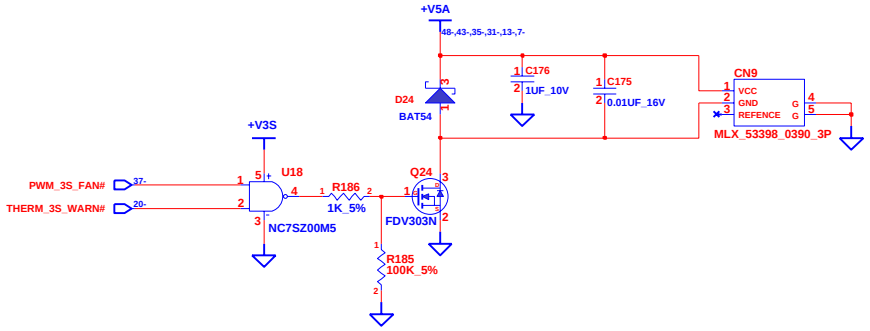
CHANGE by	Kevin_Tseng	4-Nov-2004	

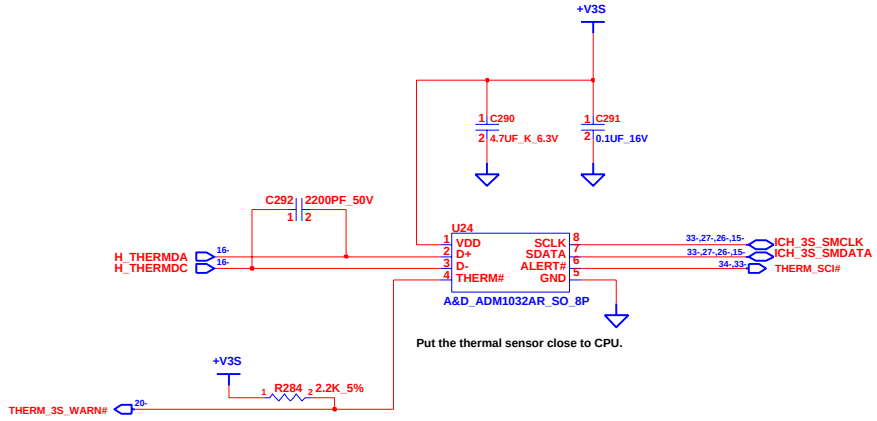
INVENTEC

TITLE: *DAVOS-DF*
DOTHAN-4

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 19 OF 65





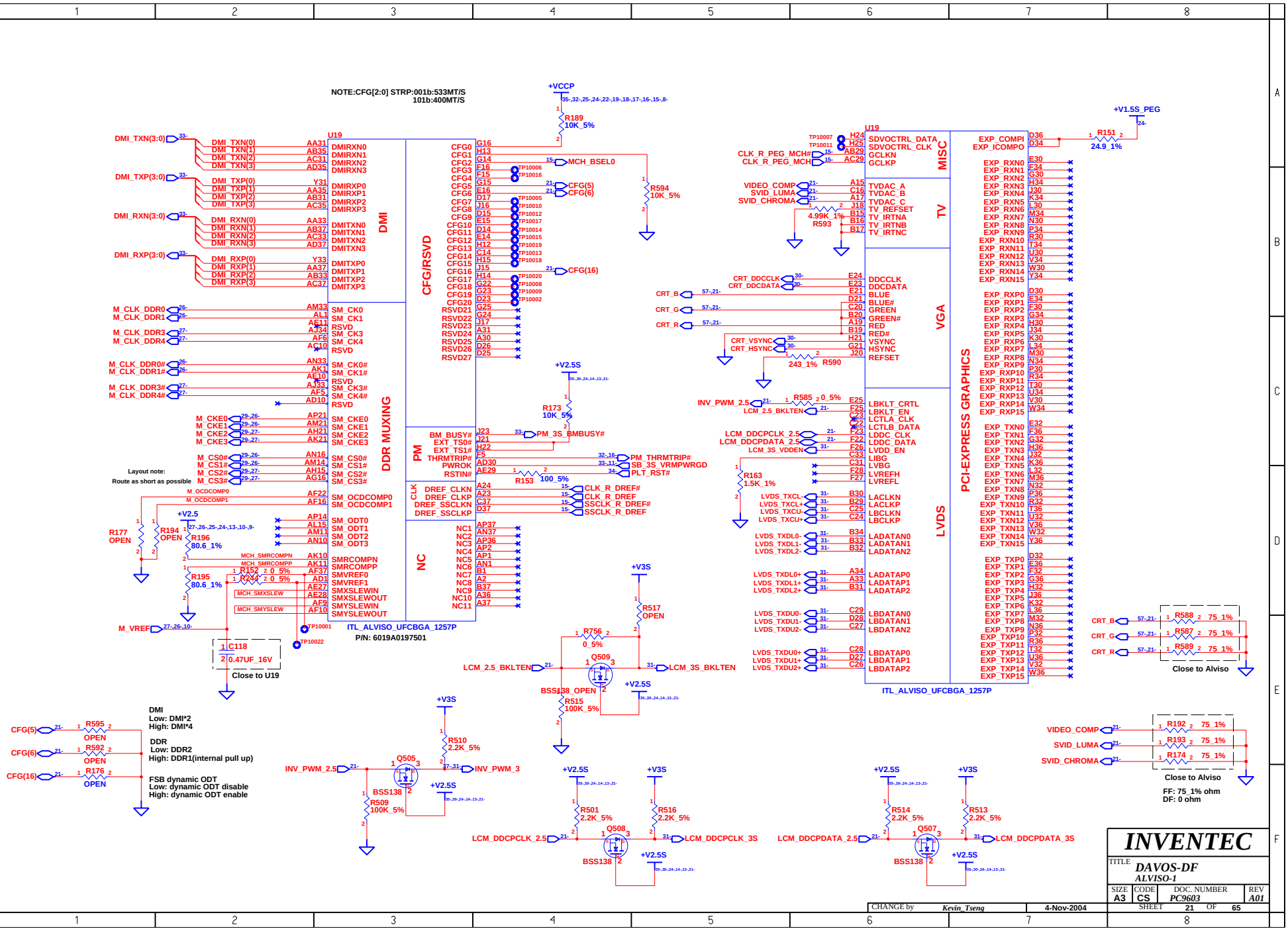
CHANGE by	*Kevin_Tseng*	4-Nov-2004	

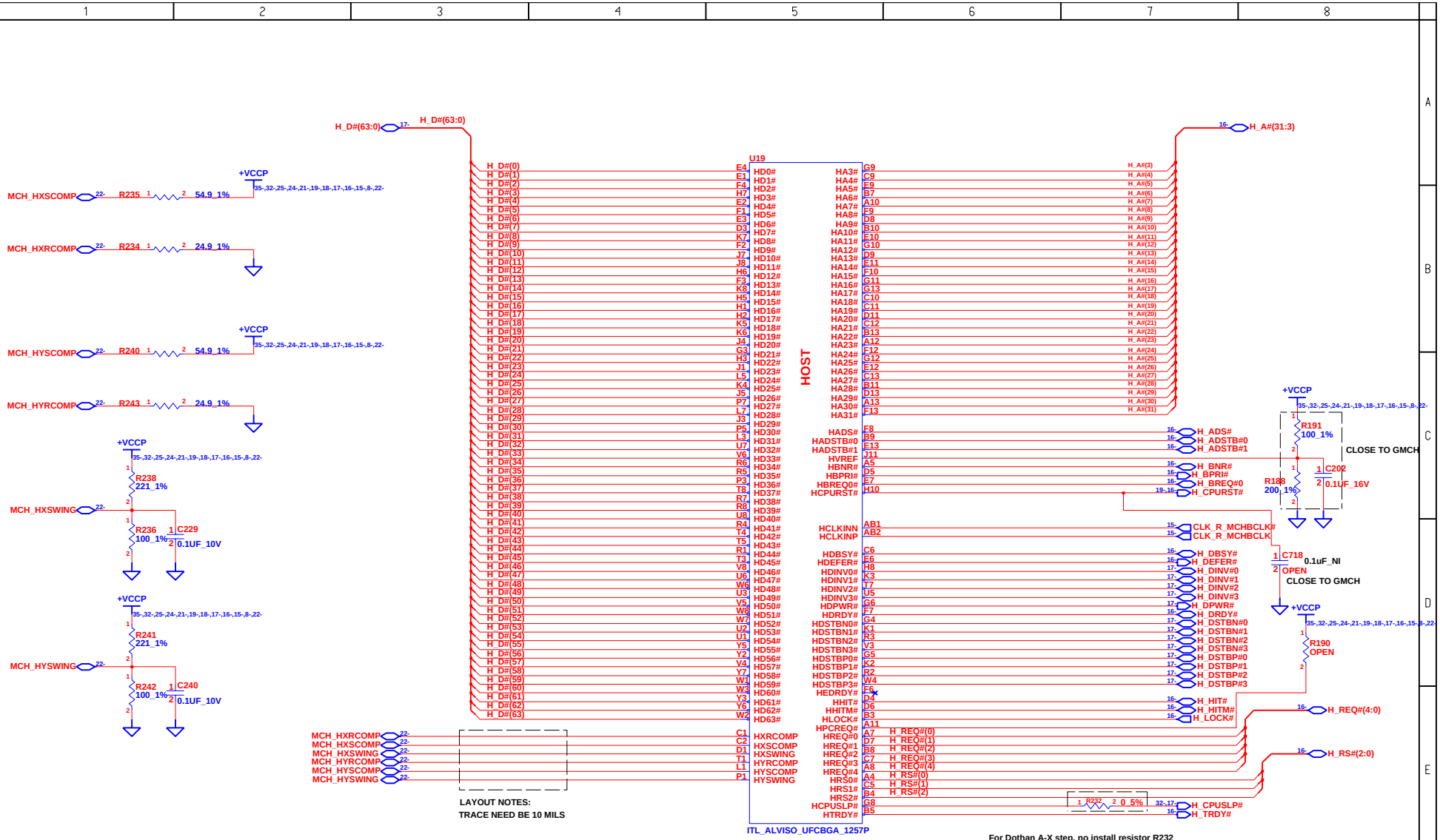
INVENTEC

TITLE: ***DAVOS-DF***
THERMAL&FAN CONTROLLER

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 20 OF 65

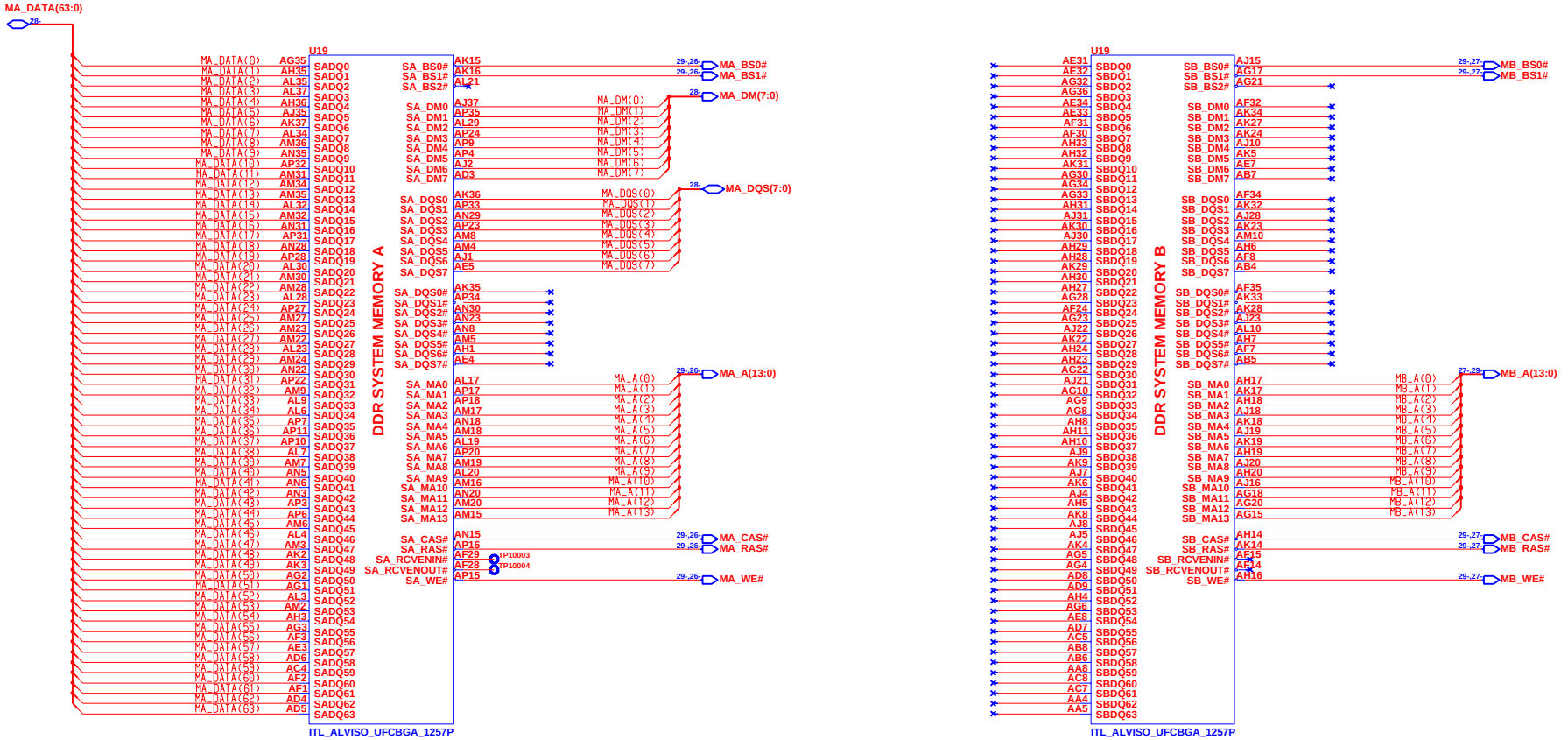

NOTE:CFG[2:0] STRP:001b:533MT/S
101b:400MT/S
U19
ITL_ALVISO_UFCBGA_1257P
P/N: 6019A0197501
DMI
DDR MUXING
CFG/RSVD
PM
CLK
NC
MISC
TV
VGA
LVDS
PCI-EXPRESS GRAPHICS
Layout note:
Route as short as possible
Close to U19
Close to Alviso
DMI
Low: DMI*2
High: DMI*4
DDR
Low: DDR2
High: DDR1(internal pull up)
FSB dynamic ODT
Low: dynamic ODT disable
High: dynamic ODT enable
FF: 75_1% ohm
DF: 0 ohm
INVENTEC
TITLE
DAVOS-DF
ALVISO-1
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 21 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004



CHANGE by	Kevin_Tseng
	4-Nov-2004

INVENTEC

TITLE	DAVOS-DF ALVISO-2-HOST		
SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01
SHEET	22	OF	65



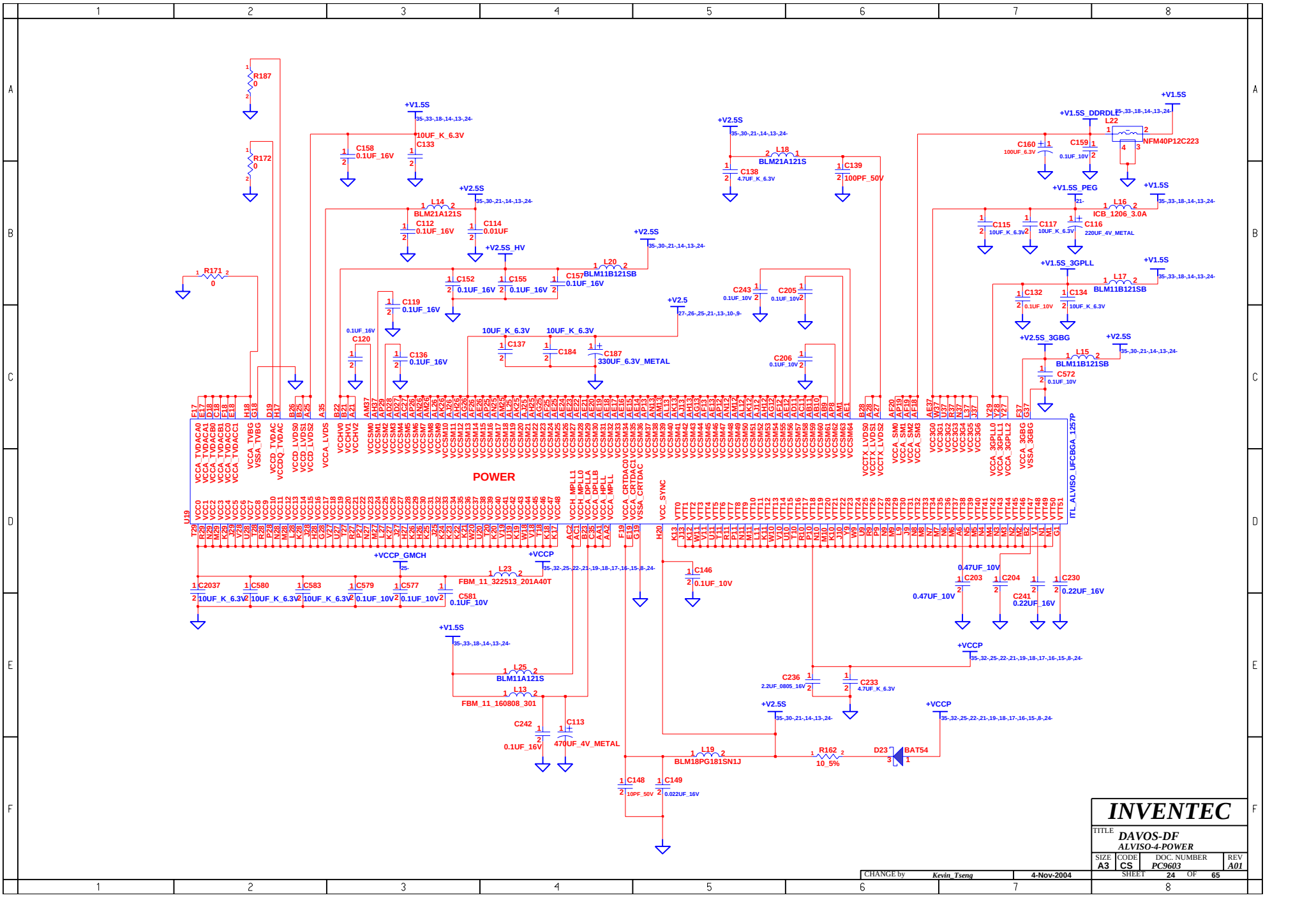
CHANGE by	Kevin_Tseng	4-Nov-2004	

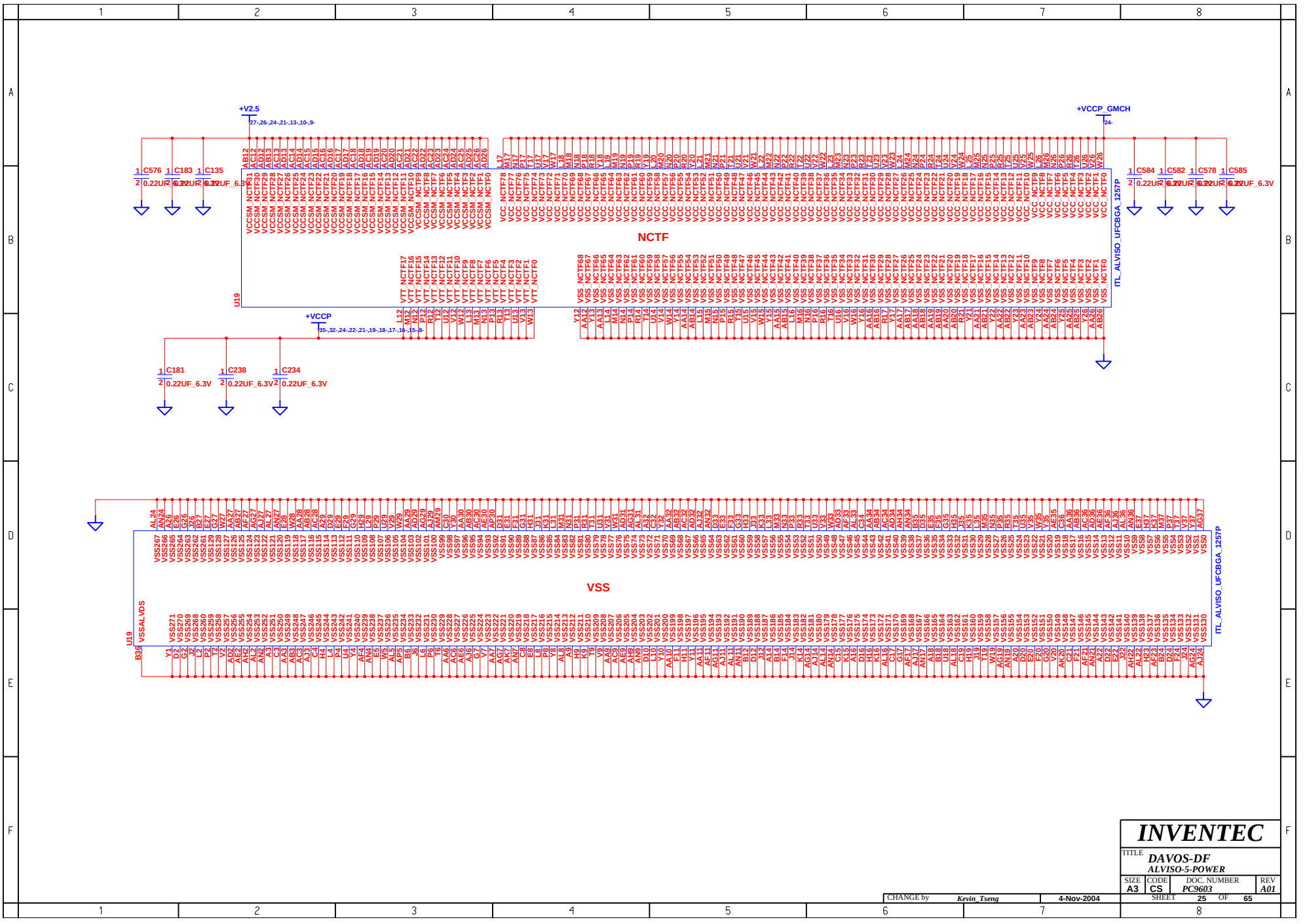
INVENTEC

TITLE: *DAVOS-DF*
ALVISO-3-DDR

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 23 OF 65


POWER
R187 0
R172 0
R171 0
+V1.5S
+V2.5S
+V2.5S_HV
+V2.5
+V1.5S_DDRDLL
+V1.5S_PEG
+V1.5S_3GPLL
+V2.5S_3GBG
+VCCP_GMCH
+VCCP
C158 0.1UF_16V
C133 10UF_K_6.3V
C112 0.1UF_16V
C114 0.01UF
C152 0.1UF_16V
C155 0.1UF_16V
C157 0.1UF_16V
C119 0.1UF_16V
C120 0.1UF_16V
C136 0.1UF_16V
C137 10UF_K_6.3V
C184 10UF_K_6.3V
C187 330UF_6.3V_METAL
C138 4.7UF_K_6.3V
C139 100PF_50V
C160 100UF_6.3V
C159 0.1UF_10V
C115 10UF_K_6.3V
C117 10UF_K_6.3V
C116 220UF_4V_METAL
C132 0.1UF_10V
C134 10UF_K_6.3V
C243 0.1UF_10V
C205 0.1UF_10V
C206 0.1UF_10V
C572 0.1UF_10V
L14 BLM21A121S
L20 BLM11B121SB
L18 BLM21A121S
L22 NFM40P12C223
L16 ICB_1206_3.0A
L17 BLM11B121SB
L15 BLM11B121SB
L23 FBM_11_322513_201A40T
L25 BLM11A121S
L13 FBM_11_160808_301
L19 BLM18PG181SN1J
C2037 10UF_K_6.3V
C580 10UF_K_6.3V
C583 10UF_K_6.3V
C579 0.1UF_10V
C577 0.1UF_10V
C581 0.1UF_10V
C146 0.1UF_10V
C203 0.47UF_10V
C204 0.47UF_10V
C241 0.22UF_16V
C230 0.22UF_16V
C236 2.2UF_0805_16V
C233 4.7UF_K_6.3V
C242 0.1UF_16V
C113 470UF_4V_METAL
C148 10PF_50V
C149 0.022UF_16V
R162 10_5%
D23 BAT54
U19 ITL_ALVISO_UFCBGA_1257P
INVENTEC
TITLE DAVOS-DF
ALVISO-4-POWER
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 24 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004



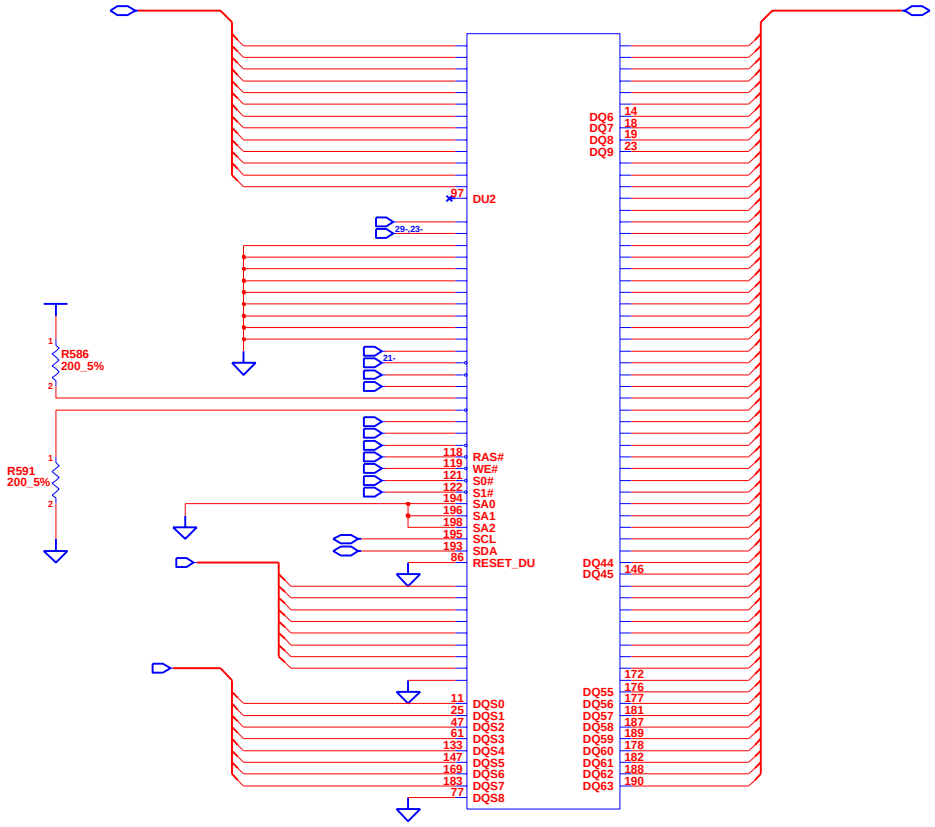
CHANGE by	Kevin_Tseng	4-Nov-2004			

INVENTEC

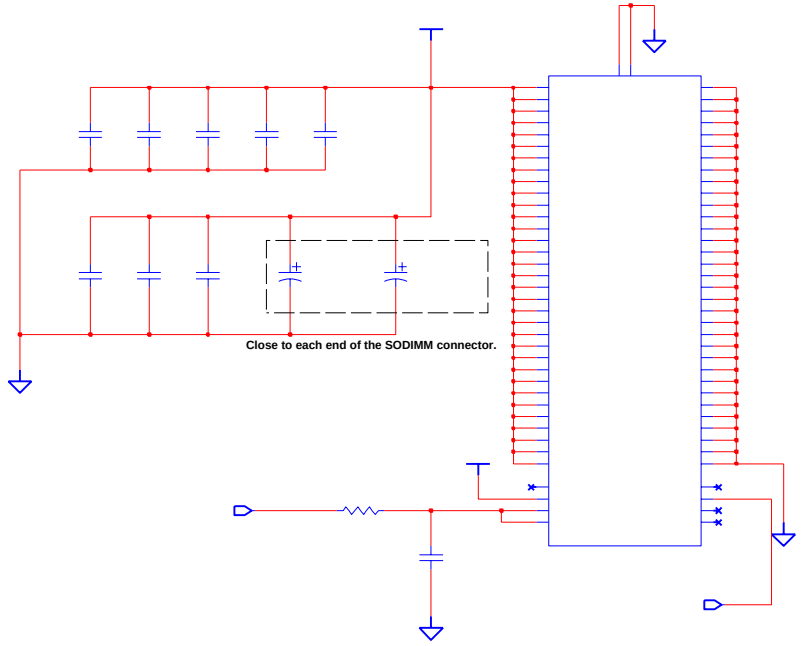
TITLE: *DAVOS-DF*
ALVISO-5-POWER

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 25 OF 65



SO DIMM0



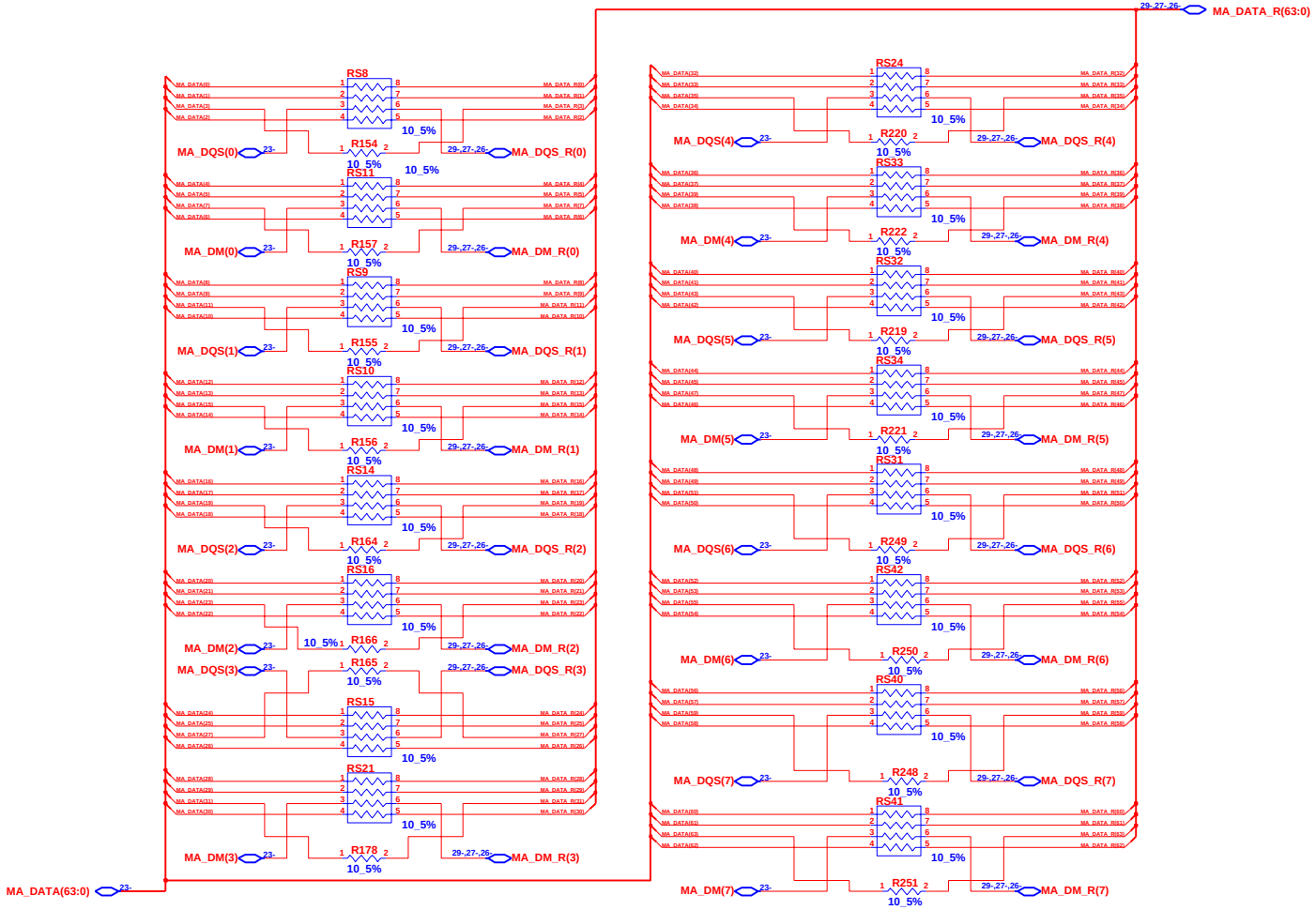
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
DDR-DIMM-0

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

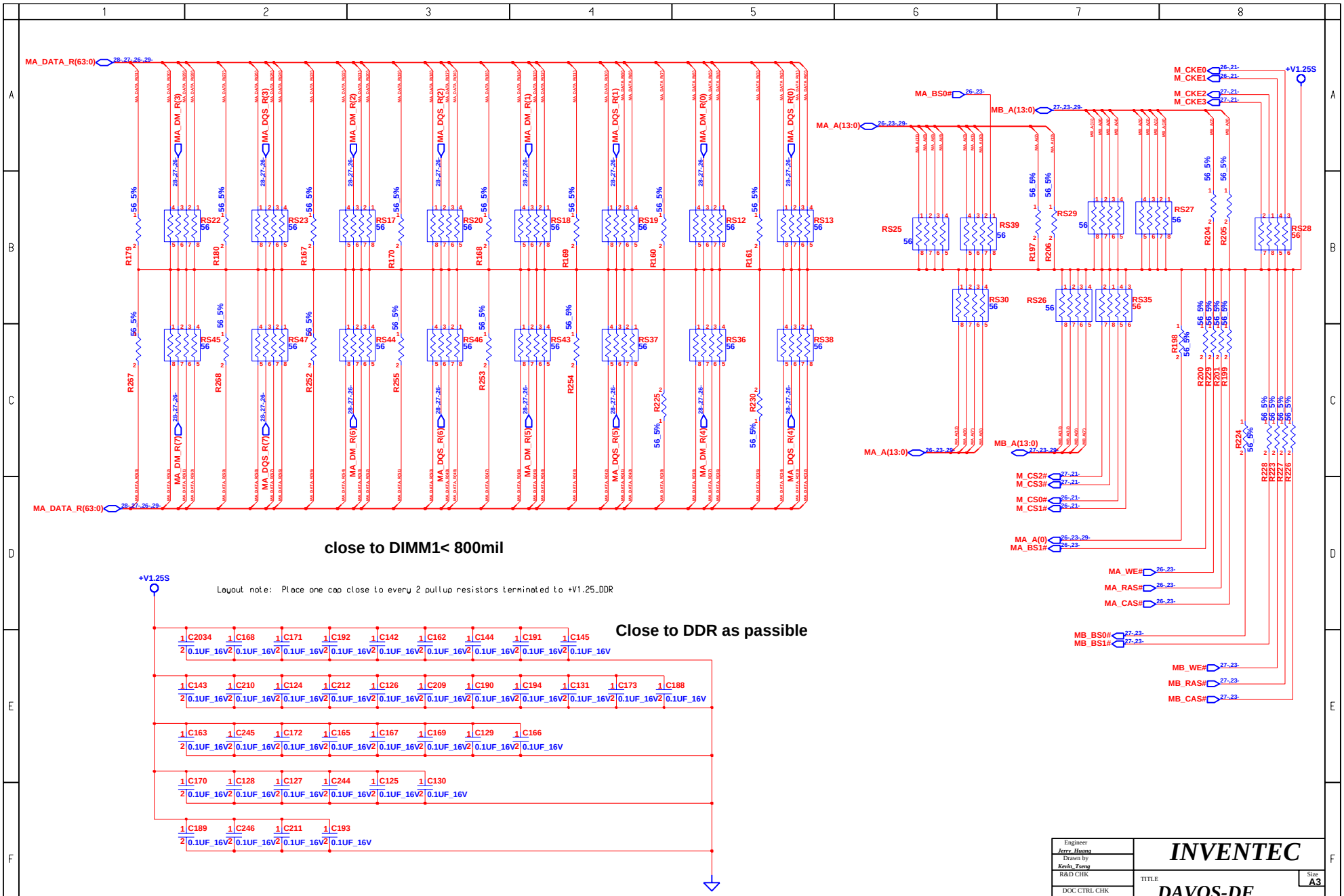
SHEET 26 OF 65



close to DIMM0<750 mil

Changed by: DAVOS	Date Changed: 4-Nov-2004	Time Changed: 6:51:15 pm	QA CHK	VER: A01	Model Number: PC9603	Sheet 28 of 65

Engineer: Jerry_Huang	**INVENTEC**	
Drawn by: Kevin_Tseng	TITLE: **DAVOS-DF**	Size: A3
R&D CHK	**DDR DAMPING RESISTORS**	
DOC CTRL CHK		
MFG ENGR CHK		



Changed by: DAVOS	Date Changed: 4-Nov-2004	Time Changed: 6:51:20 pm	QA CHK	VER: A01	Model Number: PC9603

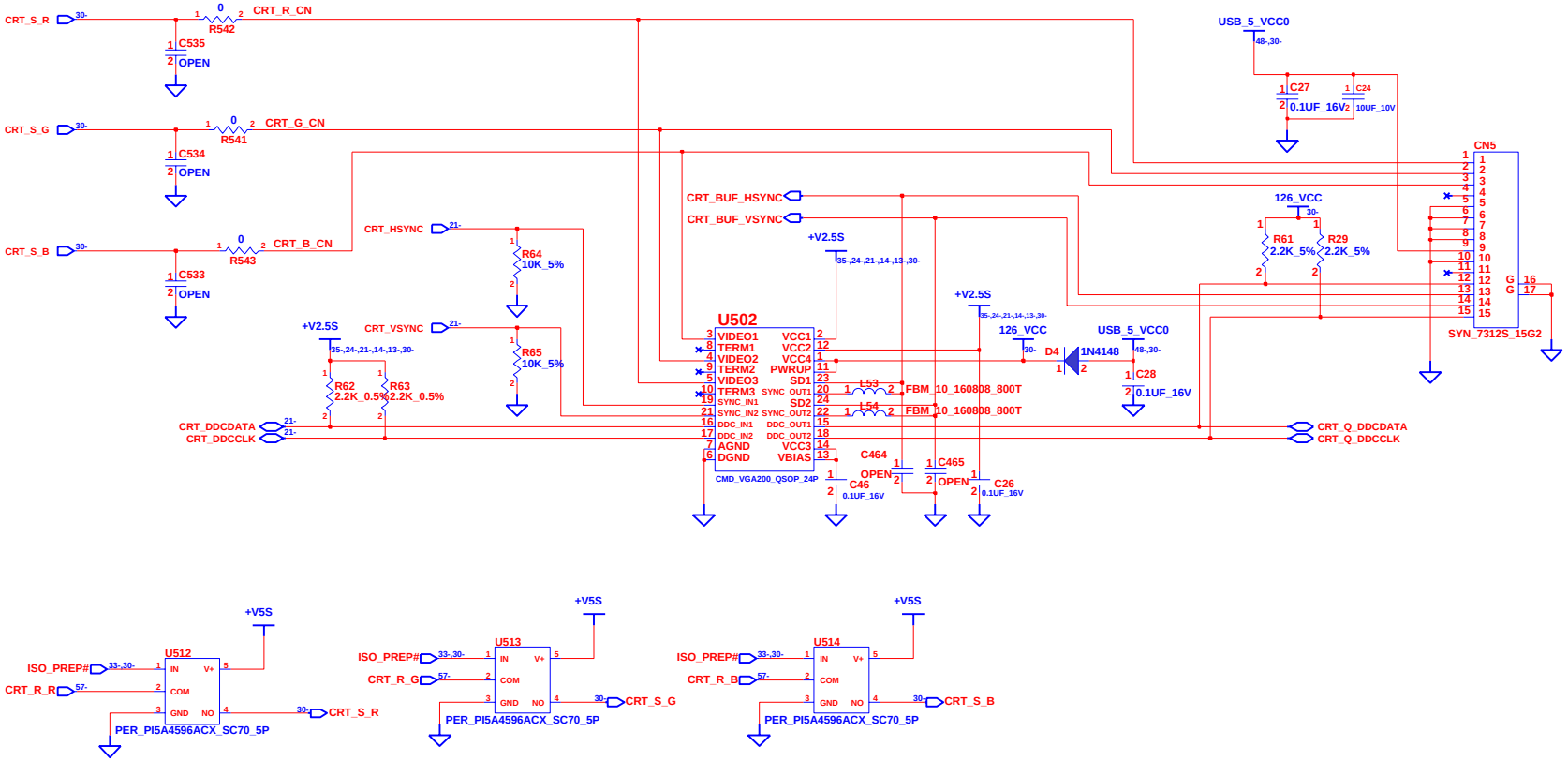
Engineer: Jerry_Huang
Drawn by: Kevin_Tseng
R&D CHK
DOC CTRL CHK
MFG ENGR CHK

INVENTEC

TITLE: DAVOS-DF
DDR TERMINAL RESISTORS

Size: A3

Sheet 29 of 65



CHANGE by	Kevin_Tseng	4-Nov-2004	

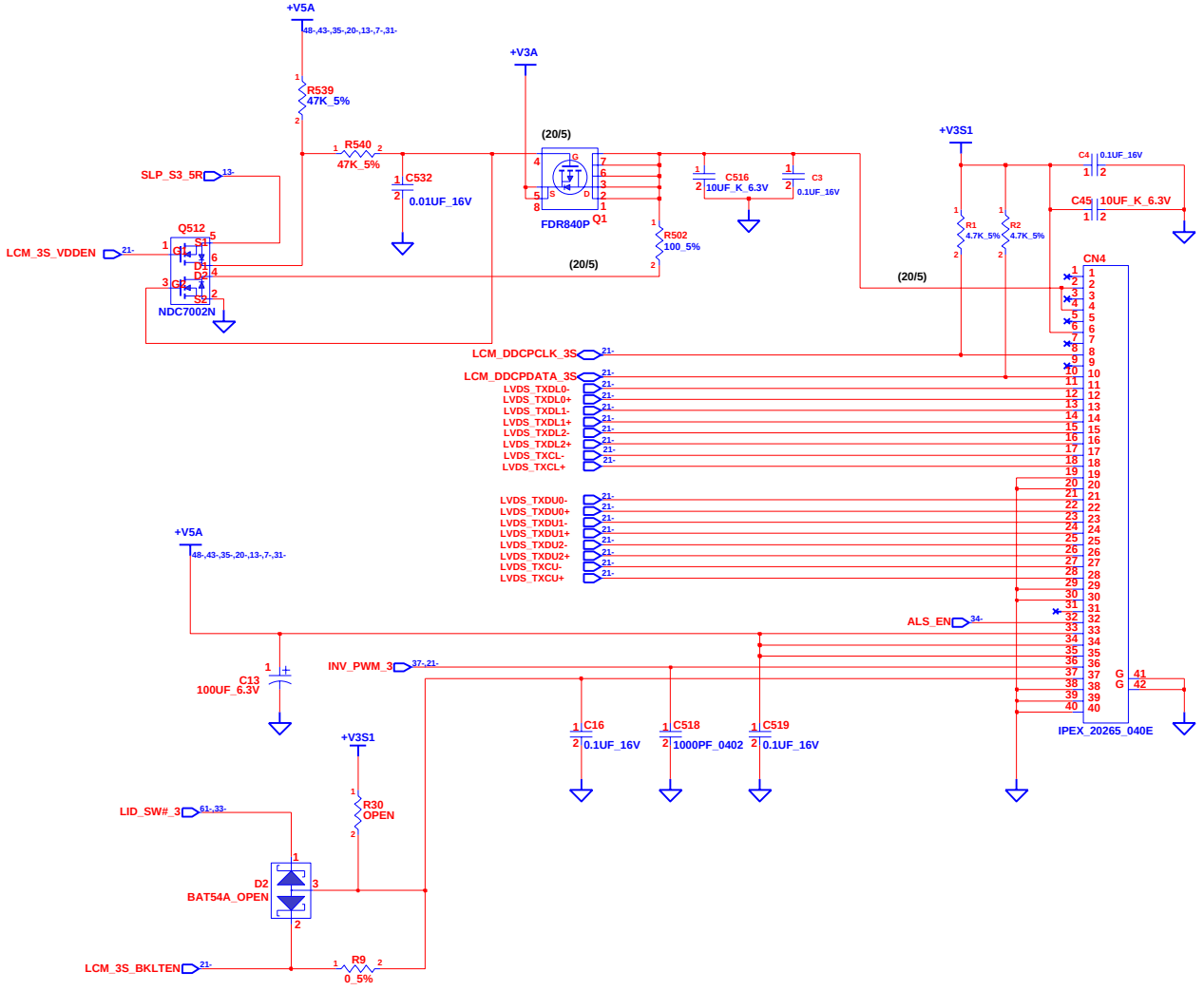
INVENTEC

TITLE: *DAVOS-DF*
CRT

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 30 OF 65

Place closed to connector



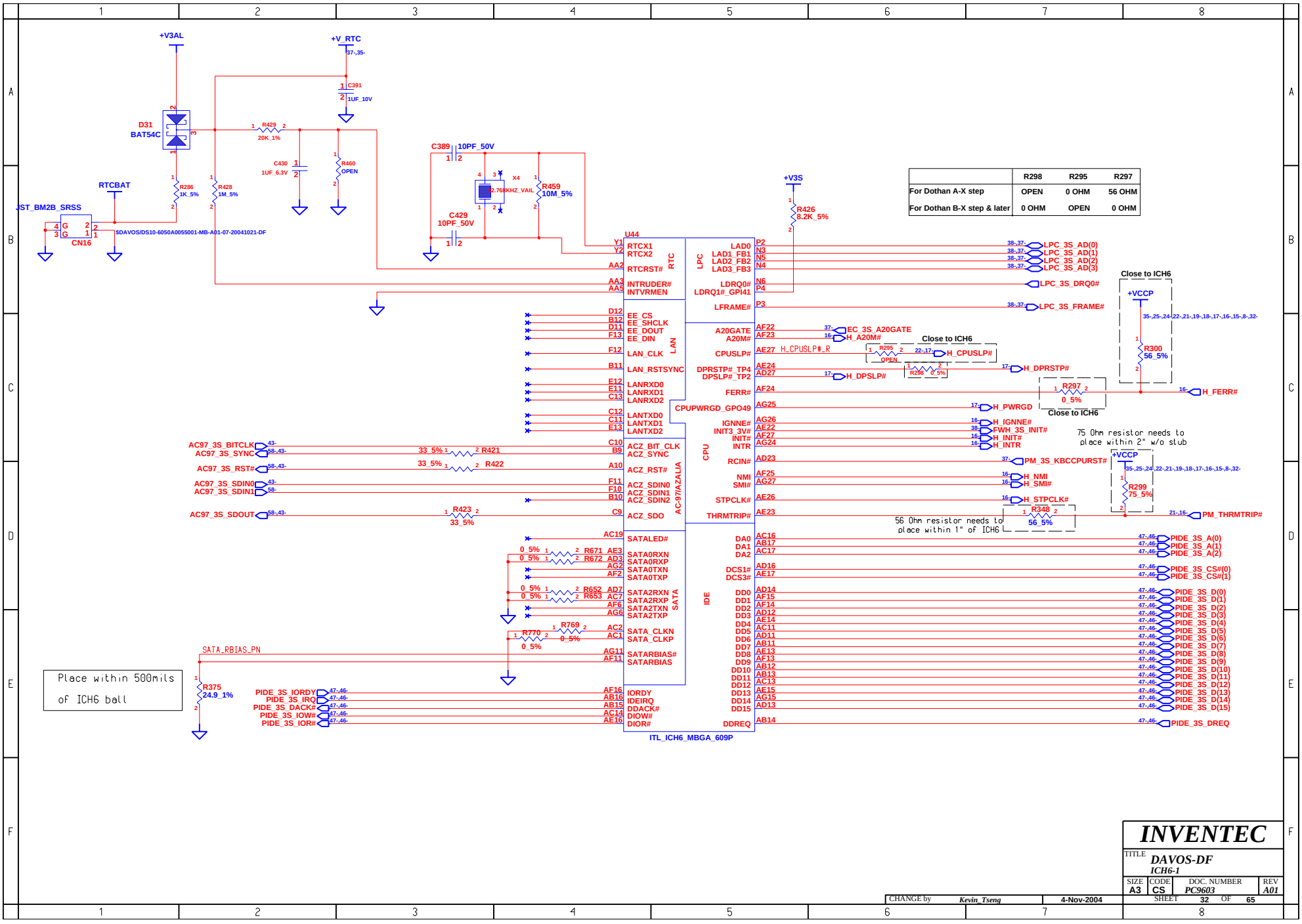
CHANGE by	*Kevin_Tseng*	4-Nov-2004		

INVENTEC

TITLE: *DAVOS-DF*
LCD CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 31 OF 65



	R298	R295	R297
For Dothan A-X step	OPEN	0 OHM	56 OHM
For Dothan B-X step & later	0 OHM	OPEN	0 OHM

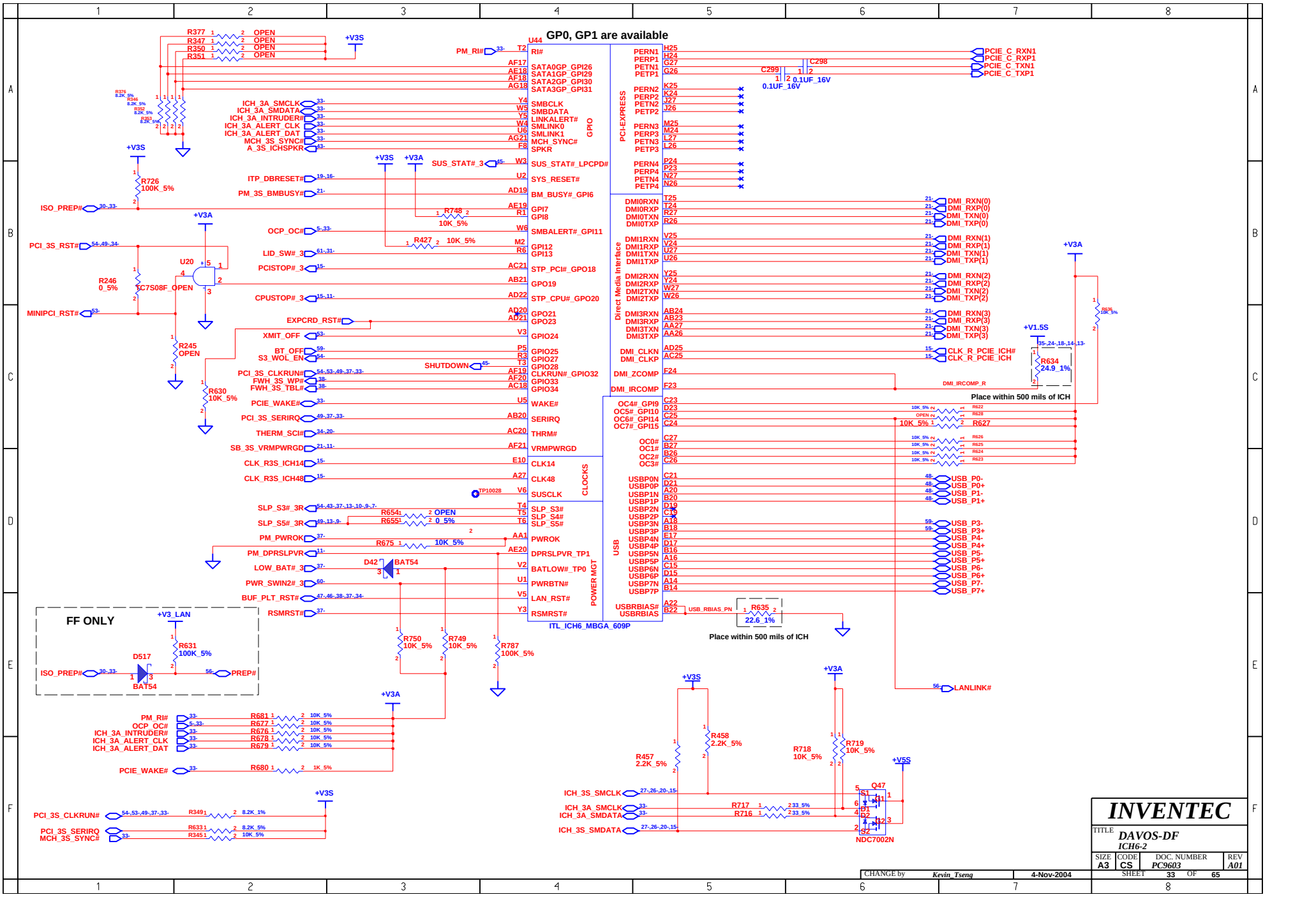
CHANGE by	Kevin_Tseng	4-Nov-2004	

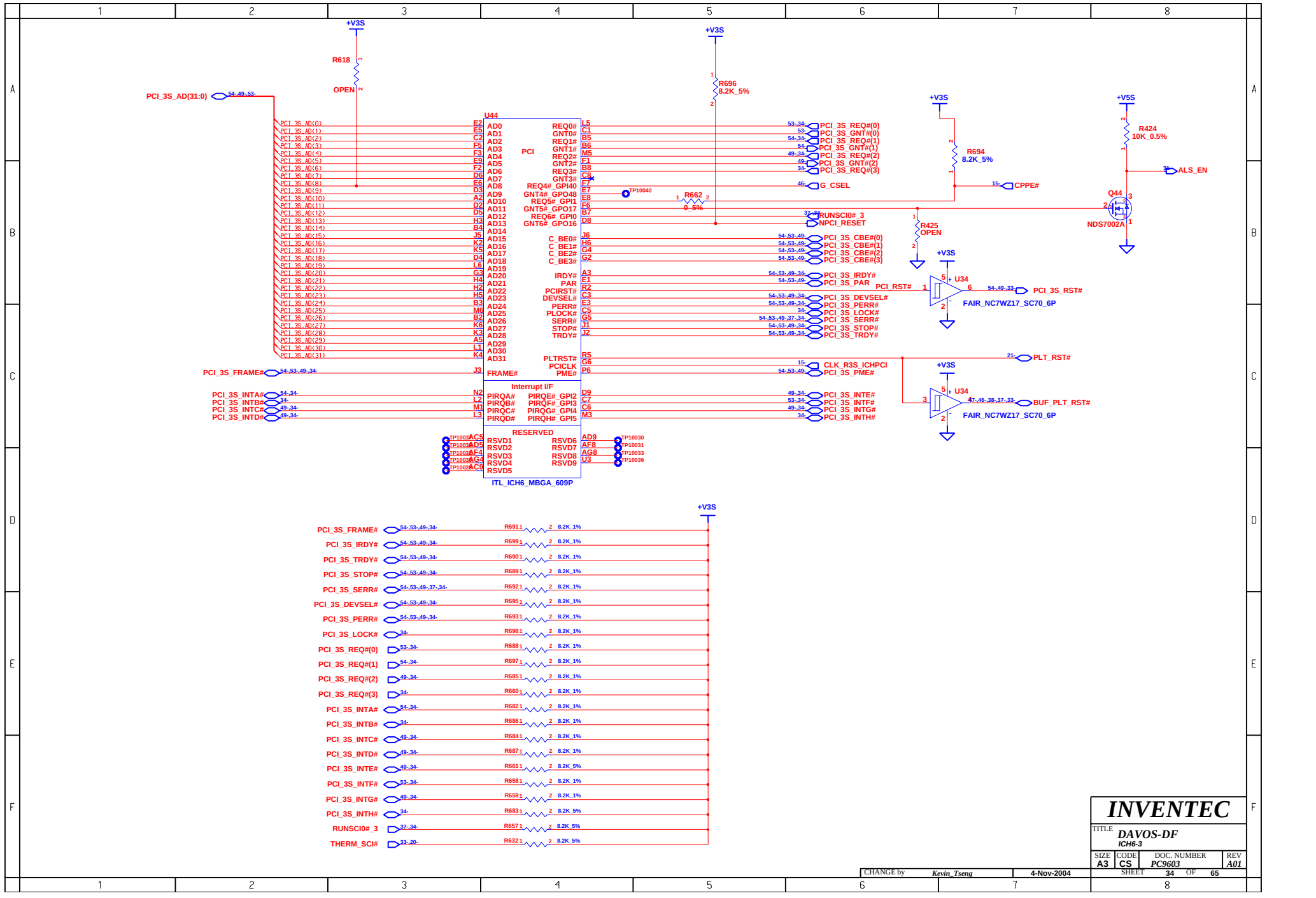
INVENTEC

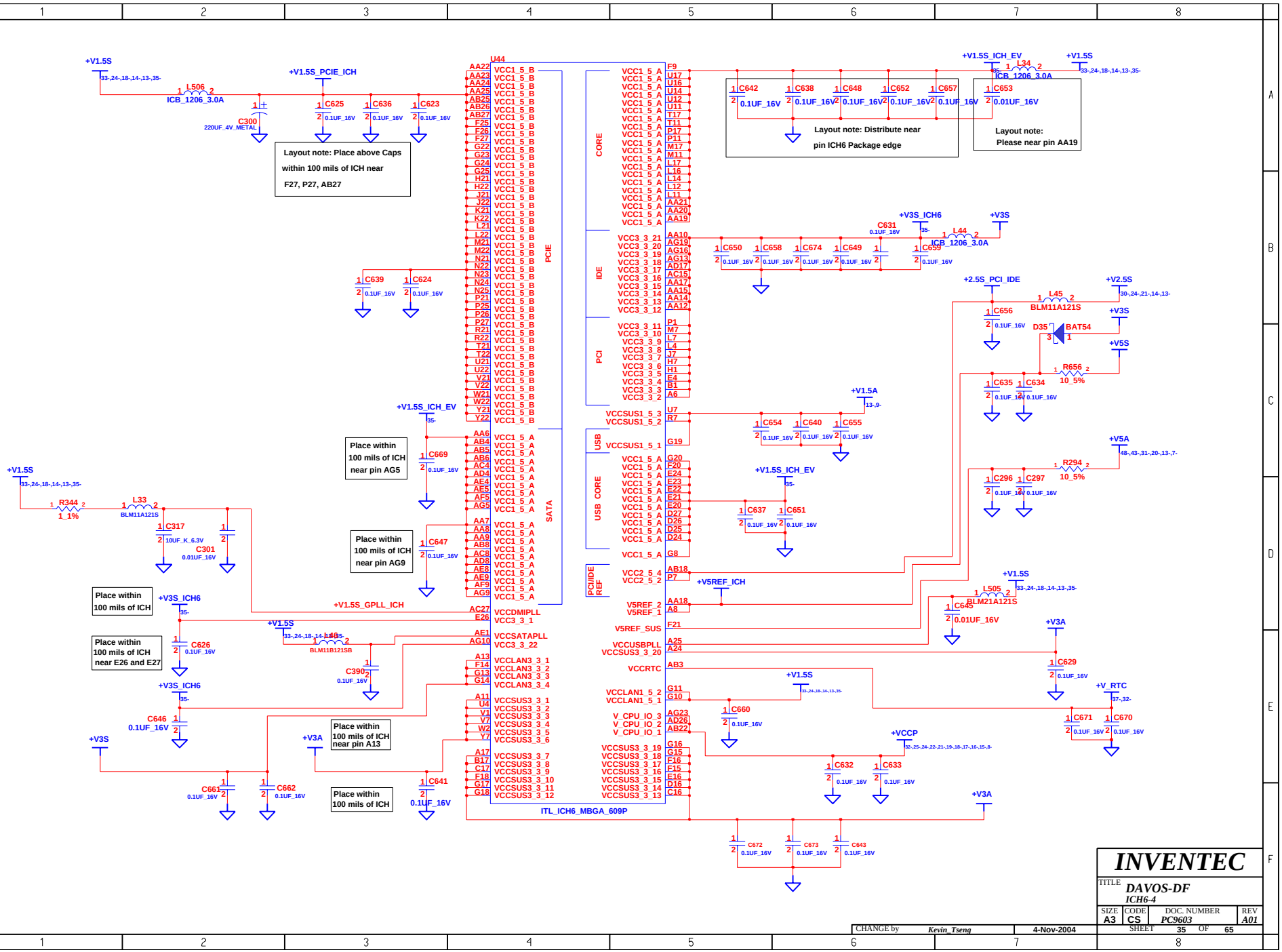
TITLE: DAVOS-DF
ICH6-1

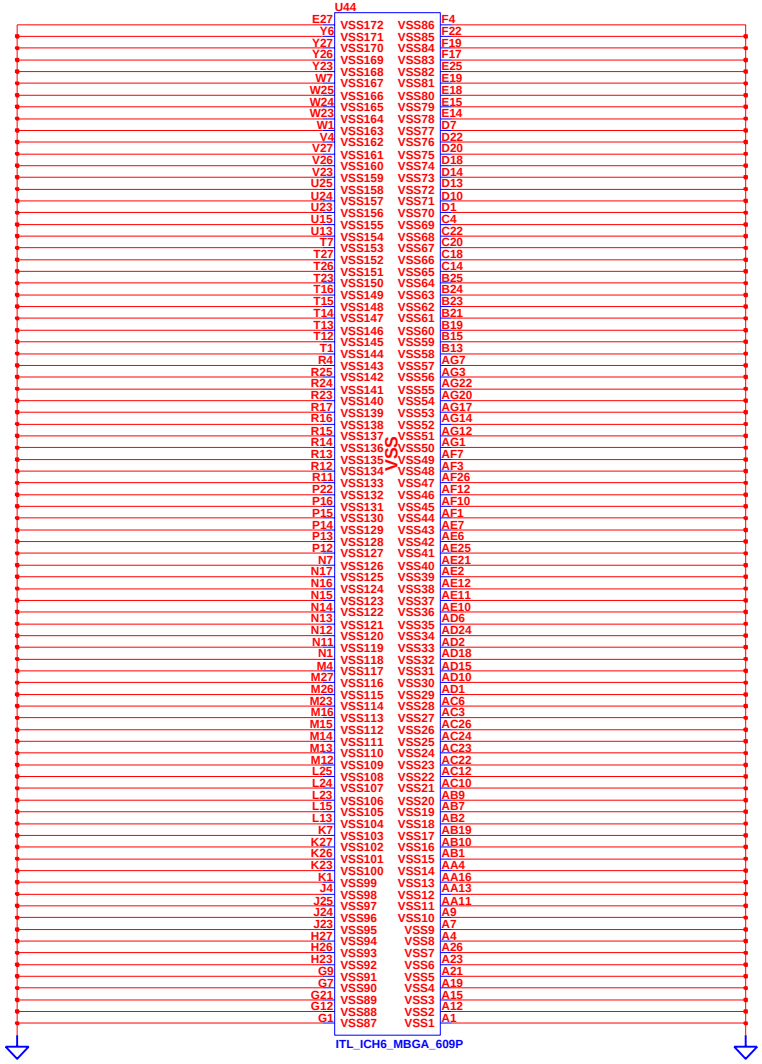
SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01

SHEET 32 OF 65


GP0, GP1 are available
U44
ITL_ICH6_MBGA_609P
PCIEXPRESS
Direct Media Interface
USB
CLOCKS
GPIO
POWER MGT
FF ONLY
Place within 500 mils of ICH
Place within 500 mils of ICH
NDC7002N
INVENTEC
TITLE
DAVOS-DF
ICH6-2
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
CHANGE by Kevin_Tseng
4-Nov-2004
SHEET 33 OF 65


+V3S
R618
OPEN
PCI_3S_AD(31:0)
U44
PCI_3S_FRAME#
PCI_3S_INTA#
PCI_3S_INTB#
PCI_3S_INTC#
PCI_3S_INTD#
Interrupt I/F
RESERVED
ITL_ICH6_MBGA_609P
R696
8.2K_5%
R694
8.2K_5%
R662
0_5%
TP10040
G_CSEL
CPPE#
RUNSCI0#_3
NPCI_RESET
R425
OPEN
Q44
NDS7002A
+V5S
R424
10K_0.5%
ALS_EN
U34
FAIR_NC7WZ17_SC70_6P
PCI_3S_RST#
PLT_RST#
BUF_PLT_RST#
CLK_R3S_ICHPCI
PCI_3S_PME#
PCI_3S_REQ#(0)
PCI_3S_GNT#(0)
PCI_3S_REQ#(1)
PCI_3S_GNT#(1)
PCI_3S_REQ#(2)
PCI_3S_GNT#(2)
PCI_3S_REQ#(3)
PCI_3S_CBE#(0)
PCI_3S_CBE#(1)
PCI_3S_CBE#(2)
PCI_3S_CBE#(3)
PCI_3S_IRDY#
PCI_3S_PAR
PCI_3S_DEVSEL#
PCI_3S_PERR#
PCI_3S_LOCK#
PCI_3S_SERR#
PCI_3S_STOP#
PCI_3S_TRDY#
PCI_3S_INTE#
PCI_3S_INTF#
PCI_3S_INTG#
PCI_3S_INTH#
R6911 R6991 R6901 R6891 R6921 R6951 R6931 R6981 R6881 R6971 R6851 R6601 R6821 R6861 R6841 R6871 R6611 R6581 R6591 R6831 R6571 R6321
8.2K_1%
8.2K_5%
RUNSCI0#_3
THERM_SCI#
INVENTEC
DAVOS-DF
ICH6-3
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 34 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004


U44
ITL_ICH6_MBGA_609P
Layout note: Place above Caps within 100 mils of ICH near F27, P27, AB27
Layout note: Distribute near pin ICH6 Package edge
Layout note: Please near pin AA19
Place within 100 mils of ICH near pin AG5
Place within 100 mils of ICH near pin AG9
Place within 100 mils of ICH
Place within 100 mils of ICH near E26 and E27
Place within 100 mils of ICH near pin A13
Place within 100 mils of ICH
+V1.5S
+V1.5S_PCIE_ICH
+V1.5S_ICH_EV
+V1.5S_GPLL_ICH
+V3S_ICH6
+V3S
+V3A
+V1.5A
+V5REF_ICH
+V2.5S_PCI_IDE
+V2.5S
+V5S
+V5A
+V_RTC
+VCCP
CORE
IDE
PCI
USB
USB CORE
PCI/IDE REF
SATA
PCIE
INVENTEC
TITLE
DAVOS-DF
ICH6-4
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
CHANGE by Kevin_Tseng
4-Nov-2004
SHEET 35 OF 65



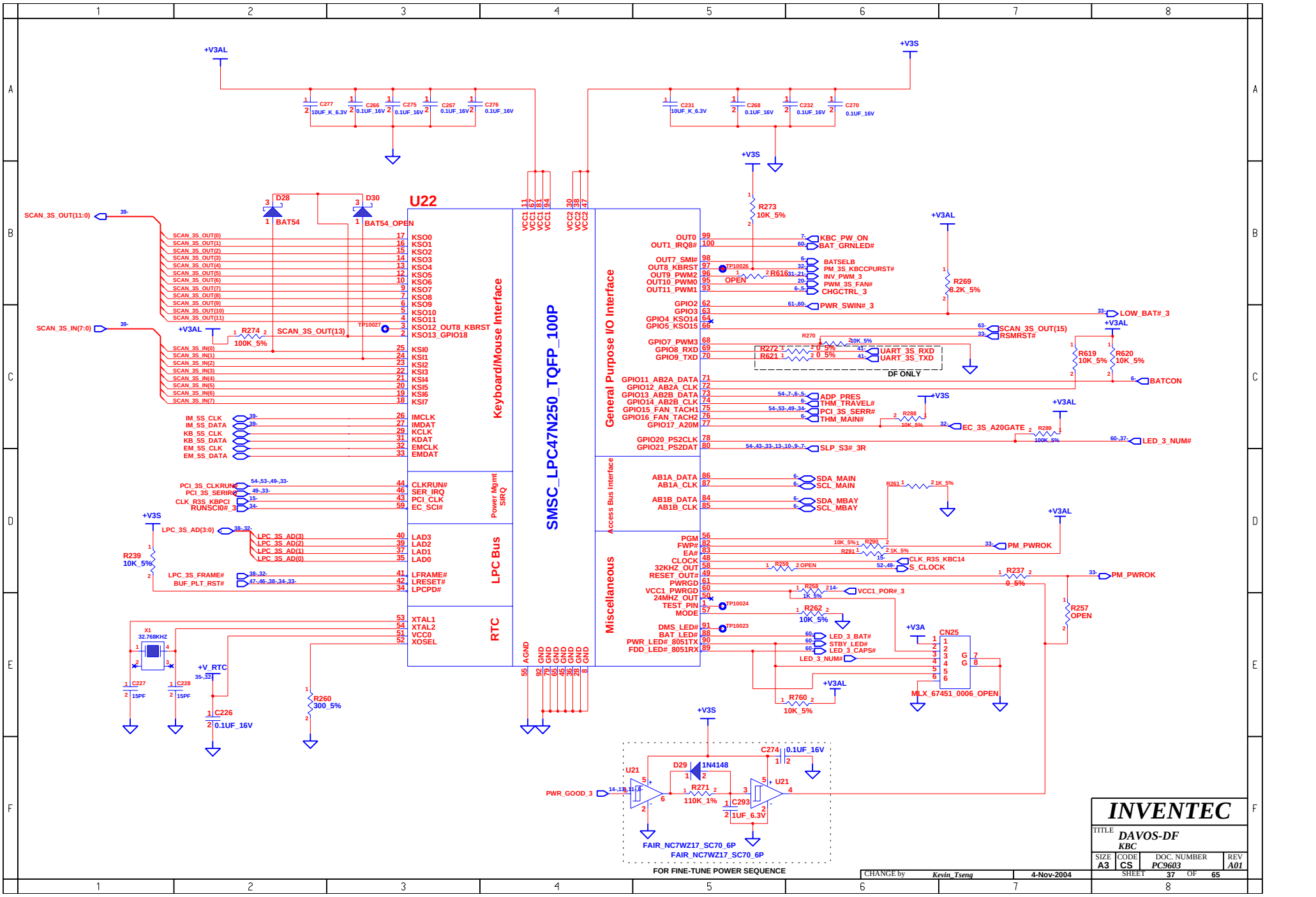
CHANGE by	Kevin_Tseng	4-Nov-2004

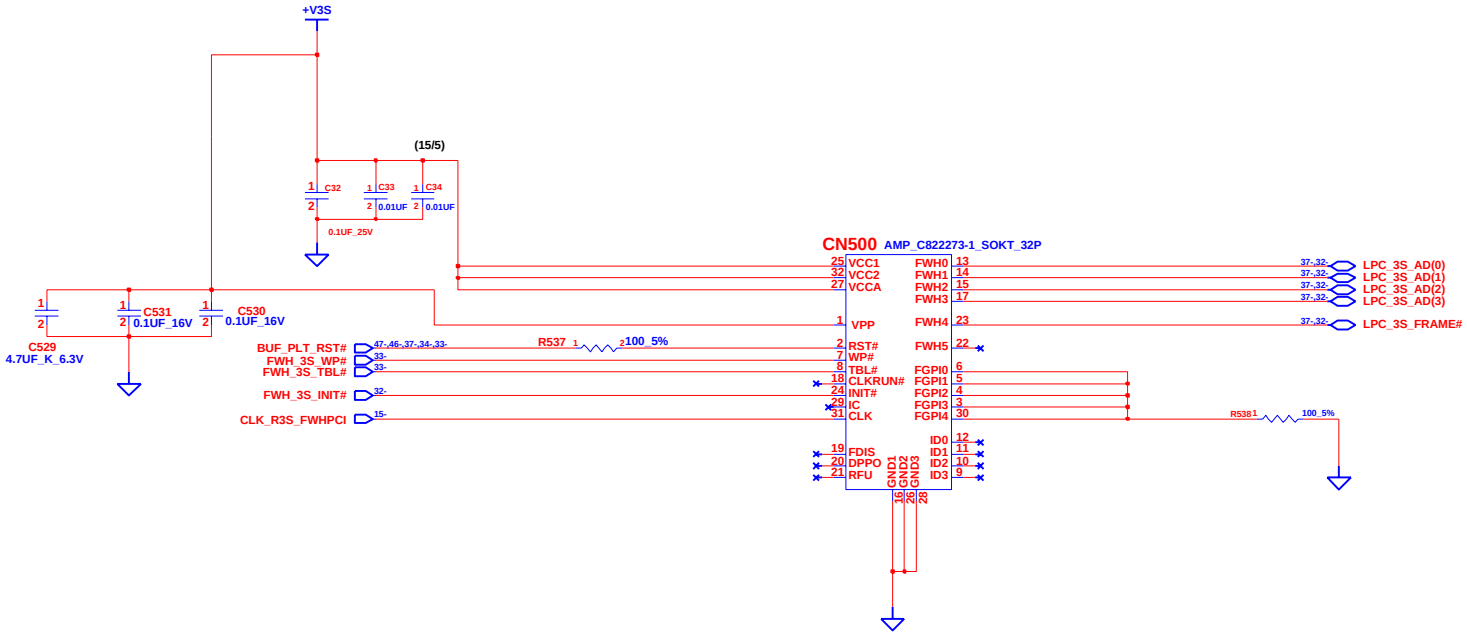
INVENTEC

TITLE: *DAVOS-DF*
ICH6-5

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 36 OF 65


U22
SMSC_LPC47N250_TQFP_100P
Keyboard/Mouse Interface
Power Mgmt SIRQ
LPC Bus
RTC
General Purpose I/O Interface
Access Bus Interface
Miscellaneous
SCAN_3S_OUT(11:0)
SCAN_3S_IN(7:0)
+V3AL
+V3S
+V_RTC
+V3A
D28
BAT54
D30
BAT54_OPEN
R274 100K_5%
SCAN_3S_OUT(13)
TP10027
IM_5S_CLK
IM_5S_DATA
KB_5S_CLK
KB_5S_DATA
EM_5S_CLK
EM_5S_DATA
PCI_3S_CLKRUN
PCI_3S_SERIRQ
CLK_R3S_KBPCI
RUNSCI0#_3
LPC_3S_AD(3:0)
LPC_3S_FRAME#
BUF_PLT_RST#
R239 10K_5%
X1 32.768KHZ
C227 15PF
C228 15PF
C226 0.1UF_16V
R260 300_5%
C277 10UF_K_6.3V
C266 0.1UF_16V
C275 0.1UF_16V
C267 0.1UF_16V
C276 0.1UF_16V
C231 10UF_K_6.3V
C268 0.1UF_16V
C232 0.1UF_16V
C270 0.1UF_16V
R273 10K_5%
R269 8.2K_5%
R616 OPEN
TP10026
KBC_PW_ON
BAT_GRNLED#
BATSELB
PM_3S_KBCCPURST#
INV_PWM_3
PWM_3S_FAN#
CHGCTRL_3
PWR_SWIN#_3
LOW_BAT#_3
SCAN_3S_OUT(15)
RSMRST#
R270 20K_5%
R272 0_5%
R621 0_5%
UART_3S_RXD
UART_3S_TXD
DF ONLY
R619 10K_5%
R620 10K_5%
BATCON
ADP_PRES
THM_TRAVEL#
PCI_3S_SERR#
THM_MAIN#
R288 10K_5%
EC_3S_A20GATE
R289 100K_5%
LED_3_NUM#
SLP_S3#_3R
SDA_MAIN
SCL_MAIN
SDA_MBAY
SCL_MBAY
R261 1K_5%
PM_PWROK
R296 10K_5%
R291 1K_5%
CLK_R3S_KBC14
S_CLOCK
R259 OPEN
R237 0_5%
R257 OPEN
R258 1K_5%
VCC1_POR#_3
R262 10K_5%
TP10024
TP10023
LED_3_BAT#
STBY_LED#
LED_3_CAPS#
CN25
MLX_67451_0006_OPEN
R760 10K_5%
C274 0.1UF_16V
D29 1N4148
R271 110K_1%
C293 1UF_6.3V
U21
PWR_GOOD_3
FAIR_NC7WZ17_SC70_6P
FOR FINE-TUNE POWER SEQUENCE
INVENTEC
TITLE DAVOS-DF
KBC
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
CHANGE by Kevin_Tseng
4-Nov-2004
SHEET 37 OF 65



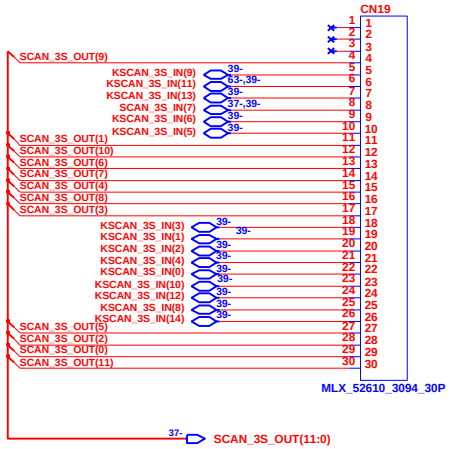
CHANGE by	Kevin_Tseng	4-Nov-2004	

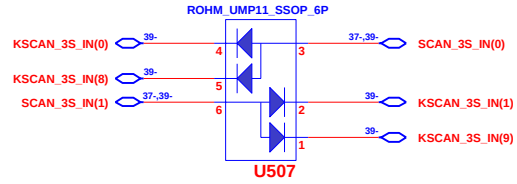
INVENTEC

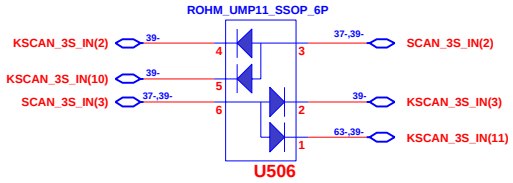
TITLE: *DAVOS-DF*
FWH

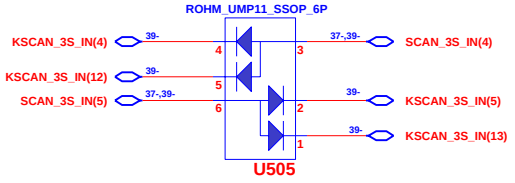
SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

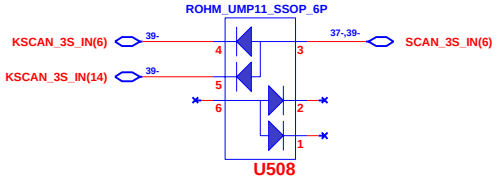
SHEET 38 OF 65

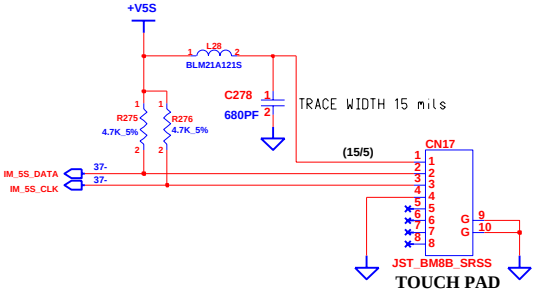


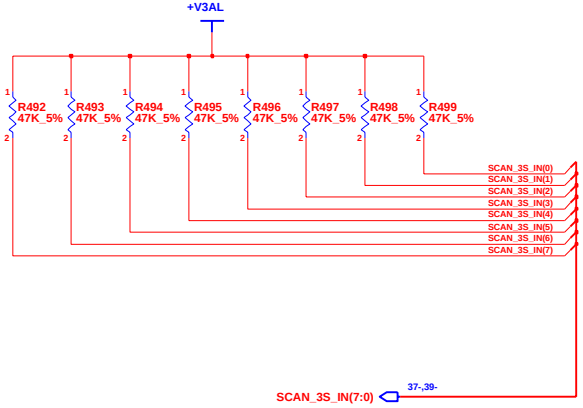












CHANGE by	*Kevin_Tseng*	4-Nov-2004	

INVENTEC

TITLE: ***DAVOS-DF***
INT. KB CONN & TOUCHPAD

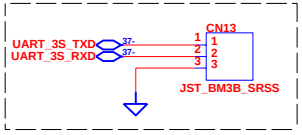
SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 39 OF 65

For FF only

INVENTEC	
TITLE	DAVOS-DF SUPER I/O
SIZE: A3	CODE: CS
DOC. NUMBER	PC9603
REV	A01
CHANGE by	Kevin_Tseng
4-Nov-2004	
SHEET	40 OF 65

DF debug only



CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

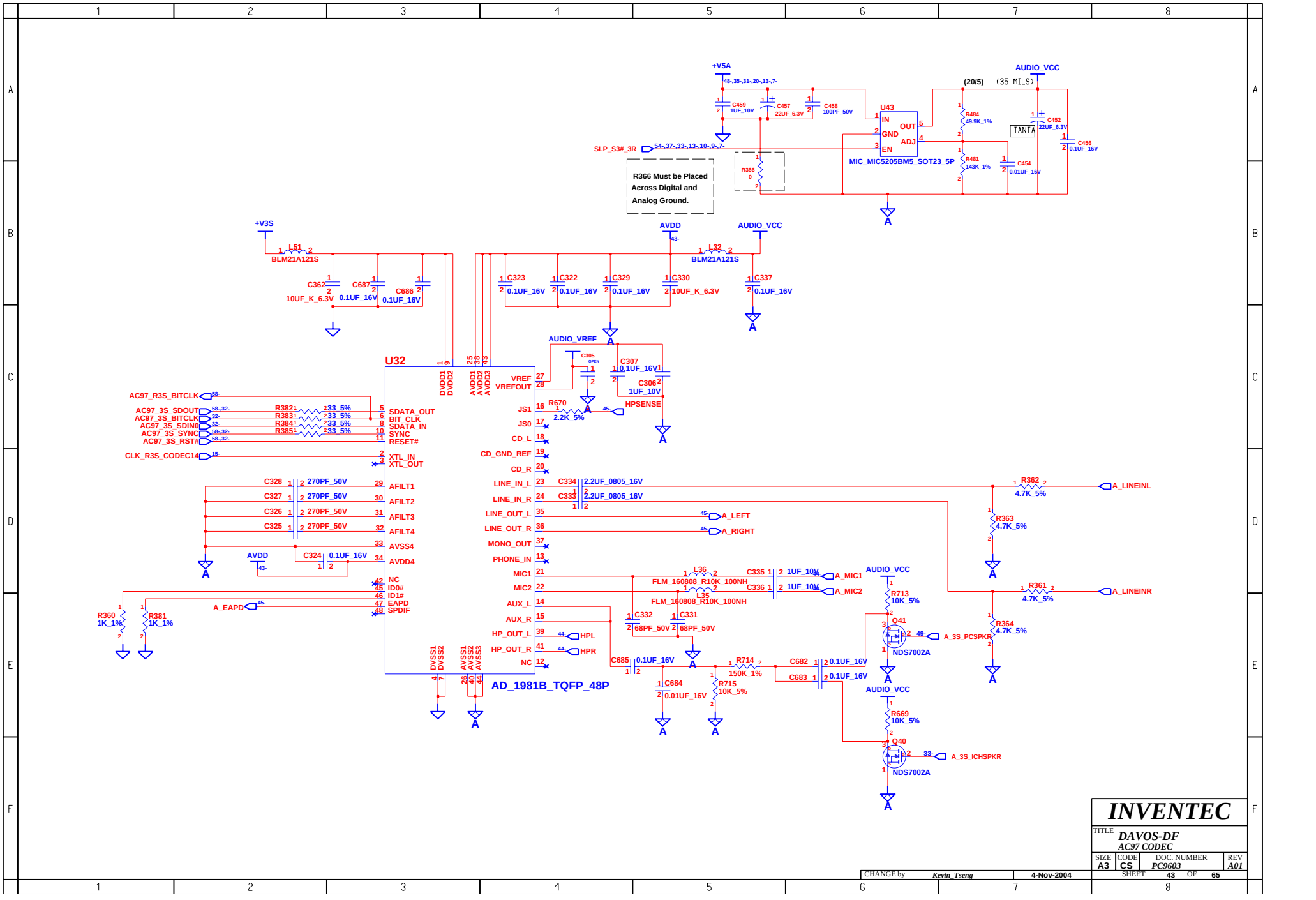
TITLE: *DAVOS-DF*
DF debug only

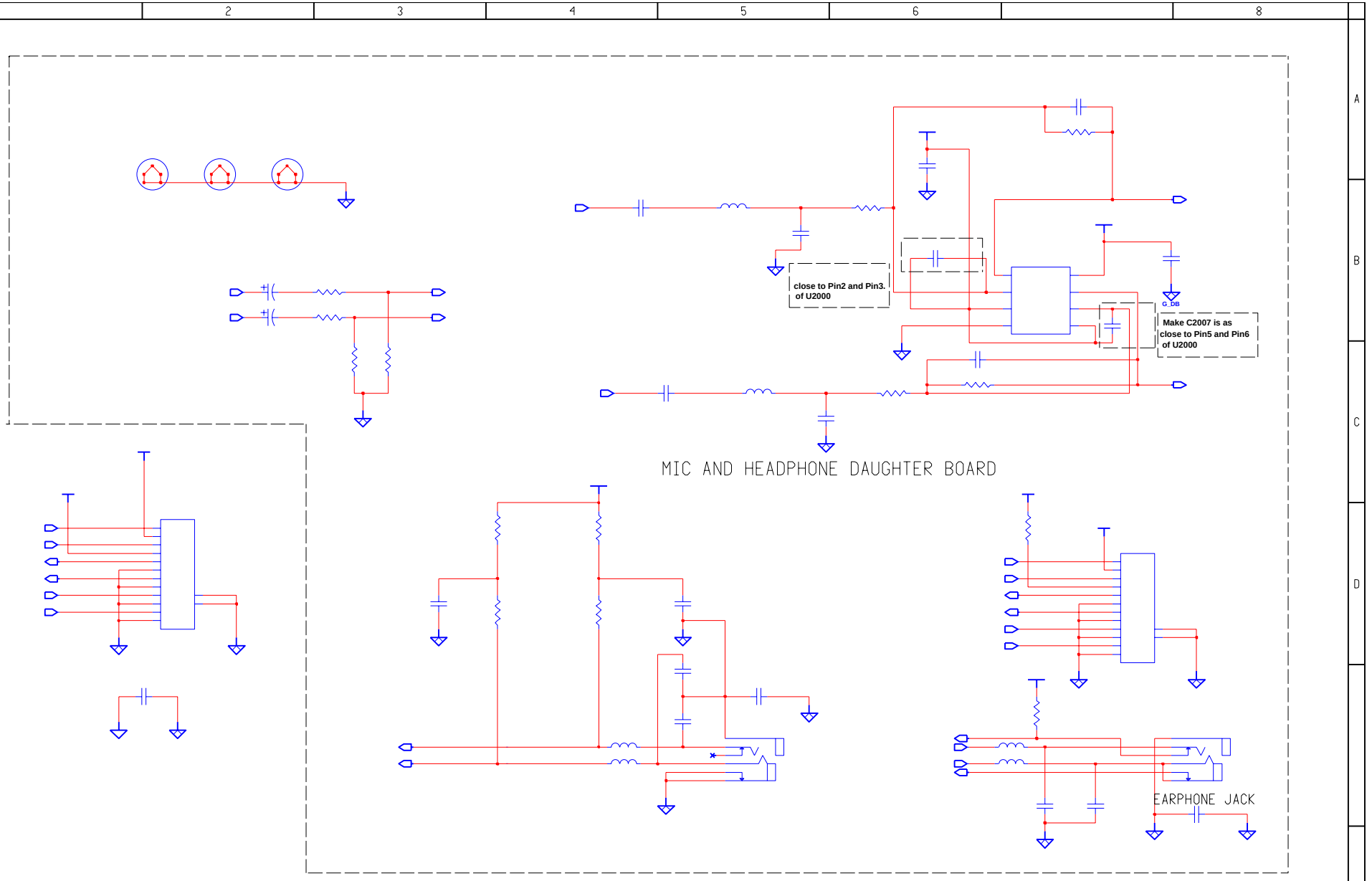
SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 41 OF 65

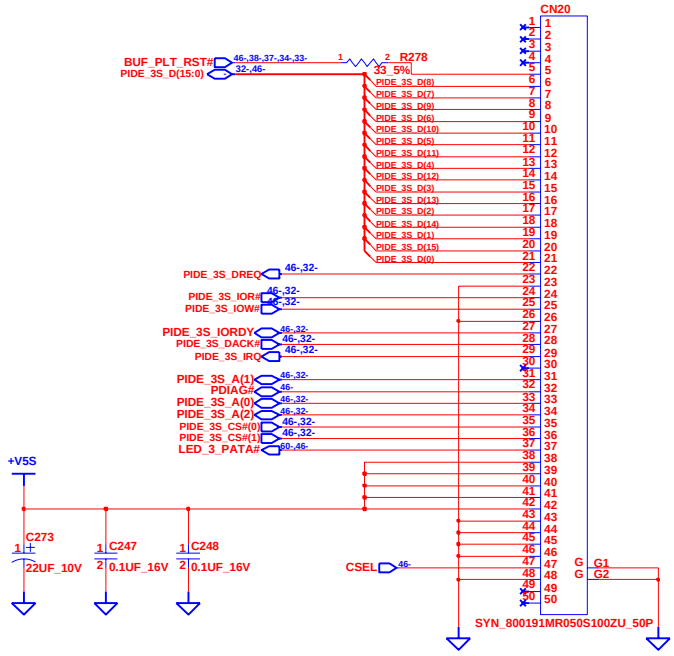
For FF only

INVENTEC	
TITLE	DAVOS-DF PARALLER PORT
SIZE A3 / CODE CS / DOC. NUMBER PC9603 / REV A01	
CHANGE by Kevin_Tseng	4-Nov-2004
SHEET 42 OF 65	


+V5A
AUDIO_VCC
(20/5) (35 MILS)
U43
MIC_MIC5205BM5_SOT23_5P
SLP_S3#_3R
R366 Must be Placed Across Digital and Analog Ground.
+V3S
AVDD
AUDIO_VCC
AUDIO_VREF
U32
AD_1981B_TQFP_48P
AC97_R3S_BITCLK
AC97_3S_SDOUT
AC97_3S_BITCLK
AC97_3S_SDIN0
AC97_3S_SYNC
AC97_3S_RST#
CLK_R3S_CODEC14
SDATA_OUT
BIT_CLK
SDATA_IN
SYNC
RESET#
XTL_IN
XTL_OUT
HPSENSE
A_LINEINL
A_LINEINR
A_LEFT
A_RIGHT
A_MIC1
A_MIC2
A_EAPD
HPL
HPR
A_3S_PCSPKR
A_3S_ICHSPKR
NDS7002A
FLM_160808_R10K_100NH
INVENTEC
DAVOS-DF
AC97 CODEC
PC9603
A01
CHANGE by Kevin_Tseng 4-Nov-2004
SHEET 43 OF 65



INVENTEC	
TITLE	
SIZE	CODE / DOC. NUMBER / REV
SHEET	OF
CHANGE by	



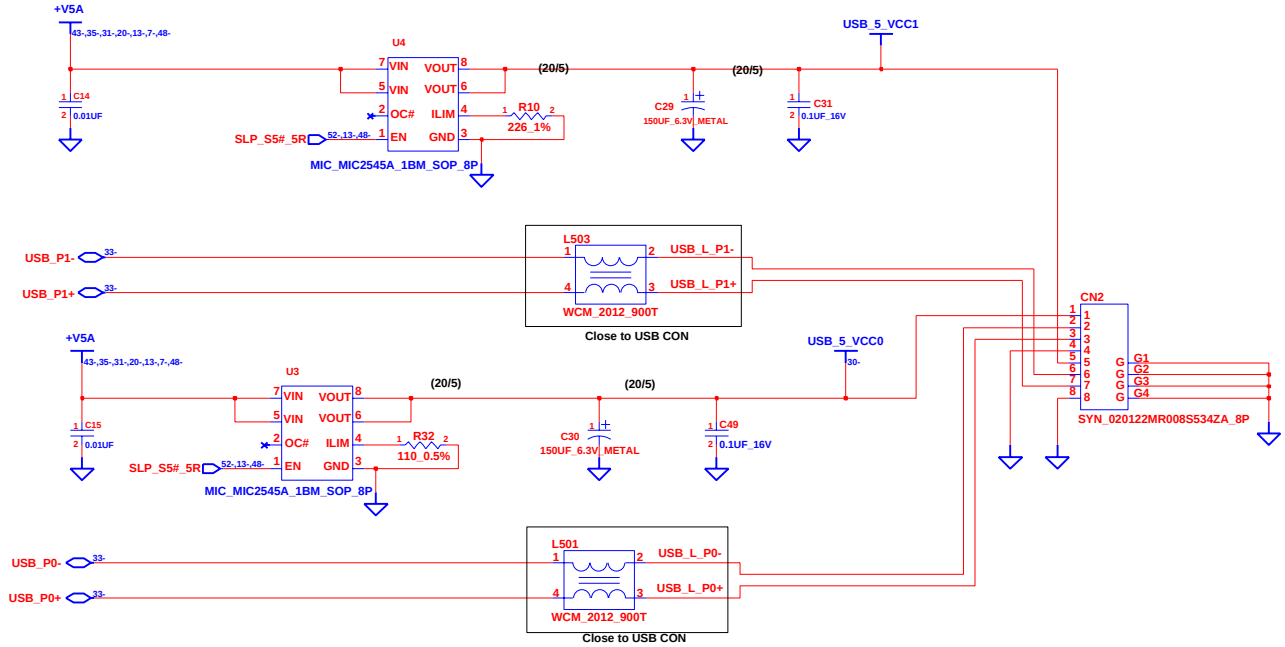
INVENTEC

TITLE: *DAVOS-DF*
OPTICAL DRIVE CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

CHANGE by: *Kevin_Tseng* 4-Nov-2004

SHEET 47 OF 65



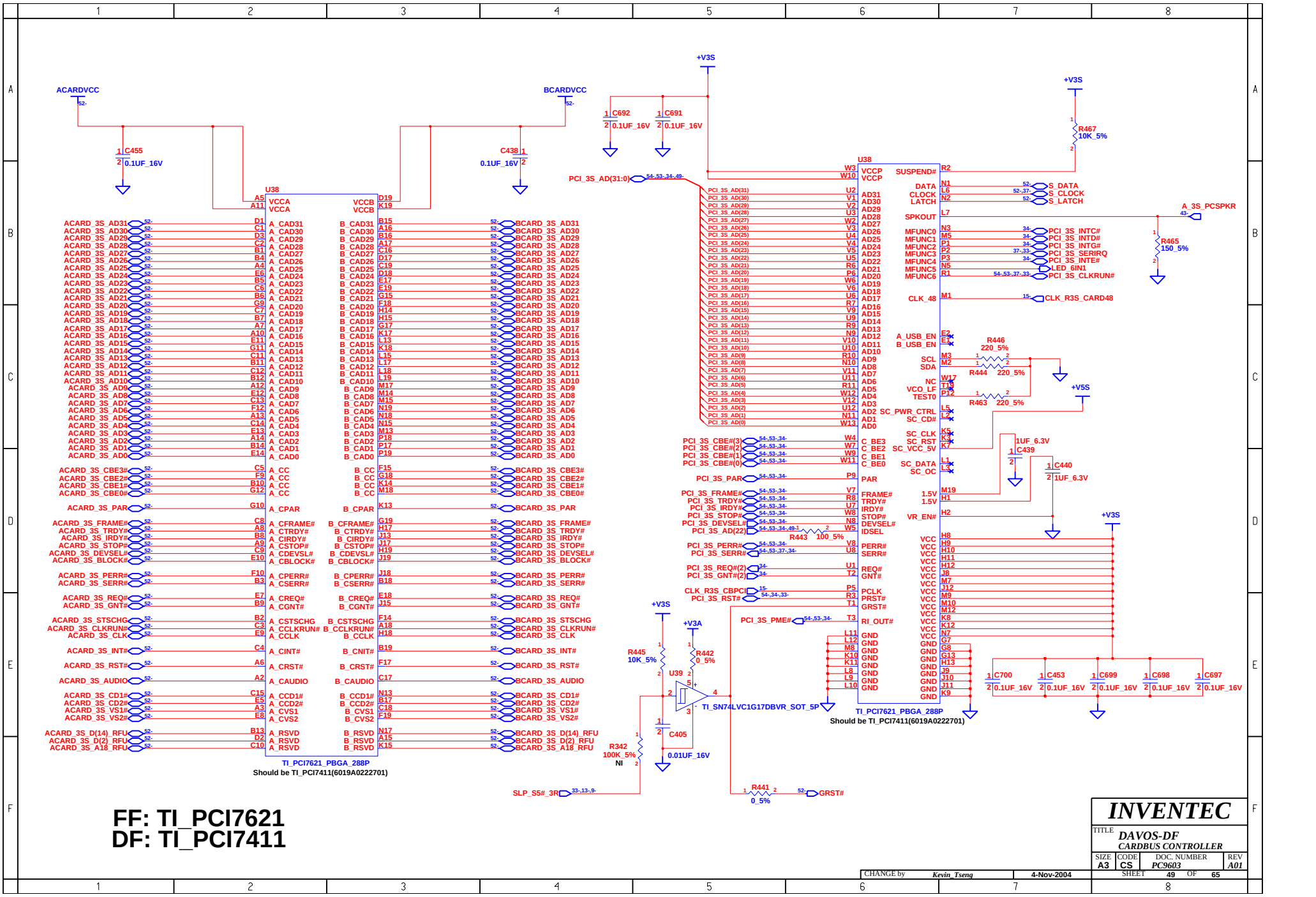
CHANGE by	*Kevin_Tseng*	4-Nov-2004	

INVENTEC

TITLE *DAVOS-DF*
USB CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 48 OF 65



FF: TI_PCI7621
DF: TI_PCI7411

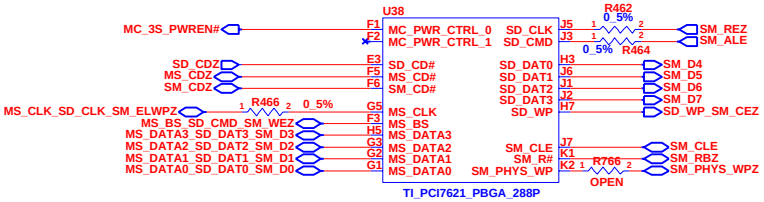
CHANGE by	Kevin_Tseng	4-Nov-2004

INVENTEC

TITLE: *DAVOS-DF*
CARDBUS CONTROLLER

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 49 OF 65



Should be TI_PCI7411(6019A0222701)

6 in 1 for FF only

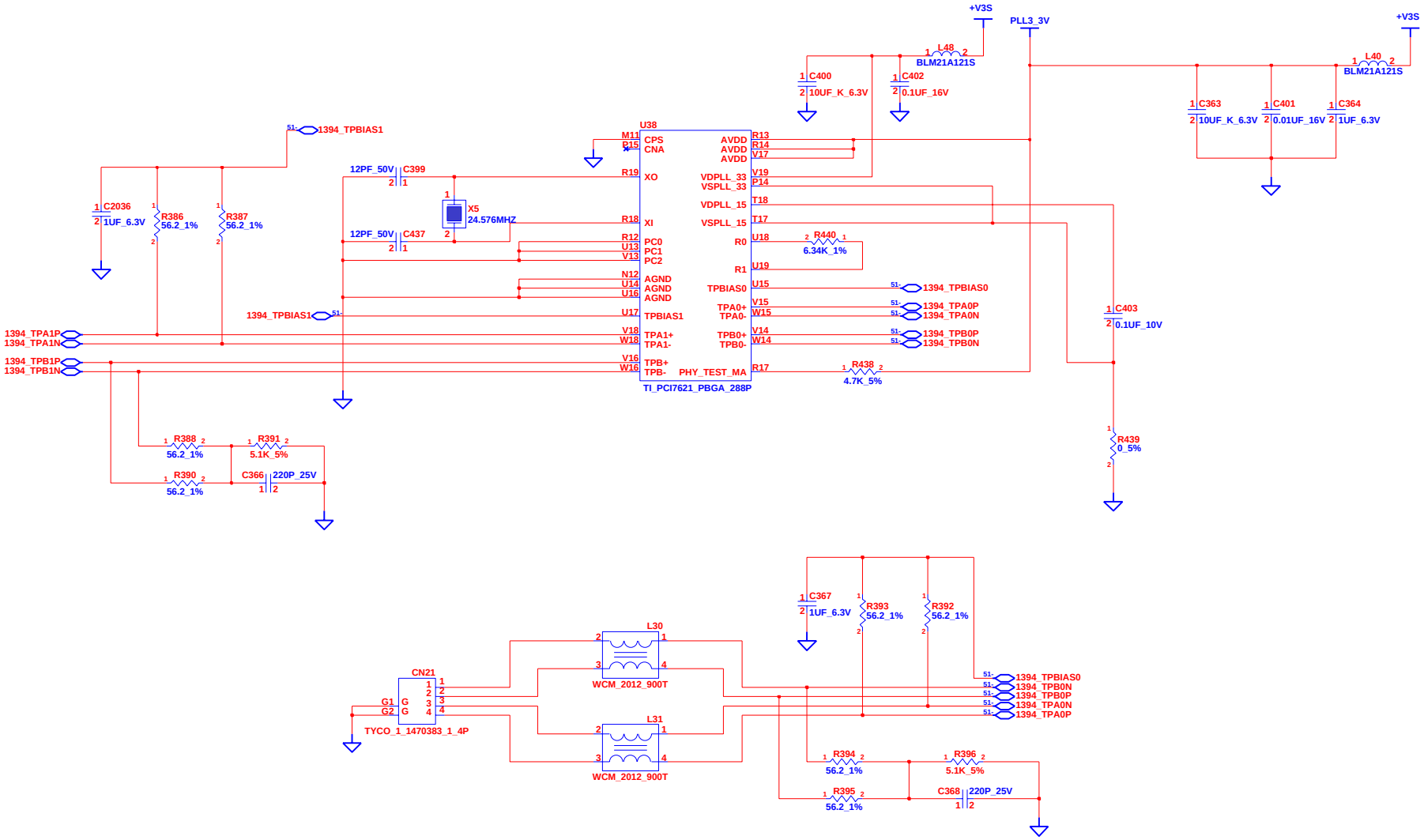
CHANGE by	Kevin_Tseng	4-Nov-2004

INVENTEC

TITLE: *DAVOS-DF*
6 IN 1 SLOT

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 50 OF 65



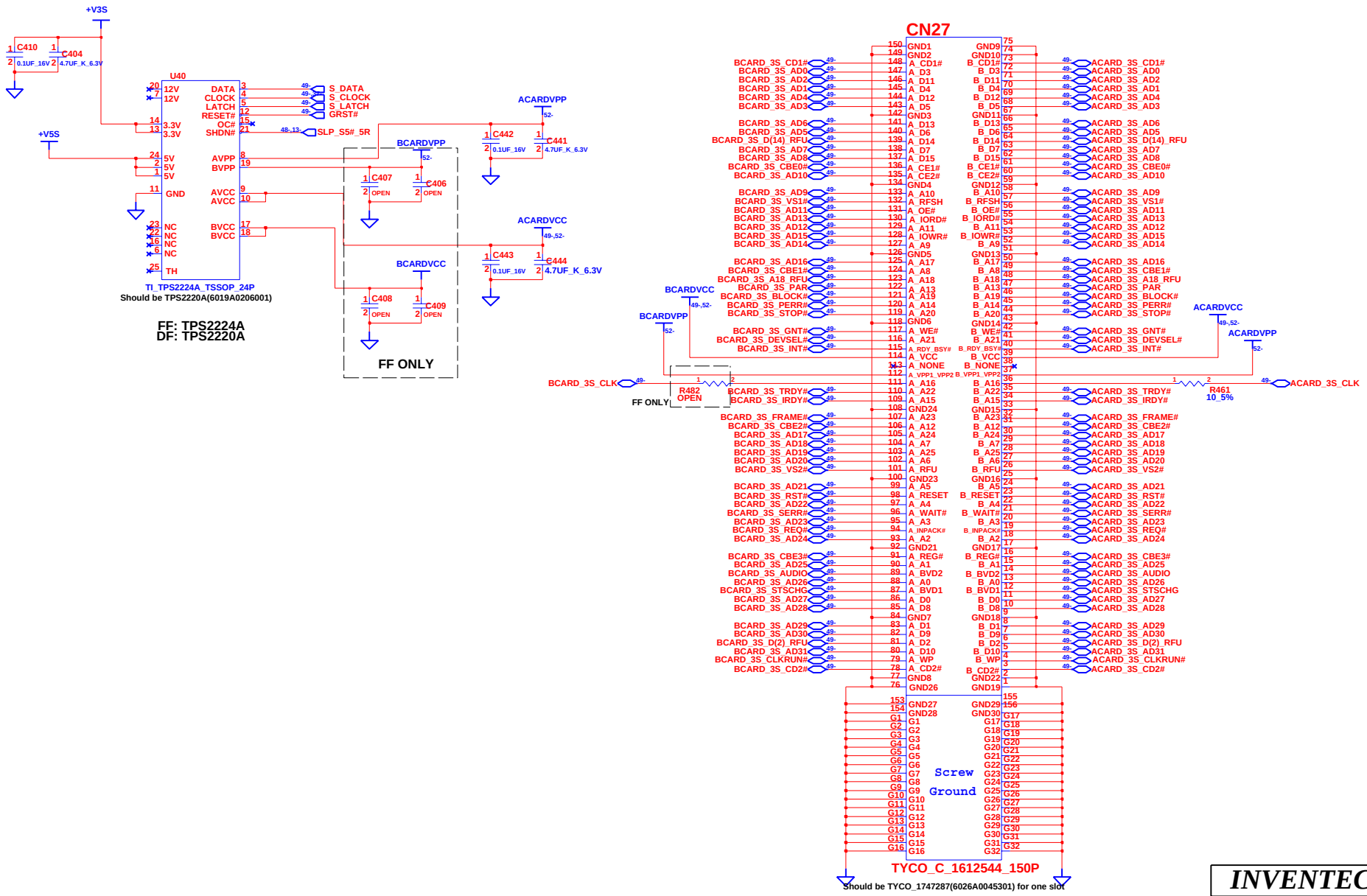
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
1394 JACK

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 51 OF 65



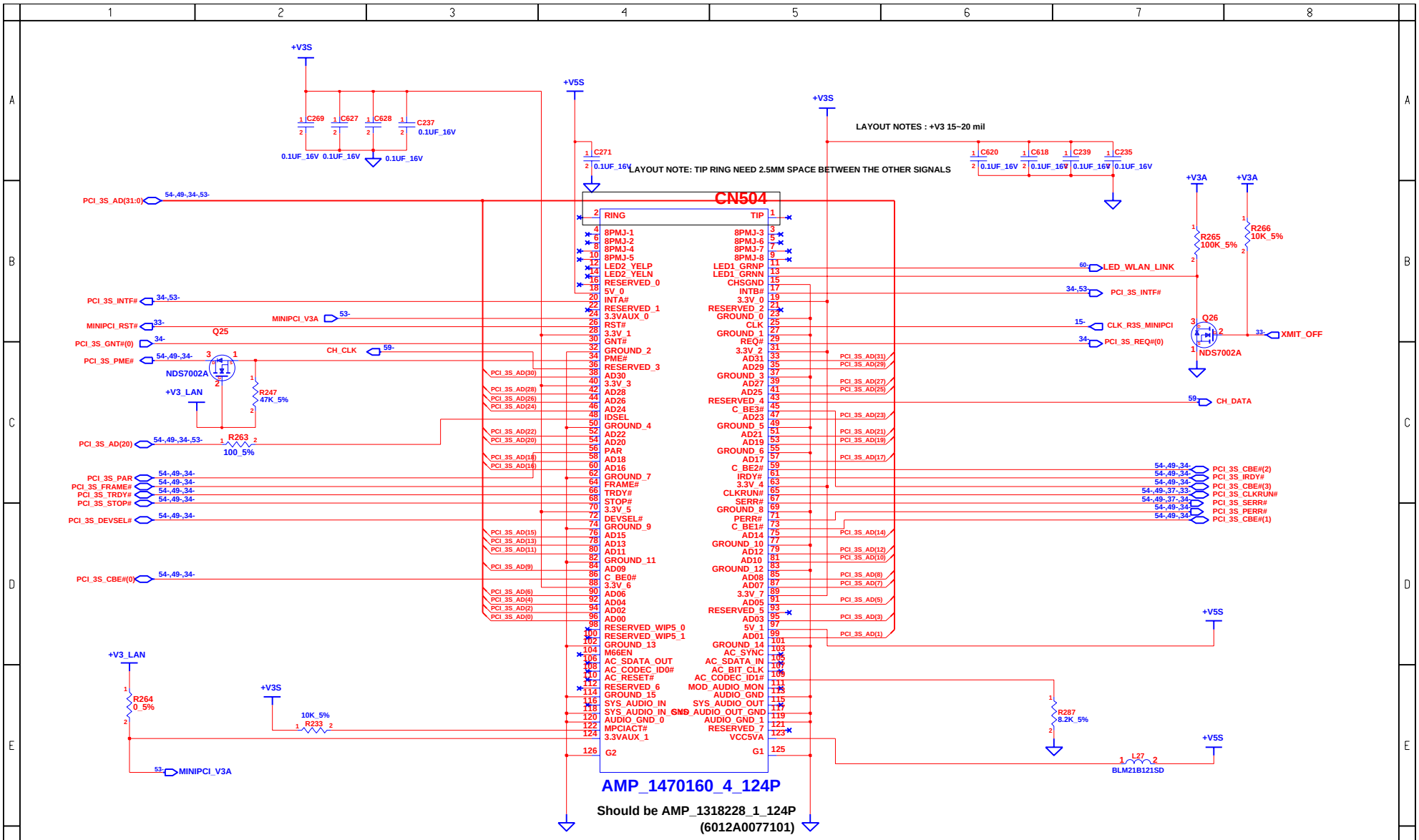
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

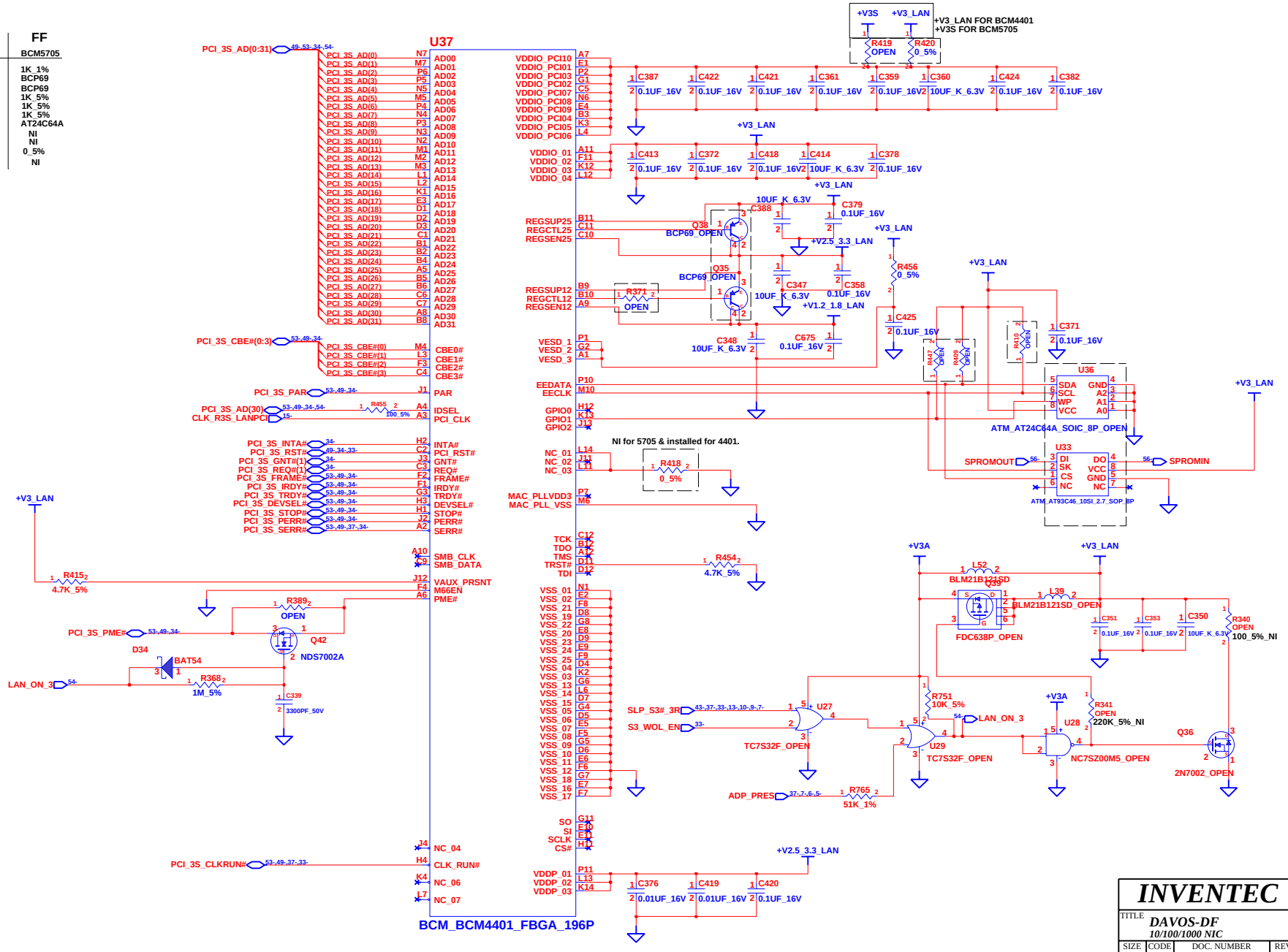
TITLE: *DAVOS-DF*
PC CARD SLOT

SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01

SHEET 52 OF 65


CN504
AMP_1470160_4_124P
Should be AMP_1318228_1_124P
(6012A0077101)
LAYOUT NOTES : +V3 15~20 mil
LAYOUT NOTE: TIP RING NEED 2.5MM SPACE BETWEEN THE OTHER SIGNALS
Q25
NDS7002A
Q26
NDS7002A
BLM21B121SD
INVENTEC
DAVOS-DF
MINIPCI CONN
SIZE A3
CODE CS
DOC. NUMBER PC9603
REV A01
SHEET 53 OF 65
CHANGE by Kevin_Tseng
4-Nov-2004

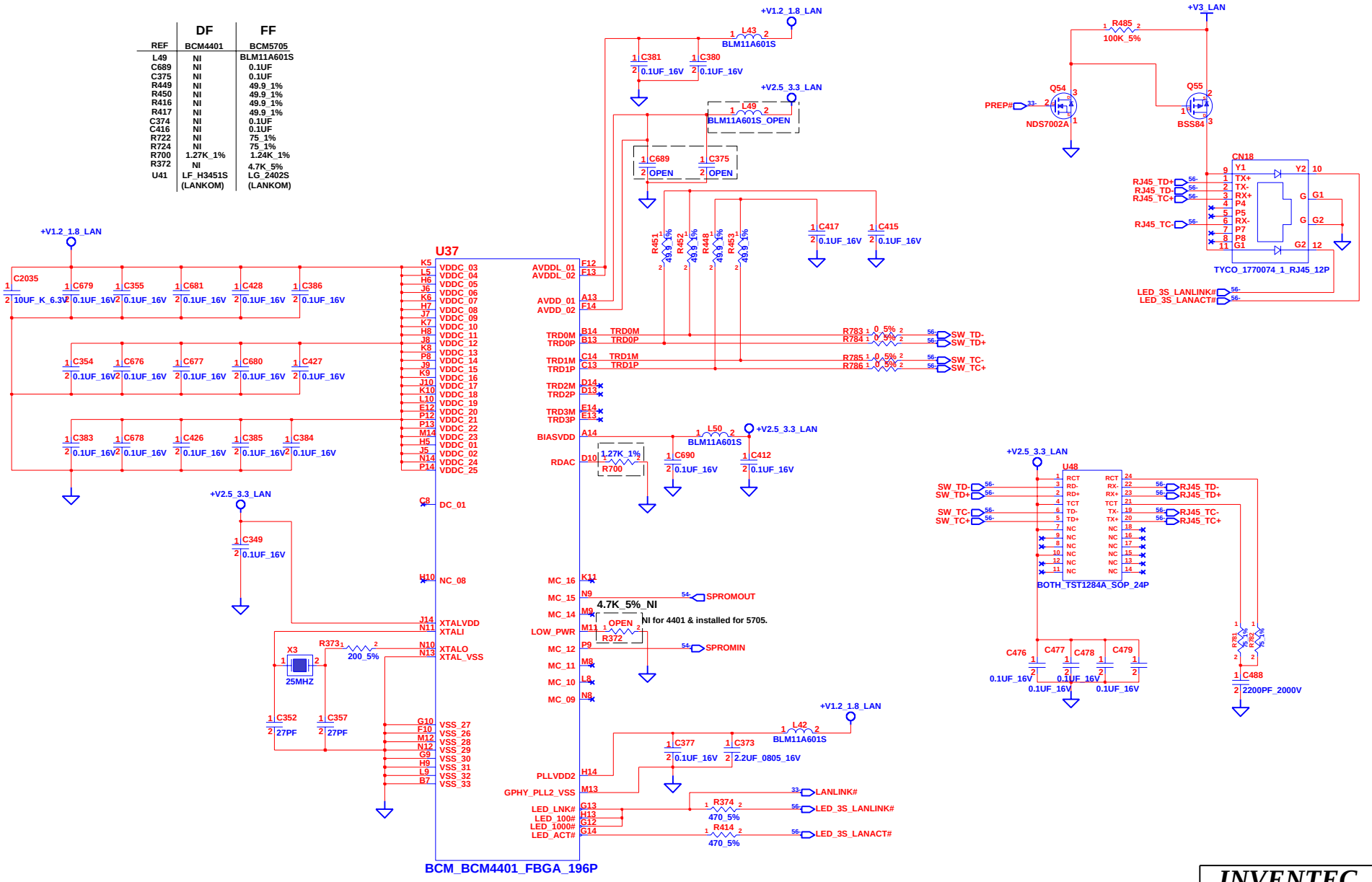
REF	DF BCM4401	FF BCM5705
R371	NI	1K_1%
Q35	NI	BCP69
Q38	NI	BCP69
R447	NI	1K_5%
R409	NI	1K_5%
R410	NI	1K_5%
U36	NI	AT24C64A
U33	AT93C46A	NI
R420	0_5%	NI
R419	NI	0_5%
R418	0_5%	NI



BLANK

INVENTEC			
TITLE	DAVOS-DF BLANK		
SIZE: A3	CODE: CS	DOC. NUMBER: PC9603	REV: A01
CHANGE by: Kevin_Tseng	4-Nov-2004	SHEET 55 OF 65	

REF	DF BCM4401	FF BCM5705
L49	NI	BLM11A601S
C689	NI	0.1UF
C375	NI	0.1UF
R449	NI	49.9_1%
R450	NI	49.9_1%
R416	NI	49.9_1%
R417	NI	49.9_1%
C374	NI	0.1UF
C416	NI	0.1UF
R722	NI	75_1%
R724	NI	75_1%
R700	1.27K_1%	1.24K_1%
R372	NI	4.7K_5%
U41	LF_H3451S (LANKOM)	LG_2402S (LANKOM)



CHANGE by	Kevin_Tseng	4-Nov-2004	

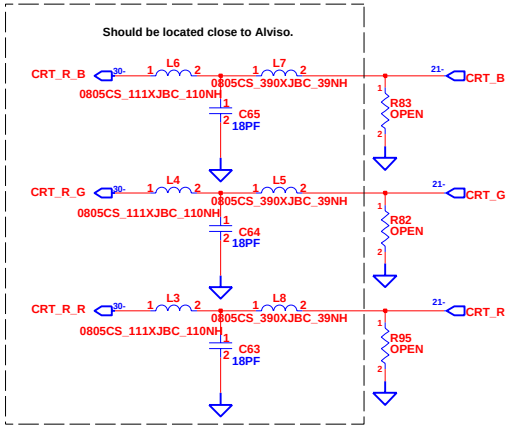
INVENTEC

TITLE: DAVOS-DF
NIC-TRANSFORMER

SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01

SHEET 56 OF 65

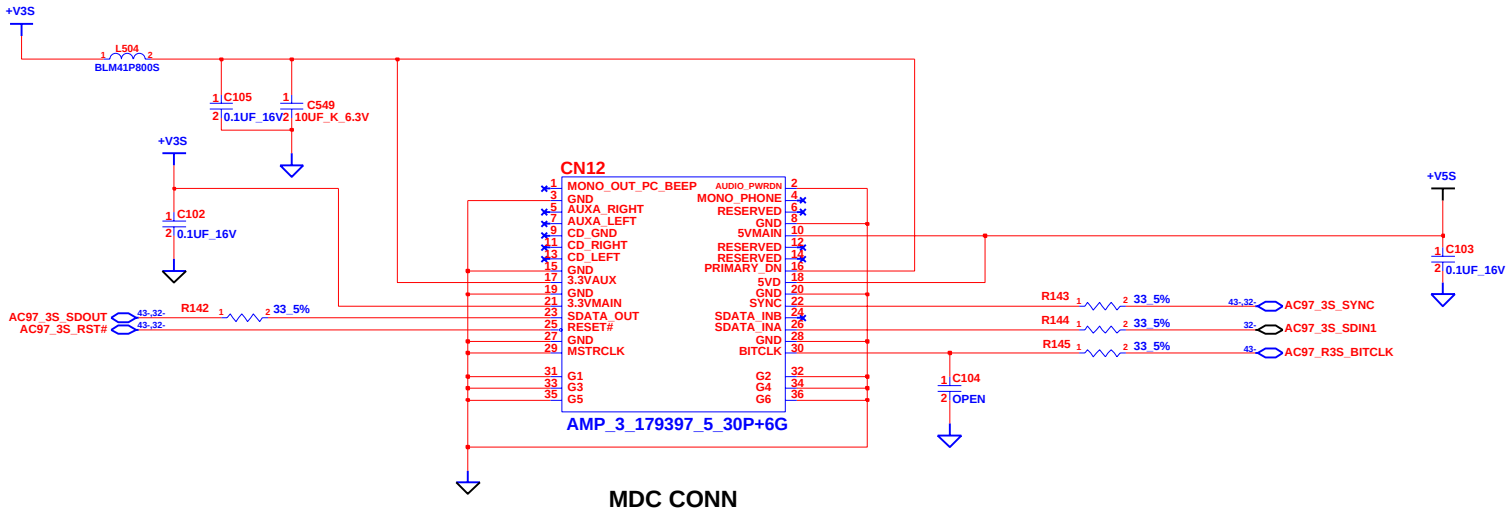
Docking for FF only



CHANGE by	Kevin_Tseng	4-Nov-2004

INVENTEC

TITLE	DAVOS-DF DOCKING CONN		
SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01
SHEET	57	OF	65

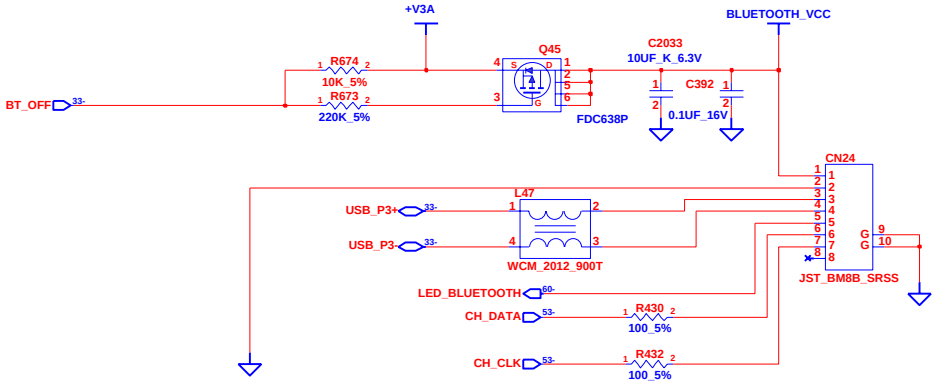


MDC CONN

CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE	DAVOS-DF MDC CNN		
SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01
SHEET	58	OF	65



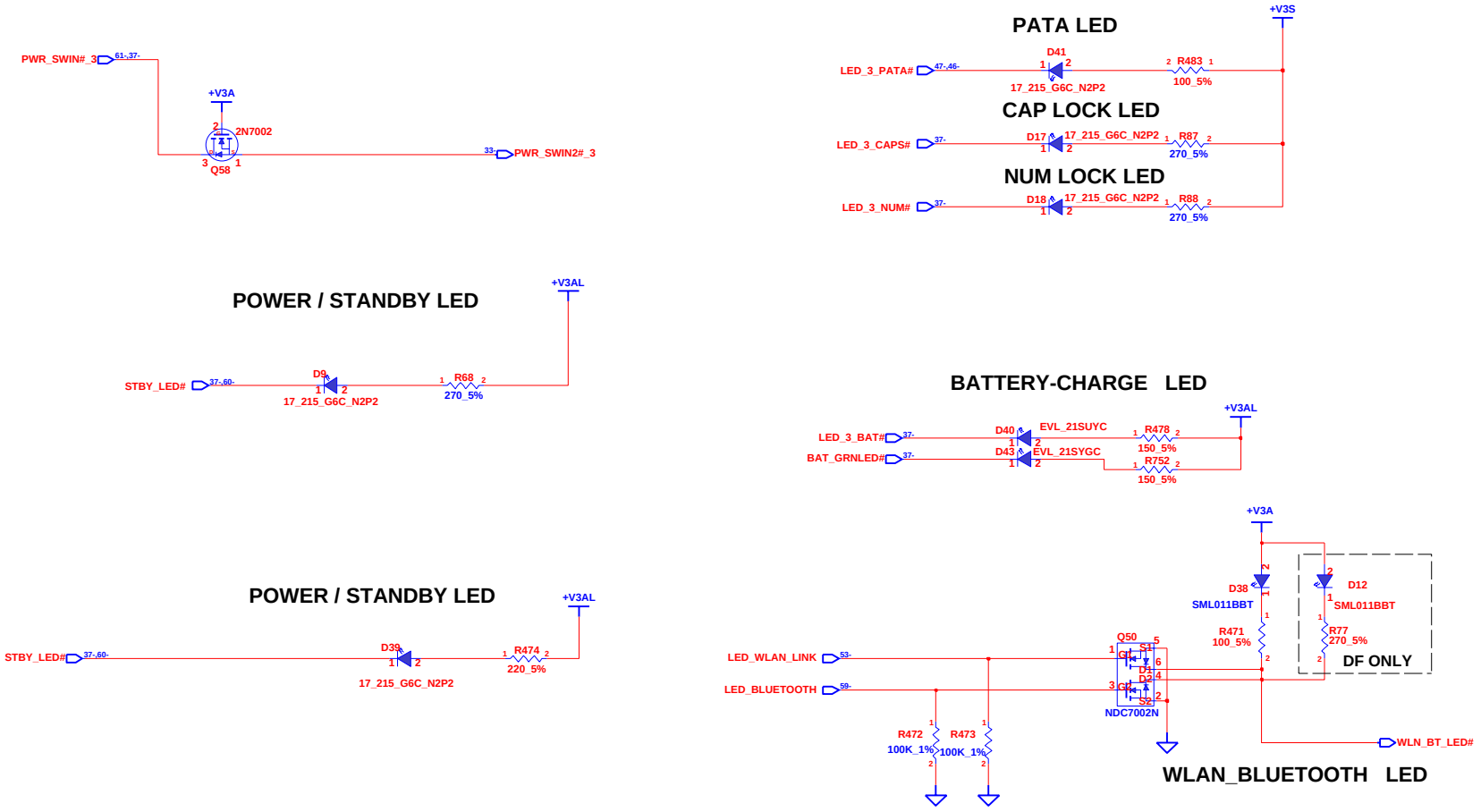
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF*
BLUETOOTH CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 59 OF 65

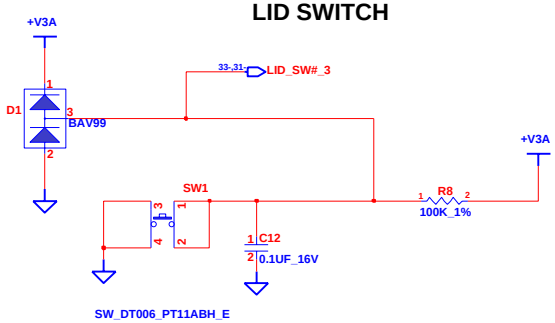


CHANGE by	Kevin_Tseng	4-Nov-2004	

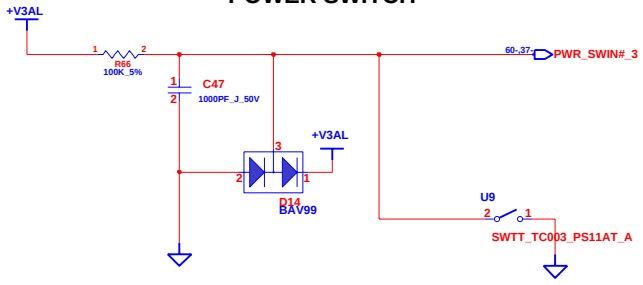
INVENTEC

TITLE	DAVOS-DF LEDS			
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	PC9603	A01	
		SHEET 60 OF 65		

LID SWITCH



POWER SWITCH



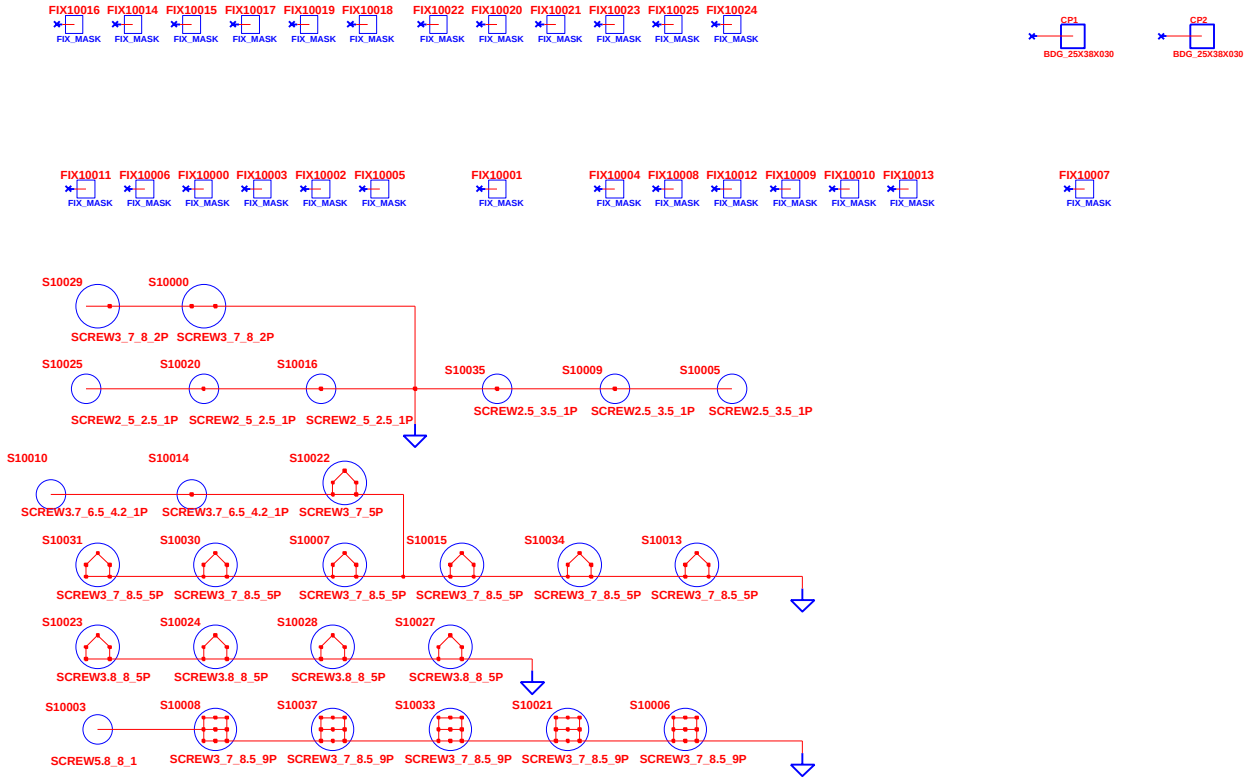
CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: *DAVOS-DF SWITCH*

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 61 OF 65



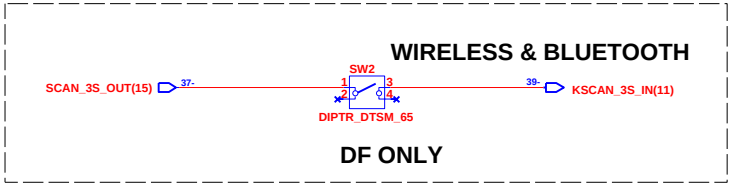
CHANGE by	Kevin_Tseng	4-Nov-2004

INVENTEC

TITLE	DAVOS-DF SCREW			
SIZE	CODE	DOC. NUMBER		REV
A3	CS	PC9603		A01
		SHEET	62 OF 65	

BUTTON & LED DAUGHTER BOARD

For FF only



CHANGE by	Kevin_Tseng	4-Nov-2004	

INVENTEC

TITLE: DAVOS-DF
BUTTON & LED Daughtor Board

SIZE	CODE	DOC. NUMBER	REV
A3	CS	PC9603	A01

SHEET 63 OF 65

LCD CABLE

1	1	1
2	+V3S	2
3		3
4	+V3S	4
5		5
6	+V3S	6
7		7
8	LCM_DDCPCLK	8
9		9
10	LCM_DDCPDATA	10
11	LVDS_TXDL0-	11
12	LVDS_TXDL0+	12
13	LVDS_TXDL1-	13
14	LVDS_TXDL1+	14
15	LVDS_TXDL2-	15
16	LVDS_TXDL2+	16
17	LVDS_TXCL-	17
18	LVDS_TXCL+	18
19	GND	19
20	GND	20
21	LVDS_TXDU0-	21
22	LVDS_TXDU0+	22
23	LVDS_TXDU1-	23
24	LVDS_TXDU1+	24
25	LVDS_TXDU2-	25
26	LVDS_TXDU2+	26
27	LVDS_TXCU+	27
28	LVDS_TXCU-	28
29	GND	29
30	GND	30
31		31
32	ALS_EN	32
33	+V5A	33
34	+V5A	34
35	+V5A	35
36	INV_PWM_3	36
37	+V3S	37
38	GND	38
39	GND	39
40	GND	40

keyboard

1	1	1
2		2
3		3
4	SCAN_3S_OUT(9)	4
5	KSCAN_3S_IN(9)	5
6	KSCAN_3S_IN(11)	6
7	KSCAN_3S_IN(13)	7
8	SCAN_3S_IN(7)	8
9	KSCAN_3S_IN(6)	9
10	KSCAN_3S_IN(5)	10
11	SCAN_3S_OUT(1)	11
12	SCAN_3S_OUT(10)	12
13	SCAN_3S_OUT(6)	13
14	SCAN_3S_OUT(7)	14
15	SCAN_3S_OUT(4)	15
16	SCAN_3S_OUT(8)	16
17	SCAN_3S_OUT(3)	17
18	KSCAN_3S_IN(3)	18
19	KSCAN_3S_IN(1)	19
20	KSCAN_3S_IN(2)	20
21	KSCAN_3S_IN(4)	21
22	KSCAN_3S_IN(0)	22
23	KSCAN_3S_IN(10)	23
24	KSCAN_3S_IN(12)	24
25	KSCAN_3S_IN(8)	25
26	KSCAN_3S_IN(14)	26
27	SCAN_3S_OUT(5)	27
28	SCAN_3S_OUT(2)	28
29	SCAN_3S_OUT(0)	29
30	SCAN_3S_OUT(11)	30

Card Bus 6 in 1

1	VCC_MC	1
2	VCC_MC	2
3	VCC_MC	3
4	VCC_MC	4
5	LCD_6IN1	5
6	GND	6
7	MS_DATA0_SD_DAT0_SM_D0	7
8	MS_DATA1_SD_DAT1_SM_D1	8
9	MS_DATA2_SD_DAT2_SM_D2	9
10	MS_DATA3_SD_DAT3_SM_D3	10
11	GND	11
12	MS_CLK_SD_CLK_SM_ELWPZ	12
13	GND	13
14	MS_BS_SD_CMD_SM_WEZ	14
15	SM_ALE	15
16	SM_CLE	16
17	MS_CDZ	17
18	SD_CDZ	18
19	SD_WP_SM_CEZ	19
20	GND	20
21	SM_REZ	21
22	GND	22
23	SM_RBZ	23
24	SM_D4	24
25	SM_D5	25
26	SM_D6	26
27	SM_D7	27
28	SM_PHYS_WPZ	28
29	SM_CDZ	29
30	GND	30

BLUETOOTH CABLE

1	BLUETOOTH_VCC	8
2	GND	7
3	USB_P3+	6
4	USB_P3-	5
5	LED_BLUETOOTH	4
6	CH_DATA	3
7	CH_CLK	2
8		1

SERIAL CABLE

1	RS232_R1IN	1
2	RS232_R2IN	2
3	RS232_R3IN	3
4	RS232_R4IN	4
5	RS232_R5IN	5
6	RS232_T1OUT	6
7	RS232_T2OUT	7
8	RS232_T3OUT	8
9	GND	9
10	GND	10

USB CABLE

1	+5A_USB	1
2	+5A_USB	2
3	+5A_USB	3
4	+5A_USB	4
5	USB_P6-	5
6	USB_P6+	6
7	USB_P7-	7
8	USB_P7+	8
9	GND	9
10	GND	10
11	GND	11
12	GND	12

TOUCHPAD CABLE

1	+V5S	1
2	IM_5S_DATA	2
3	IM_5S_CLK	3
4	GND	4
5		5
6		6
7		7
8		8

RJ45

CN1RJ45

1	RJ45TC+	3
2	RJ45_TD-	2
3	LED_3S_LANLINK#	12
4	LED_3S_LANACT#	11
5	+V3S	10
6	RJ45_TA-	8
7	RJ45_TA+	7
8	+V3S	9
9	RJ45_TB-	6
10	RJ45_TB+	5
11	RJ45_TC-	4
12	RJ45_TD+	1

1		1
2		2
3		3
4		4
5		5
6		6
7		7
8		8

RJ11

RJ11

1	TIP	1
2	RING	2

1	1
2	2

SWITCH PCB CABLE

1	+V3A	12
2	+V3S	11
3	A_EAPD	10
4	WLN_BT_LED#	9
5	SCAN_3S_OUT(15)	8
6	KSCAN_3S_IN(8)	7
7	KSCAN_3S_IN(9)	6
8	KSCAN_3S_IN(10)	5
9	KSCAN_3S_IN(11)	4
10	KSCAN_3S_IN(12)	3
11	KSCAN_3S_IN(13)	2
12	GND	1

AUDIO CABLE

1	PR_AOUTL_DB	1
2	AUDIO_VCC_DB	2
3	PR_AOUTR_DB	3
4	AUDIO_VREF_DB	4
5	HPL_DB	5
6	GND	6
7	HPR_DB	7
8	GND	8
9	A_MIC1_DB	9
10	GND	10
11	A_MIC2_DB	11
12	GND	12

CHANGE by *Kevin_Tseng* 4-Nov-2004

INVENTEC

TITLE *DAVOS-DF*
Cables

SIZE	CODE	DOC. NUMBER	REV
A3	CS	*PC9603*	*A01*

SHEET 64 OF 65

17	NIC	56	Zone E8, Remove CN28 for lan signal to docking going to on board.	2004/10/8	IEC	Open	.--Fixed in 10/08.
18	NIC	57	Zone E8, Remove CN10 for lan signal to docking going to on board.	2004/10/8	IEC	Open	.--Fixed in 10/08.
19	All	All	Change reference, C5003->C713, C5004->C714, C5005->C715, C5006->C716, C5007->C717, C5008->C718, U2001->U512, U2001->U512, U2002->U513, U2003->U514.	2004/10/8	IEC	Open	.--Fixed in 10/08.
20	Power	7	Add 1000pf cap(C719). from R743p1 to GND Add .1uf cap(C720). from U510p2 to GND Add 220k(R776) from U509p1 ()to U509p3. Disconnect V3AL_OFF# from U510p7 Disconnect Q533p3 from R742 Add NFET(Q534), tie gate to Q533p1, drain to Q533p3 and to V3AL_OFF#, and source to R742p2 & U510p1. Change U509 to MIC5235 Change R740 to 30.1k	2004/10/9	HP	Open	Issue was discovered with U510 pg7 in which it could begin to oscillate pulling down +V5AL prohibiting the system from booting. .--Fixed in 10/11 schematic.
21	VGA	30	Change pin 5 of U512/U513/U514 from +V2.5S to +V5S, Change pin1 from PREP# to ISO_PREP.	2004/10/11	IEC	Open	Change its VCC to +V5S for having a lower Ron, change enable pin of switch from PREP# to ISO_PREP# for preventing switch active when system off. .--Fixed in 10/11.
22	Audio	43	Change pin2 of L51 connect to pin1 of C362/C687/C686.	2004/10/11	IEC	Open	For SI2 Audio low level test failed issue. .--Fixed in 10/11.
23	Power	13	Change R332 to 1K.	2004/10/12	HP	Open	470 pullup for PWR_GOOD is too strong. .--Fixed in 10/12.
24	ITP	13	Add C721, C722, C723, C724, C725, C726--0.027uF no install caps on following signals--H_BPM0_ITP#, H_BPM1_ITP#, H_BPM2_ITP#, H_BPM3_ITP#, H_BPM4_PRDY#, H_BPM5_PREQ#, H_TCK.	2004/10/12	HP	Open	AC/DC transition & hang issue. .--Fixed in 10/12.
25	Power	5	Zone A4, R14 from 909 change to 221----Change OCP value.	2004/10/13	IEC	Open	Issue for OCP is throttling the CPU on AC .--Fixed in 10/22
26	Power	11	Zone B5, Add a 300 ohm resistor(R777) between C592 and +VBATR that can reduce Vcore drop during +VbatR raising.	2004/10/14	IEC	Open	AC/DC transition & hang issue. .--Fixed in 10/22
27	Audio	45	Zone B7, Remove C286 in order to place two transformers.	2004/10/14	IEC	Open	Fixed in 10/22
28	Power	10	Delete original circuits, add U47(TPS51100), R778, C466, C467, C468, C469, C470, C471.---For M_VREF is going to 0V when the failing system (Davos SI2 Model 1 with 256MB ProMOS memory) is returning from S3.	2004/10/26	IEC	Open	Fixed in 10/26
29	NIC	56	Change U46 to Pericom PI3L301DA, add C472, **C5005**, C474, C475. Add one more transformer(U48) Bothhand GST5009 and C476, C477, C478, C479, R779, R780, R781, R782, C488. Also change U41 to GST5009. Add no install resistors--R783, R784, R785, R786 for DF only.	2004/10/22	IEC	Open	Fixed in 10/22--update in 11/04
30	Power	11	Add eight 0.01uF(no install) caps(C480, C481, C482, C483, C484, C485, C486, C487), **and C473 no install bulk cap** on +VBATR plane.--for decoupling noise from +VBATR	2004/10/22	IEC	Open	Fixed in 10/22--update in 11/04

31	RTC	32	Zone B3, Change X4 to vail used, change C389, C429 from 15pF to 10pF in bom.	2004/10/27	IEC	Open	Fixed in 10/27
32	Alviso	21	Zone C6, Change R590 from 255_1% to 243_1% to modify R,G,B max Voltage and this change can make R,G,B max Voltage meet the SPEC value.	2004/10/27	HP	Open	Fixed in 10/27
33	Switch board	63	Change R4001, R4002 from 270_5% to 1.8K--change LED brightness.	2004/10/27	HP	Open	Fixed in 10/27
34	LEDs	60	Change R483, R471 from 270_5% to 100_5%, **change R478, R752 to 150_5%, change R474 to 220_5%**-change LED brightness.	2004/10/27	HP	Open	Fixed in 10/27---update in 11/04
35	NIC-DF only	54	~~Re-install Q39, Q36, L39, U27, U29, U28, R340, R341, remove L52, R751--For BCM4401 have worse power consumption.~~---- **We drop this change because OTS144058 had been closed by waiver approved.**	2004/10/27	IEC	Open	Fixed in 10/27---update in 11/04
36	Alviso	21	Zone D1, Change R177, R194 to open.---Intel has also stated that the fix for this problem with the Alviso pulling too much current is to remove the OCDCOMP[1:0] resistors on Alviso	2004/10/28	HP	Open	Fixed in 10/28
37	ICH6	33	Zone E3, Add 100k pulldown(R787) from PM_DPRSLPVR to GND.	2004/10/28	HP	Open	Fixed in 10/28