

# Compal Confidential

## JAL90 M/B Schematics Document

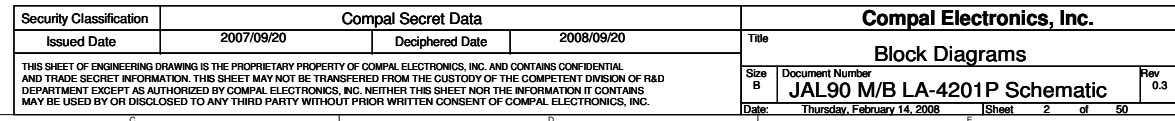
Intel Penryn Processor with Cantiga + DDRII + ICH9M

2008-02-20

REV : 0 . 3

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**Model Name : JAL90**  
**File Name : LA-4201P**



Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+1.25VS	1.25V switched power rail	ON	OFF	OFF
+1.5V	1.5V power rail for HDA	ON	ON	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail for SB	ON	ON	X
+3V_LAN	3.3V power rail for LAN	ON	ON	X
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON\* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
--------	--------	-----------	------------

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	ADI ADT7421	1001 100X b
EEPROM(24C16/02)	1010 000X b		
GMT G781-1	1001 101X b		

ICH9M SM Bus address

Device	Address
Clock Generator (ICS9LPRS367, SLG8SP556V)	1101 001Xb
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

STATE	SIGNAL				SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON				
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW				
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF				
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF				
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF				

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 5%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

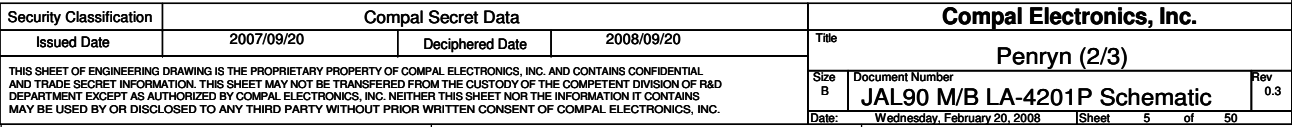
Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	1.0
4	1A
5	
6	
7	

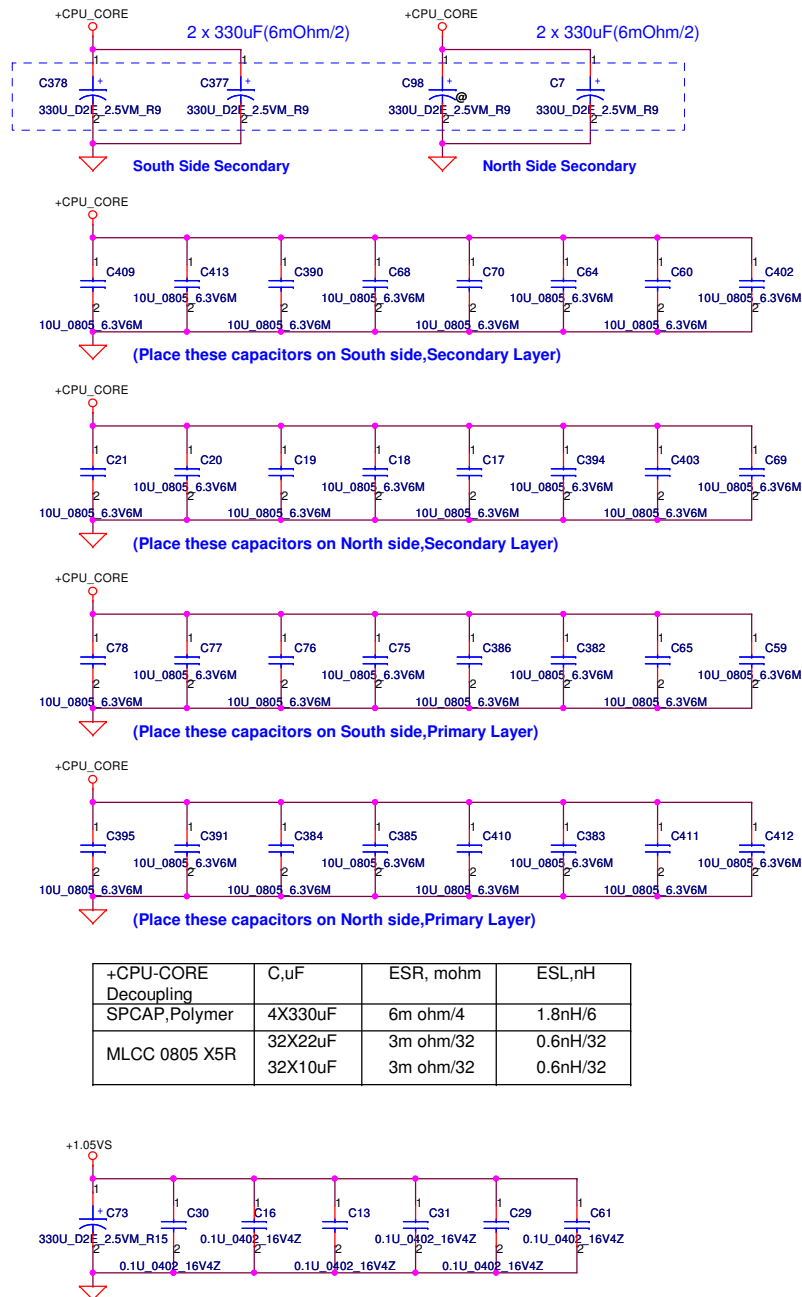
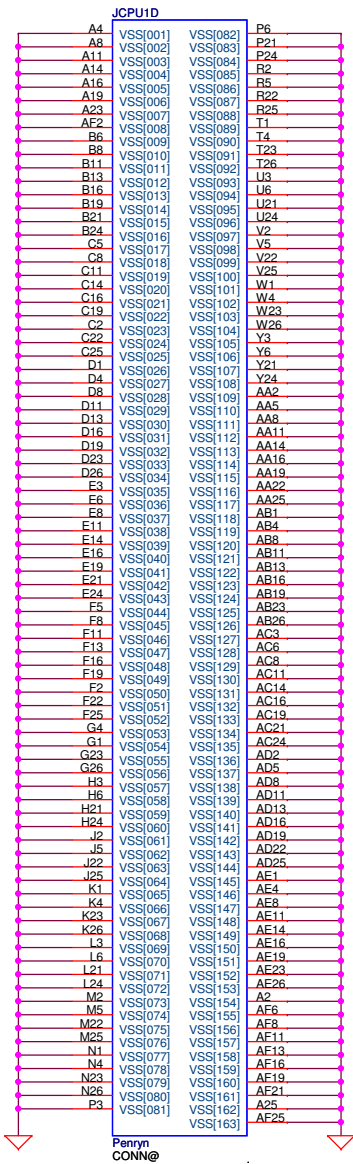
BTO Option Table

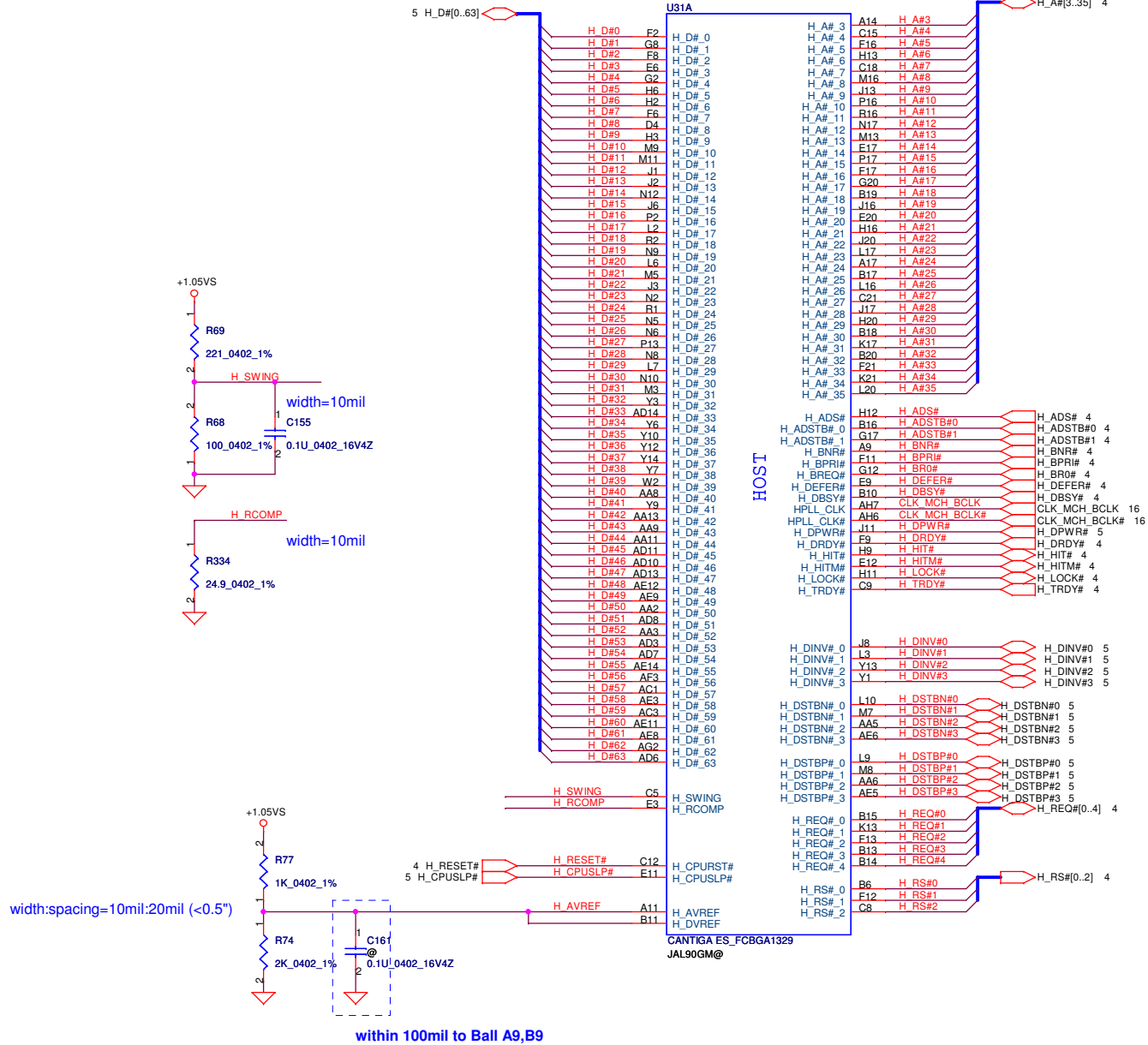
BTO Item	BOM Structure
JAL90	JAL90@
JAW50	JAW50@
UMA	GM@
JAL90-UMA	JAL90GM@
JAW50	GLPM@
Discrete	PM@
Discrete	PM@
ALC888VC	888VC@
ALC888VB	888VB@
8111C	8111C@
8102E	8102E@
ALC268	268@

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				Size B	Document Number
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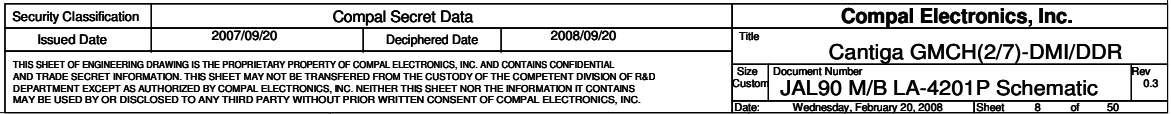




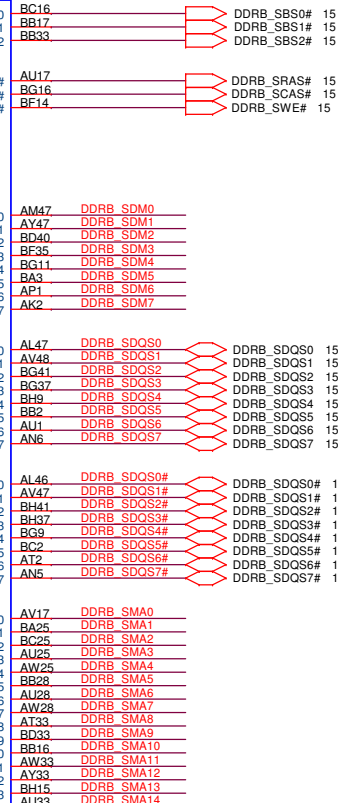
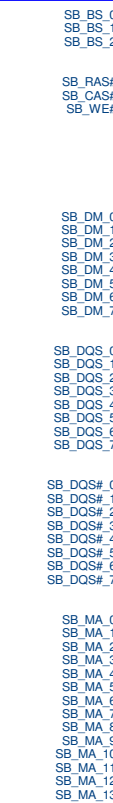
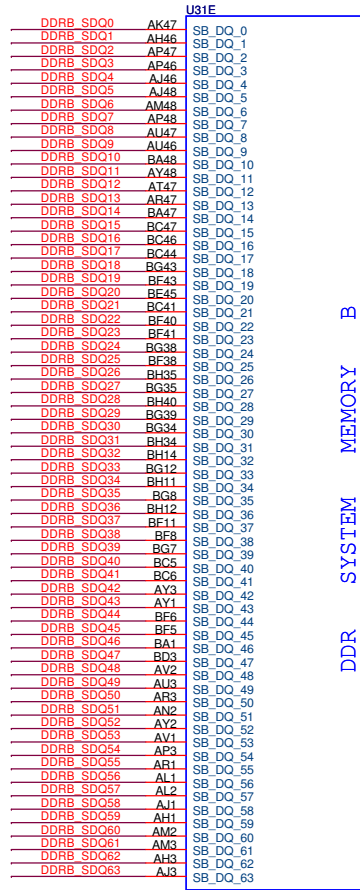
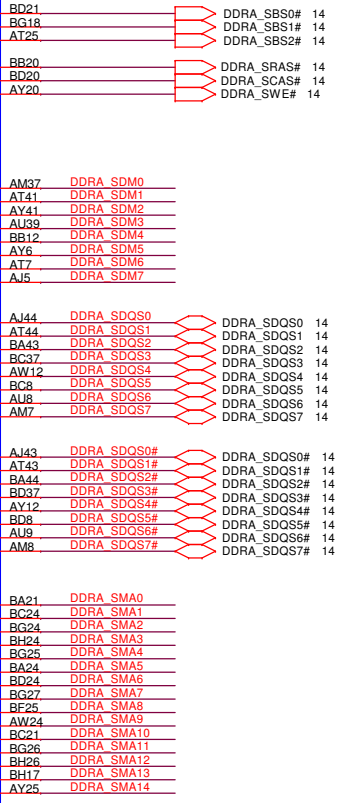
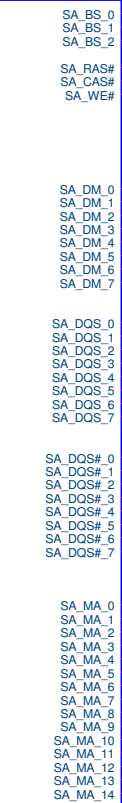
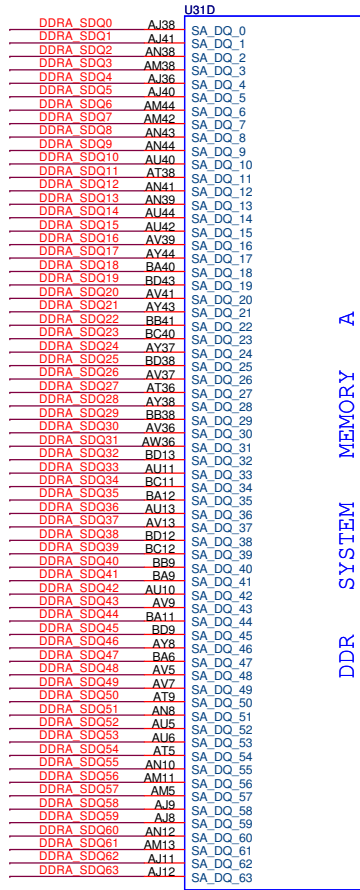
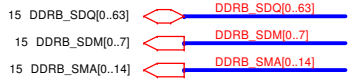
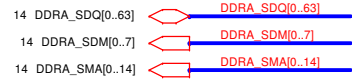












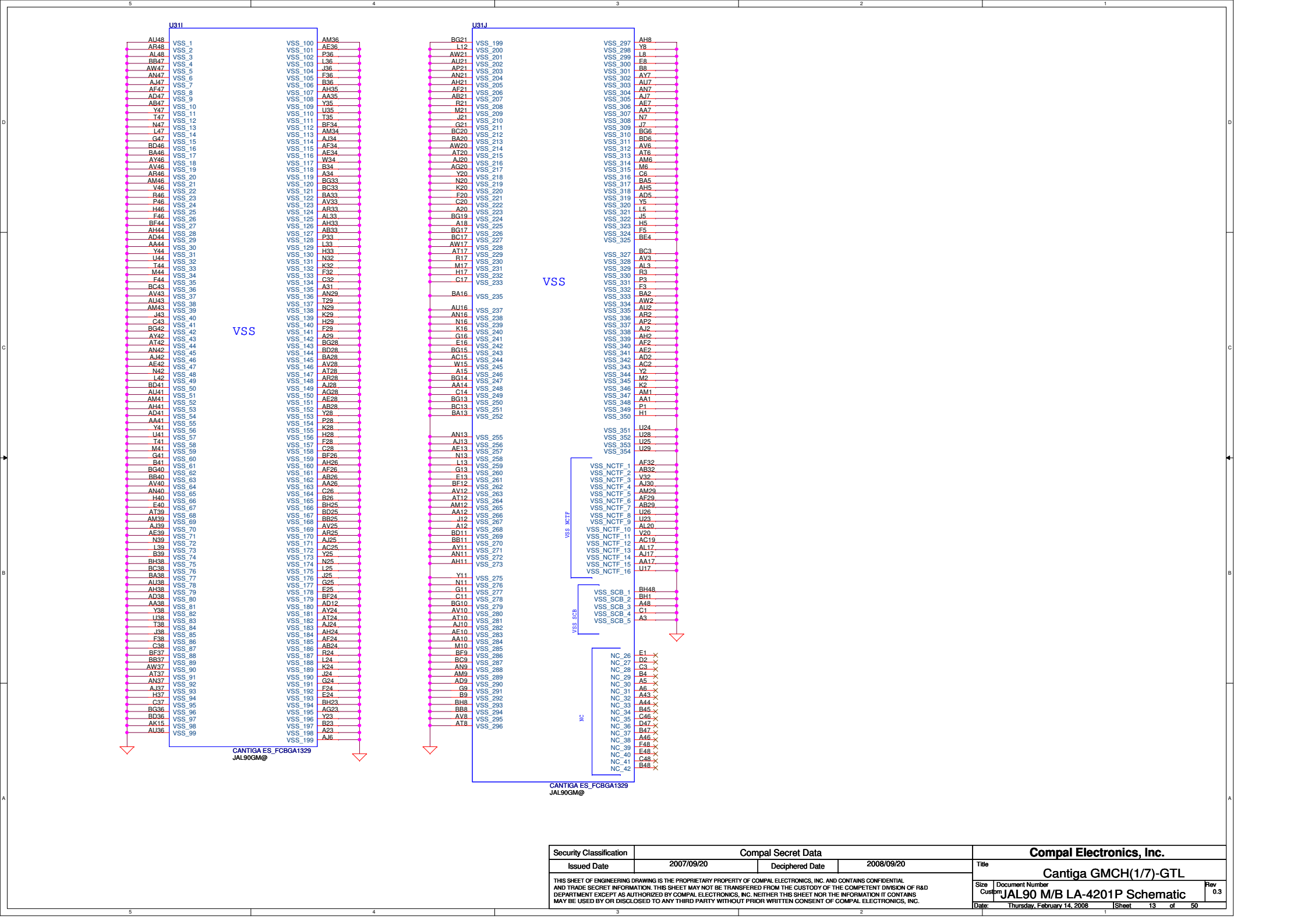
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JAL90GM@

CANTIGA ES\_FCBGA1329  
JAL90GM@

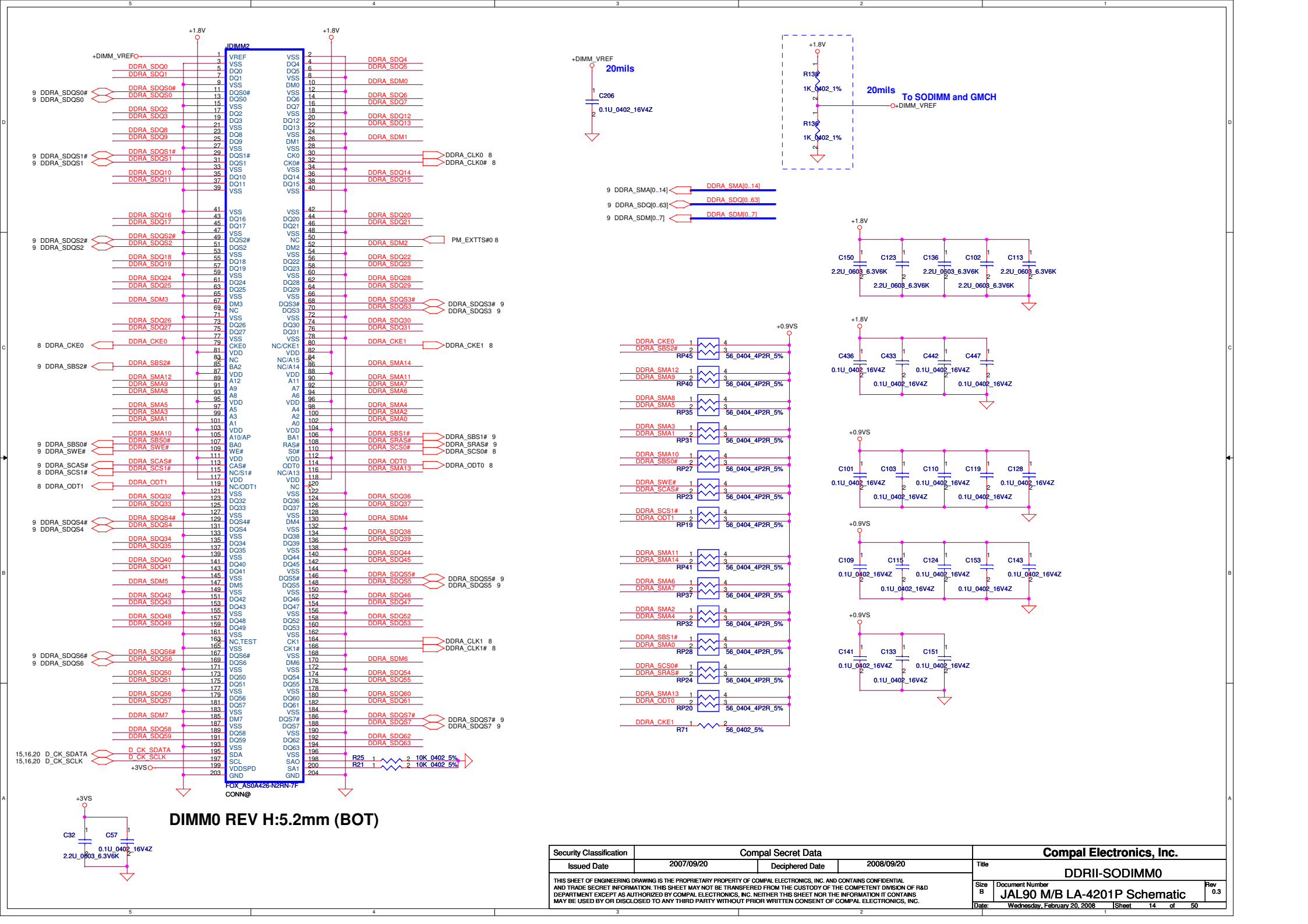


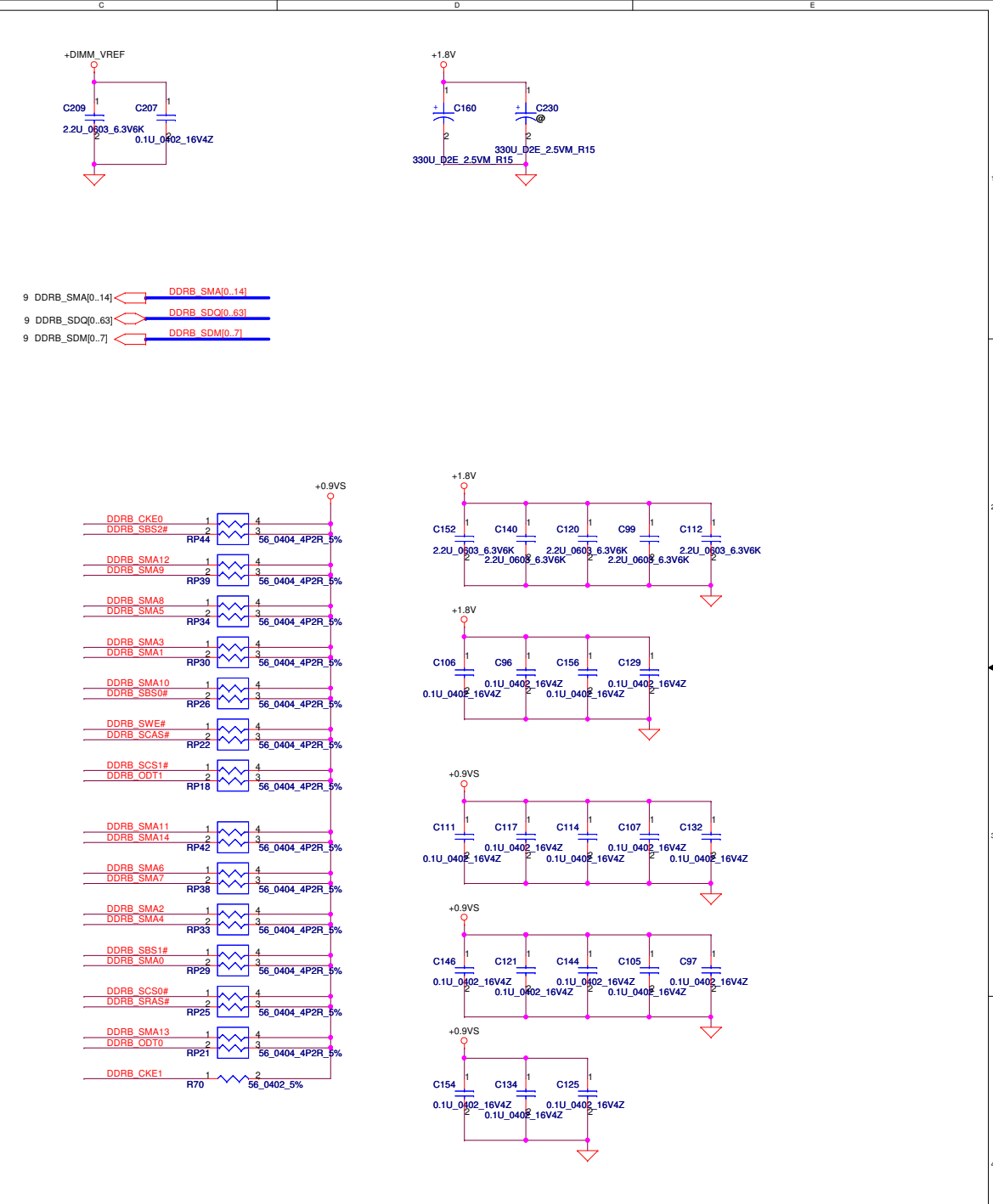








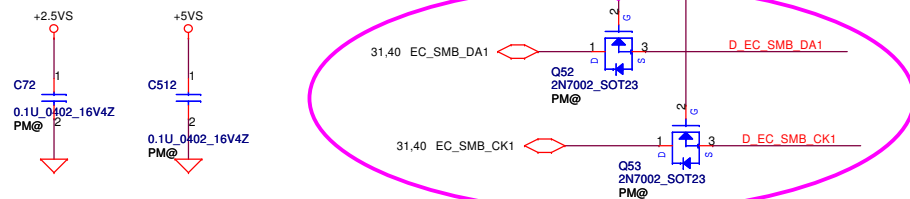
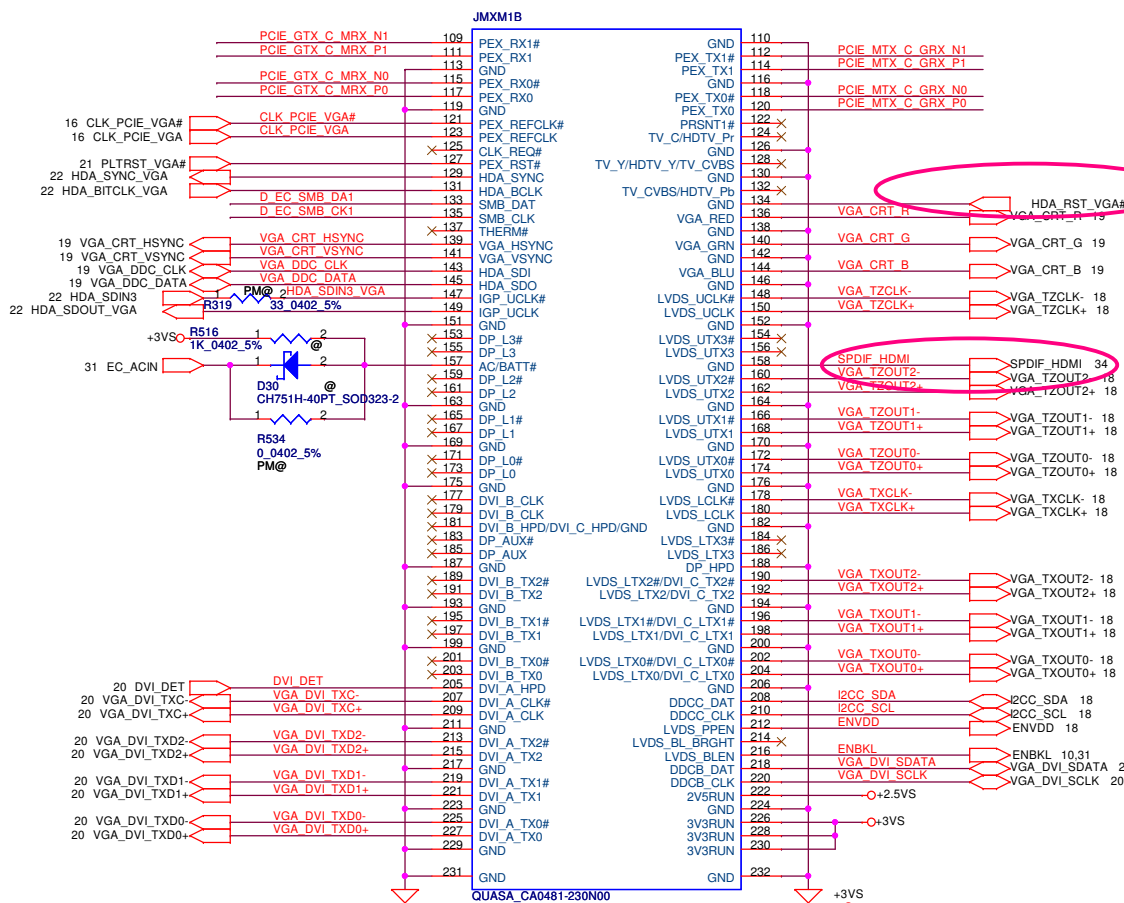
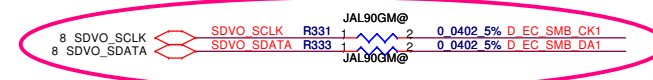




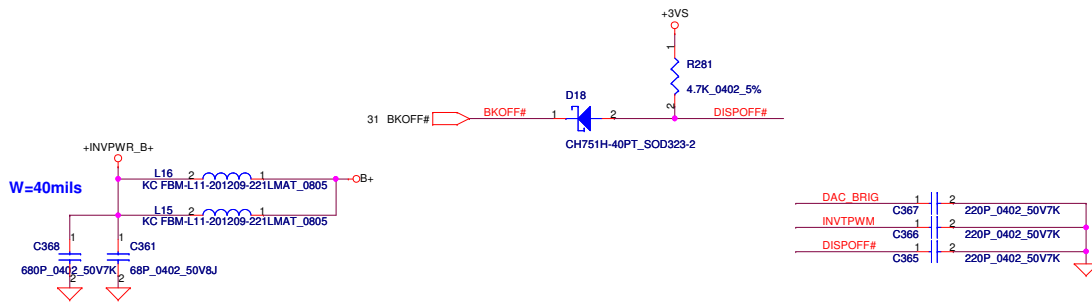
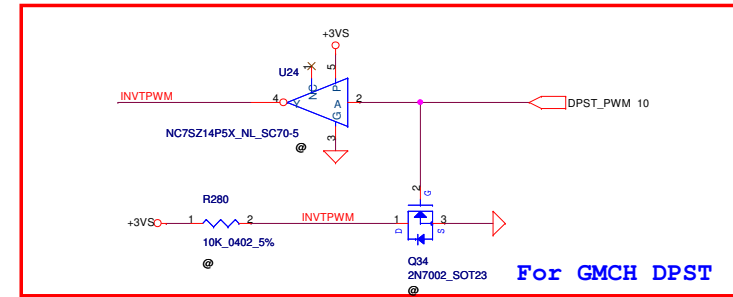
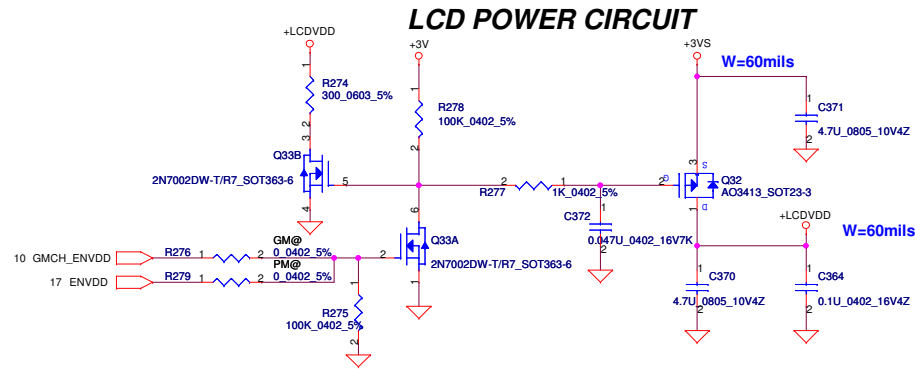
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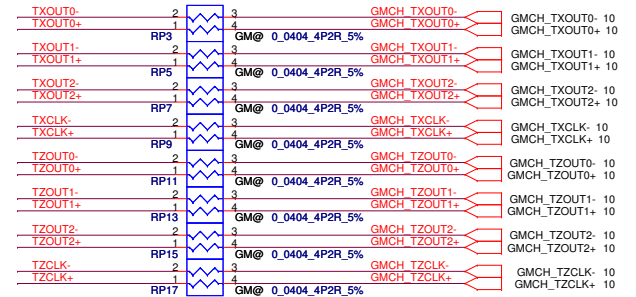
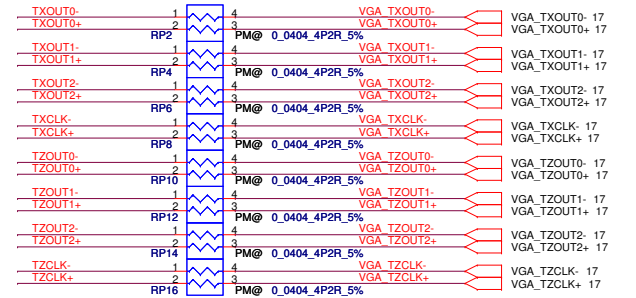
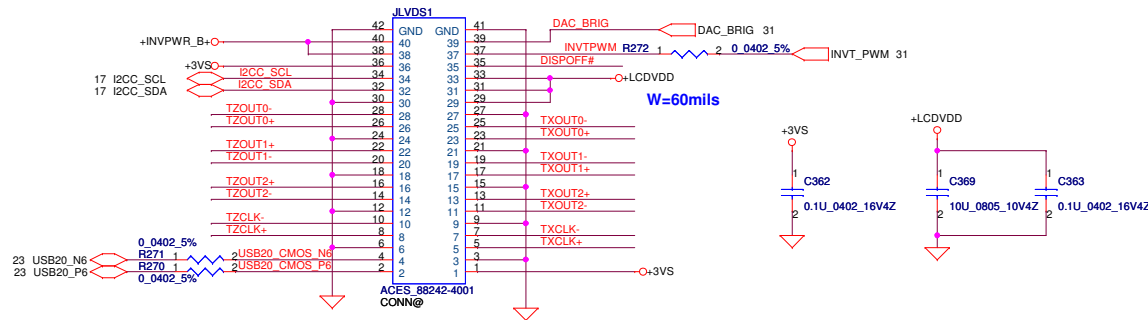




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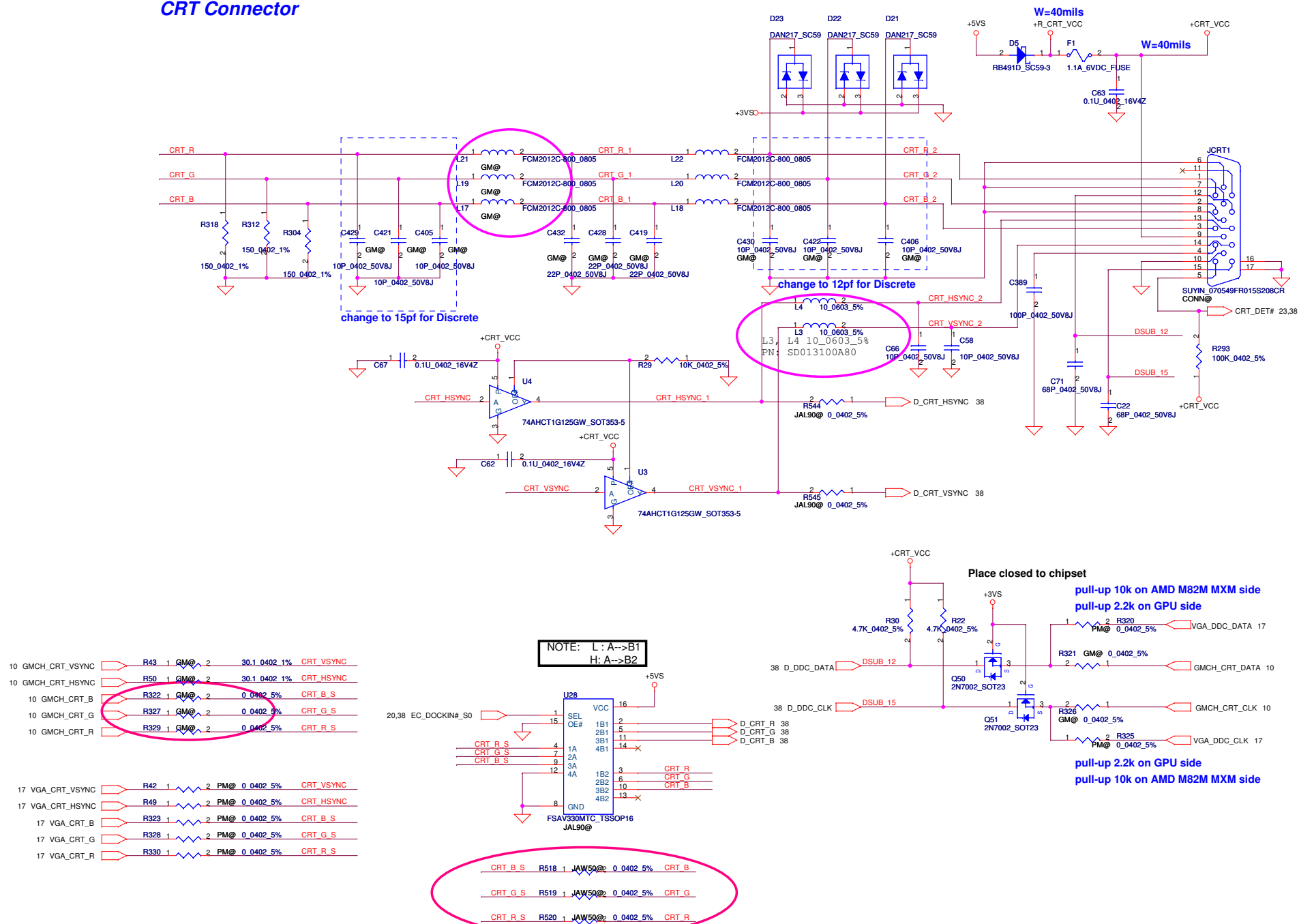


LCD/PANEL BD. Conn.

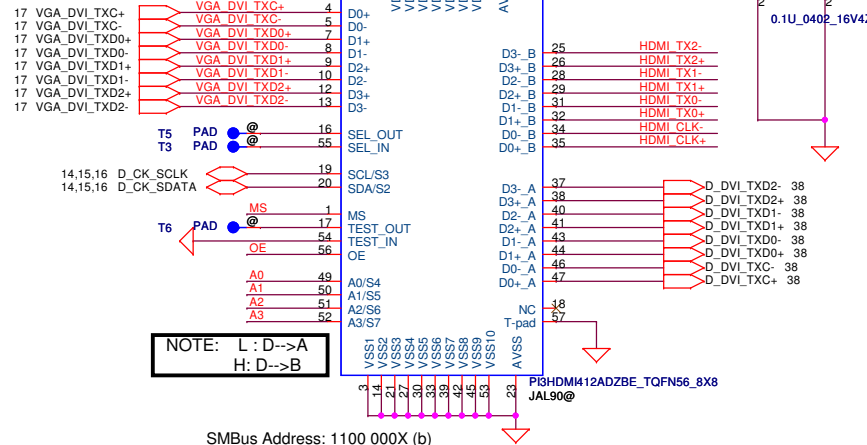
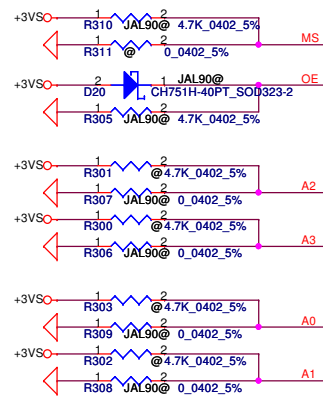
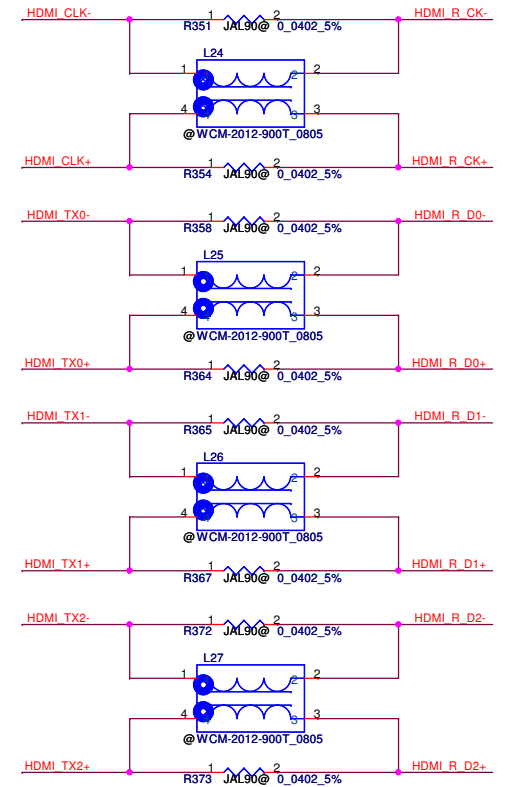
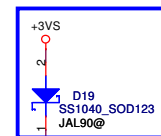
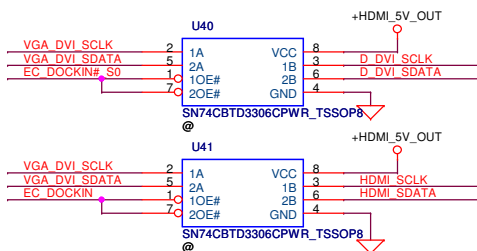
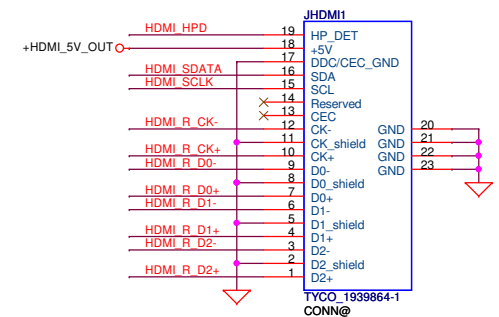
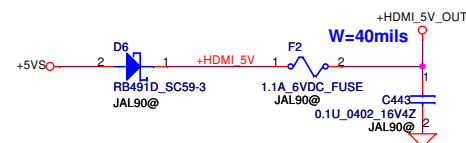
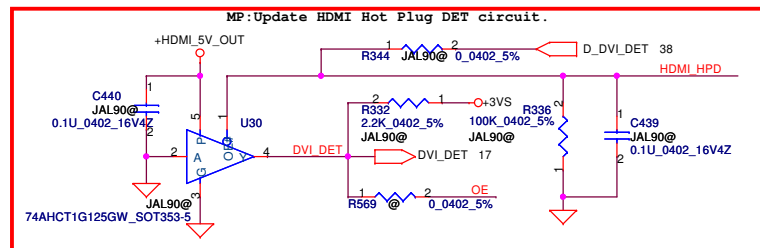
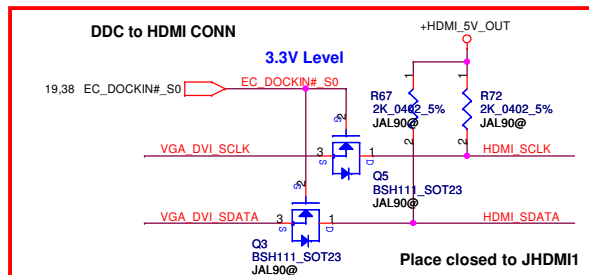
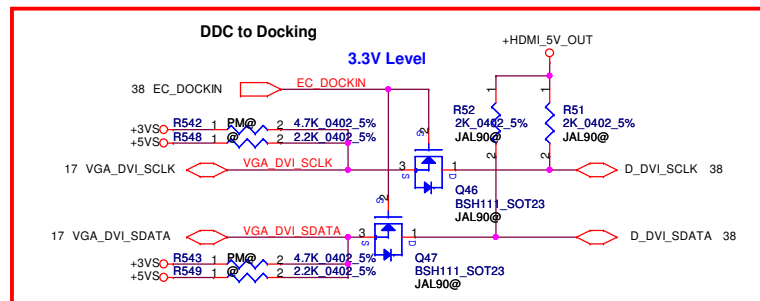


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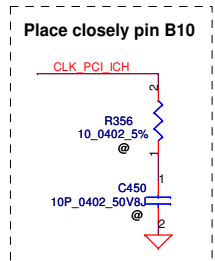
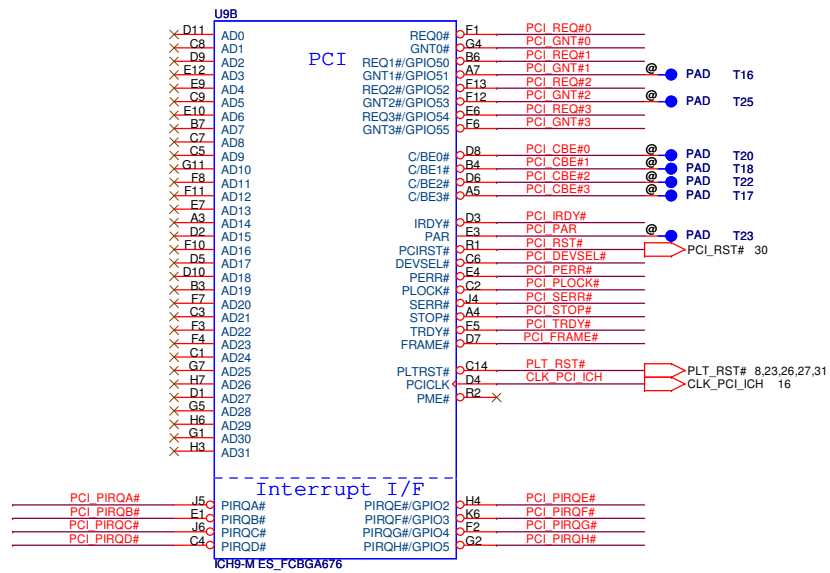
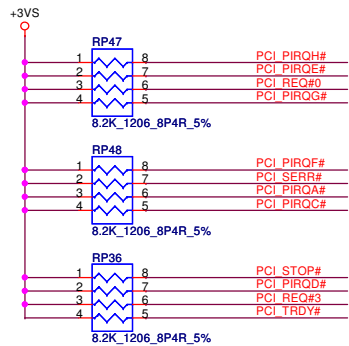
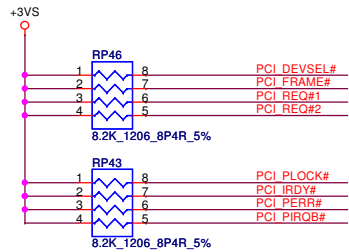
## CRT Connector



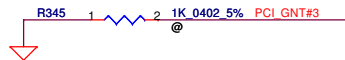
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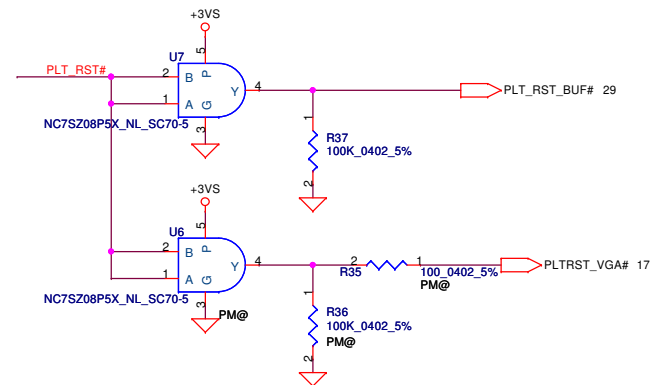
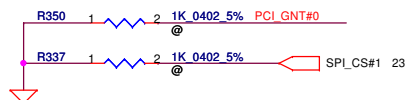
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A16 Swap Override Strap	
PCI_GNT#3	Low= A16 swap override Enable High= Default*



Boot BIOS Strap		
PCI_GNT#0	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC*



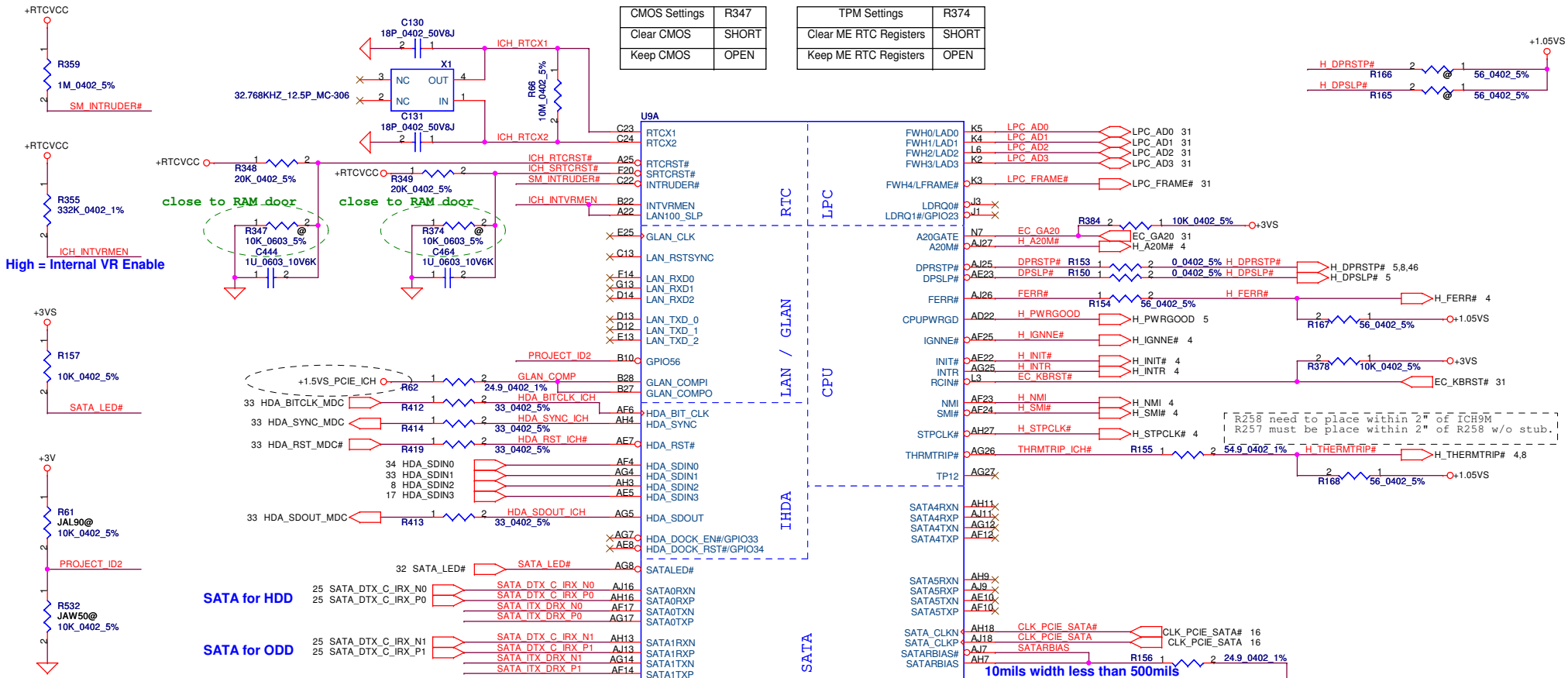
For VGA/B

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CMOS Settings	R347
Clear CMOS	SHORT
Keep CMOS	OPEN

TPM Settings	R374
Clear ME RTC Registers	SHORT
Keep ME RTC Registers	OPEN



HDA for AUDIO

HDA for GMCH

HDA for VGA

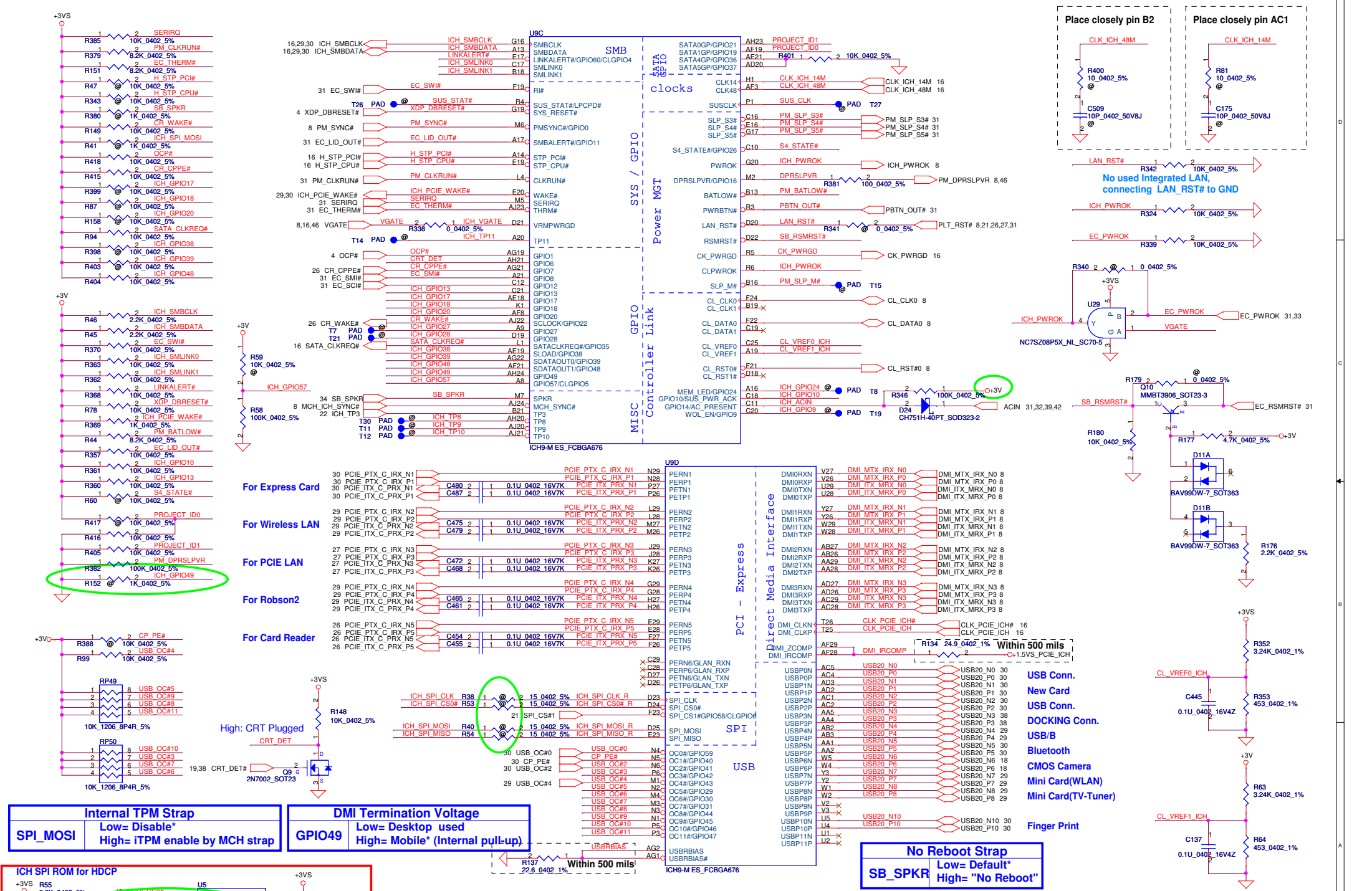
XOR Chain Entrance Strap		
ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation
1	1	Set PCIe port config bit 1

**Flash Descriptor Security Override Strap**  
GPIO33 Low= Descriptor Security override  
High= Default\* (Internal pull-up)

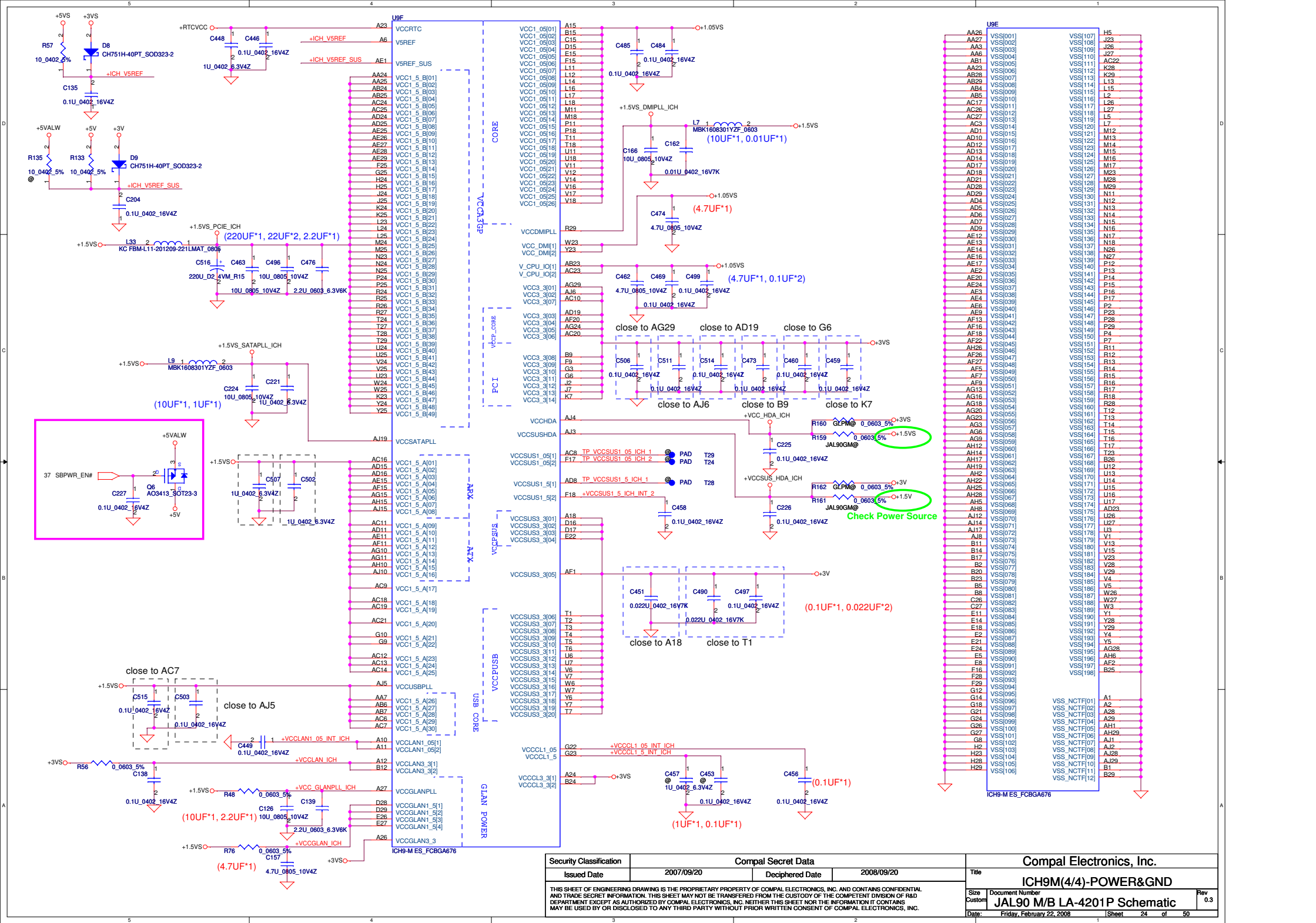
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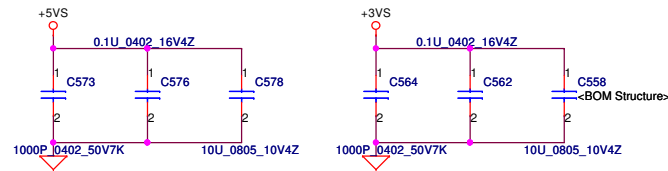
Compal Electronics, Inc.			
Title			
ICH9M(2/4)-LAN, IDELPC, RTC			
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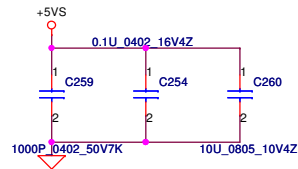
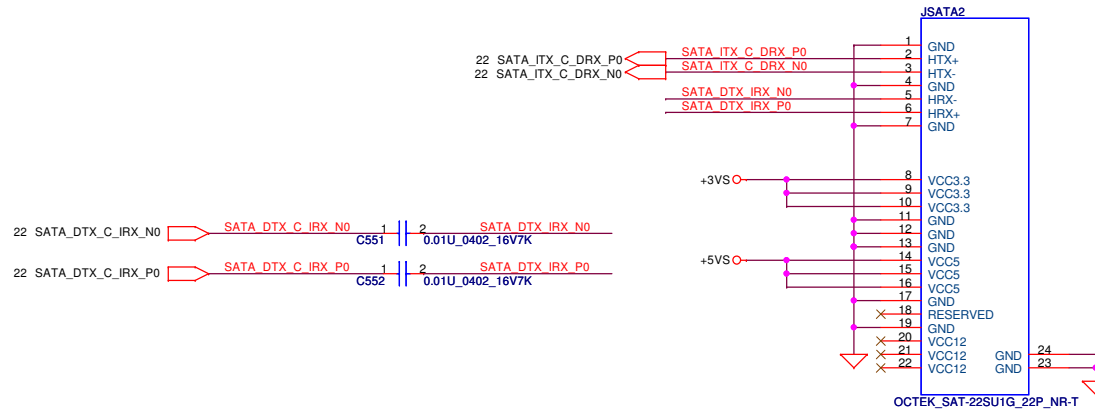


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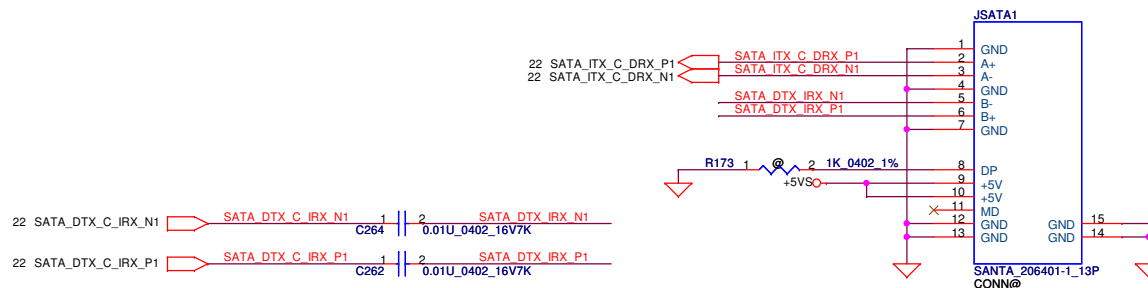




## SATA HDD Conn.



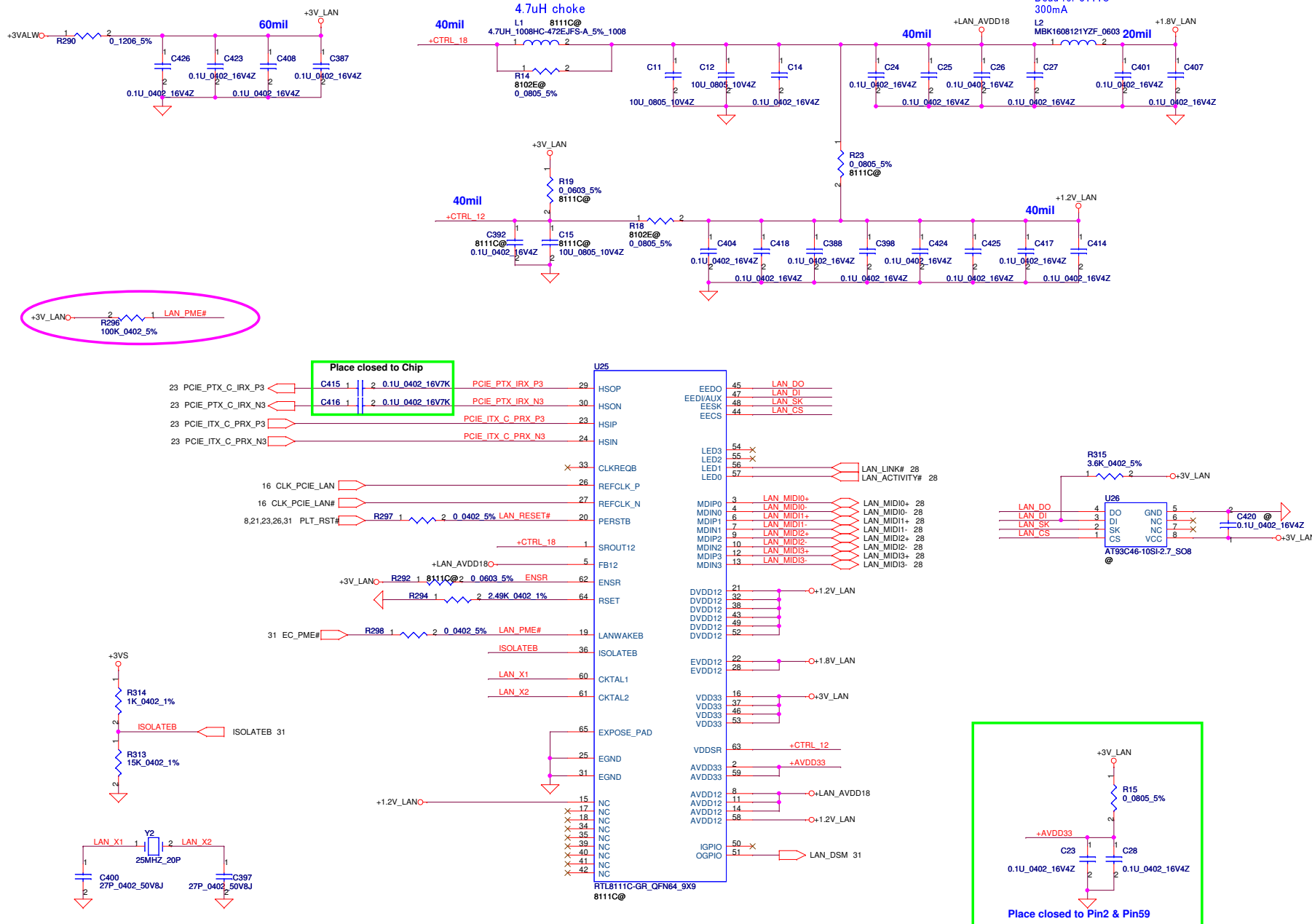
## SATA ODD Conn.



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Size	Document Number	JAL90 M/B LA-4201P Schematic		Rev	0.3
Customer	Date	Wednesday, February 20, 2008	Sheet	25	of 50

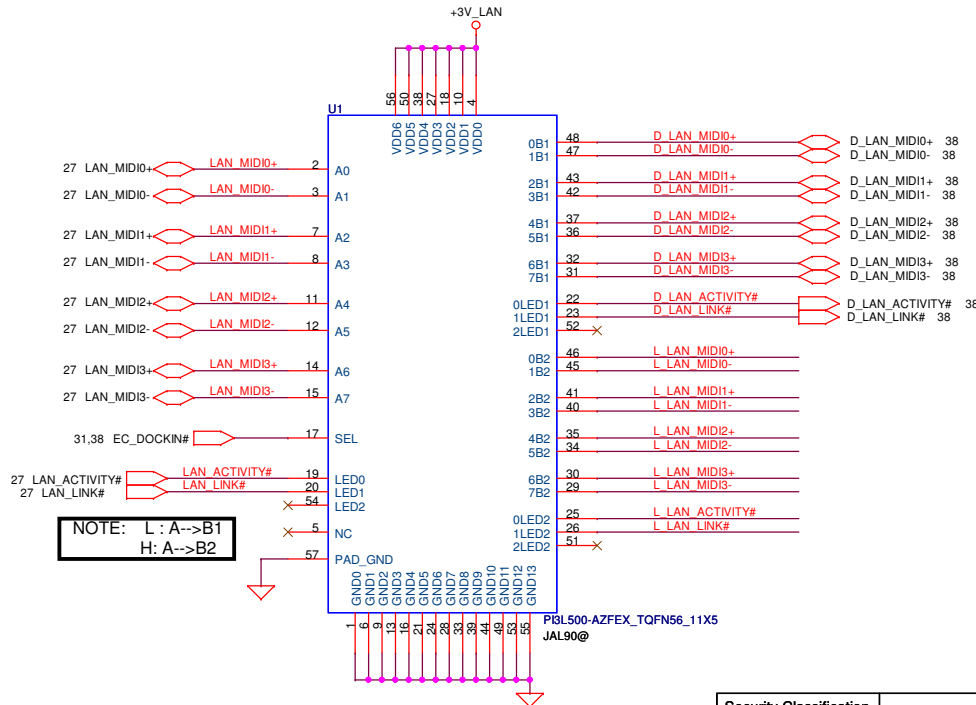
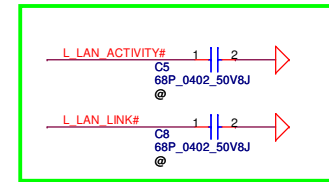
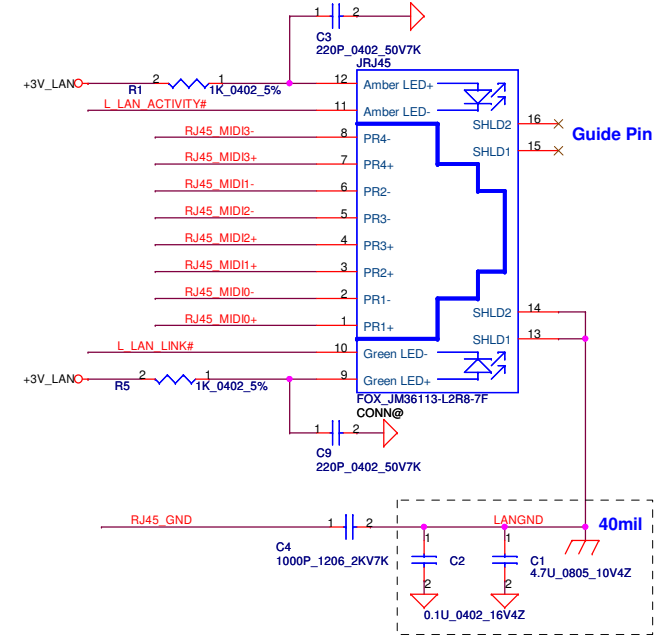
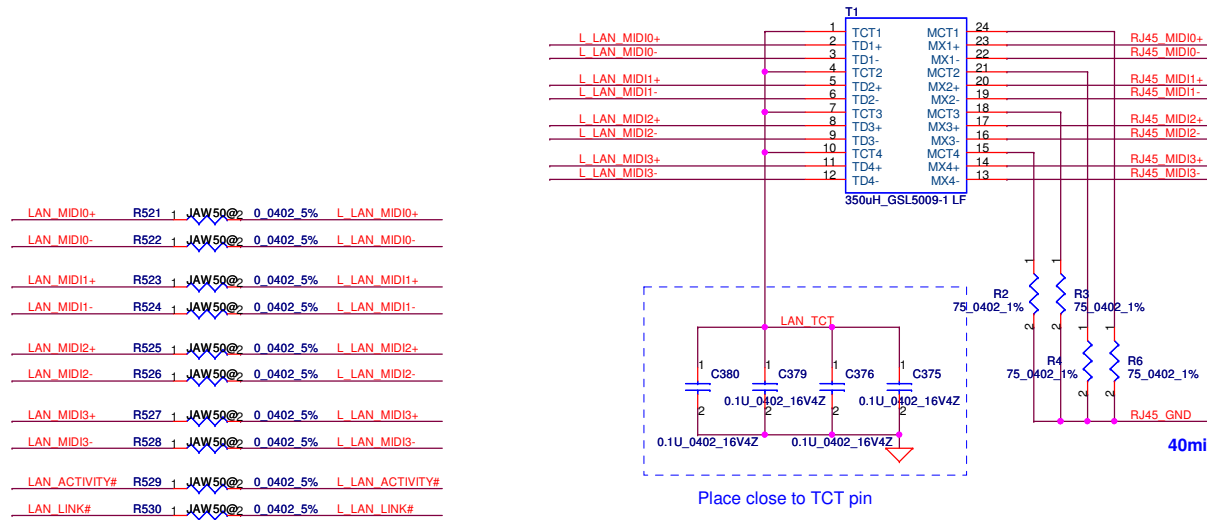


# LAN RTL8111C/8102E



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# LAN RTL8111C/8102E





The diagram illustrates the JMINI1 module's internal connections and external interfaces. Key components and connections include:

- Power Inputs:** Two sections at the top, each receiving +1.5V and +3V3 inputs. Each section contains a JAL90 pin, a 0.1U\_0402\_16V4Z capacitor, and a 0.1U\_0402\_16V4Z capacitor.
- JMINI1 Connector:** A central connector with pins 1 through 51. Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51 are shown.
- Signal Connections:**
  - CLK\_PCIE\_MINI1# (pin 1)
  - CLK\_PCIE\_MINI1 (pin 2)
  - CLK\_PCIE\_IRX\_N4 (pin 3)
  - CLK\_PCIE\_IRX\_P4 (pin 4)
  - CLK\_PCIE\_ITX\_N4 (pin 5)
  - CLK\_PCIE\_ITX\_P4 (pin 6)
  - PLT\_RST\_BUF# (pin 18)
  - USB20\_N7 (pin 23)
  - USB20\_P7 (pin 23)
  - LED\_WWAN# (pin 42)
  - LED\_WLAN# (pin 43)
- Debug Section:** A dashed blue box labeled 'debug' containing a JAL90 pin, a 0.1U\_0402\_16V4Z capacitor, and a 0.1U\_0402\_16V4Z capacitor.
- FOX\_AS0B226-S99N-7F Connector:** A connector at the bottom right with pins 1 through 51.

Mini Card Power Rating			
Power	Primary Power (mA)		Auxiliary Power (mA)
	Peak	Normal	Normal
+3VS	1000	750	
+3V	330	250	250 (wake enable)
+1.5VS	500	375	5 (Not wake enable)

The schematic diagram illustrates the MINICARD Port80 Debug interface. It shows the connection between a MINICARD (JMINI2) and a debug board. Key components include resistors R487, R478, R251, R246, R237, R242, R230, and R531; capacitors C569, C347, C311, C318, C335, C345, and C569; and a 1.5V regulator. The MINICARD pins are connected to various signals: ICH\_PCIE\_WAKE#, WLAN\_BT\_DATA, WLAN\_BT\_CLK, MINI2\_CLKREQ#, CLK\_PCIE\_MINI2#, PCIE\_PTX\_C\_IRX\_N2, PCIE\_PTX\_C\_IRX\_P2, PCIE\_ITX\_C\_PRX\_N2, PCIE\_ITX\_C\_PRX\_P2, USB20\_N8, USB20\_P8, and MINI1\_LED#. The debug board also includes a 3V3\_WLAN regulator and a 3V3\_WLAN signal. A note indicates the current for MINI1\_LED# is 9~16mA.

**80mil**

JP11

1 2 3 4 5 6 7 8 9 10

1 → +5VALW

2 → SYSON# 30,37,38

3 → USB20\_N4 23

4 → USB20\_P4 23

5 → USB\_OC#4 23

6 → GND

7 → GND

8 → GND

9 → GND

10 → GND

ACES\_87212-08G0L

+5VALW

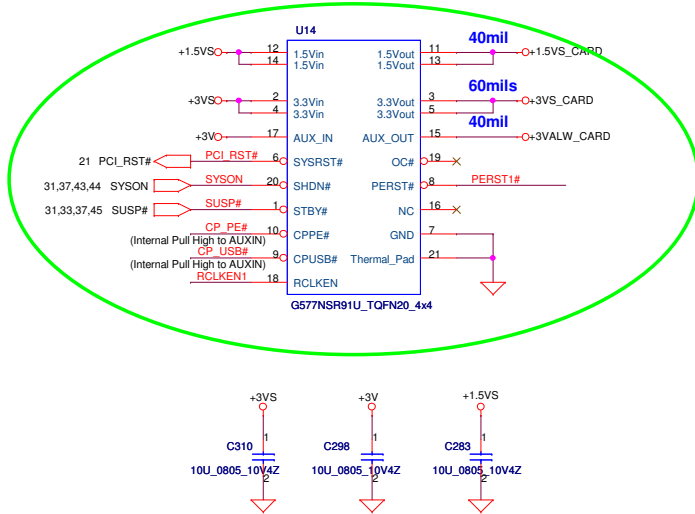
C6

4.7U\_080 10V4Z

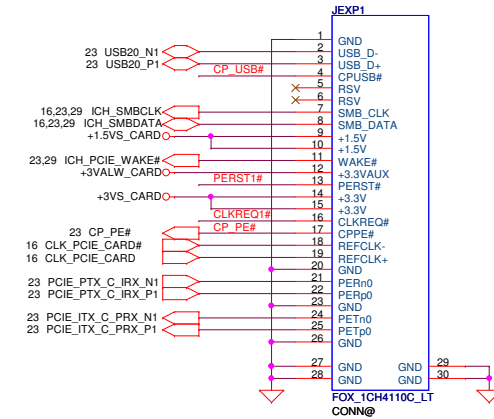
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title MINI CARD (WLAN & TV-Tuner)		
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				JAL90 M/B LA-4201P Schematic		
				Date	Wednesday, February 20, 2008	



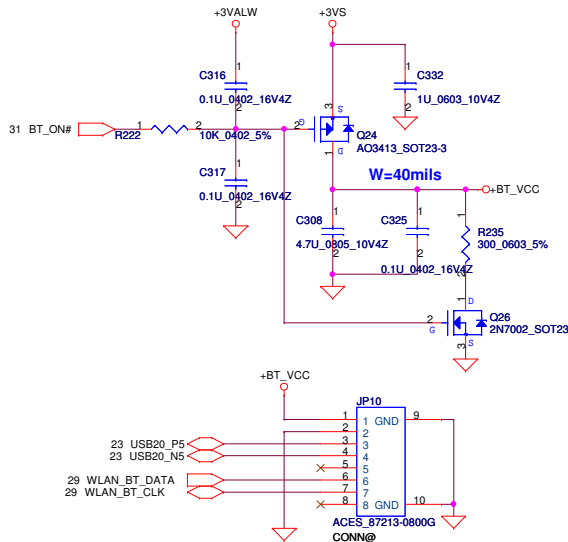
## New Card Power Switch



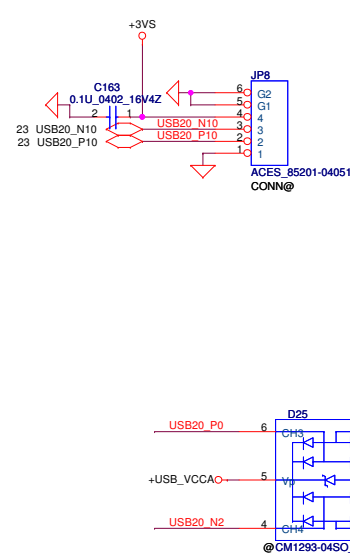
## New Card Socket (Left/TOP)



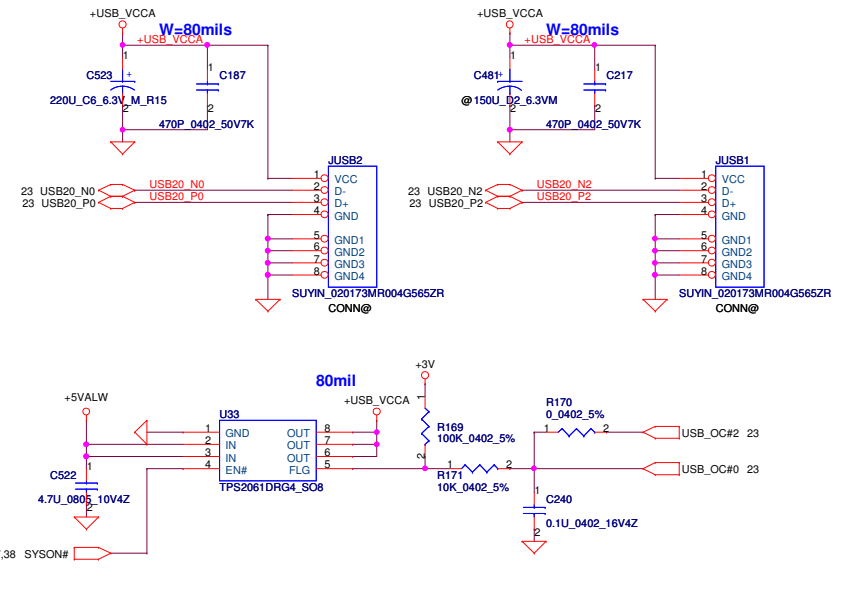
## Bluetooth Conn.



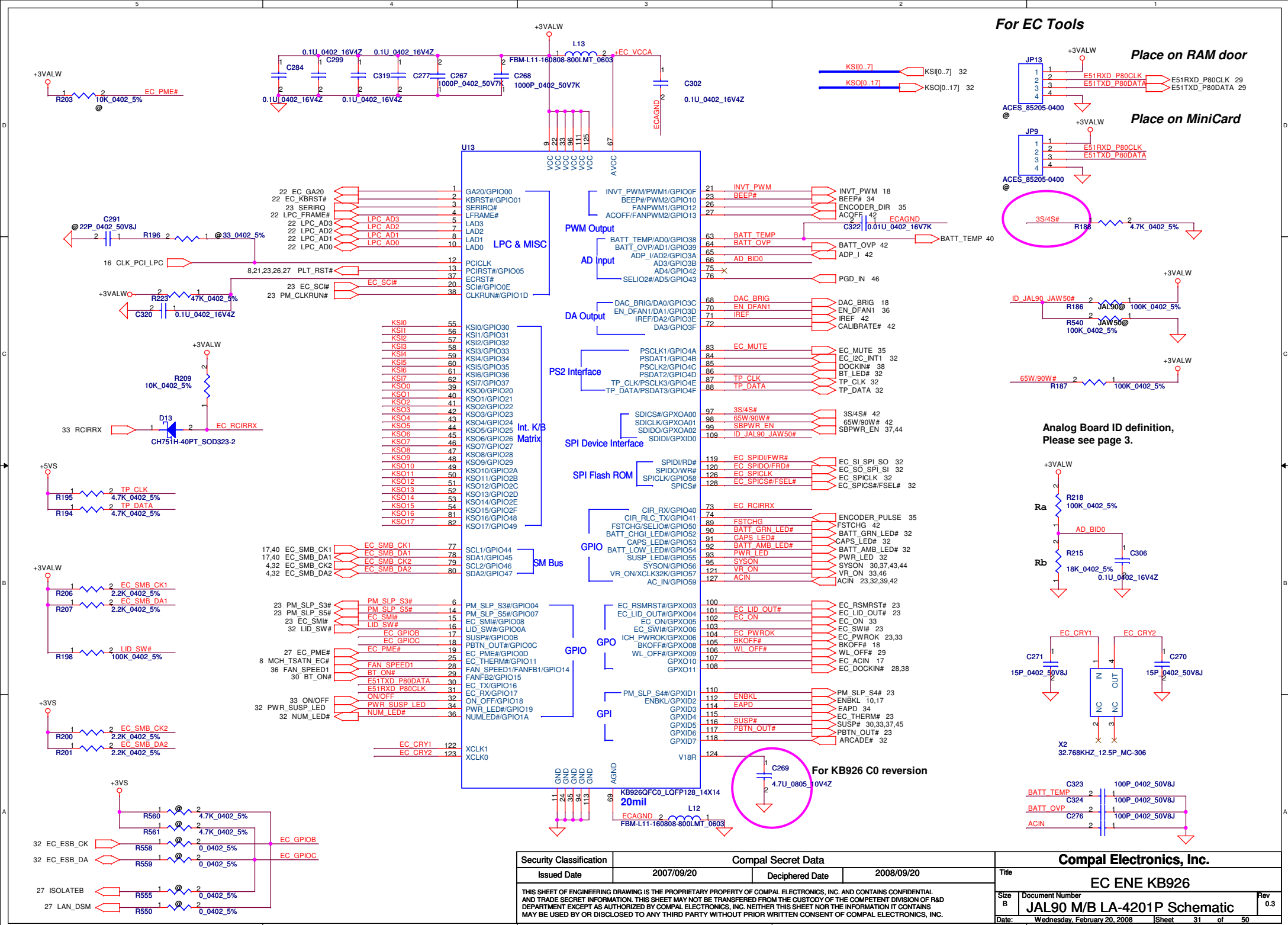
## Finger Print Conn.



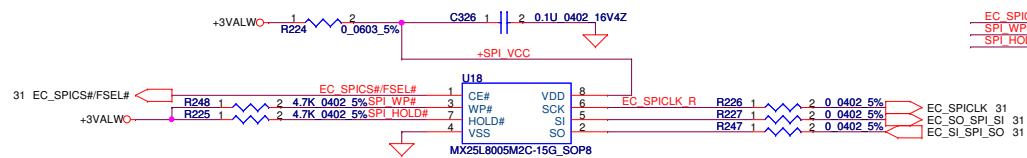
## USB CONN. (Stack-up Type)



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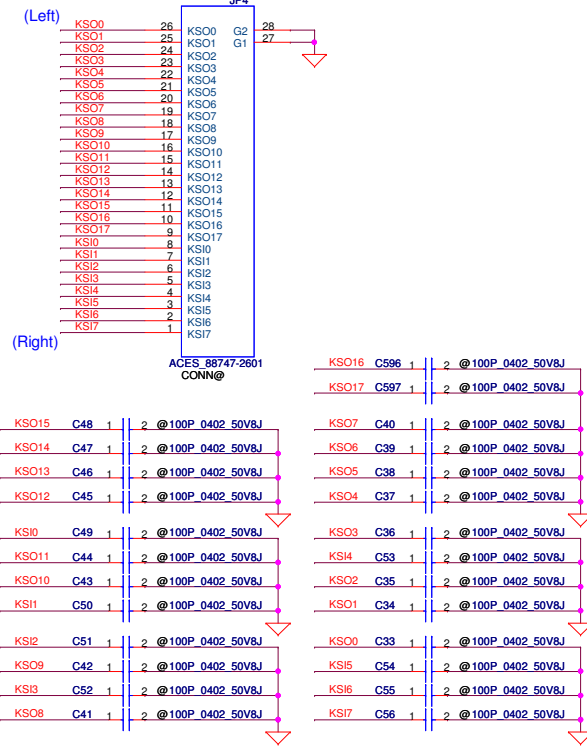


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				B	JAL90 M/B LA-4201P Schematic	0.3
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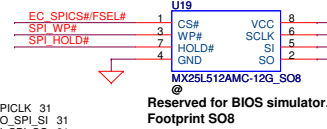
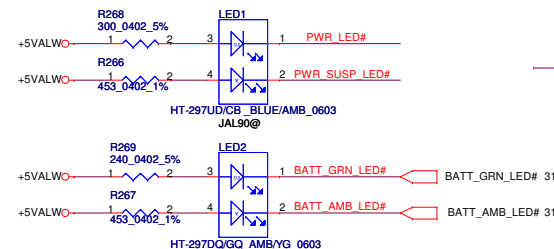


ENE suggestion SPI Frequency over 66MHz  
SST: 50MHz  
MXIC: 70MHz  
ST: 40MHz

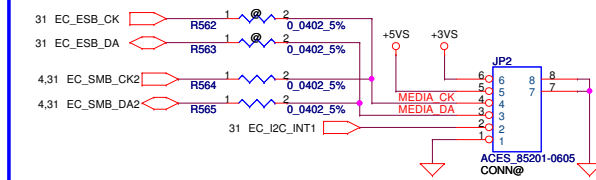
## INT\_KBD Conn.



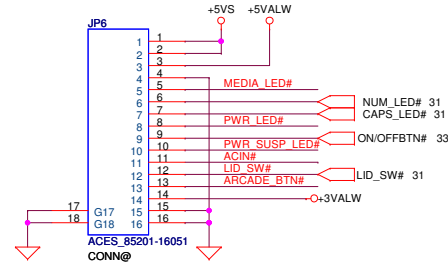
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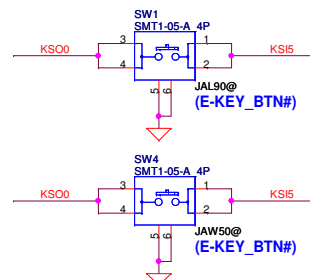
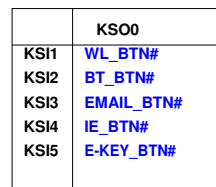
## To Media/B Conn.



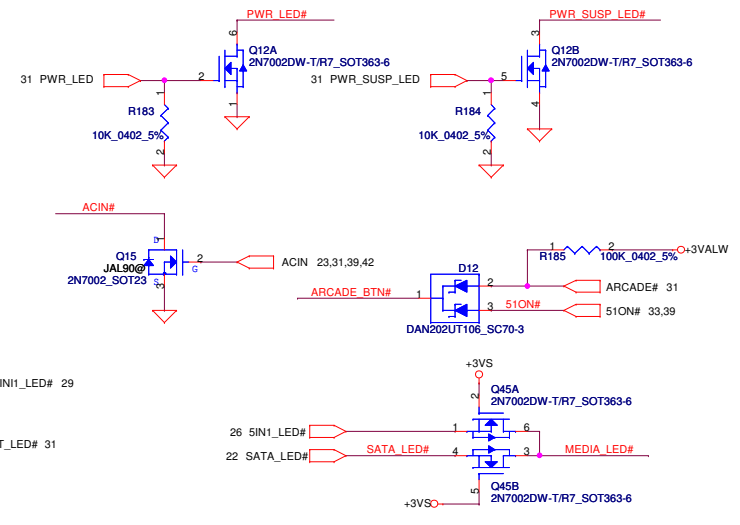
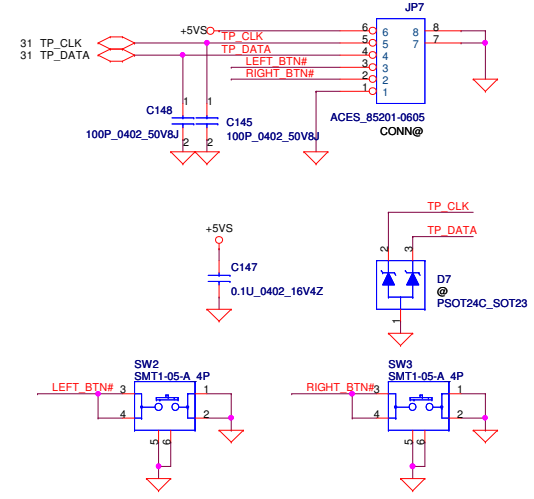
## To LED/B Conn. (POWER/B)



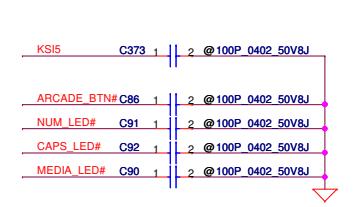
## To BTN/B Conn.



## To TP/B Conn.



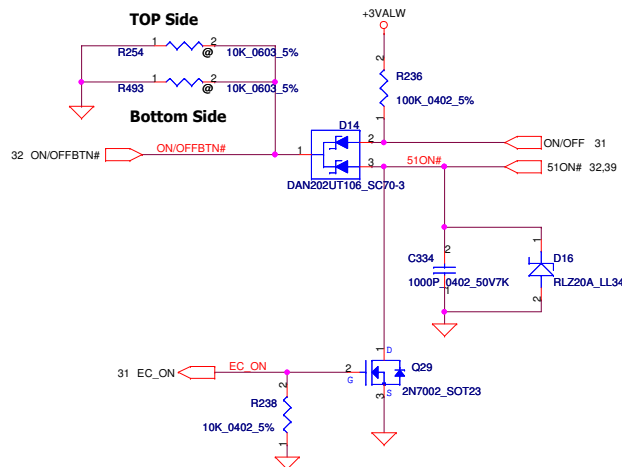
## FOR EMI



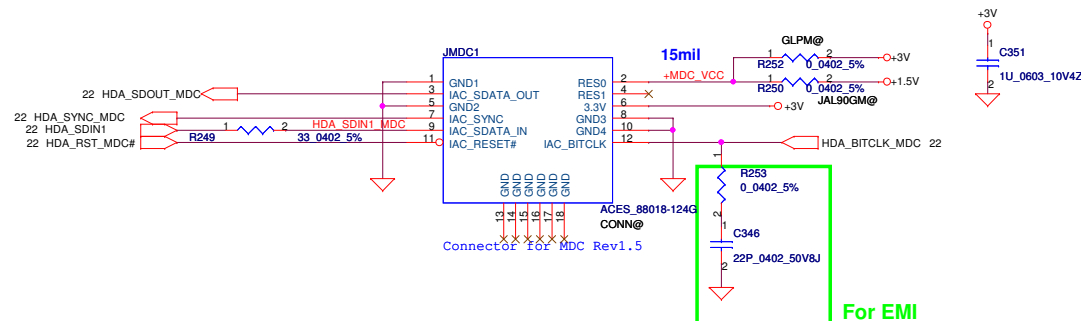
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## Power Button

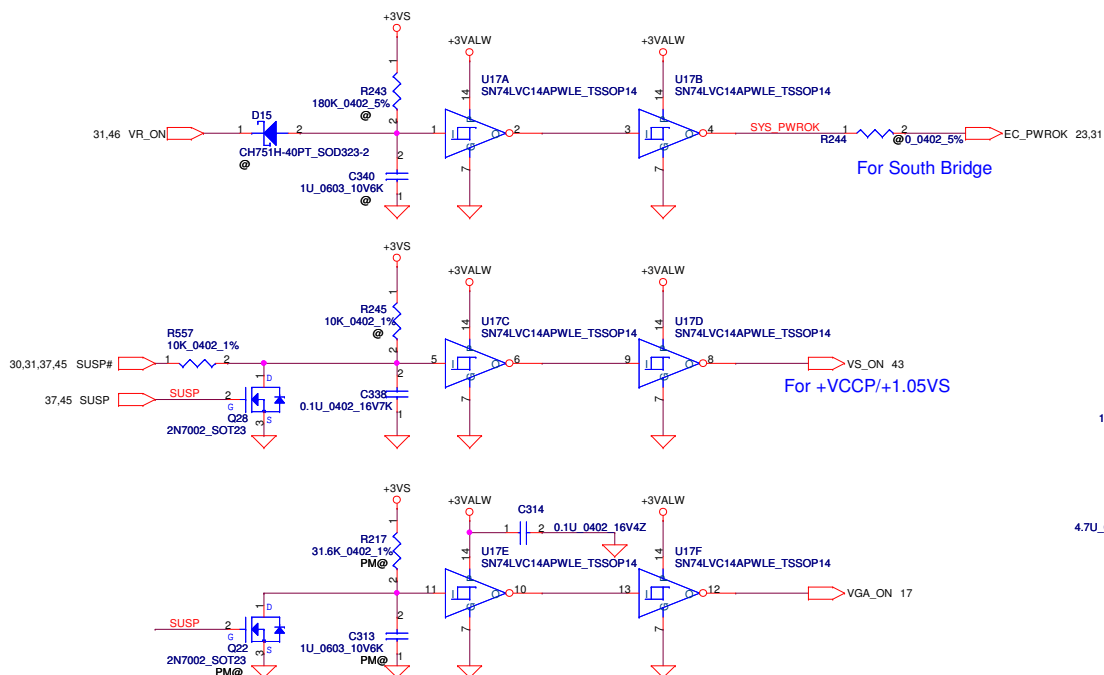
ON/OFF switch



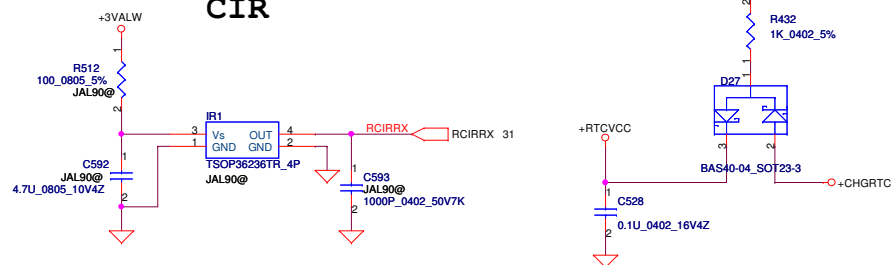
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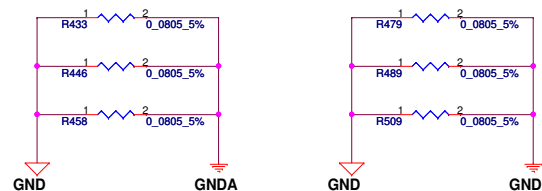
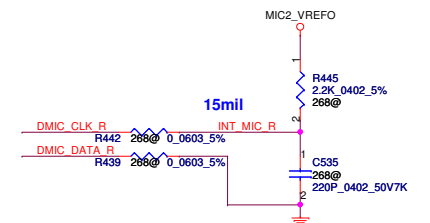
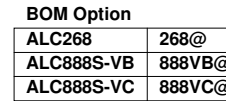
## Power ON Circuit



## CIR



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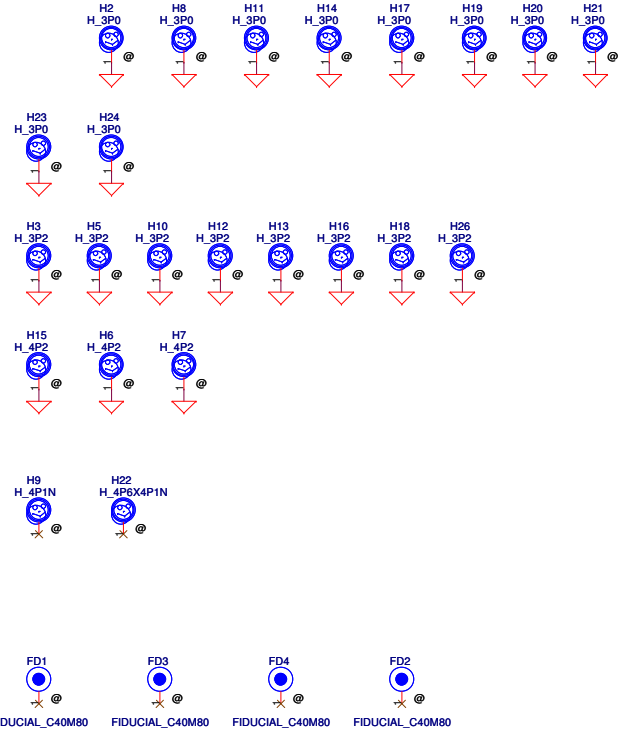
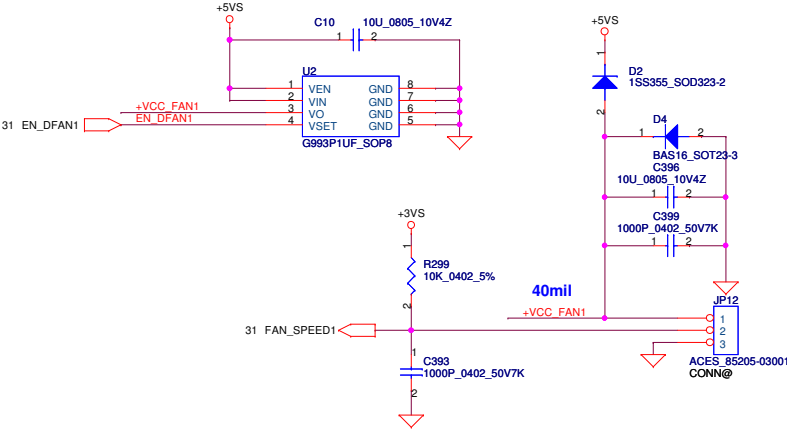


Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)

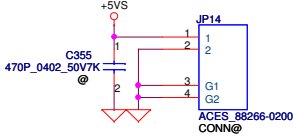
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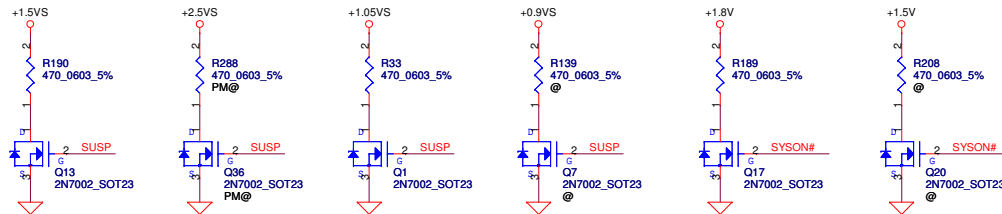
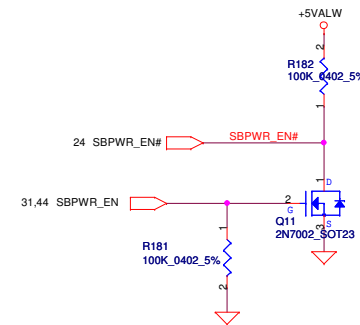
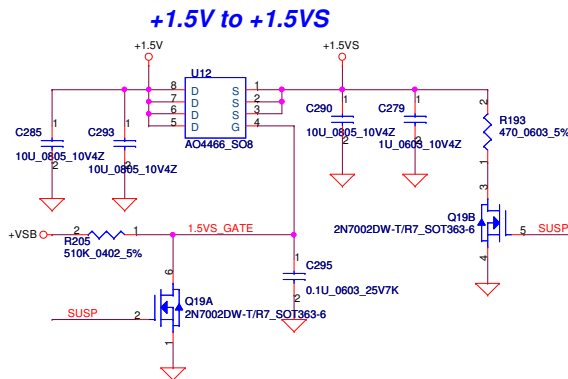
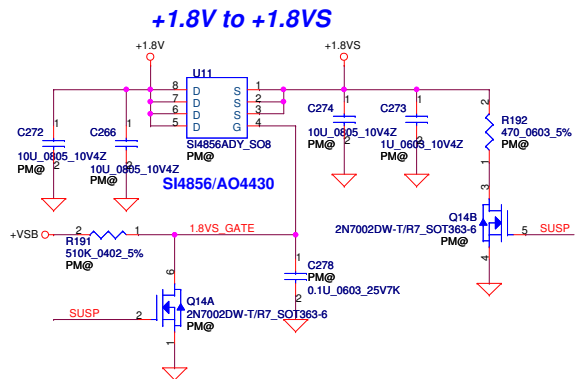
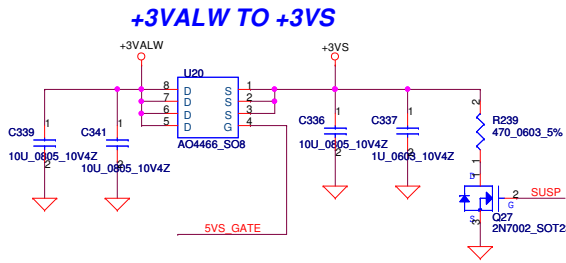
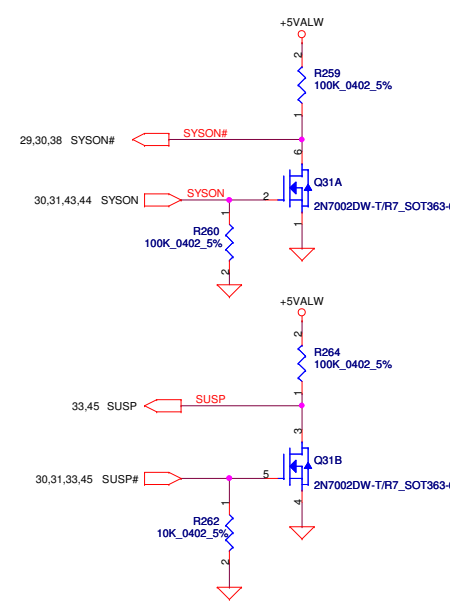
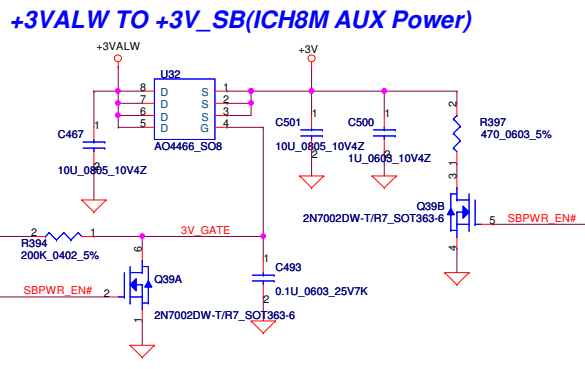
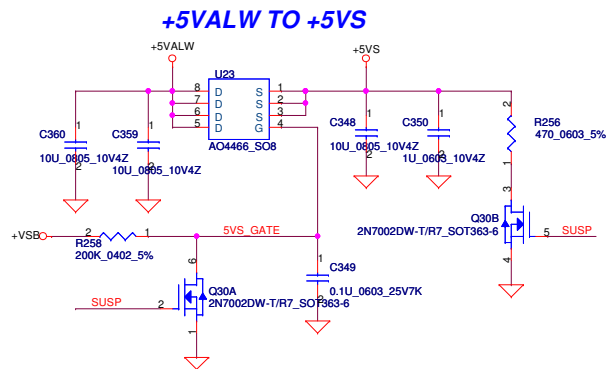
FAN1 Conn



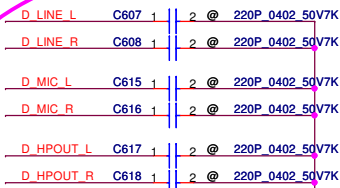
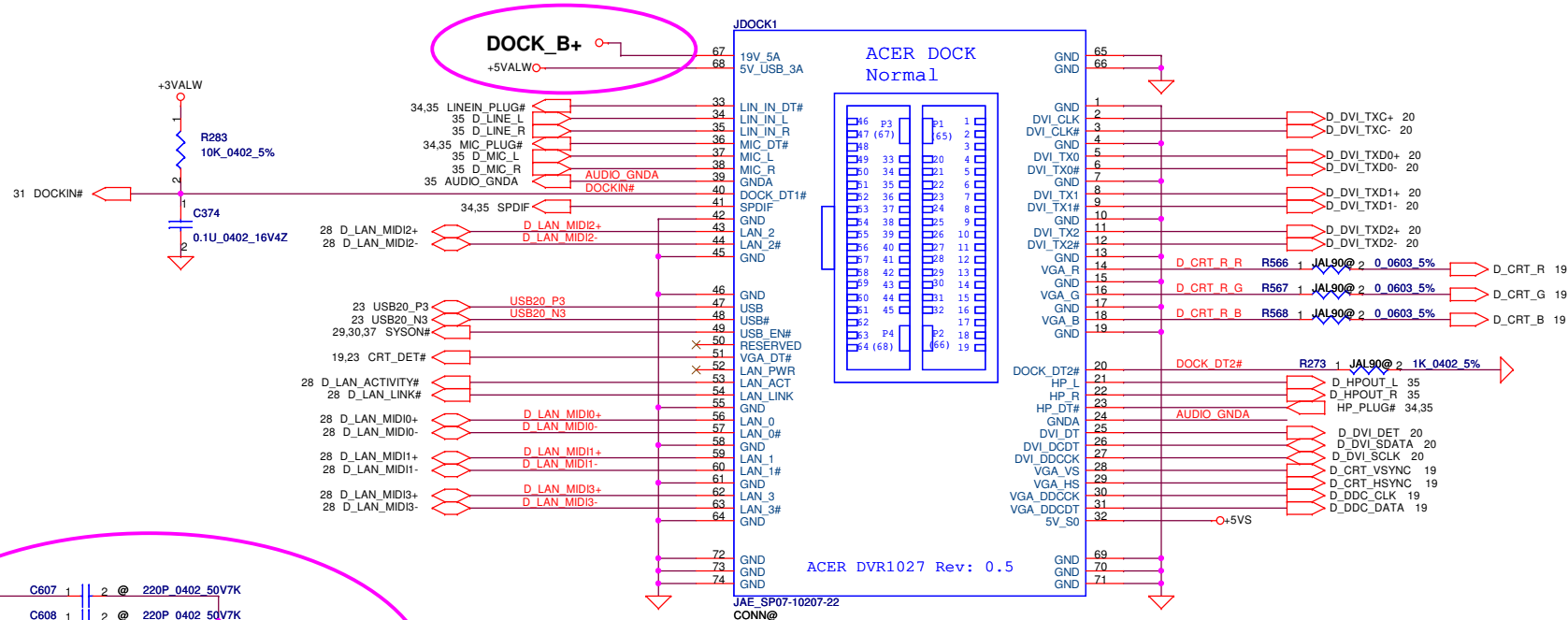
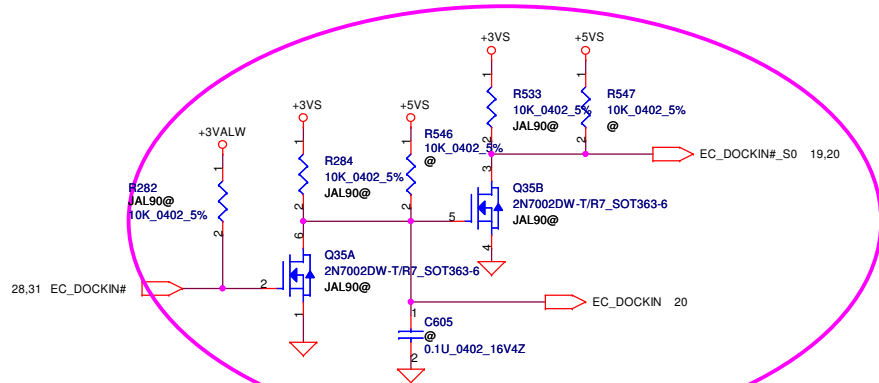
COVER LIGHTConn





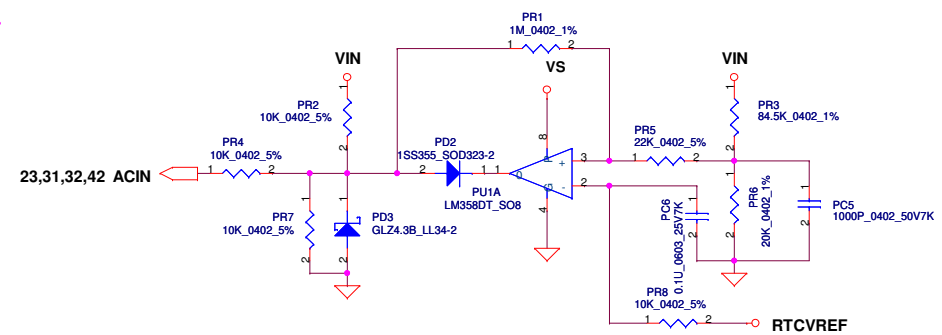
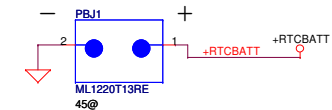
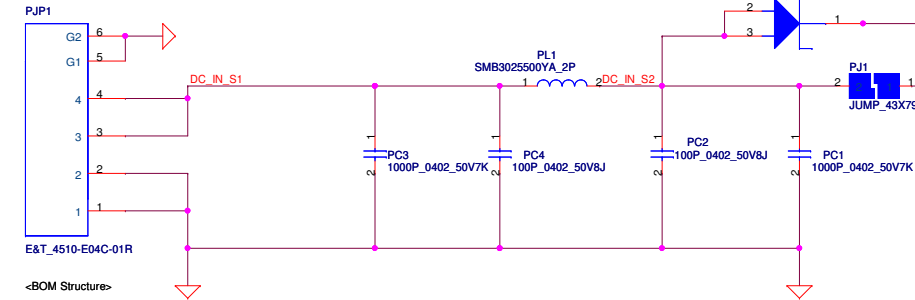


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				JAL90 M/B LA-4201P Schematic	
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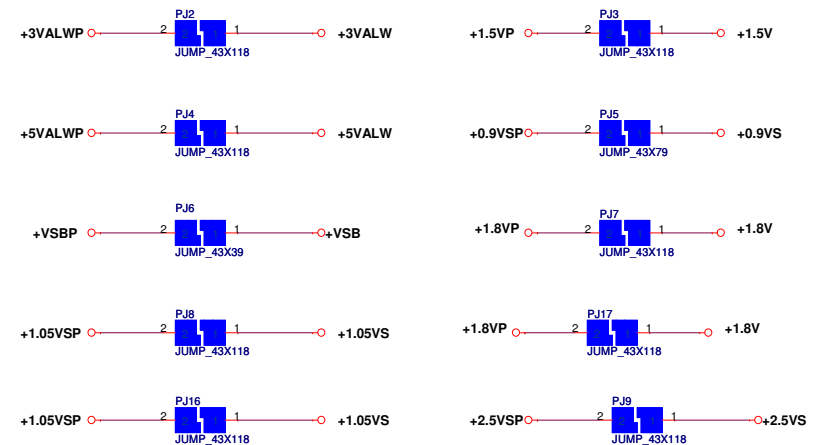
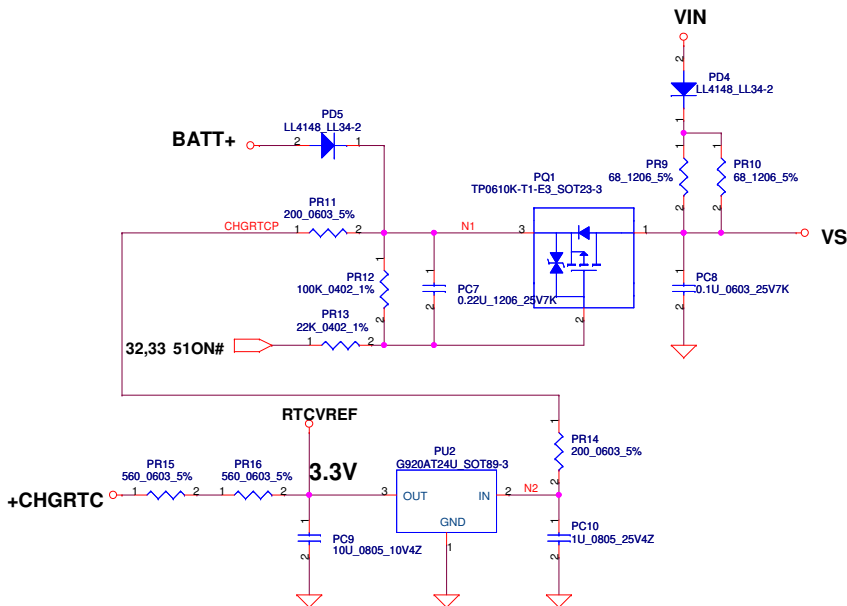


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Size	Document Number	JAL90 M/B LA-4201P Schematic		Rev	
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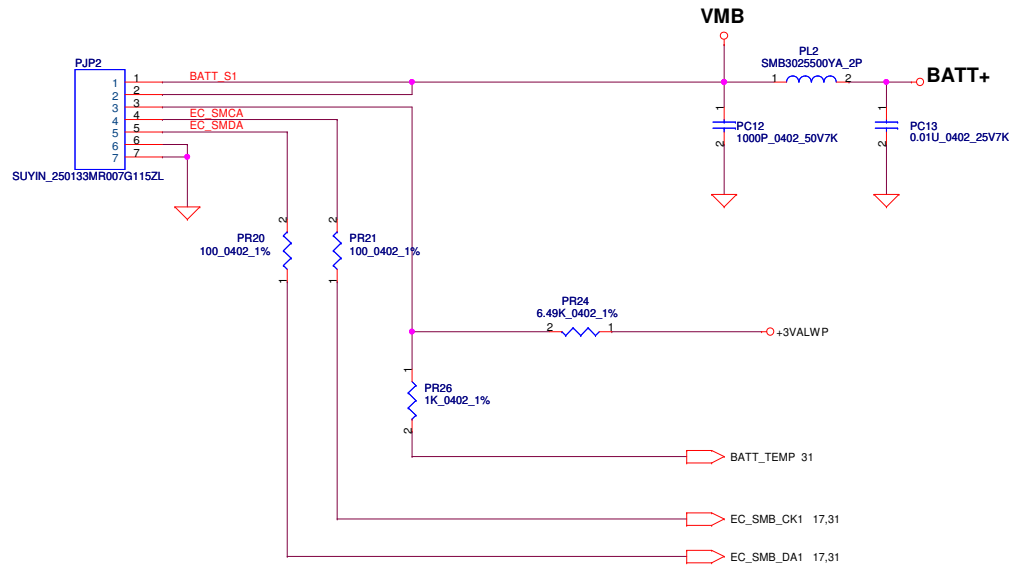
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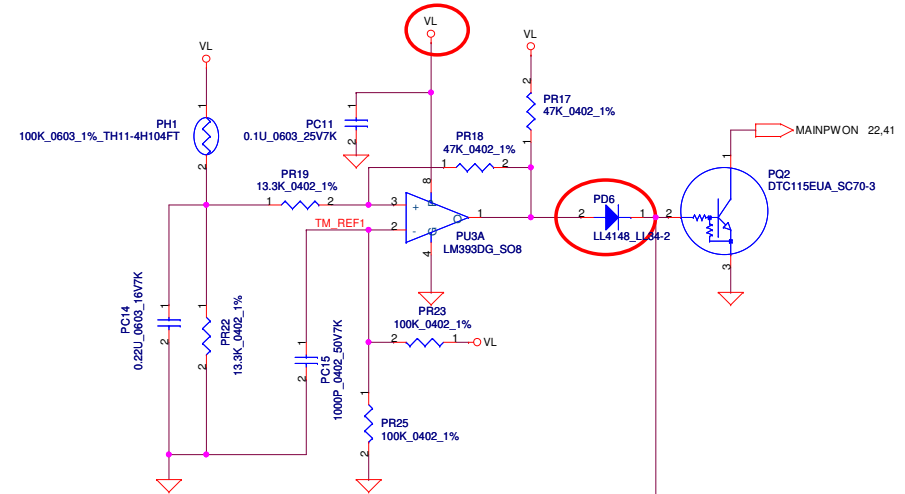
Vin Dectector			
	Min.	Typ	Max.
H-->L	16.976V	17.525V	17.728V
L-->H	17.430V	17.901V	18.384V



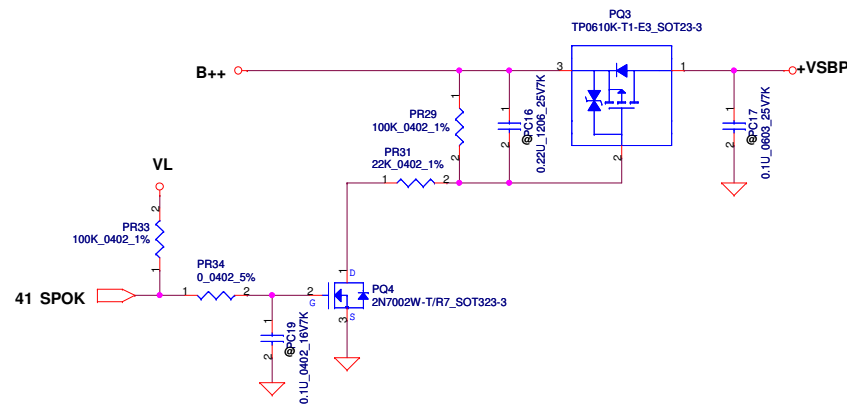
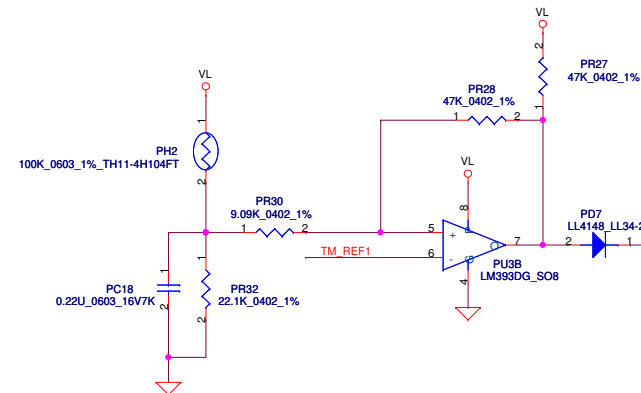
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PH1 under CPU botten side :  
CPU thermal protection at 96 degree C  
Recovery at 60 degree C



PH2 near main Battery CONN :  
BAT. thermal protection at 79 degree C  
Recovery at 47 degree C



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FBMA-L11-322513-151LMA50T\_1210  
PL12  
B++  
PC145  
470P\_0402\_50V7K  
PC146  
470P\_0402\_50V7K  
PC147  
470P\_0402\_50V7K  
PC148  
470P\_0402\_50V7K  
PC20  
47U\_1206\_25V6K  
PC21  
47U\_1206\_25V6K  
PC22  
2200P\_0402\_50V7K

ISL6237\_B+

ISL6237\_B+

+3.3VALWP Ipeak=8.444A ; Imax=5.91A  
Choke DCRmax=60m ohm, DCRtyp=54m ohm  
Rds(on)=18m ohm(max) ; Rds(on)=15m ohm(typical)  
Vlimit=(5E-06 \* 330K)/10=165mV  
Ilimit=165mV/18m ~ 165mV/15m  
=9.167A ~ 11A  
Iocp=Ilimit+Delta I/2  
=10.134A ~ 11.967A  
Delta I=1.934A (Freq=300KHz)

22,40 MAINPWON

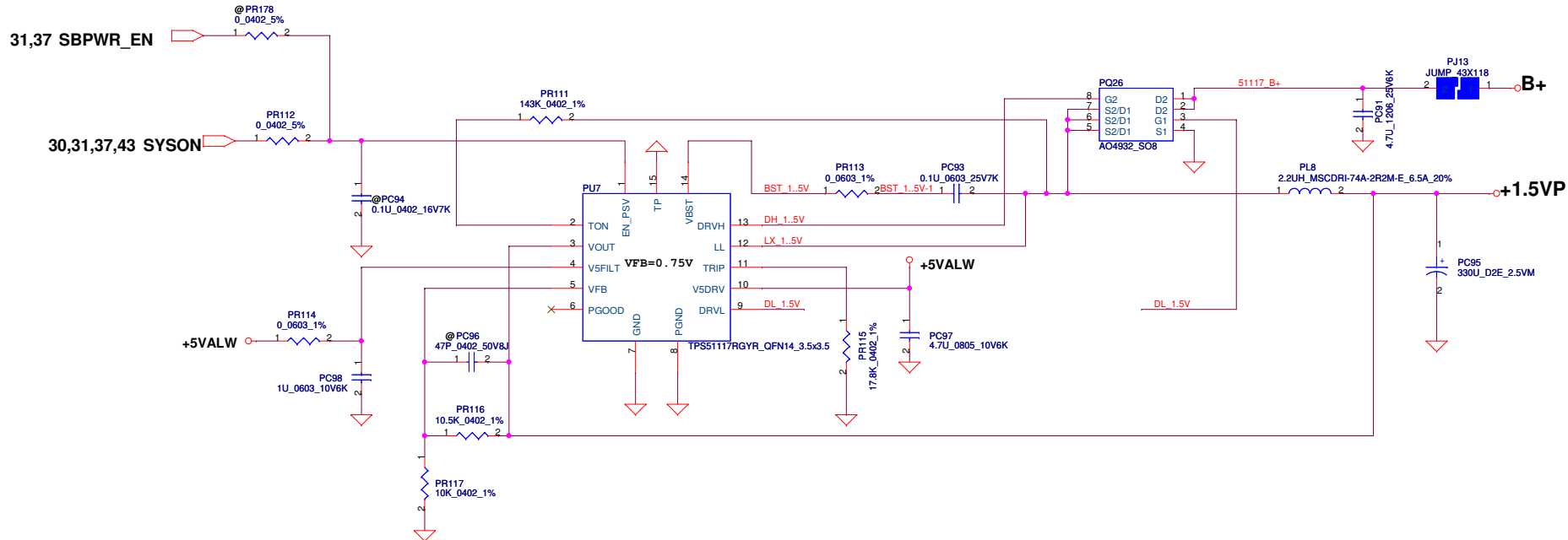
+5VALWP Ipeak=8.444A ; Imax=5.91A  
Choke DCRmax=60m ohm, DCRtyp=54m ohm  
Rds(on)=18m ohm(max) ; Rds(on)=15m ohm(typical)  
Vlimit=(5E-06 \* 330K)/10=165mV  
Ilimit=165mV/18m ~ 165mV/15m  
=9.167A ~ 11A  
Iocp=Ilimit+Delta I/2  
=10.147A ~ 11.980A  
Delta I=1.96A (Freq=400KHz)

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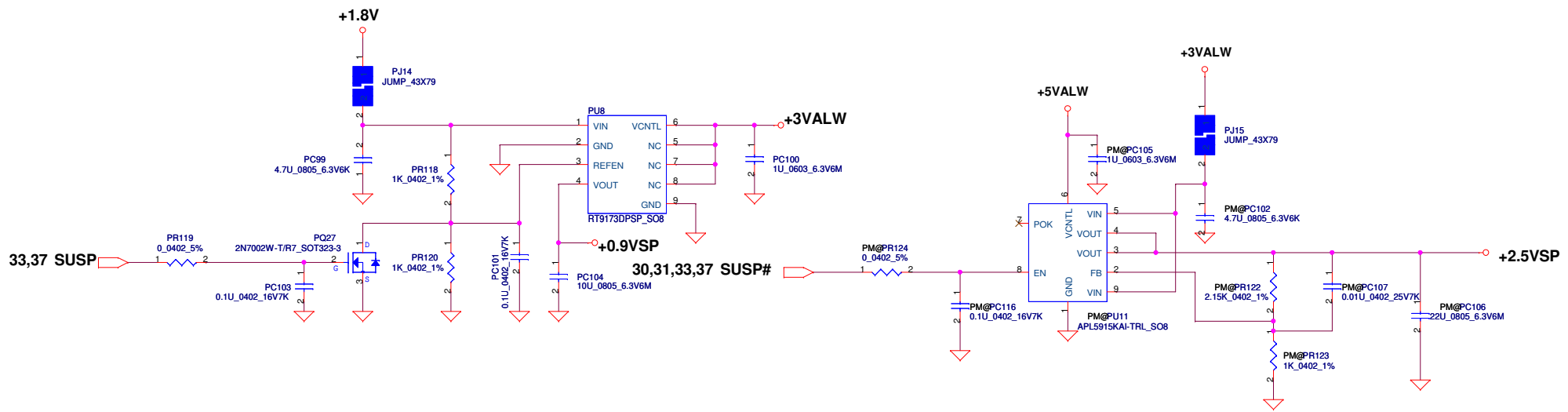




VFB=0.75V  
 $V_o = VFB * (1 + PR87 / PR88) = 0.75 * (1 + 10K / 10K) = 1.5V$   
 $Ton = 19 * e^{-12 * 143000 * ((2/3) * V_o + 100mV) / 19} + 50ns$   
 $= 2.645e-7 \text{ us}$   
 $=> V_o / Vin = D = Ton / Ts \Rightarrow Ts = 3.35us$   
 $Fsw = 298KHz$

Cout ESR=15m ohm  
 $I_{peak} = 4.71A, I_{max} = 3.297A, I_{ocp} = 5.652A$   
 $\Delta I = ((19 - 1.5) * (1.5 / 19)) / (L * Fsw) = 2.107A$   
 $=> 1/2 \Delta I = 1.053A$   
 $V_{trip} = R_{trip} * I_{ocp} = 17.8K * 10uA = 0.178V$   
 $I_{ocpmin} = V_{trip} / R_{dsonmax} * 1.2 + 1.053A$   
 $= 0.178 / (0.027 * 1.2) + 1.053 = 5.493A + 1.053A = 6.546A$   
 $I_{ocpmax} = (0.178 / (0.021 * 1.1)) + 1.053A = 7.705A + 1.053A$   
 $= 8.758A$   
 $I_{ocp} = 6.546A \sim 8.758A$

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## Version change list (P.I.R. List)

Page 1 of 2  
for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	ISL6237 can't shutdown while battery only.	ISL6237 can't shutdown while battery only.	0.1	41	Add PQ35 SB906100210 TP0610K.	20071031	EVT
2	Delete PD1	Because we can cost down and B+ has another one.	0.2	39	Delete PD1 SCSB540C080 (S SCH DIO B540C-13-F SMC)	20071115	DVT
3	3/5V exit on battery mode shutdown.	To prevent 3/5V exit on battery mode shutdown.	0.3	41	Add SC100001K00 (S DIO 1SS355 SOD323 T/R-5K	20071211	DVT
4	PD11 has over temp. issue.	Because PD11 has over temperature issue in JAQ60, we change it to a 10A diode.	0.3	39	Change PD11 from SCSB540C080 to SCS00002F00 .	20071211	DVT
5	Add snubber in 3/5V by EMI request.	Add snubber in 3/5V by EMI request.	0.3	41	Add PR36 and PR39 to SD001470B80	20071211	DVT
6	Down size.	Down size. by sourcer request.	0.3	46	Change PC136 from SE025821K80 to SE000003W00	20071211	DVT
7	Down size.	Down size. by sourcer request.	0.3	46	Change PC120 and PC129 from SE024681J80 to SE074681K80	20071211	DVT
8	Down size.	Down size. by sourcer request.	0.3	43	Change PC72 and PC74 from SE068102J80 to SE074102K80	20071211	DVT
9	2nd source trial run TI controller.	2nd source trial run TI controller.	0.3	41	Add PC143 SE080105K80	20071211	DVT
10	Add snubber in 3/5V by EMI request.	Add snubber in 3/5V by EMI request.	0.3	41	Add PC33 and PC34 SE074681K80	20071211	DVT
11	To meet Jeta SPEC.	To meet Jeta SPEC.	0.3	42	Add PC144 SE074102K80	20071211	DVT
12	Increase +5VALWP	HW requirement.	0.4	41	Change PR41 from SD034619280(S RES 1/16W 61.9K 0402 1%) to SD03463K280(S RES 1/16W 63.4K 0402 1%)	20080123	PVT
13	Increase +5VALWP	HW requirement.	0.4	43	change PR94 from SD034604280(S RES 1/16W 60.4K 0402 1%) to SD034576280(S RES 1/16W 57.6K 0402 1%)	20080123	PVT
14	Increase +5VALWP	HW requirement.	0.4	44	Change PR16 from SD034100280(S RES 1/16W 10K 0402 1%) to SD034105280(S RES 1/16W 10.5K 0402 1%)	20080123	PVT
15	Add EMI solution.	For EMI requirement.	0.4	41, 42, 43, 46	Change PR37, PR40, PR61, PR106, PR109, PR138, PR152 from SD013000080(S RES 1/10W 0 +-5% 0603) to SD013220B80 (S RES 1/10W 2.2 +-5% 0603)	20080123	PVT
16	Add EMI solution.	For EMI requirement.	0.4	43	Add PR64, PR104 and PR108 SD001470B80(S RES 1/4W 4.7 +-5% 1206)	20080123	PVT
17	Add EMI solution.	For EMI requirement.	0.4	43	Add PC55, PC83 and PC89 SE074681K80(S CER CAP 680P 50V K X7R 0402)	20080123	PVT
18	Add EMI solution.	For EMI requirement.	0.4	41	Add PC145, PC146, PC147, PC148 SE074471K80 (S CER CAP 470P 50V K X7R 0402)	20080123	PVT
19	Add EMI solution.	For EMI requirement.	0.4	41	Add PLI2, PLI3 SM010016410 (S SUPPRE KC FBMA-L11-322513-151LMA50T)	20080123	PVT
20	AO4916 will be EOL.	AO4916 will be EOL.	0.5	44	Change PQ26 from SB000002W80 S TR AO4916 2N S08 to SB0000BG00 S TR AO4932_S08	20080201	PVT
21							
22							
23							

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				PIR (PWR)	
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A --> B Change List

12/13-----  
Page 12, Mount R83, C179, C188  
Change R91 BOM structure to @  
Page 19, Mount D21, D22, D23  
Page 20, Change U40, U41 BOM structure to @  
12/12-----  
Page 19, Change C430, C422, C406 with BOM structure GM@  
Page 20, Add R548, R549 2.2K\_0402 with BOM structure @  
Change R542, R543 BOM structure to PM@  
Change Q3, Q5, Q46, Q47 to BSH111  
Page 28, Change T1 to SP050003T10  
Page 34, Change C603, C604 location and BOM structure to JAL90@  
Page 35, Change R449 to 43K\_0402\_1%  
Page 27, Add R550 with BOM structure @  
Page 49, Add option component for C430, C422, C402, C429, C421, C405  
Del C419, C428, C432, C513, C214 (Option Component)  
12/10-----  
Page 19, Add R544, R545 0\_0402 with BOM structure JAL90@  
Page 20, Add U40, U41 SN74CBTD3306CPWR\_TSSOP8 with BOM structure JAL90@  
Page 35, Change R486, R477, R485, R476 to 56.2\_0603 1%  
Page 38, Add R546, R547 10\_0402\_5% with BOM structure @  
Update MCH and ICH part number.  
U31 --> SA00001ZO30 (PM)  
SA00001P930 (GM)  
U9 --> SA00002AN10 (ICH9)  
12/07-----  
Page 12, Add C606, 220U\_D2\_4VM\_R15 with BOM Structure GM@  
Change C435 to 220uF\_D2 with BOM Structure @  
Page 17, Update JMXM1 PCBfoot to QUASA\_CA0481-230N00\_230P-T  
Page 20, Add R542, R543 4.7K\_0402 with BOM Structure JAL90@  
Update Power Schematics  
12/06-----  
Page 4, Add R541 10K\_0402 with BOM Structure @  
Page 17, Add R534 0\_0402 with BOM Structure PM@  
Change D30, R516 BOM Structure to @  
Page 31, Add R540 100K\_0402 with BOM structure JAW50@  
Change R186 BOM Structure JAL90@  
Page 38, Add R533 10K\_0402 with BOM Structure JAL90@  
Add C605 0.1U\_0402 with BOM Strcture @  
Change Q35 to 2N7002DW-T/R7\_SOT363-6  
12/05-----  
Page 16, Update U15 to ICS9LPRS387BKLFT (SA000020H10)  
Page 31, Delete R534  
Page 34, Delete R533  
Page 38, Update JDOCK1 CIS Symbol to JAE\_SP07-10207-19\_68P-T  
12/04-----  
Page 16, 17, 19 Delete Q23, Q37, Q38 2N7002DW-T/R7\_SOT363-6  
Add Q48, Q49, Q50, Q51, Q52, Q53 2N7002\_SOT23  
Page 22, Add 10K\_0402 with BOM Structure JAW50@  
Change R61 BOM Structure to JAL90@  
Page 31, Change R215 to 8.2K\_0402  
Page 34, Add C603, C604 220P\_0402\_50V8J with BOM structure @  
11/30-----  
Page 18, C368 change to 680P\_0402\_50V7K  
Page 20, D20 Change to CH751H-40PT\_SOD323-2  
Delete Q4  
Add Q46, Q47 2N7002\_SOT23 with BOM Structure JAL90@  
Page 26, Delete R532, Q46  
Add R538, R539 0\_0402 with BOM Structure @  
Change D31 with BOM Structure @  
Change U34 PN to SA00001W910  
Page 31 Change EC\_SMB\_CK2/DA2 Pull High to +3VS  
Page 32, Add SW4 with BOM Structure JAW50@  
Change SW1 BOM Structure to JAL90@  
Change JP2 Pin3/Pin4 to EC\_SMB\_CK2/DA2

11/28-----  
Page 12, Add R536, R537 0\_0402 with BOM Structure PM@  
Page 30, Change U14 to G577NSR91U  
Page 34, Change R438, R439, R441, R442 to 0\_0603 (BOM Error)  
Page 35, Change R473, R467 to 2.2K\_0402\_5%  
Change R9, R11, R26, R28 to 0\_0603 (BOM Error)  
11/27-----  
Page 19, Change D5 to RB491D\_SC59-3 accordig to Module design  
Page 25, Update JSATA2(HDD) PCB footprint to OCTEK\_SAT-22SU1G\_22P\_NR-T  
Page 33, C313 change to 1U\_0402\_6.3V6K  
Page 37, Mount R189, Q17 for +1.8V discharge circuit.  
11/22-----  
Page 26, Add R535 22\_0402  
Page 31, Add R534 0\_0402 for +RTCVCC  
11/19-----  
Rev A  
Page 8, R482, R480 Pull down (CLK\_DREF\_96M#/CLK\_DREF\_SSC#)  
Page 12, L32, L10, C505, C513, C263, C214 BOM Structure change to GM@  
Add C513, C214 0\_0402 (PM@) for Option Component (Page 49)  
Page 23, Unmount R341  
Mount R342  
Page 28, Update R521, R522, R523, R524, R525, R526, R527, R528, R529, R530 function field.  
Page 29, SWAP function of JMINI1 and JMINI2 (JMINI1 for Robson2, JMINI2 for WLAN)  
Delete C355 0.1U\_0402  
Add R531 0\_0402  
Page 34, Add R533 0\_0402 for ALC888VB DMIC\_CLK  
11/14-----  
Rev B  
Page 7, 8, 9, 10, 11, 12, 13 change U31 BOM Structure from GM@ to JAL90GM@  
Page 8, R102 BOM Structure change from GM@ to JAL90GM@  
Page 10, C210, C218, C222, C228, C208, C215, C220, C223 add BOM Structure PM@  
Page 49, Add C210, C218, C222, C228, C208, C215, C220, C223 option component BOM Structure JAL90GM@  
Add U31 Cantiga-GL, LED1 with BOM Structure JAW50@  
Page 12, R396, C498 BOM Structure change from GM@ to JAL90GM@  
R395 BOM Structure change from PM@ to GLPM@.  
Page 17, R331, R333 BOM Structure change from GM@ to JAL90GM@  
Page 19, R322, R327, R329 BOM Structure change from GM@ to JAL90GM@  
U28 add BOM Structure JAL90@  
Add R518, R519, R520 0\_0402\_5% for JAW50 CRT signal  
Page 20, R51, R52, Q4, R344, R336, C439, C440, R332, U30, R67, R72, Q3, Q5, D6, F2, C443, R351, R354, R358, R364, R365, R367, R372, R373, D19, C427, C431, U27, D20, R305, R306, 307, R308, R309, R310 add BOM Structure JAL90@  
Page 22, R408, R409, R410, R411 change BOM Structure from GM@ to JAL90GM@  
Page 23, R38, R53, R40, R54, R55, R34, U5 change BOM Structure from GM@ to JAL90GM@  
Page 24, R160, R162 BOM Structure change from PM@ to GLPM@  
R159, R161 BOM Structure change from GM@ to GLPM@  
Page 28, Add R521, R522, R523, R524, R525, R526, R527, R528, R529, R530 0\_0402\_5% for JAW50  
U1 add BOM Structure JAL90@  
Page 29, C343, C352, C342, C344, C312, C355 add BOM Structure JAL90@  
Page 32, Q15, LED1 add BOM Structure JAL90@  
Page 33, R512, C592, IR1, C593 add BOM Structure JAL90@  
R252 change BOM Structure from PM@ to GLPM@  
R250 change BOM Structure from GM@ to JAL90GM@  
Page 34, C583, C582, R445, C535, R442,R439 change BOM Structure from @ to 268@  
R490, R506 change BOM Structure from 268@ to @  
R441, R438 add BOM Structure 888VC@  
Page 35, R485, R476, R457, R460, R440, R444, R434, U39, R263, R255, R257, R261, C356, C353, C354, U21, R265, C358, U22, C357, Q43 add BOM Structure JAL90@  
Page 37, C272, C266, U11, C274, C273, R192, Q14, C278, R191 add BOM Structure PM@  
Page 38, R282, R284, Q35, R273 add BOM Structure JAL90@  
Rev A  
Page 37, Change R262 from 100K\_0402 to 10K\_0402 for Power Require(for 2.5V LDO).

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B --> C Change List

0220-----  
Page 6, Change C98 BOM structure to @

0218-----  
Page 20, Add R569 with BOM structure @  
Page 23, R34, R38, R40, R53, R54, R55 change BOM structure to @  
Page 31, Change R215 to 18K  
Update Power Schematics

0213-----  
Page 11, Delete R113  
Add J1 JUMP\_43X79 with BOM structure @  
Page 31, R558, R559 0 ohm with BOM structure @  
R560, R561 4.7K with BOM structure @  
Page 32, R562, R563 0 ohm with BOM Structure @  
R564, R565 0 ohm  
Page 38, Delete C619  
Add R566, R567, R568 0\_0603 with BOM Structure JAL90@

0204-----  
Page 12, Change L31 to MBK1608301YZF\_0603 with BOM structure GM@  
Change R163 to 0\_0805\_5%  
Page 23, Change BOM Structure of U5 to @  
Page 27, Change BOM Structure of R555 and R550 to @  
Page 33, Change R217 to 31.6K\_0402\_1%  
Change C313 to 1U\_0603\_10V6K  
Page 34, Change R503 to FBMA-L10-160808-301LMT\_0603

01/31-----  
Page 23, Delete U10

01/29-----  
Page 23, Change R152 BOM Structure to @

01/24-----  
Page 4, Change U8 to SA00001Z700 (EMC1402)  
Page 33, Change C338 to SE076104K80  
Page 35, Mount C584

01/23-----  
Page 38 Delete F3, R558~560, C609~614

01/22-----  
Page 11, Delete R79  
Change J1 Symbol to JUMP\_43X79  
Page 33, Add R557 10K (Check)  
Change R245 BOM Structure with @  
Page 38, Add C609~614, R558~560 (Check)  
C607,608, 615~619 (Check)

01/17-----  
Page 11, Add R79 0\_0805  
Update Power Schematics

01/16-----  
Page 11, Delete R79 0\_0805  
Add J1 JUMP\_43X79  
Page 16, Change C296, R301 to 27P\_0402  
Page 19, Change L17, L19, L21 BOM structure to GM@  
Page 23, Mount U29, R339  
Add U10 with BOM structure @ (Co-lay with U5)  
Change R340 Bom structure to @  
Change U5 to MX25L4005AMC-12G\_SO8 (SA00002A900)

Page 27, Change U26, C420 BOM structure to @  
Change R550 to 0\_0402  
Add R555 0\_0402  
Page 32, Change R269 to 240\_0402\_5%, R267 to 453\_0402\_1%  
Change R268 pin1 connect to +5VALW  
Page 33, Change R217 to 18K\_0402\_1% with BOM structure PM@  
Page 35, Add R551,R552, R553, R554 75\_0603\_1% with BOM structure JAL90@  
Add D32 PJDLC05\_SOT23-3  
Page 38, Add F3 3A\_15VDC\_SMD2920P300TF/15  
Page 49, Add R551,R552, R553, R554 1K\_0603\_1% with BOM structure 268@  
Add L17, L19, L21 0\_0805 with BOM structure PM@  
Update U38 (ALC268-VB1-GR ) PN:SA00001GD10 for JAW50

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## PCB

ZZZ



PCB 047 LA-4201P REV0 M/B

LA4201MB Rev0: DA600007400

LA4201MB Rev1:

## IC

U31



CANTIGA ES\_FCBGA1329

CANTIGA PM: SA00001ZO30

U31



CANTIGA ES\_FCBGA1329

CANTIGA GL: ????????????

(GM:SA00001P930)

U25



RTL8102E-GR\_QFN64P\_9X9

RTL8102E-GR: SA00001YY00

U38



ALC888S-VB\_LQFP48\_7x7

ALC888S-VB: SA000026V10

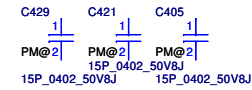
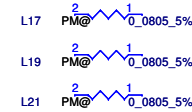
U38



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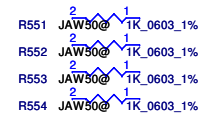
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## CRT

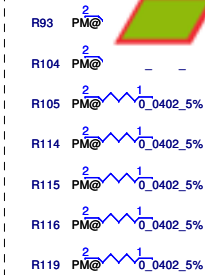
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## AUDIO

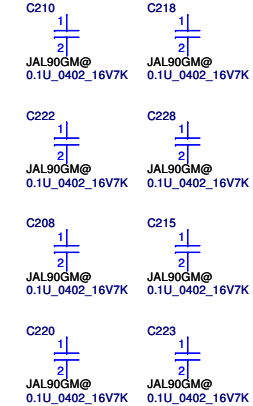


1K\_0603\_1%: SD014100180



0\_0402\_5%: SD028000080

## GM45-HDMI



0.1U\_0402\_16V7K: SE076104K80

LED1



HT-297DQ/GQ\_AMB/YG\_0603

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Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	Option Component
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