

**Compal Confidential**

**Model Name :**  
**File Name : LA-8671P**

# Compal Confidential

## TN-Note Schematic Document

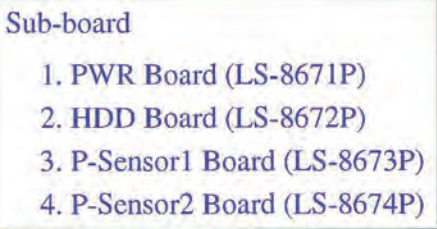
2012-07-27

REV:1.0\_0727A

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<http://www.vinafix.vn>

**Model Name : TN-Note**  
**File Name : Block Diagram**





## Voltage Rails

Power plane / State	+B	+5VALW / +3VALW	+1.5V	+3VM / +1.05VM	+5VS +3VS +1.8VS +1.5VS +1.05VS +VCC_GFXCORE_ANG +CPU_CORE +VCCSA +0.75VS
S0	O	O	O	O	O
S3	O	O	O	O	X
M3	O	O	O	O	X
S4/S5 - AC	O	O	X	X	X
S4/S5 - BATT ONLY	O	X	X	X	X
S4/S5 - NO AC & BATT	X	X	X	X	X

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS
Full ON	H	H	H	ON	ON	ON
S3 (Suspend to RAM)	L	H	H	ON	ON	OFF
S4 (Suspend to Disk)	L	L	H	ON	OFF	OFF
S5 (Soft OFF)	L	L	L	ON	OFF	OFF

## BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	0.4
4	0.5
5	
6	
7	

## EC SM Bus1 address

Device	Address	HEX
Smart Battery	0001-011xb	16H
Charger	0001-0010b	12H

## EC SM Bus2 address

Device	Address	HEX
Thermal sensor	1001-101xb	9AH
PCH (SML1DATA / GPIO75)	1001-0110 b	96H

## SM Bus Controller

Device	Address	HEX
Security Rom	1010-100xb	A8 H
DDR DIMM0	1010-000xb	A0 H

## SM Bus Controller 0

Device	Address	HEX
No-use	No-use	No-use

## Sensor HUB

Device	Address	HEX
ALS	0100-100xb	48h
APS	0011-001xb	32h
Gyroscope	0110-101xb	6Ah
e-Compass	0011-110xb	3Ch

## BOM Structure

	SDV	FVT	SIT	SVT	SOVP
@ : No Stuff					
ME@ : ME components					
CPUx@ : CPU SKU	V	V	V	V	
DDRx@ : RAM SKU	V	V	V	V	
SBA@ : SBA	V	V	V	V	
NOSBA@ : NO SBA					
ID4@ : Intel Deep S4	V				
AOAC@ : AOAC	V	V	V	V	
TPM@ : TPM		V	V	V	
Short@ : 0ohm short pad					

USB3.0	USB2.0	NOTE
1	0	
2	1 *	USB3.0/2.0 Conn
3	2	USB3.0/2.0 Conn
4	3	
	4	Sensor Hub
	5	USB Camera
	6	X
	7	X
	8	Touch Panel
	9 *	WWAN
	10	WLAN
	11	Finger Printer**
	12	
	13	Bluetooth**

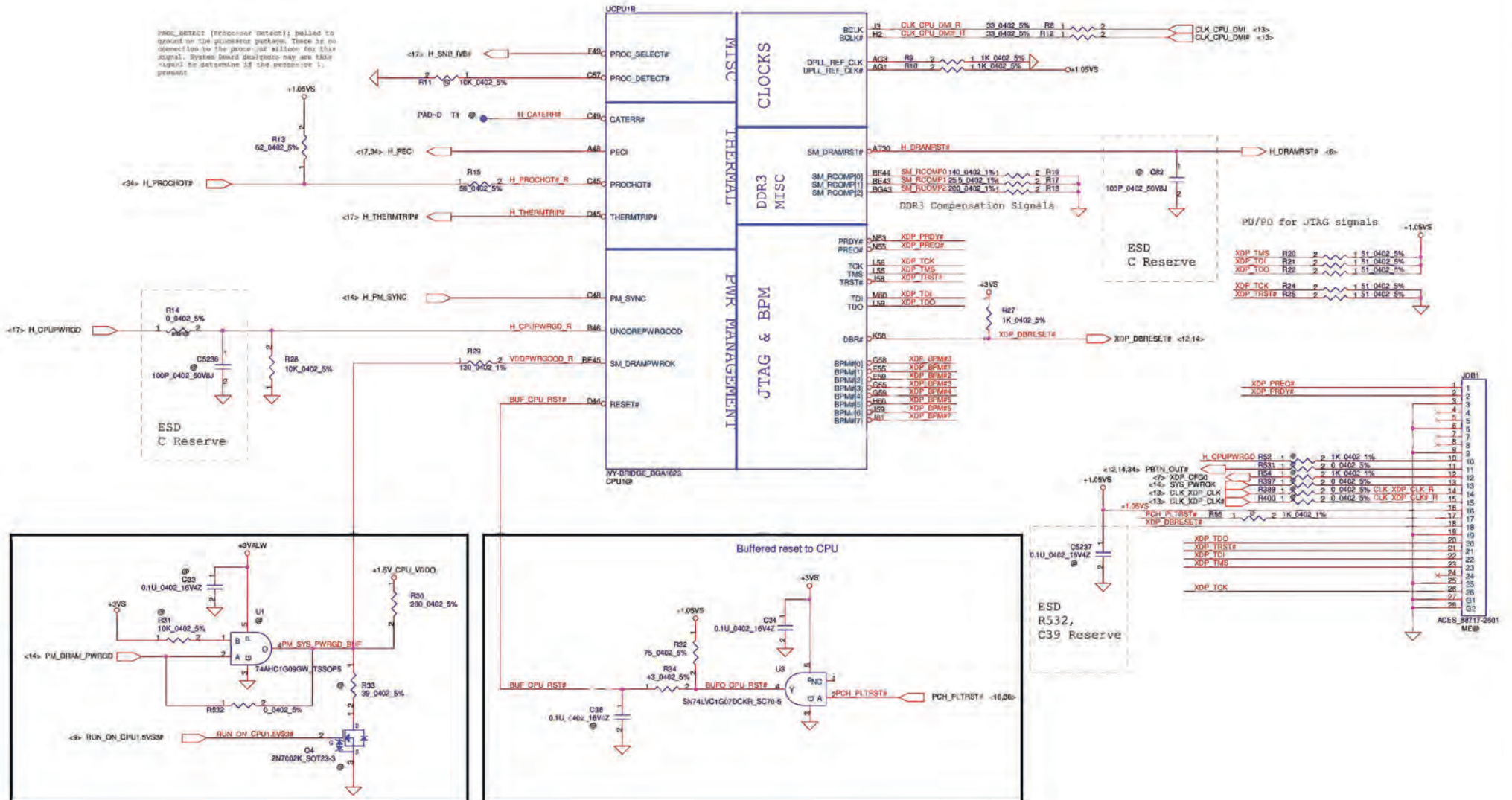
\* Debug Port  
\*\* Not Use

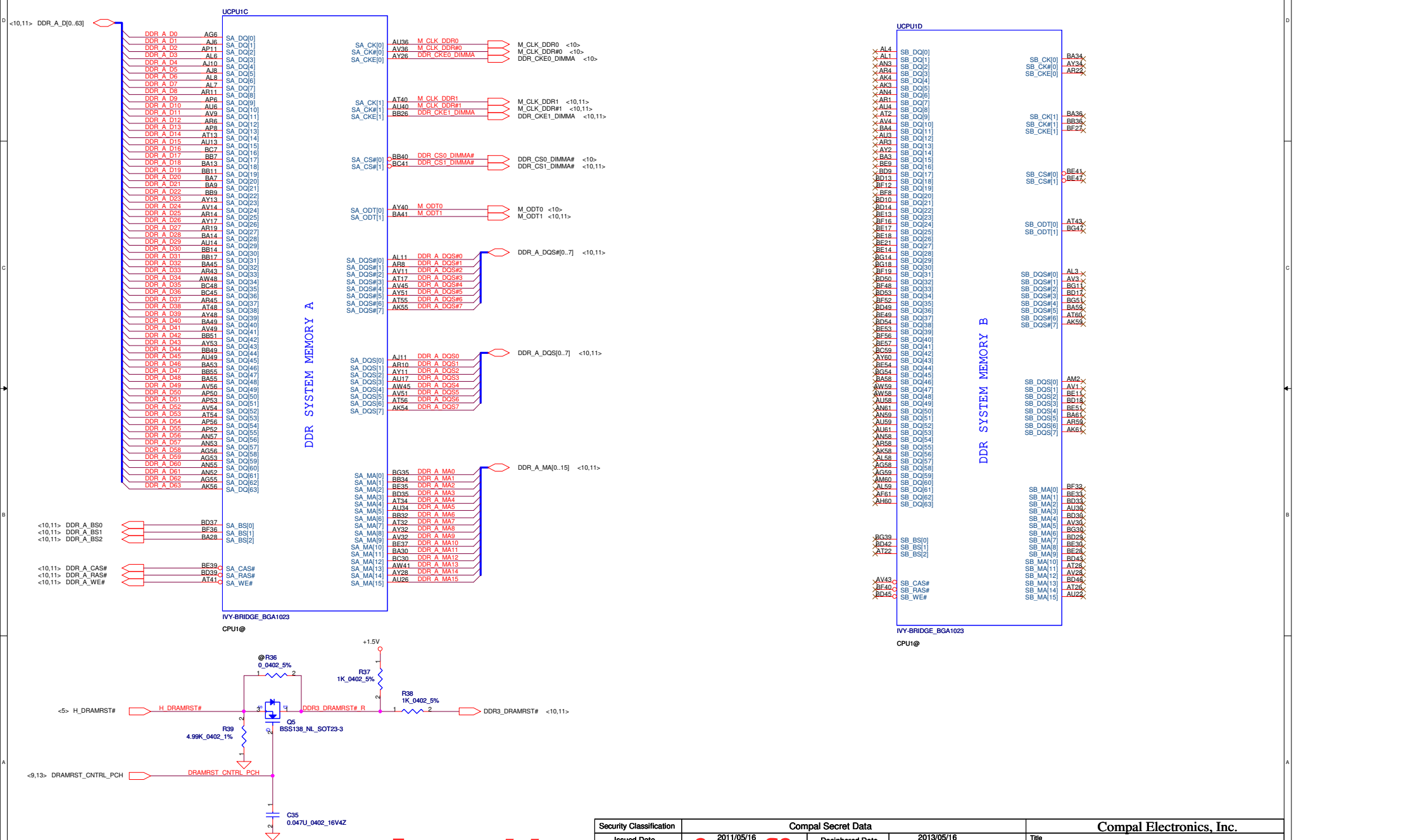
<http://www.vinalix.vn>

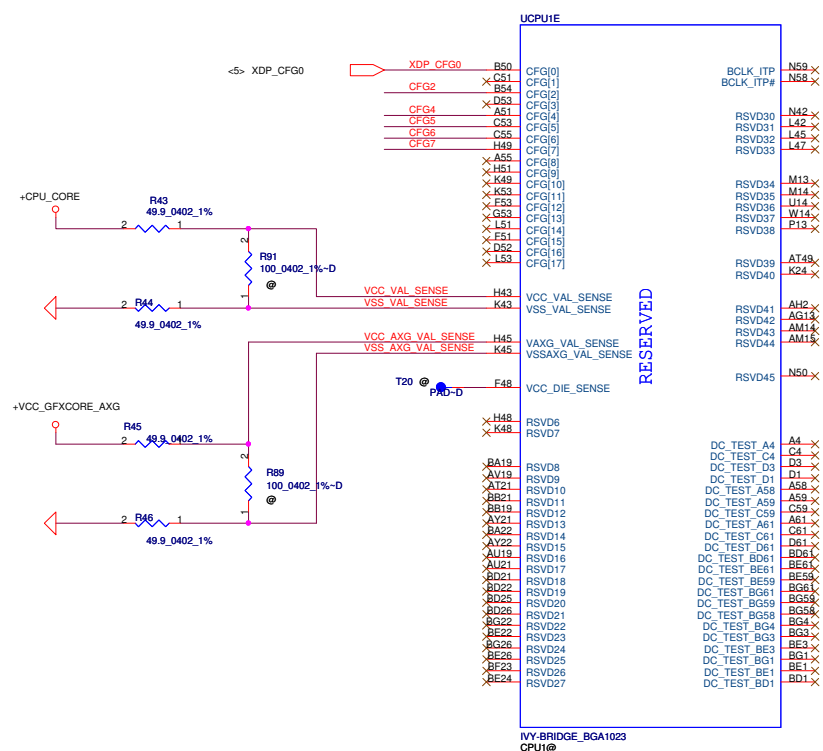
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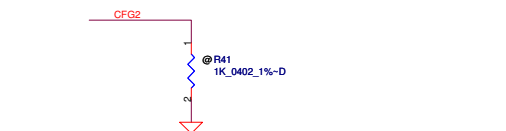




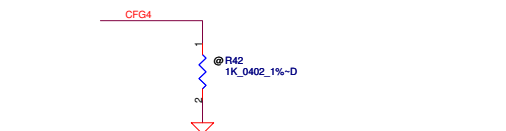




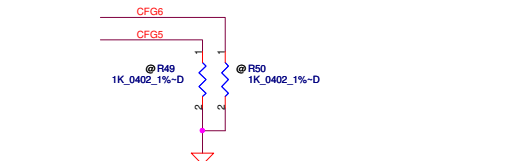
## CFG Straps for Processor



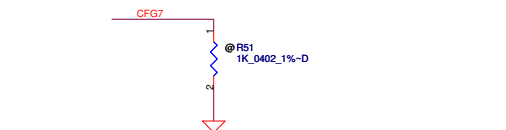
PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	*1: (Default) Normal Operation; Lane # definition matches socket pin map 0: Lane Reversed



Display Port Presence Strap	
CFG4	* 1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port



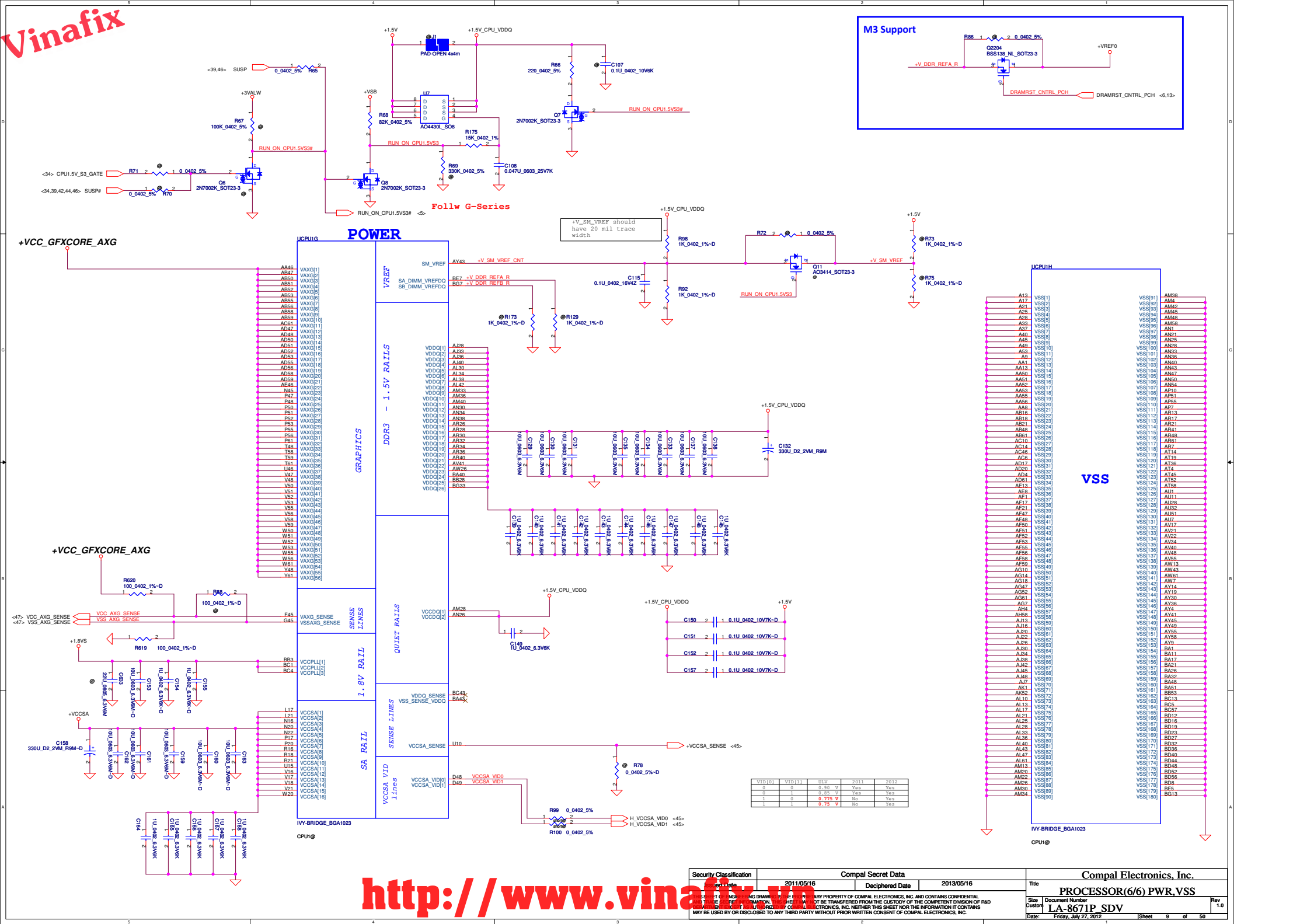
PCIe Port Bifurcation Straps	
CFG[6:5]	*11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

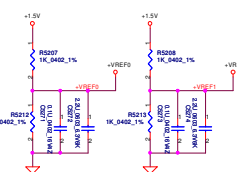
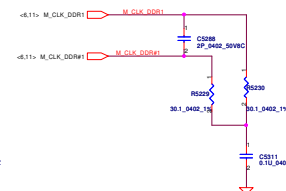
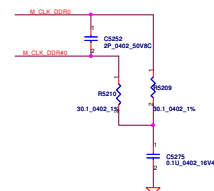
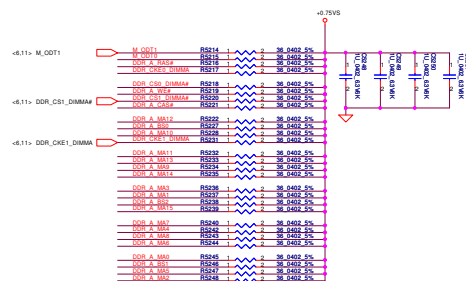
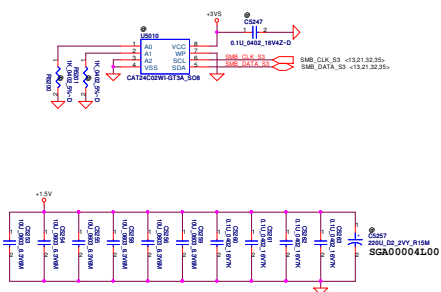
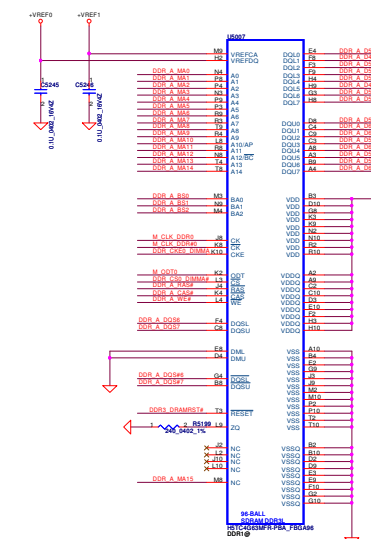
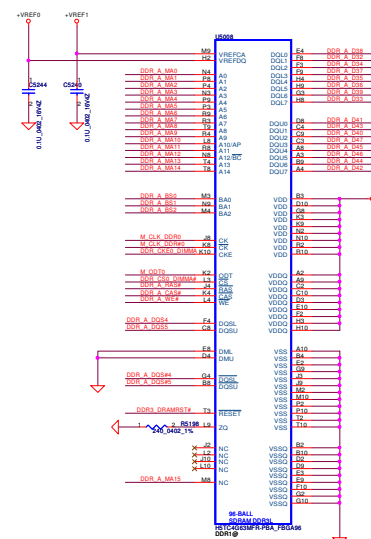
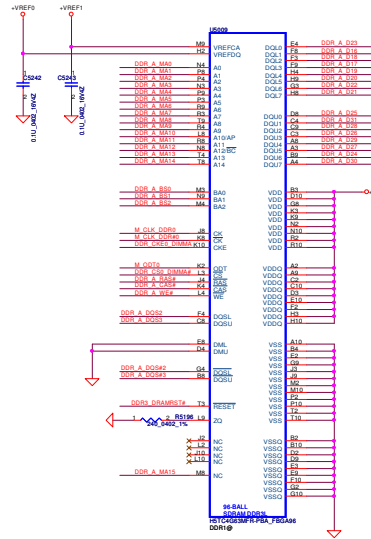
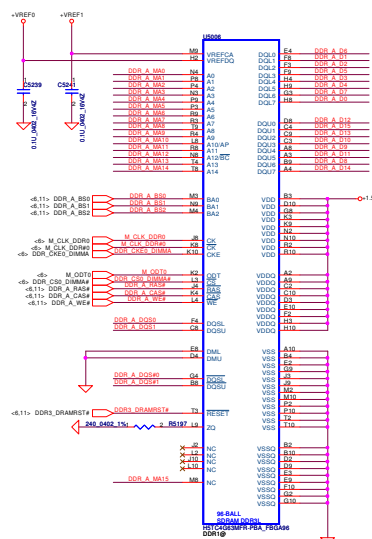


PEG DEFER TRAINING	
CFG7	*1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training

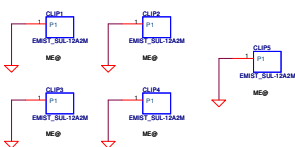
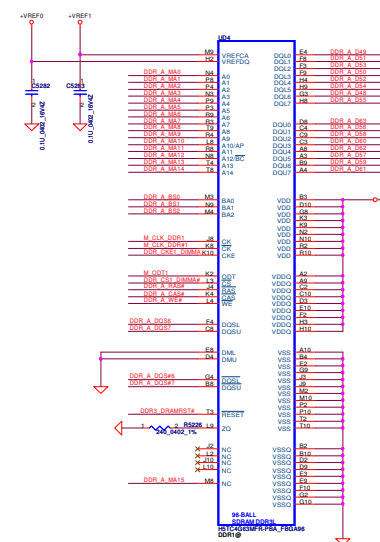
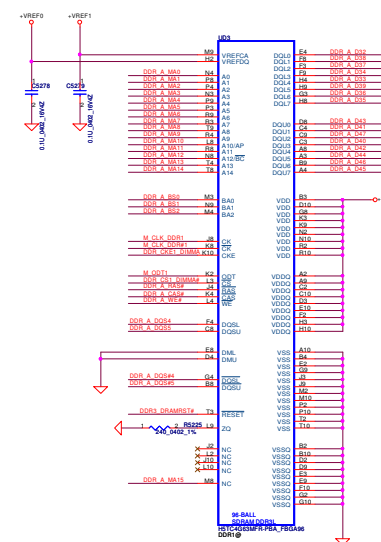
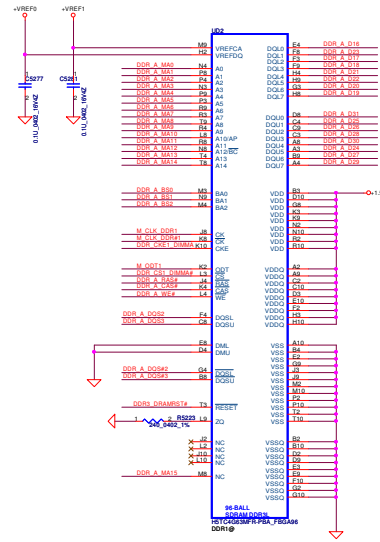
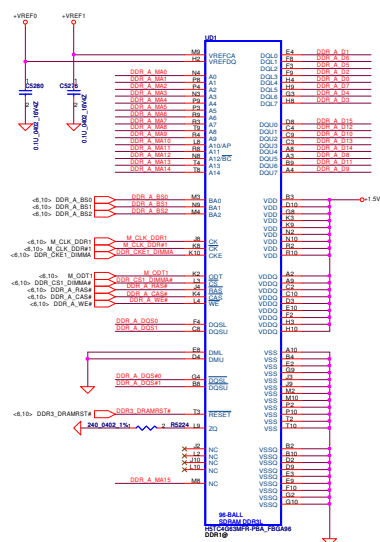








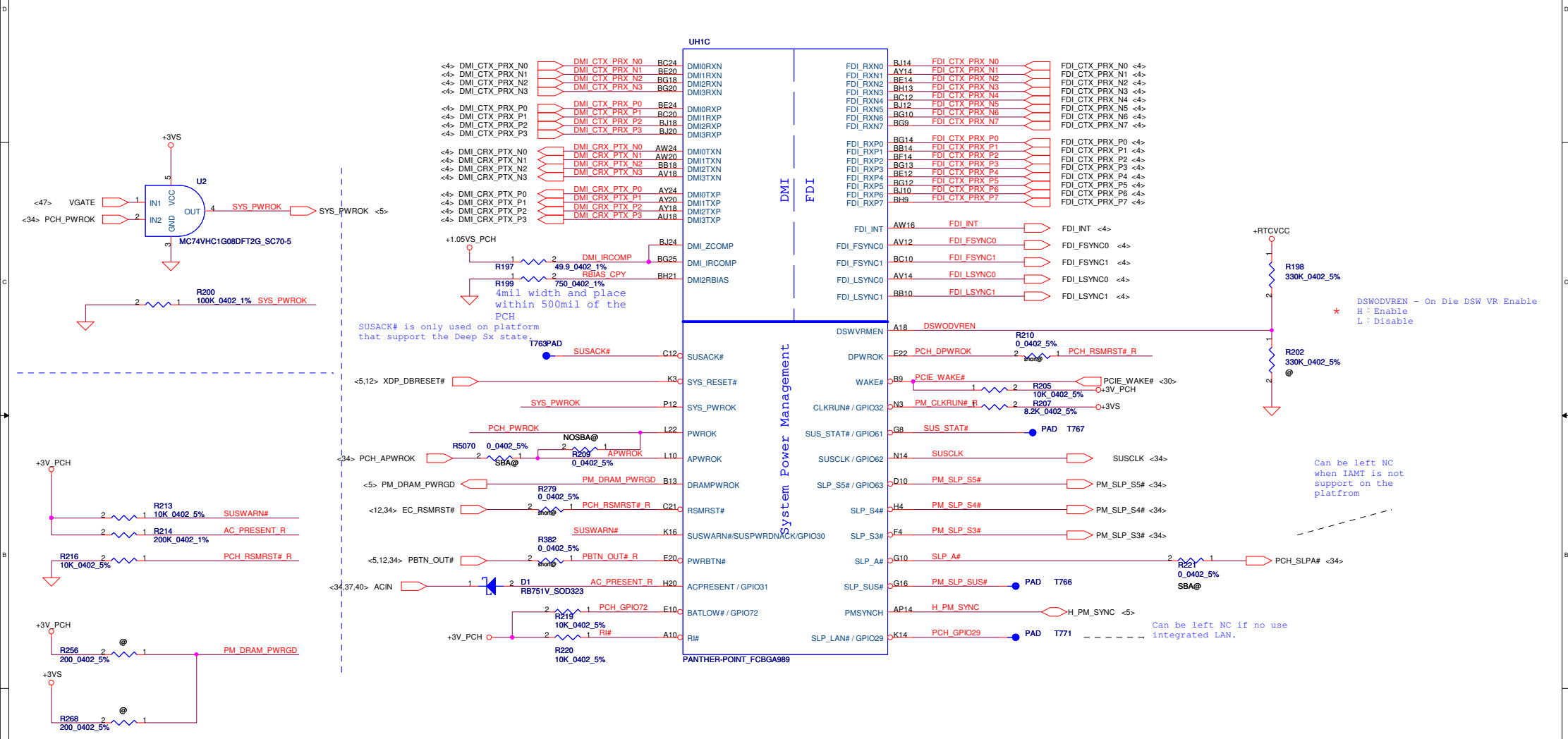
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Date: 2017-06-27 15:42:27			Sheet	15 of 40





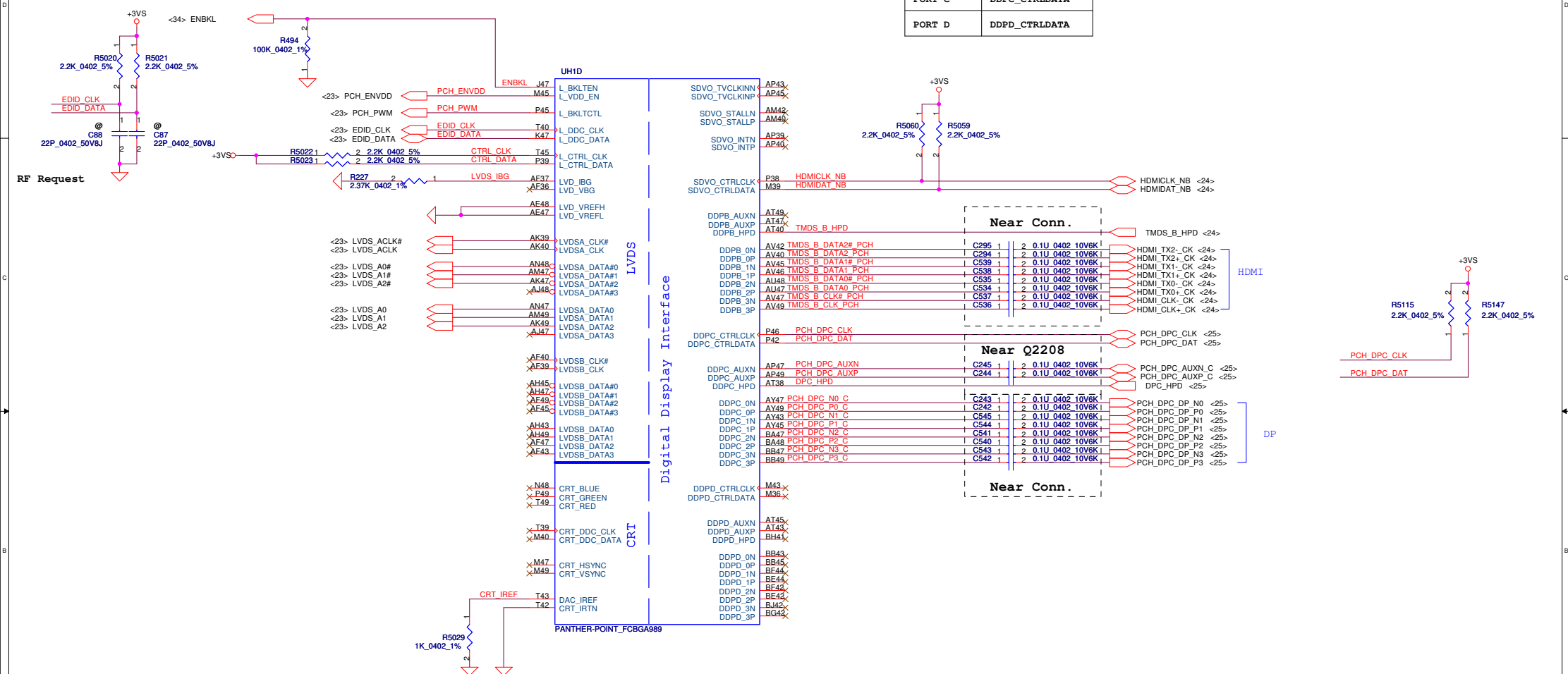


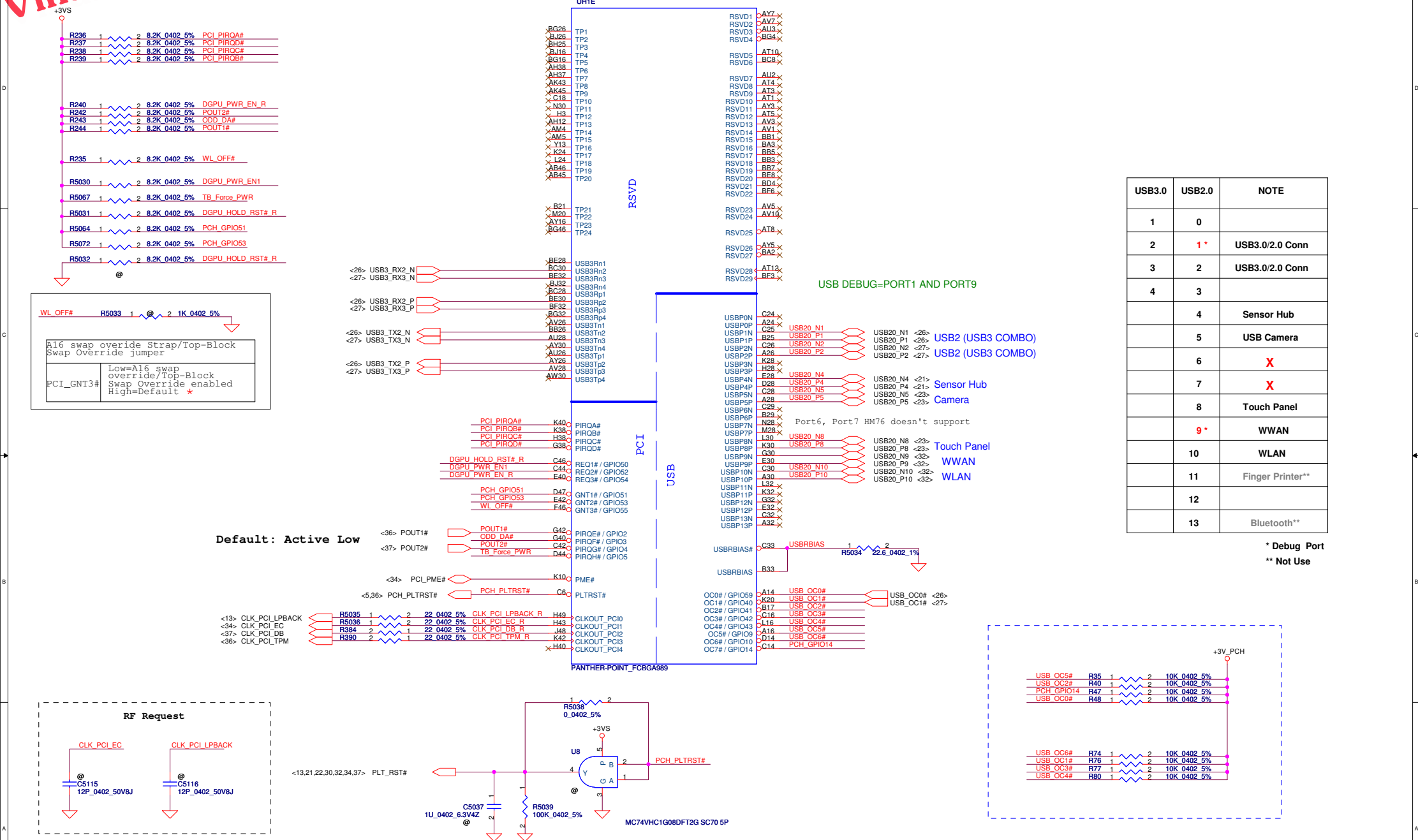




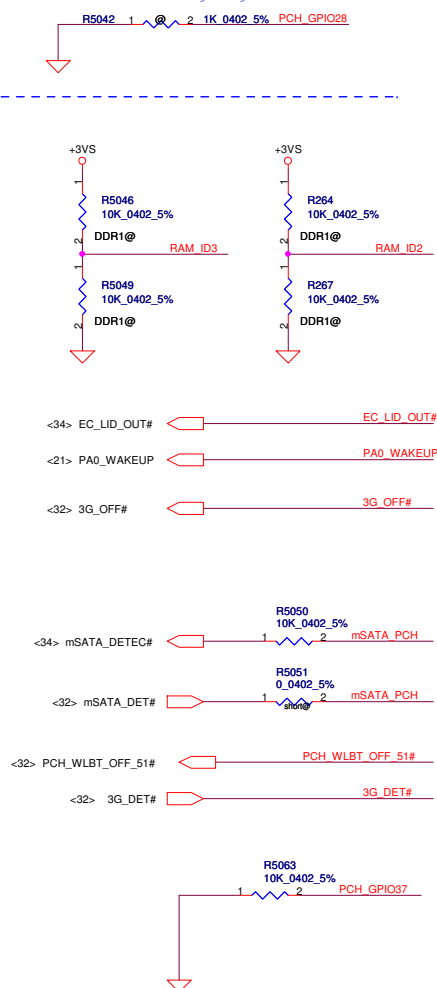
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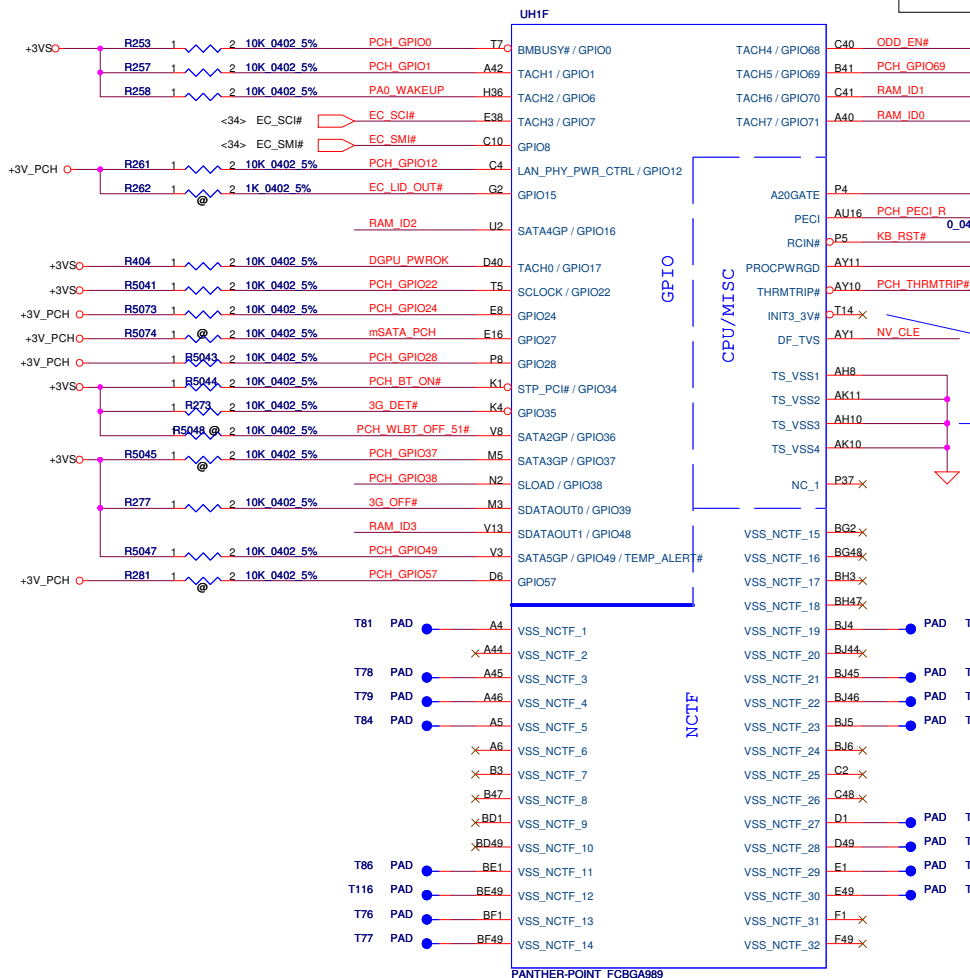




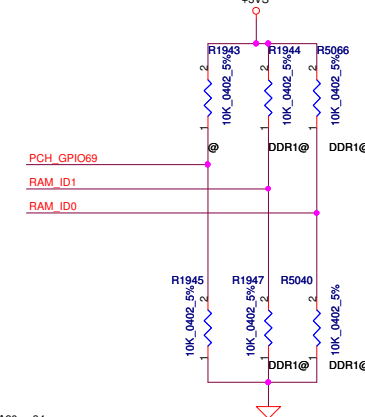
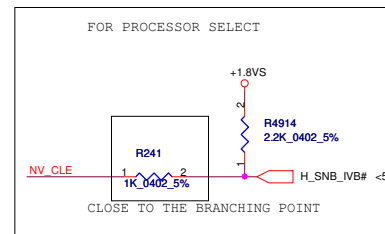
**GPIO28**  
On-Die PLL Voltage Regulator  
This signal has a weak internal pull up  
★ H : On-Die voltage regulator enable  
L : On-Die PLL Voltage Regulator disable



PCH_GPIO69	PCH_GPIO38	PCH_GPIO67	Function	GPU_ID
0	0	0	PX4.0	0
0	0	1	Reserved	1
0	1	0	DIS	2
0	1	1	UMA	3



PANTHER-POINT\_FCBGA989




RAM_ID3	RAM_ID2	RAM_ID1	RAM_ID0	RAM
0	0	0	0	ELPIDA 4GB
0	0	0	1	SAMSUNG 4GB
0	0	1	0	HYNIX 4GB
0	0	1	1	ELPIDA 8GB
0	1	0	0	SAMSUNG 8GB
0	1	0	1	HYNIX 8GB
0	1	1	0	ELPIDA 2GB
0	1	1	1	SAMSUNG 2GB
1	0	0	0	HYNIX 2GB
1	0	0	1	TBD
1	0	1	0	TBD
1	0	1	1	TBD
1	1	0	0	TBD
1	1	0	1	TBD
1	1	1	0	TBD
1	1	1	1	TBD

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5
4
3
2
1



1.05US

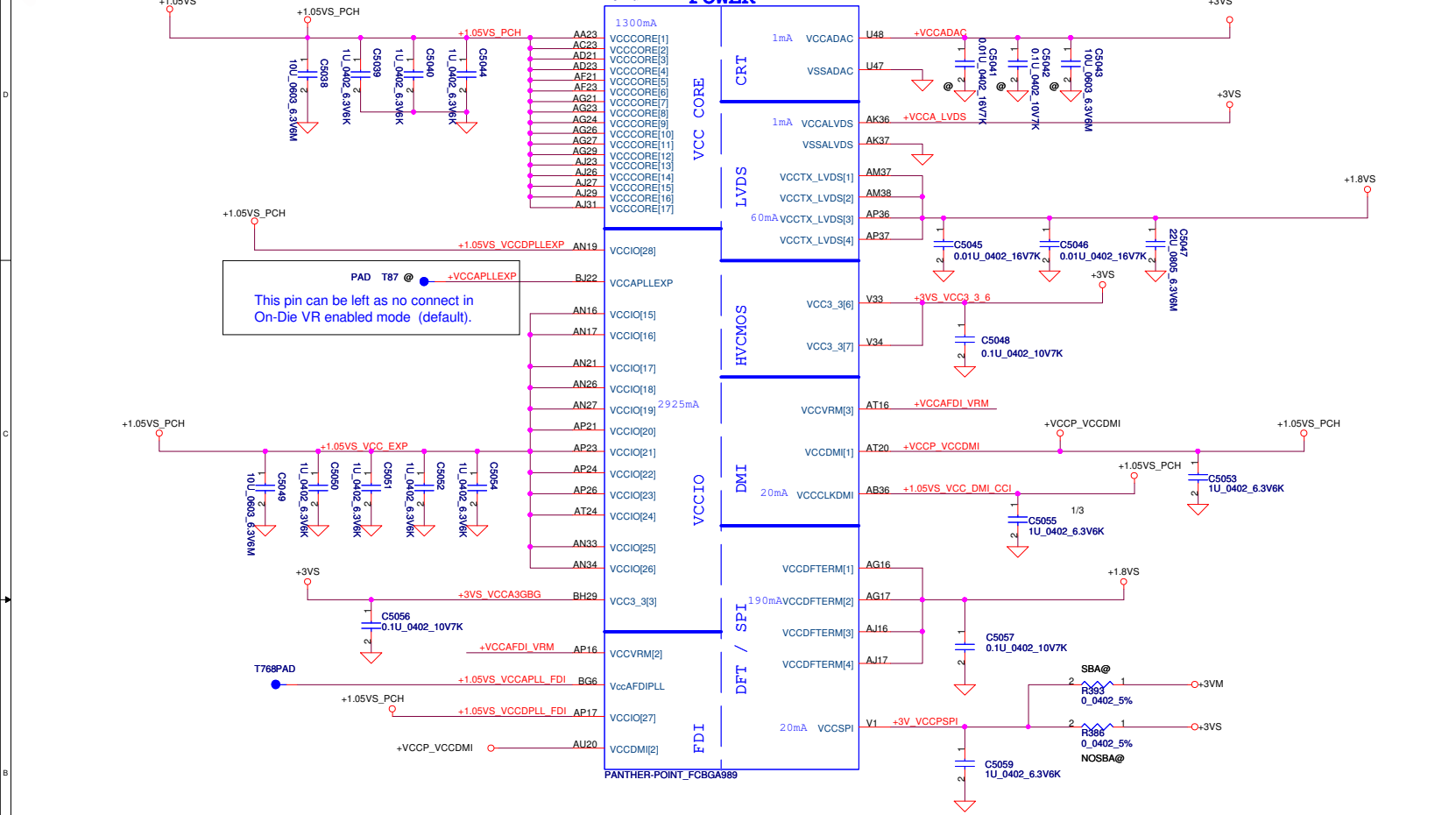
UH1G

POWER

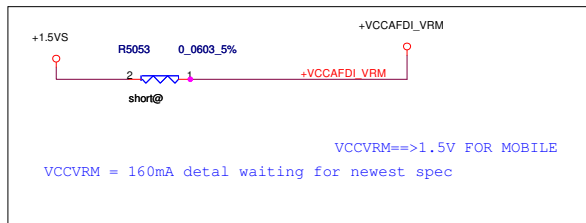
2VS

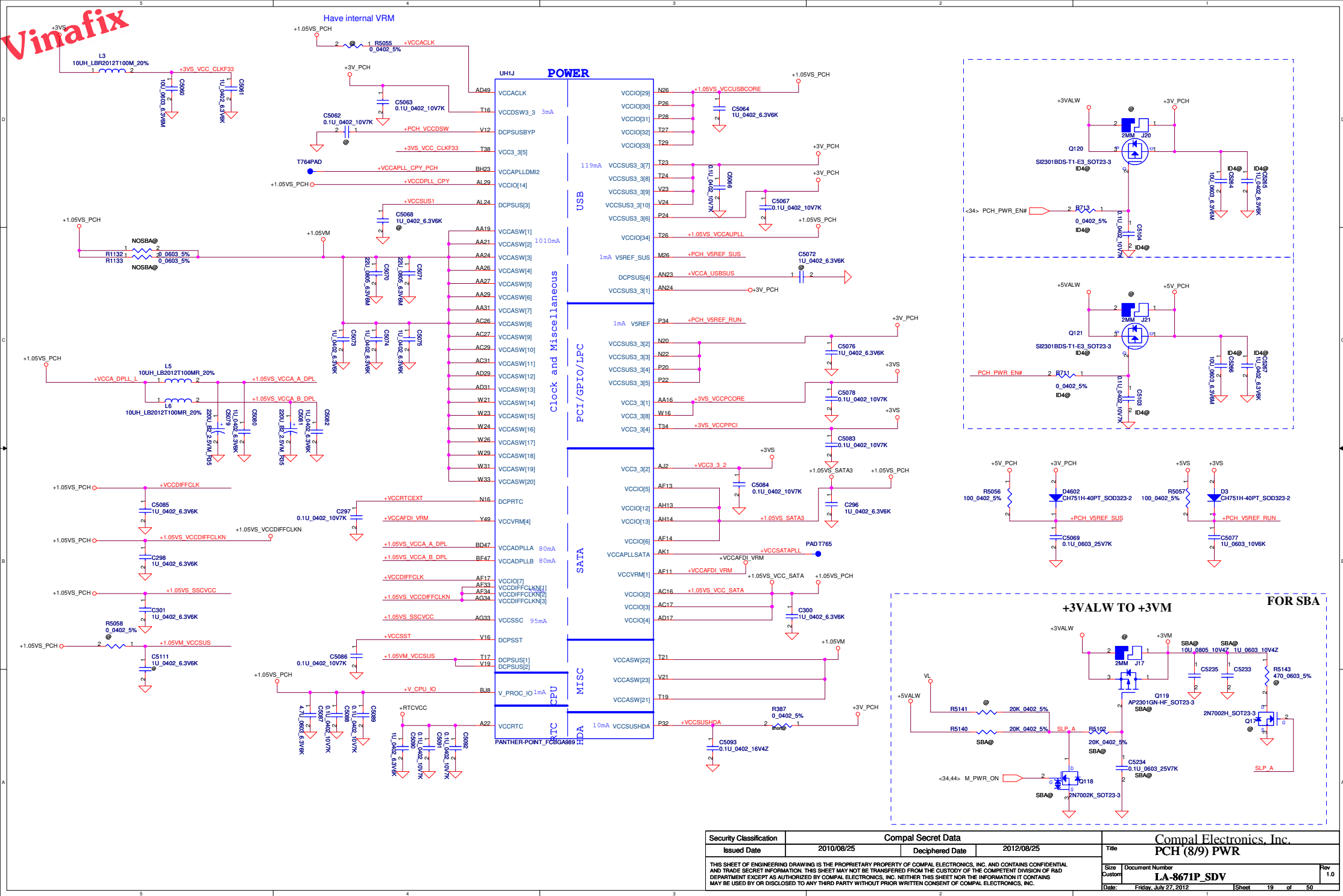
PCH Power Rail Table

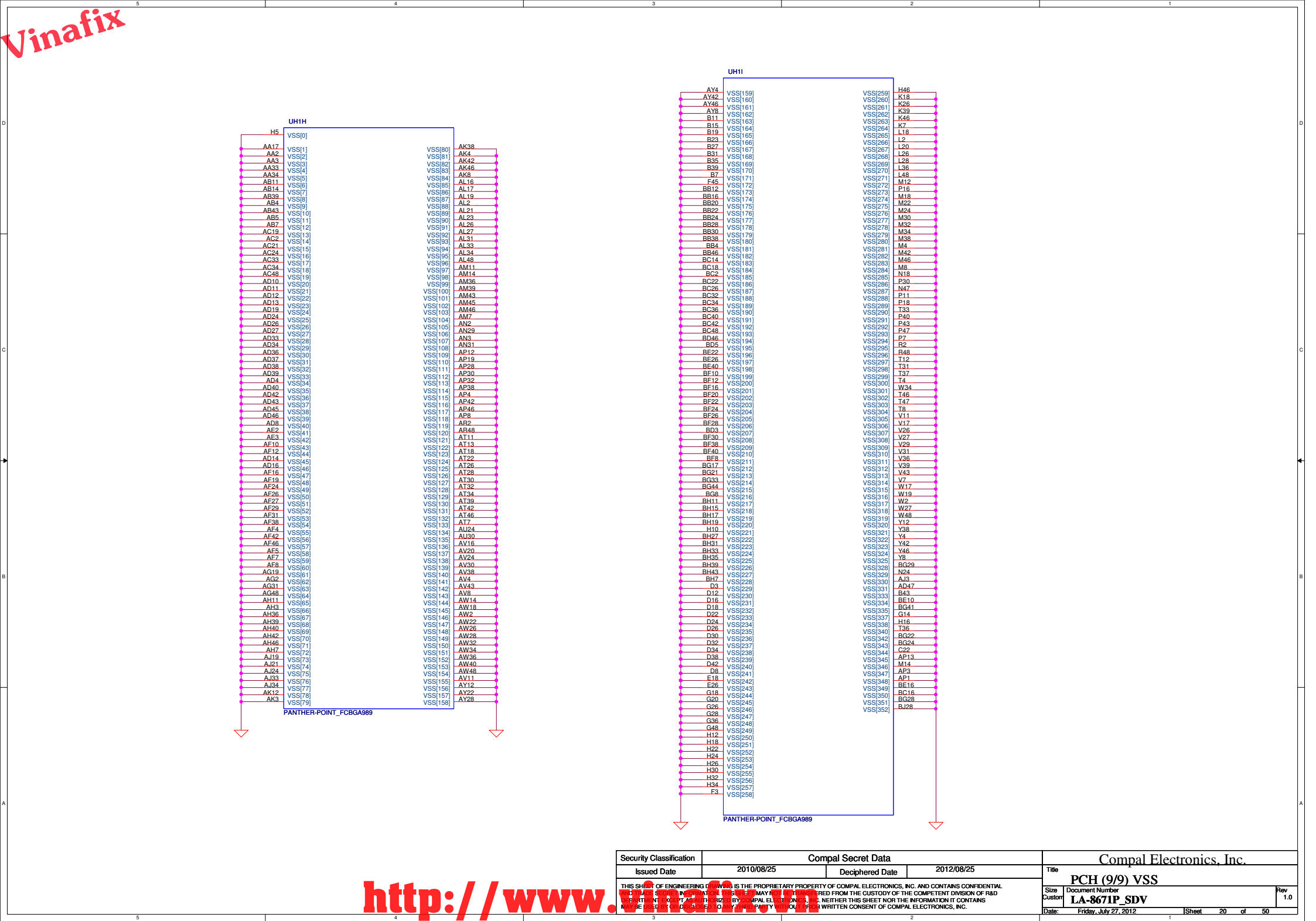
PCH Power Rail Table	



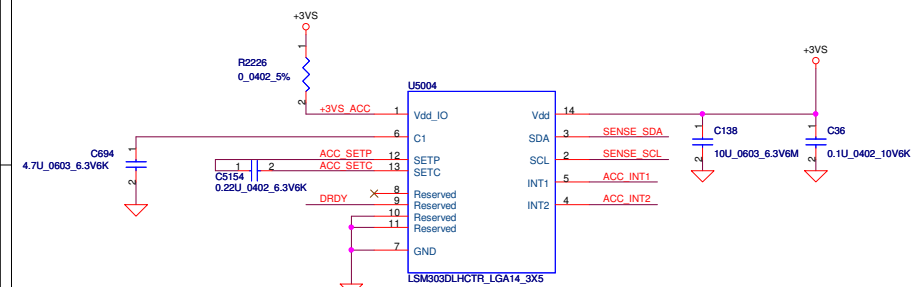
PCH Power Rail Table		
Voltage Rail	Voltage	SO Iccmax Current (A)
V_FROC_IO	1.05	0.001
V5REF	5	0.001
V5REF_Sus	5	0.001
Vcc3_3	3.3	0.266
VccADAC	3.3	0.001
VccADPLLA	1.05	0.08
VccADPLLB	1.05	0.08
VccCore	1.05	1.3
VccDMI	1.05	0.042
VccIO	1.05	2.925
VccASW	1.05	1.01
VccSPI	3.3	0.02
VccDSW	3.3	0.003
VccpNAND	1.8	0.19
VccRTC	3.3	6 uA
VccSus3_3	3.3	0.119
VccSusHDA	3.3 / 1.5	0.01
VccVRM	1.8 / 1.5	0.16
VccCLKDMI	1.05	0.02
VccSSC	1.05	0.095
VccDIFFCLKN	1.05	0.055
VccALVDS	3.3	0.001
VccTX_LVDS	1.8	0.06



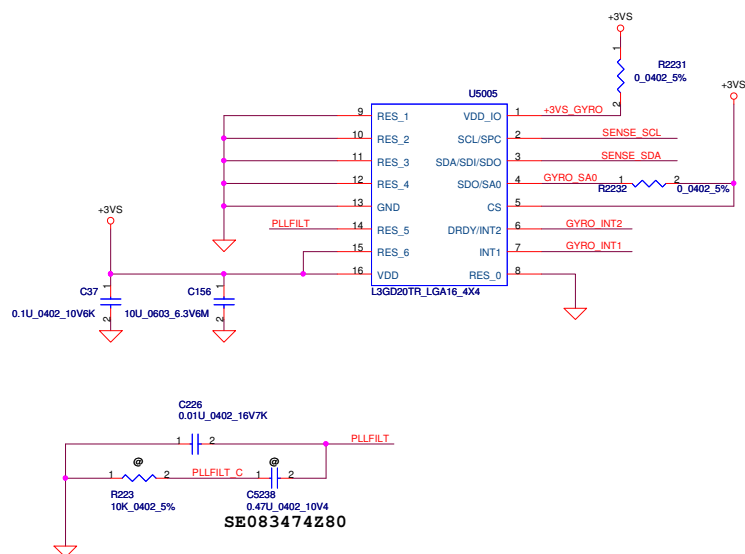




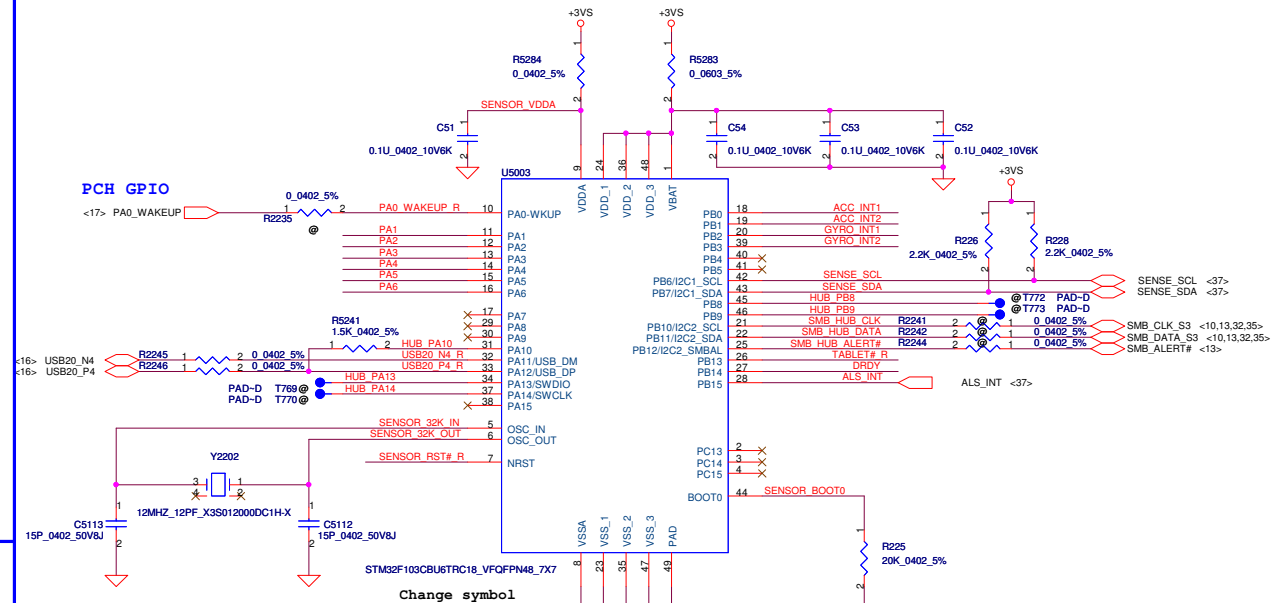
## Accelerometer + eCompass



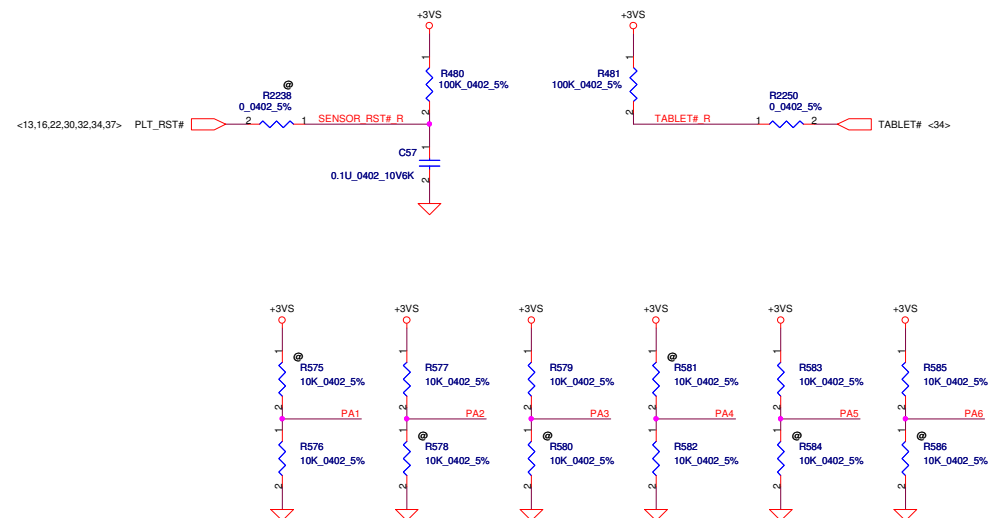
Gyro



## Sensor Hub

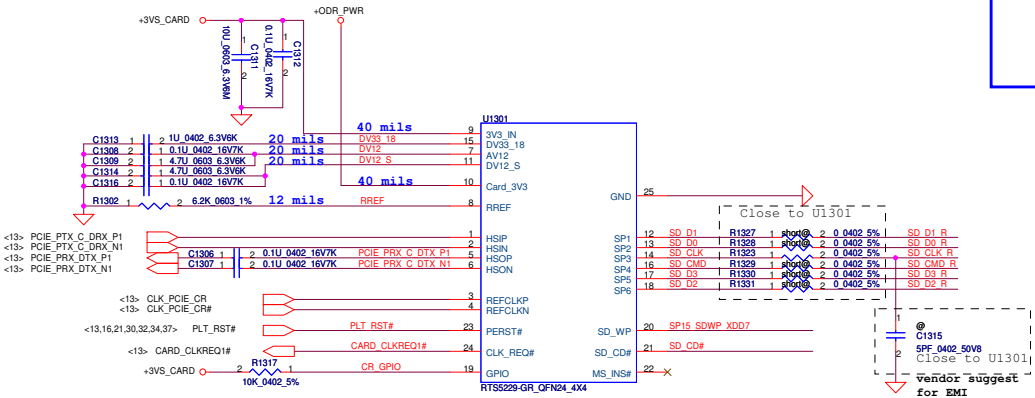
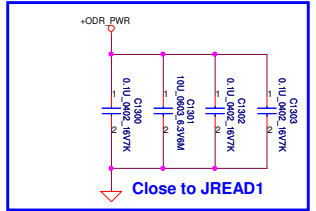
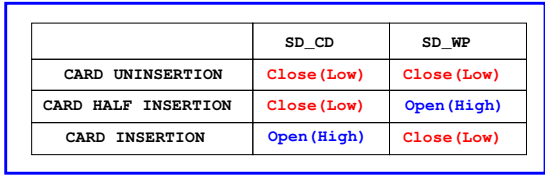


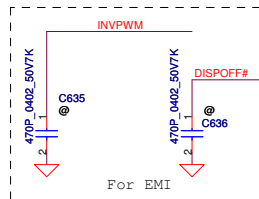
Change symbol



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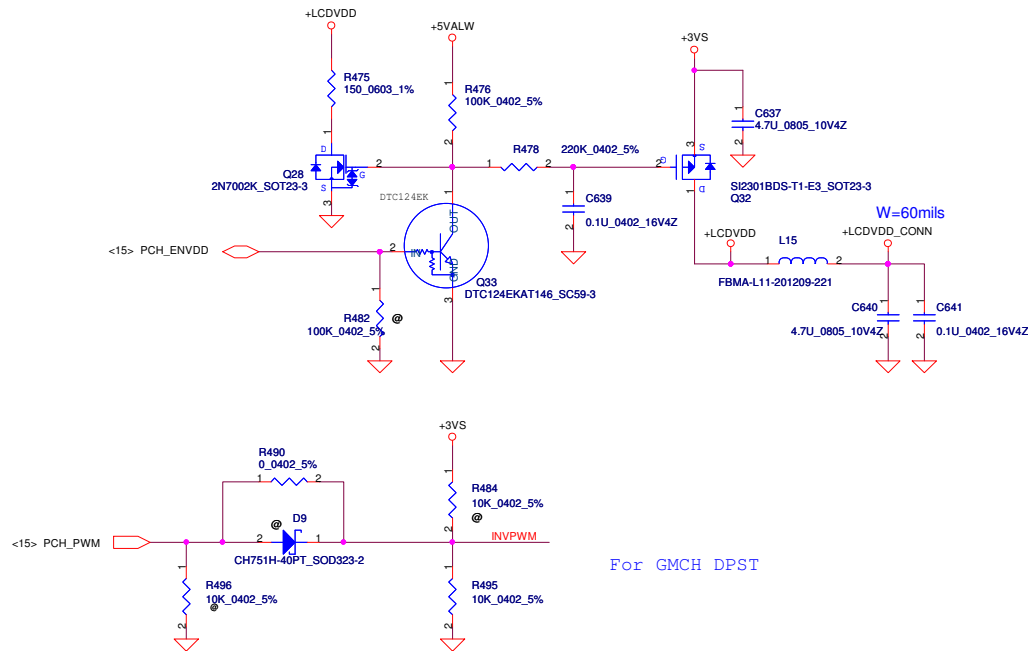




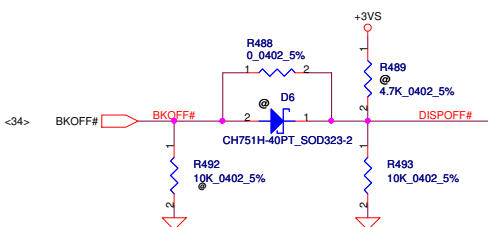


W=60mils

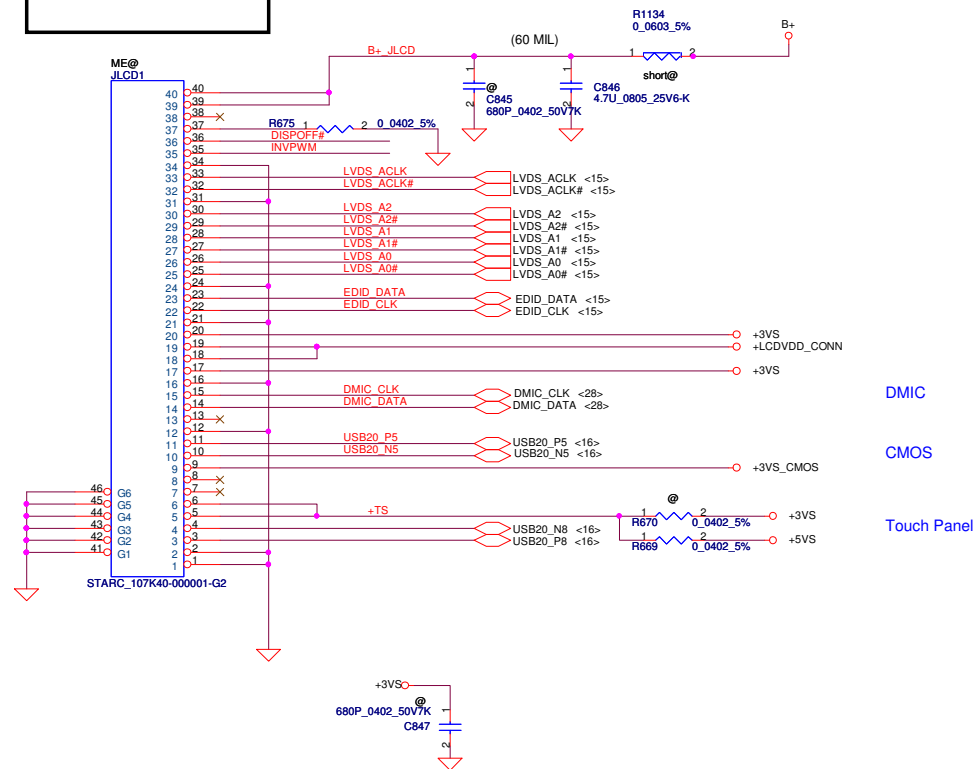
## LCD POWER CIRCUIT



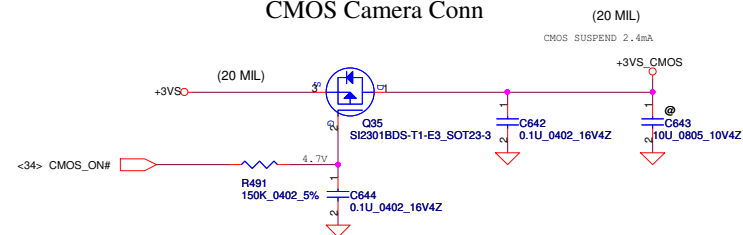
For GMCH DPST



PN:SP01000XE00



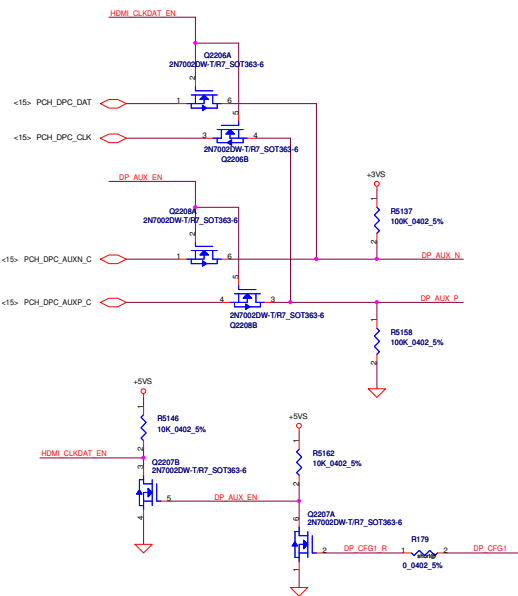
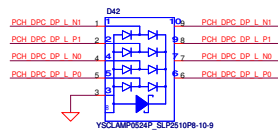
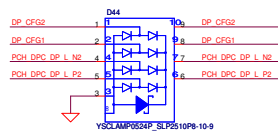
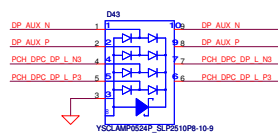
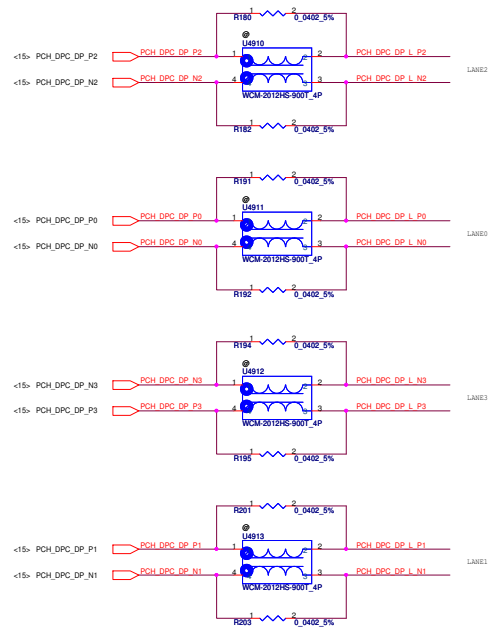
## CMOS Camera Conn



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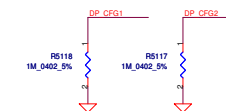


Close to JmDP1

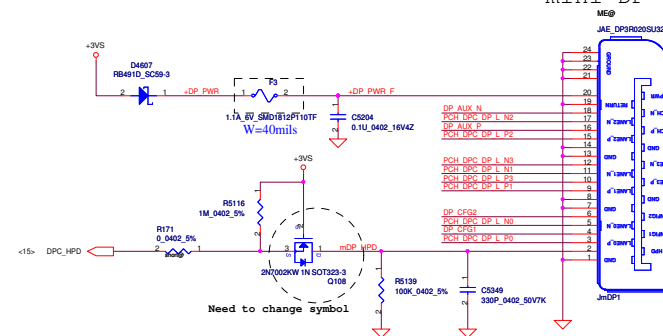


TBT, HDMI, DP mode table

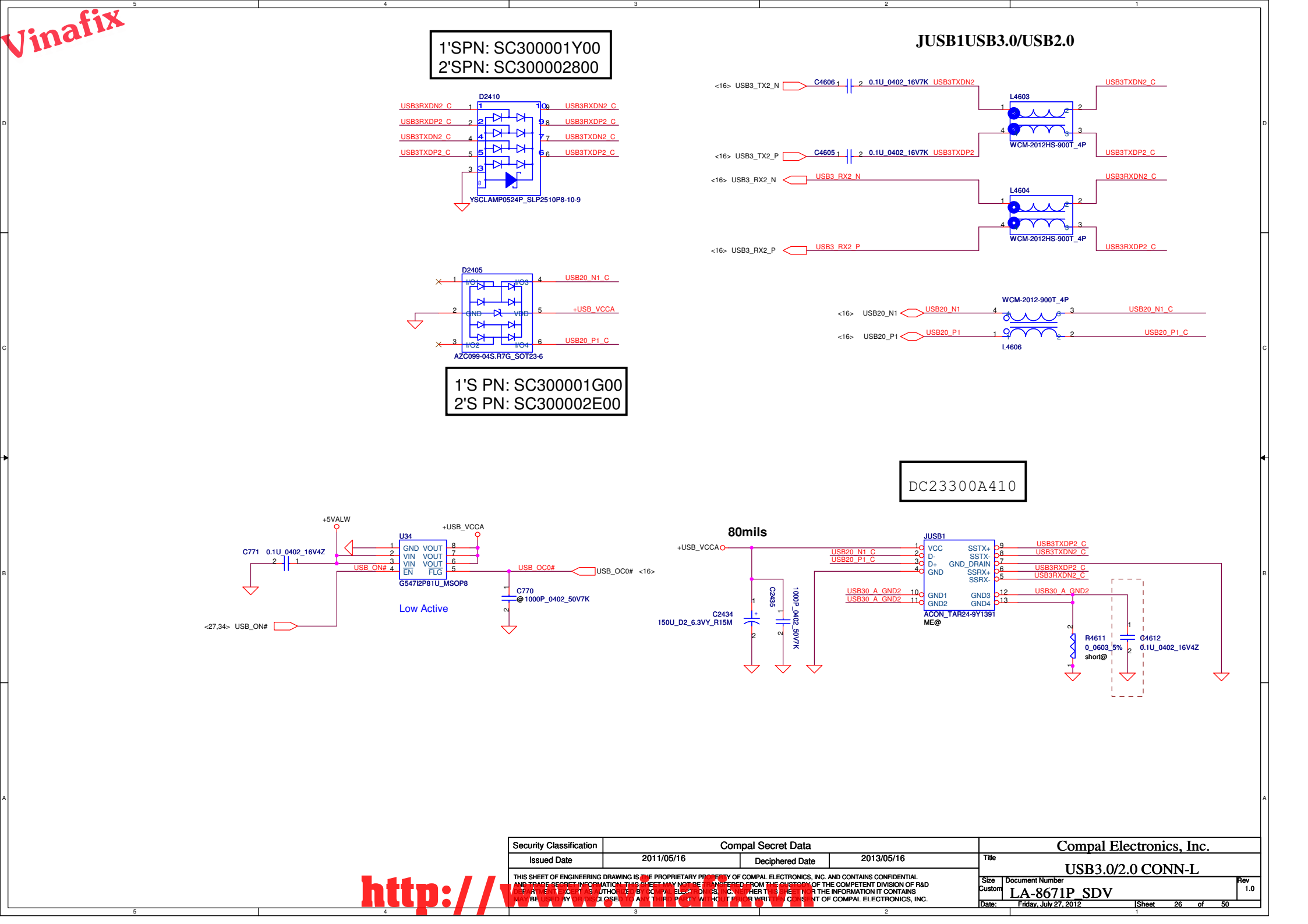
HPD	CFG1	CFG2	LSRX	Mode
1	0	0	X	DP
1	1	X	X	HDMI
0	0	1	1	TBT

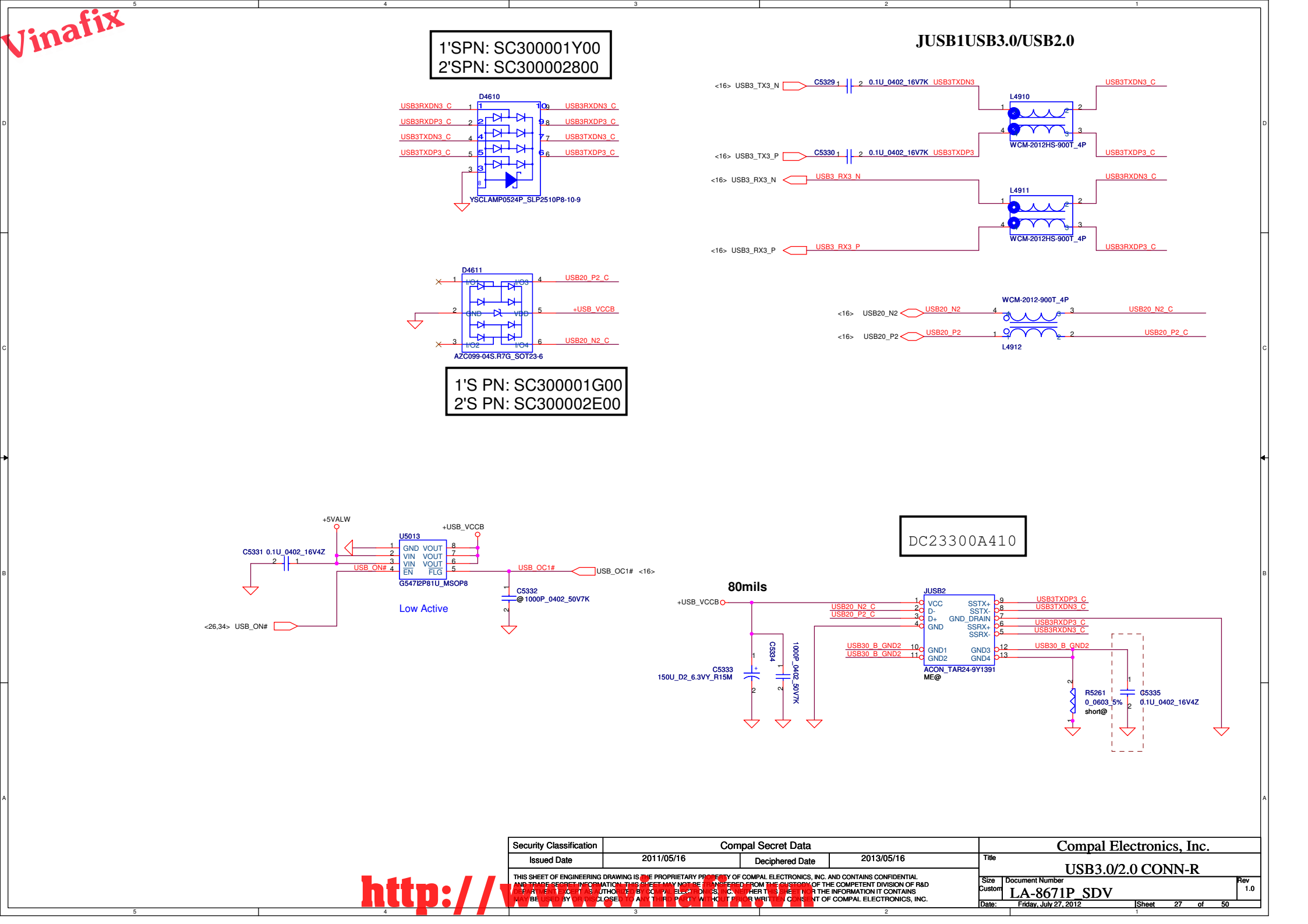


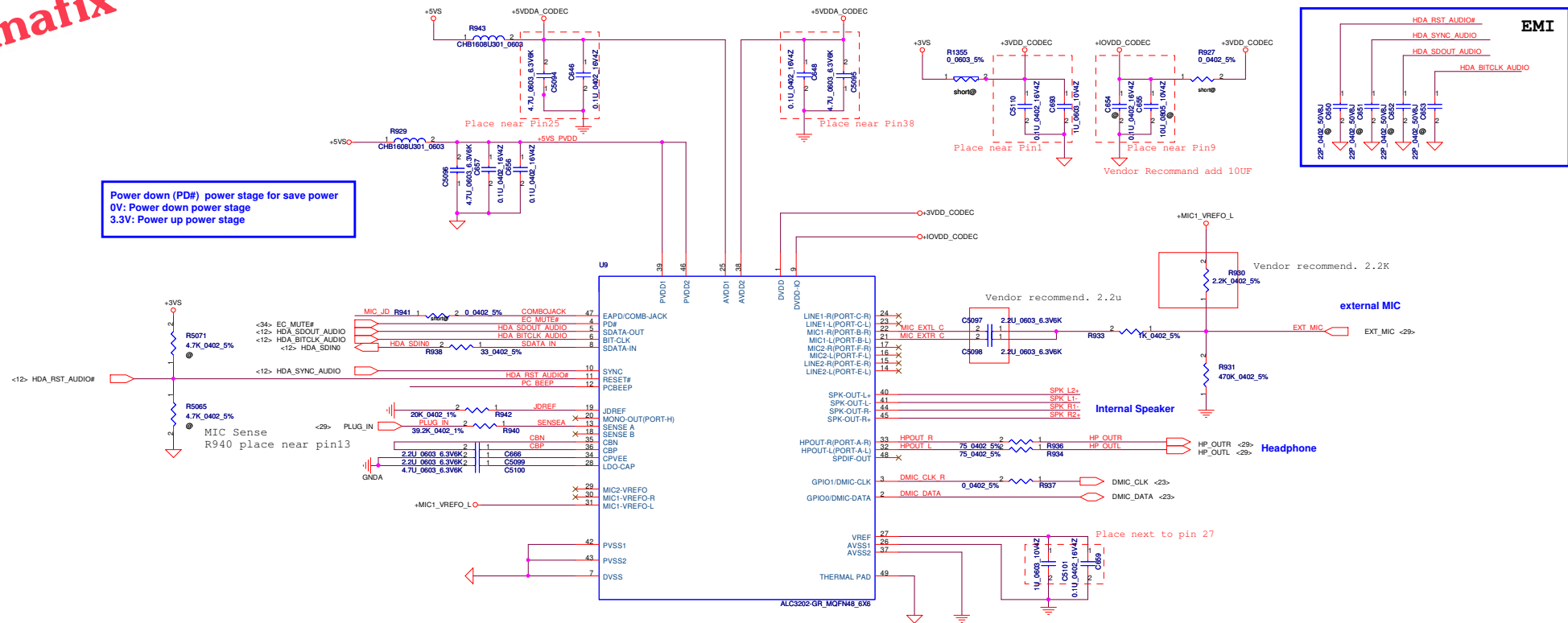
mini DP type



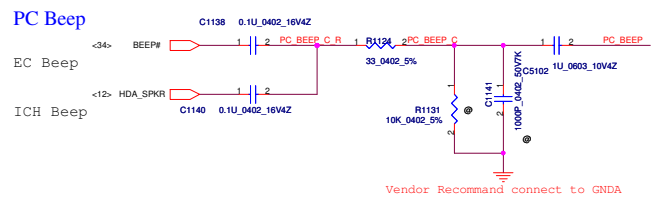
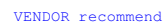




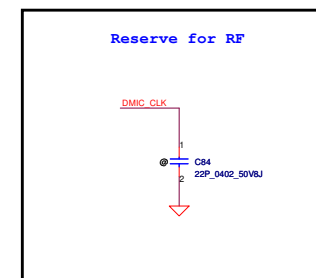
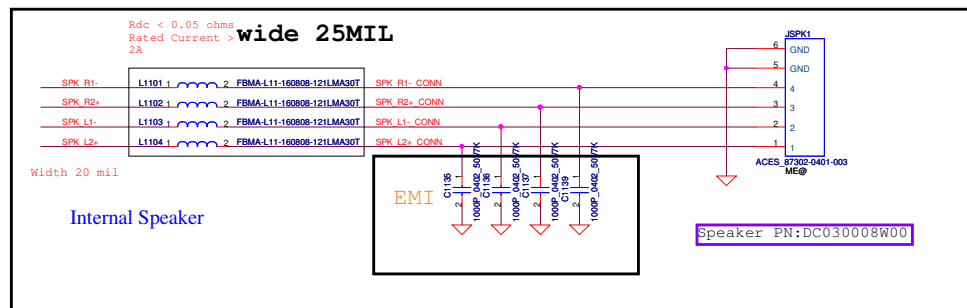
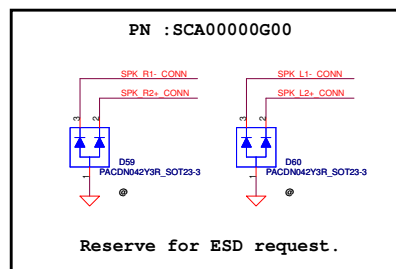
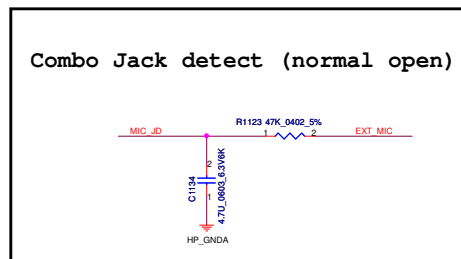
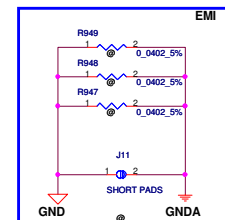




SA000058310  
Change symbol

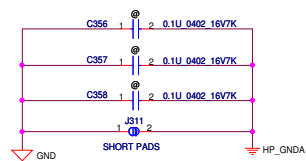
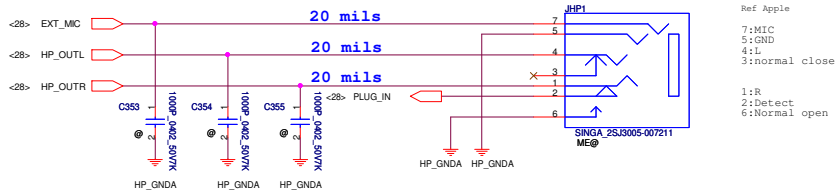


Pin Assignment	Location	Function
SPK-OUT (Pin40/41/44/45)	Internal	Int Speaker
Capless HP-OUT (Pin32/33)	External	Headphone out
MIC1 (Pin21/22)	External	Mic in

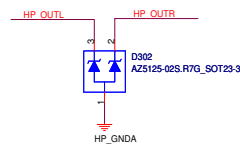


PN:DC230008E00

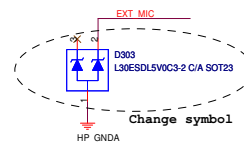
Choose normal-open type!



GND

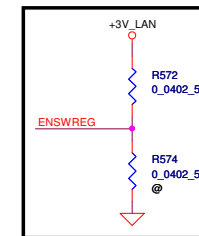
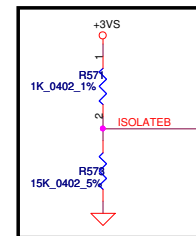
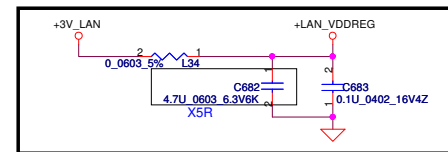
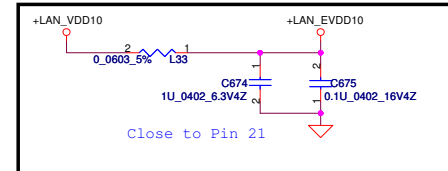
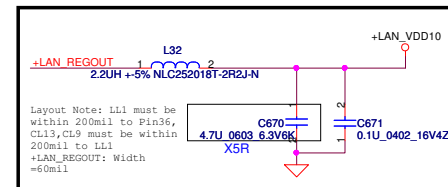
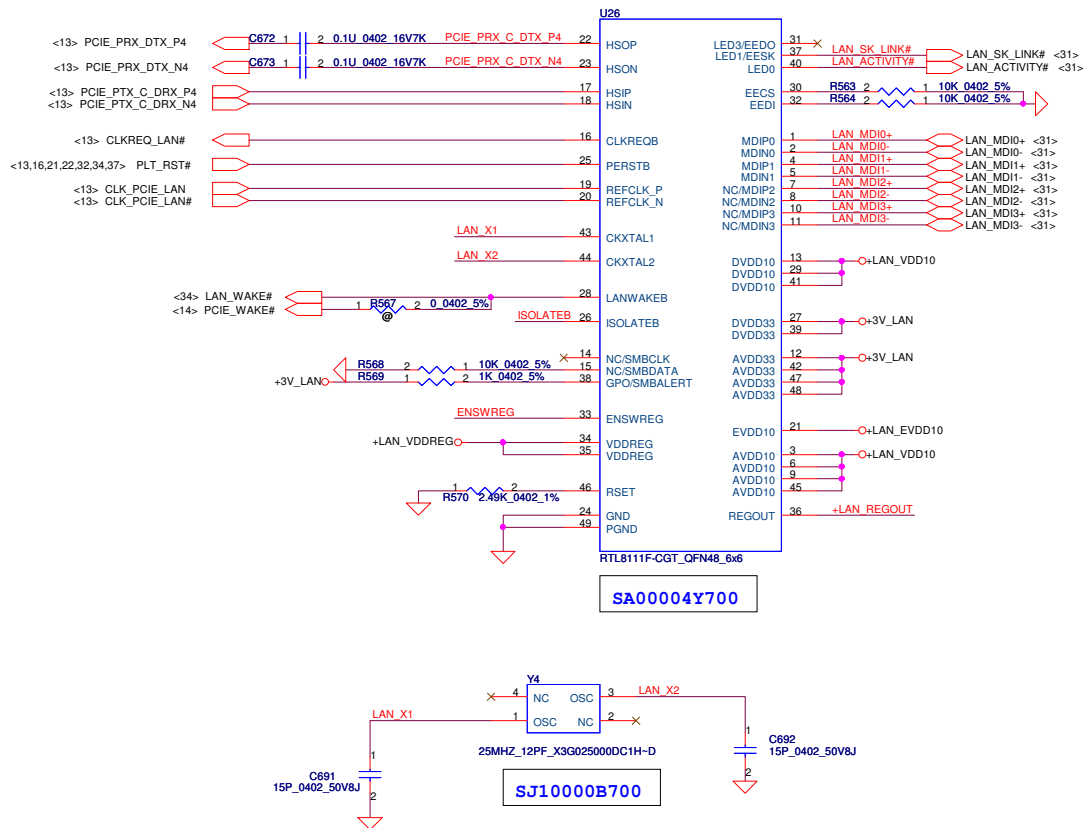


PN: SCA00001I00

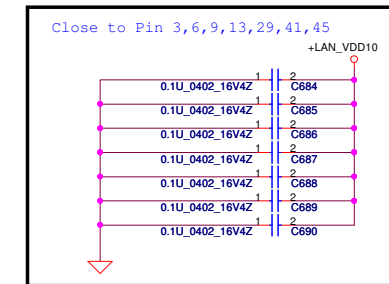
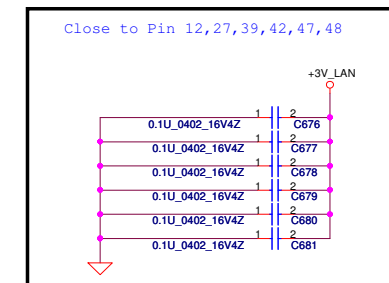
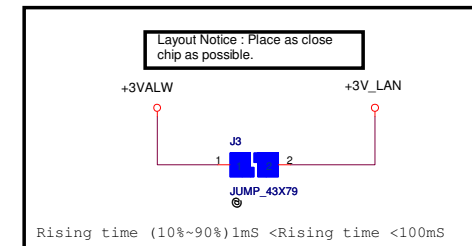


PN: SCA00001L00

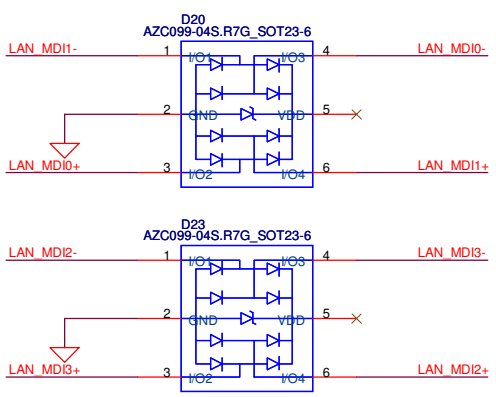
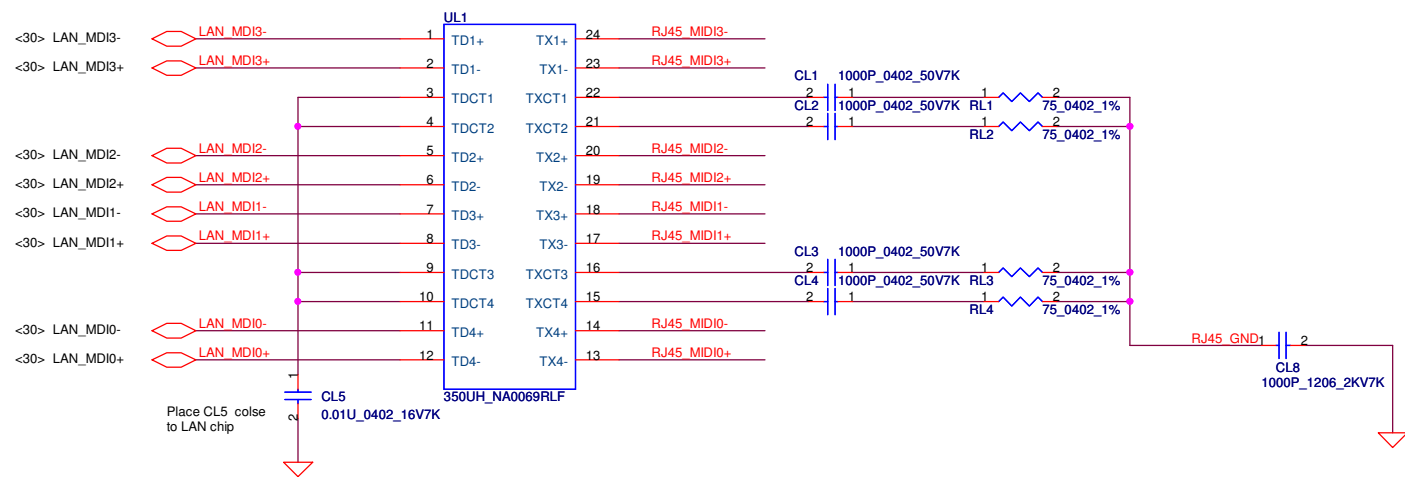




H: Enable internal Regular  
L: Disable



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Size				Document Number				LA-8671P_SDV			
Date				Friday, July 27, 2012				Sheet 30 of 50			



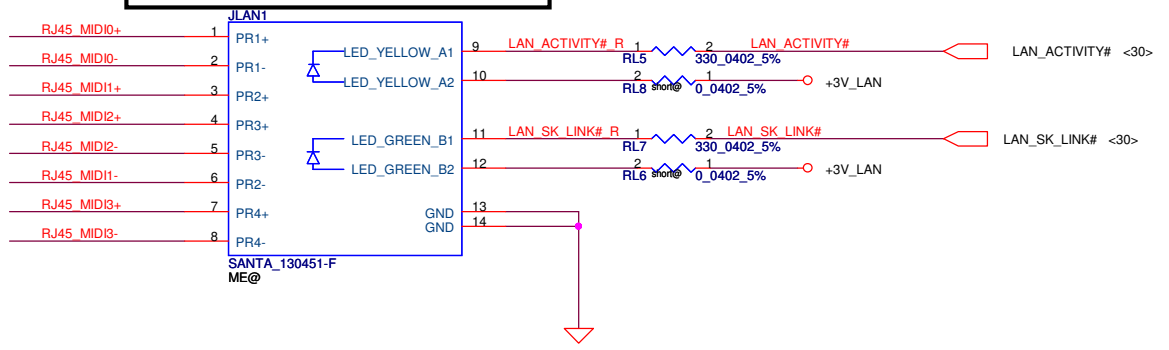
**D20/D23**  
**1'S PN:SC300001G00**  
**2'S PN:SC300002E00**

For EMI's request



**DC234005300**  
**LAN Conn.**

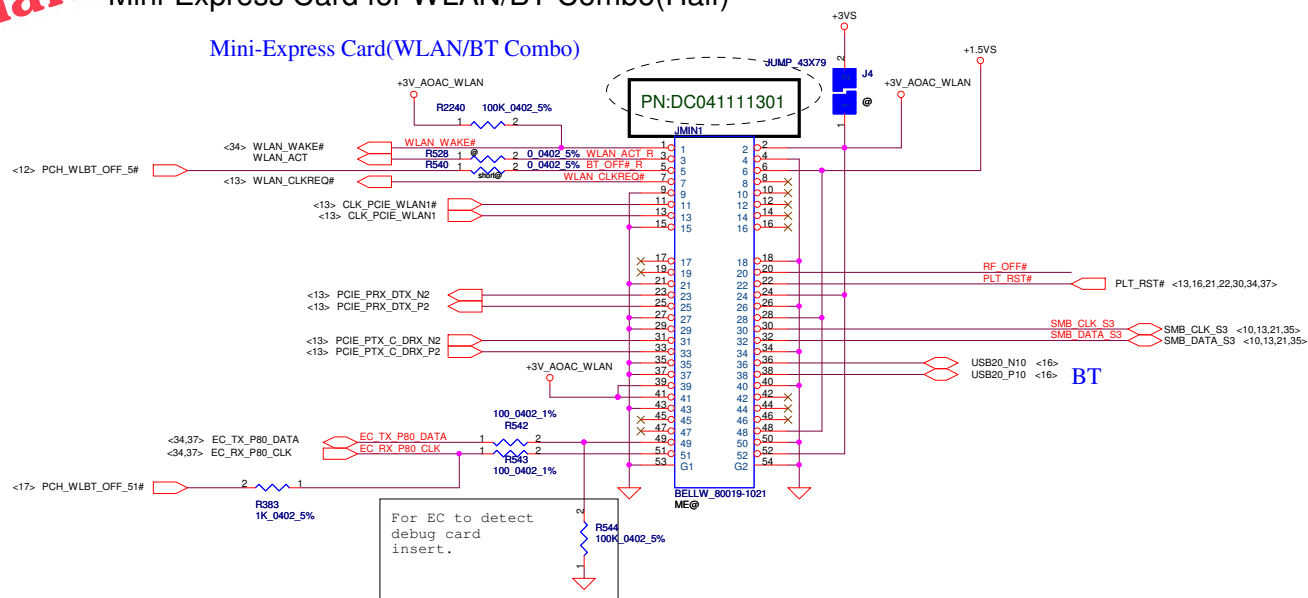
RL5,RL7 Vendor recommend 510 ohm



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				Size	Document Number	Rev
				Custom	LA-8671P_SDV	1.0
Date: Friday, July 27, 2012				Sheet	31	of 50

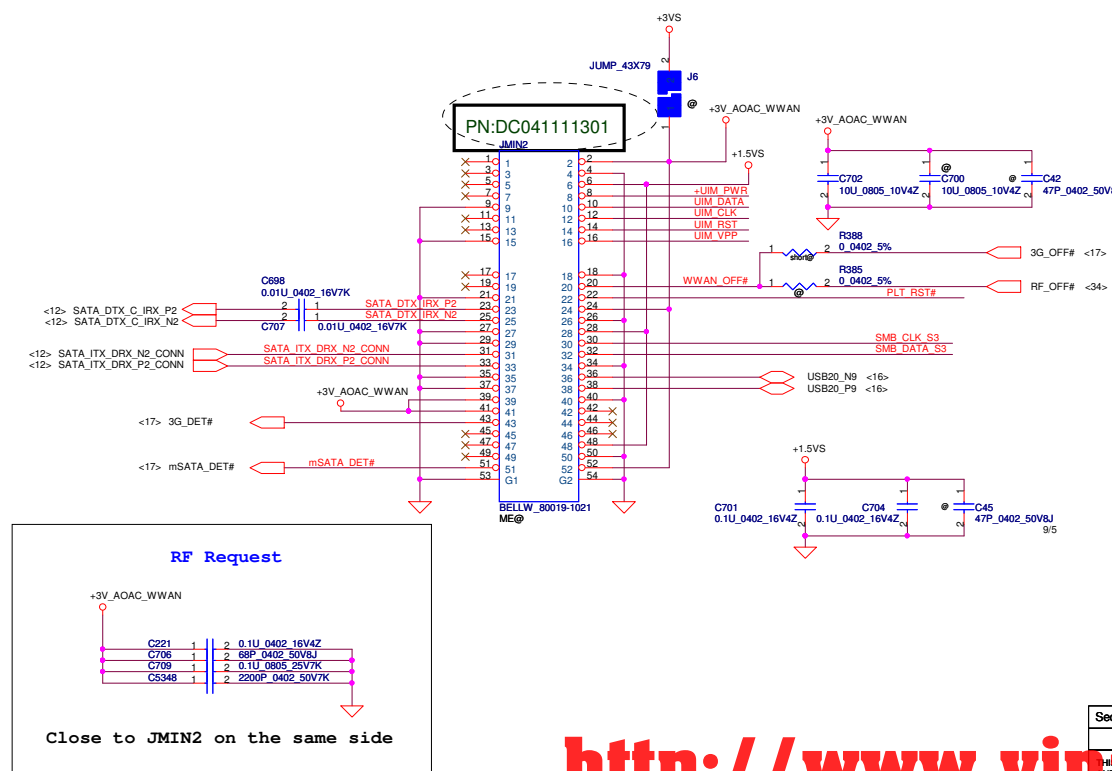
## Mini-Express Card for WLAN/BT Combo(Half)

### Mini-Express Card(WLAN/BT Combo)

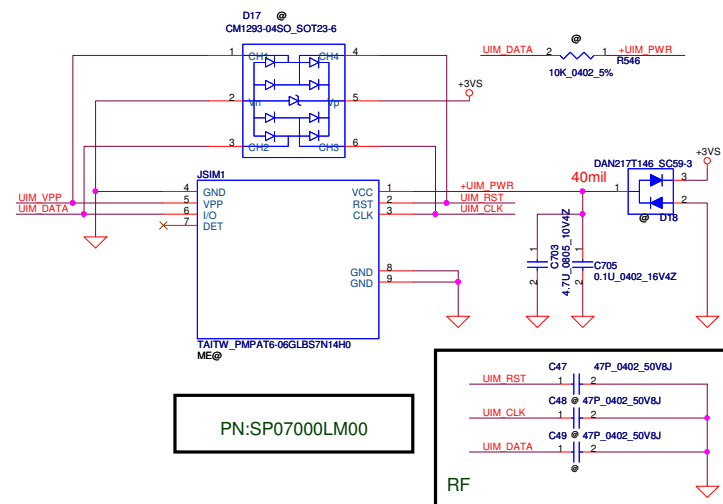
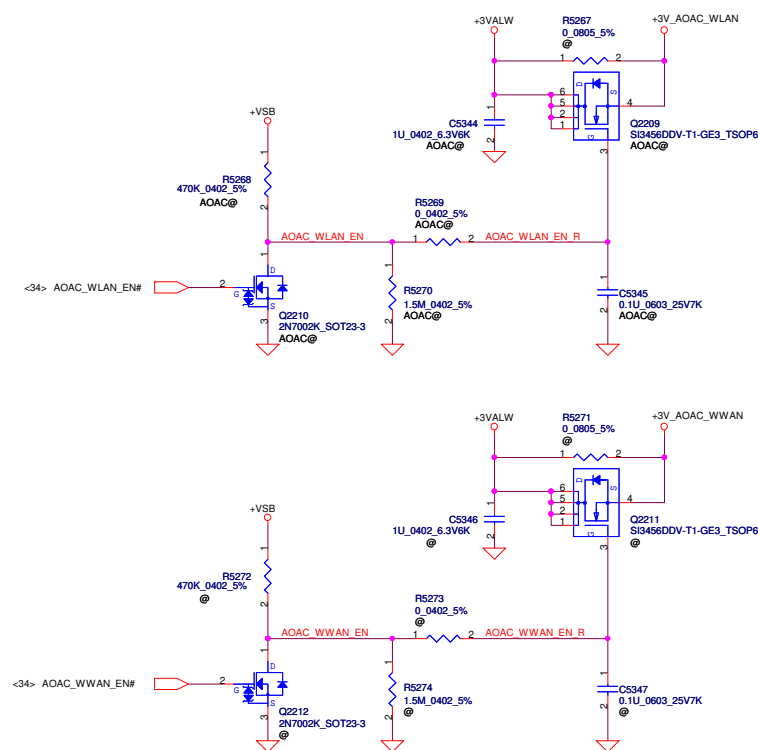


## Mini-Express Card for WWAN/mSATA(Full)

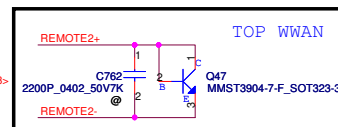
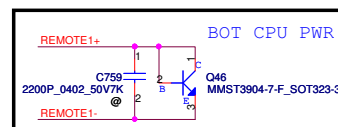
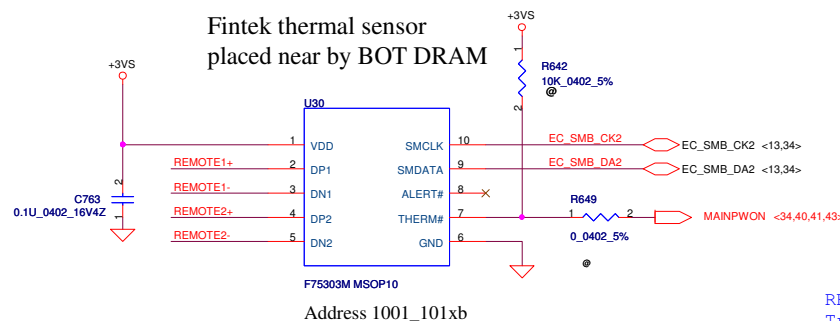
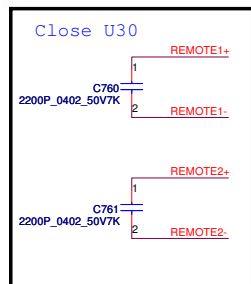
### Mini-Express Card(WWAN 3G)



## AOAC

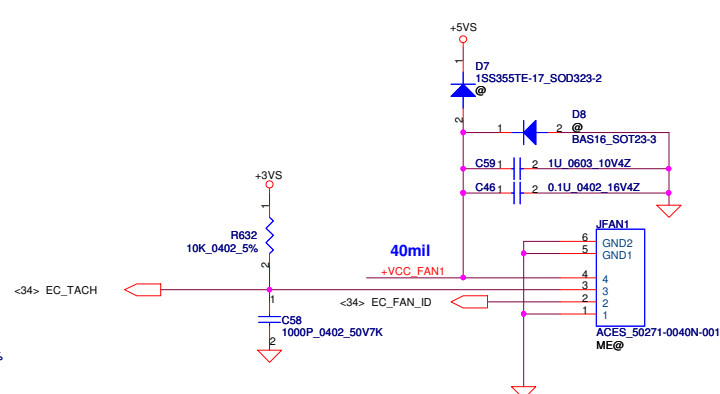
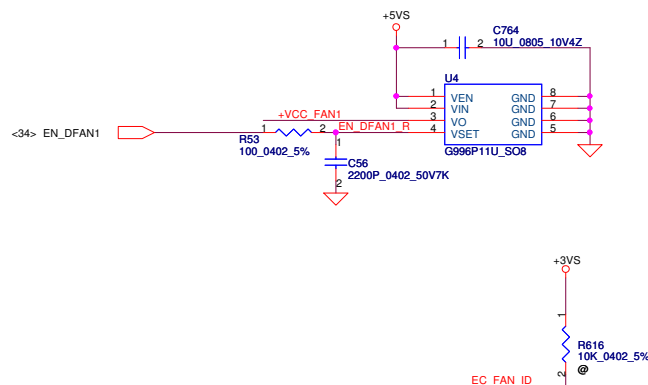


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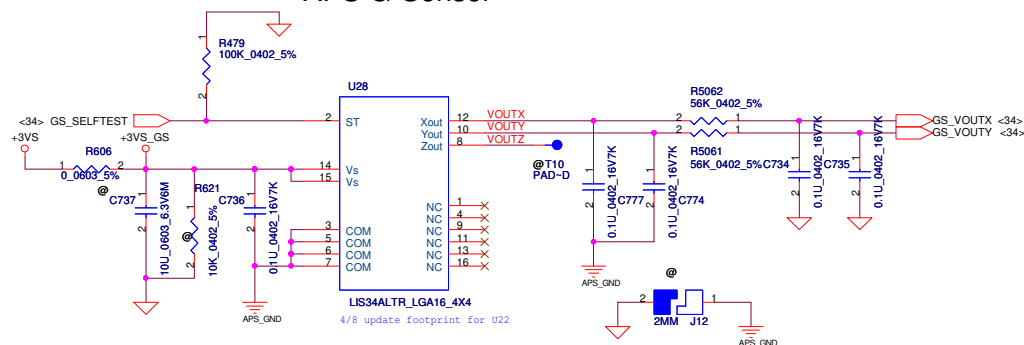
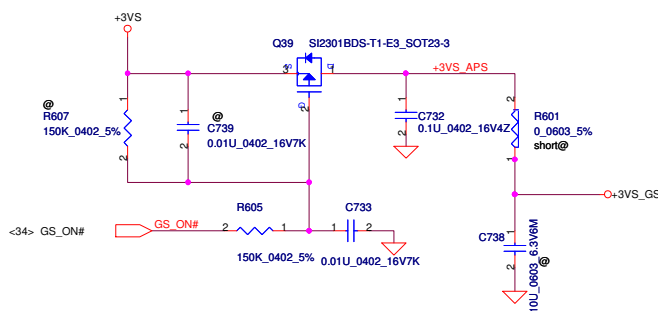
REMOTE1,2+/-:  
Trace width/space:10/10 mil  
Trace length:<8"

## FAN +5VS DROOP



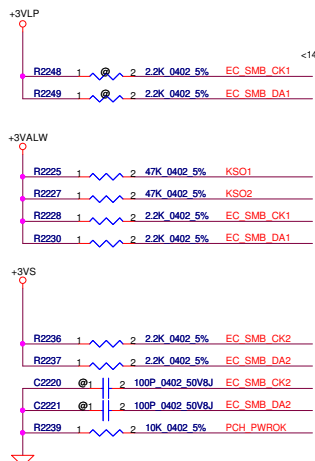
**SP02000TS00**

## APS G-Sensor

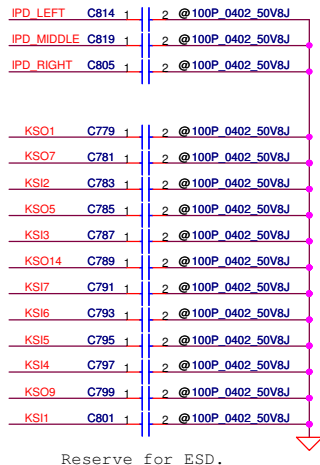
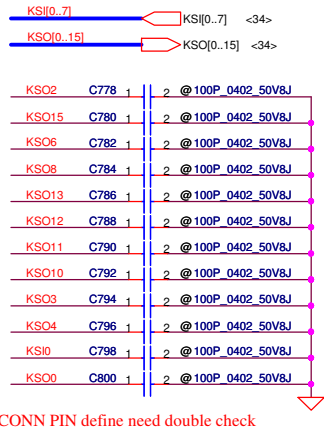


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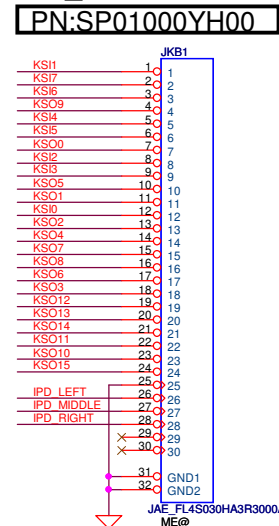




### INT\_KBD Conn.

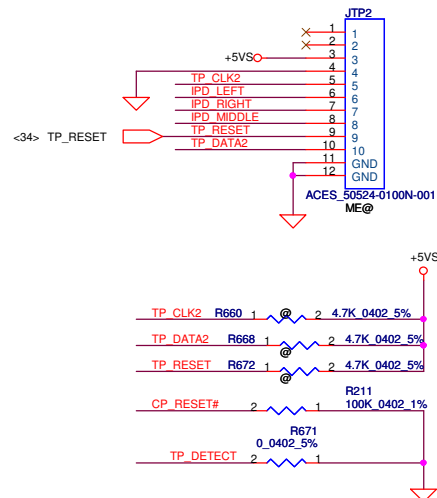


### INT\_KBD Conn.



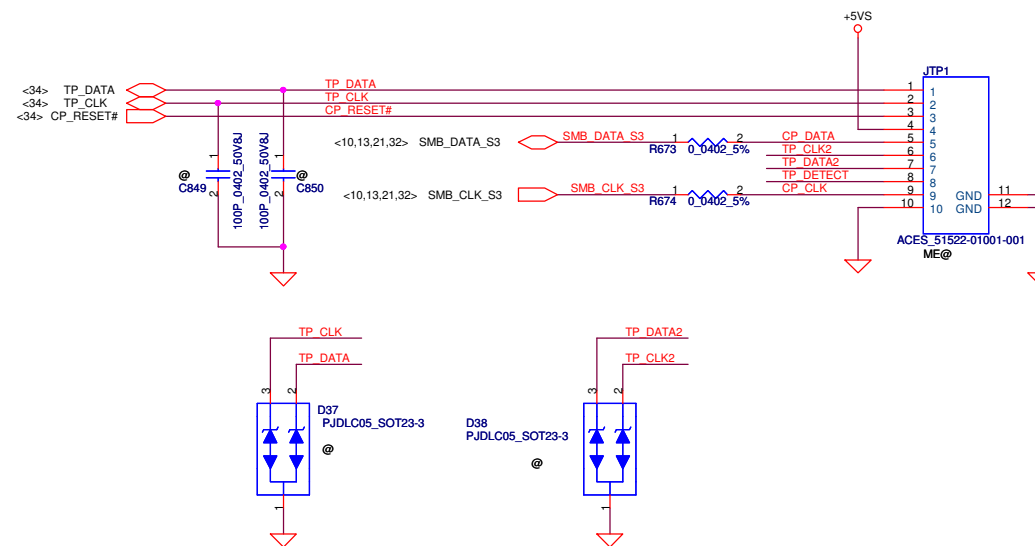
### Track point

PN:SP01001CH00



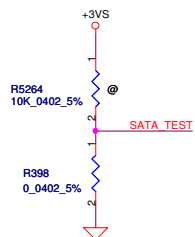
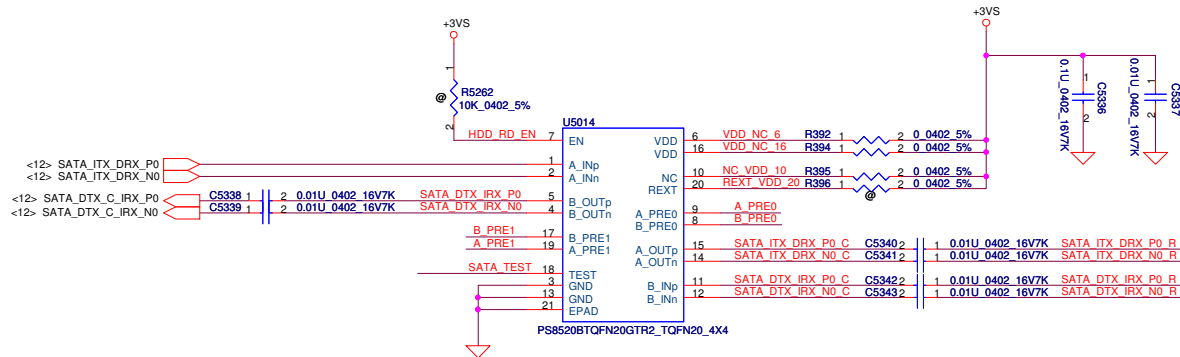
### Click pad 10PIN

SP01001AL00

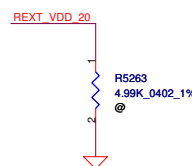


PN: SCA00000U10 X 2

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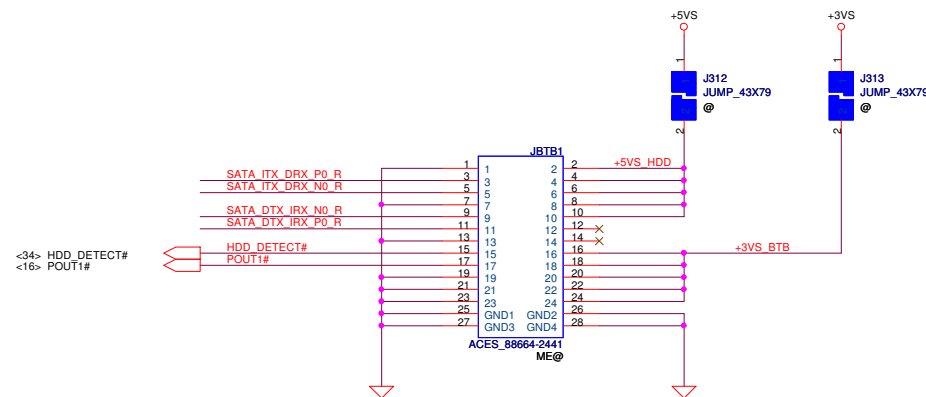
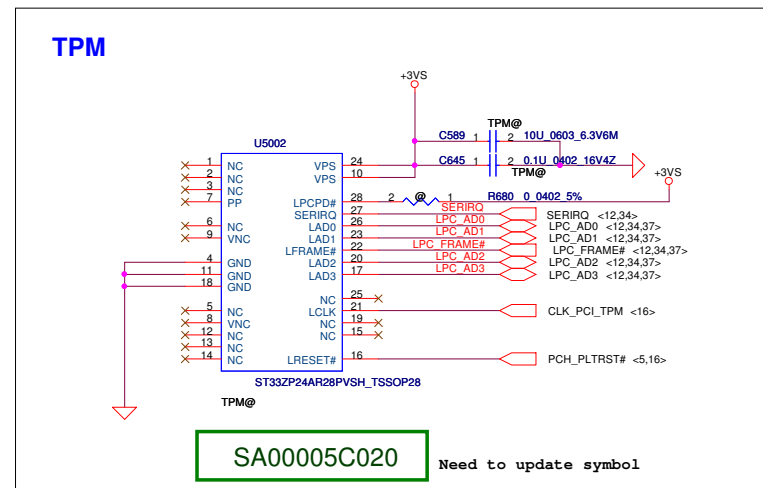


	TEST (Internal Low)
Normal Mode (Default)	LOW
Test Mode	HIGH

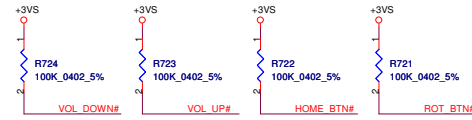
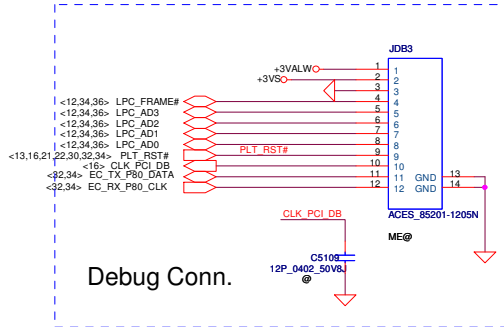
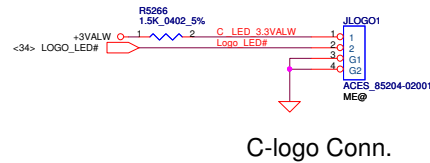
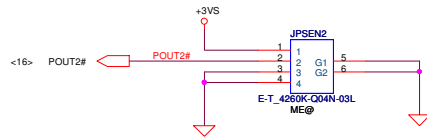
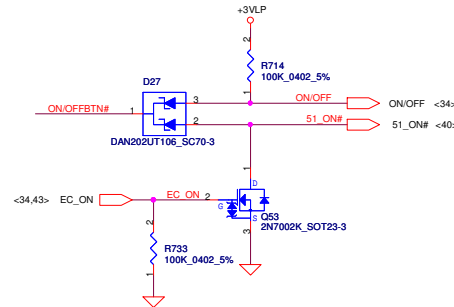
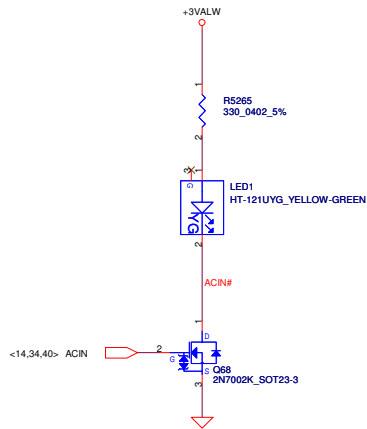


Remove pull-low

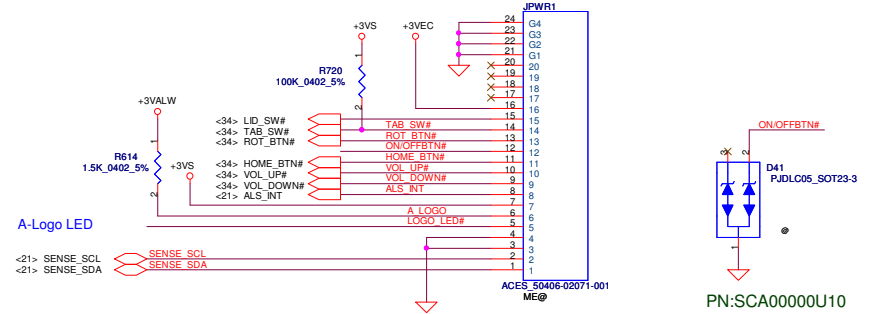
	A_PRE1/B_PRE1 (Internal pull Low)	A_PRE0/B_PRE0 (Internal pull Low)
0dB, no pre-emphasis	Low	Low
1.5dB pre-emphasis is selected	Low	High
2.5dB pre-emphasis is selected	High	Low
3.5dB pre-emphasis is selected	High	High



## AC-IN LED (Color : Green)

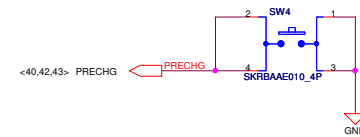


SP010016L00



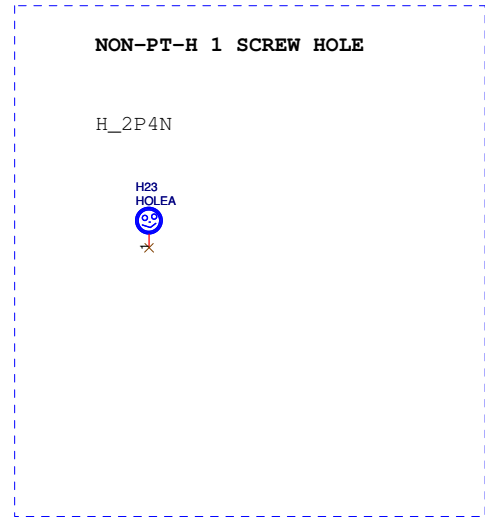
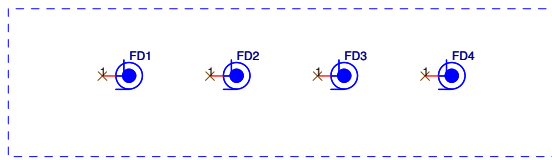
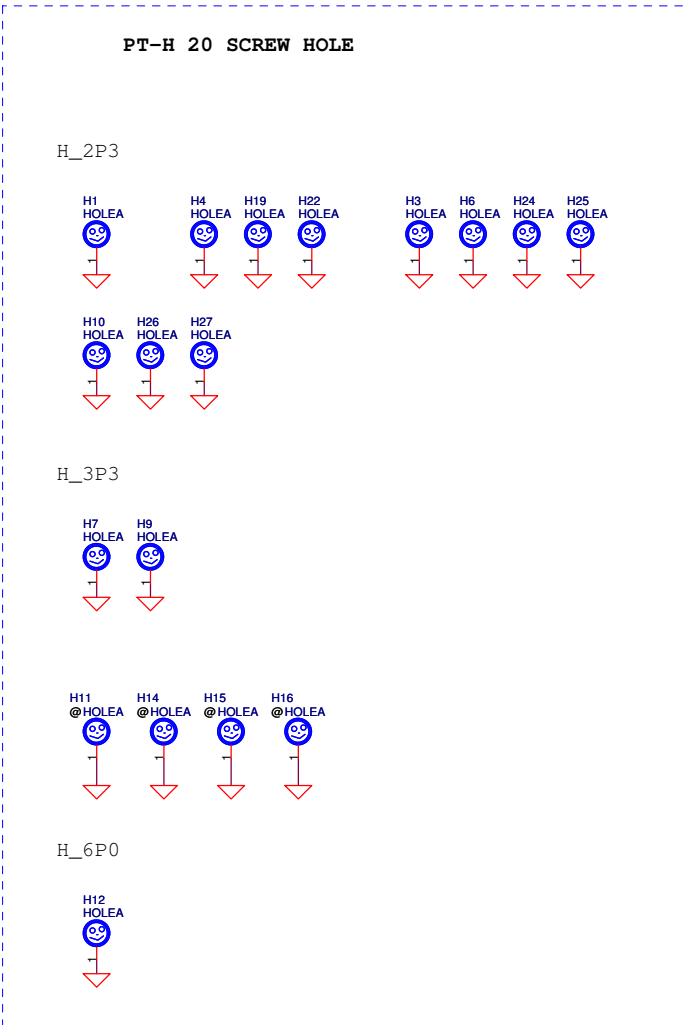
## Power Board connector

BOTTOM SIDE  
SN111005800 by ME drawing



For Power Reset

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Size	Document Number	Rev		
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**Vinafix**

**+5VALW TO +5VS**

**+3VALW TO +3VS**

**+1.5V to +1.5VS**

**+1.8VS**

**+1.5V**

**+1.05VS**

**+0.75VS**

**+RTCVCC**

**+5VALW**

**+5VALW**

**For Intel S3 Power Reduction.**

**Security Classification**

**Compal Secret Data**

**Compal Electronics, Inc.**

**DC Interface**

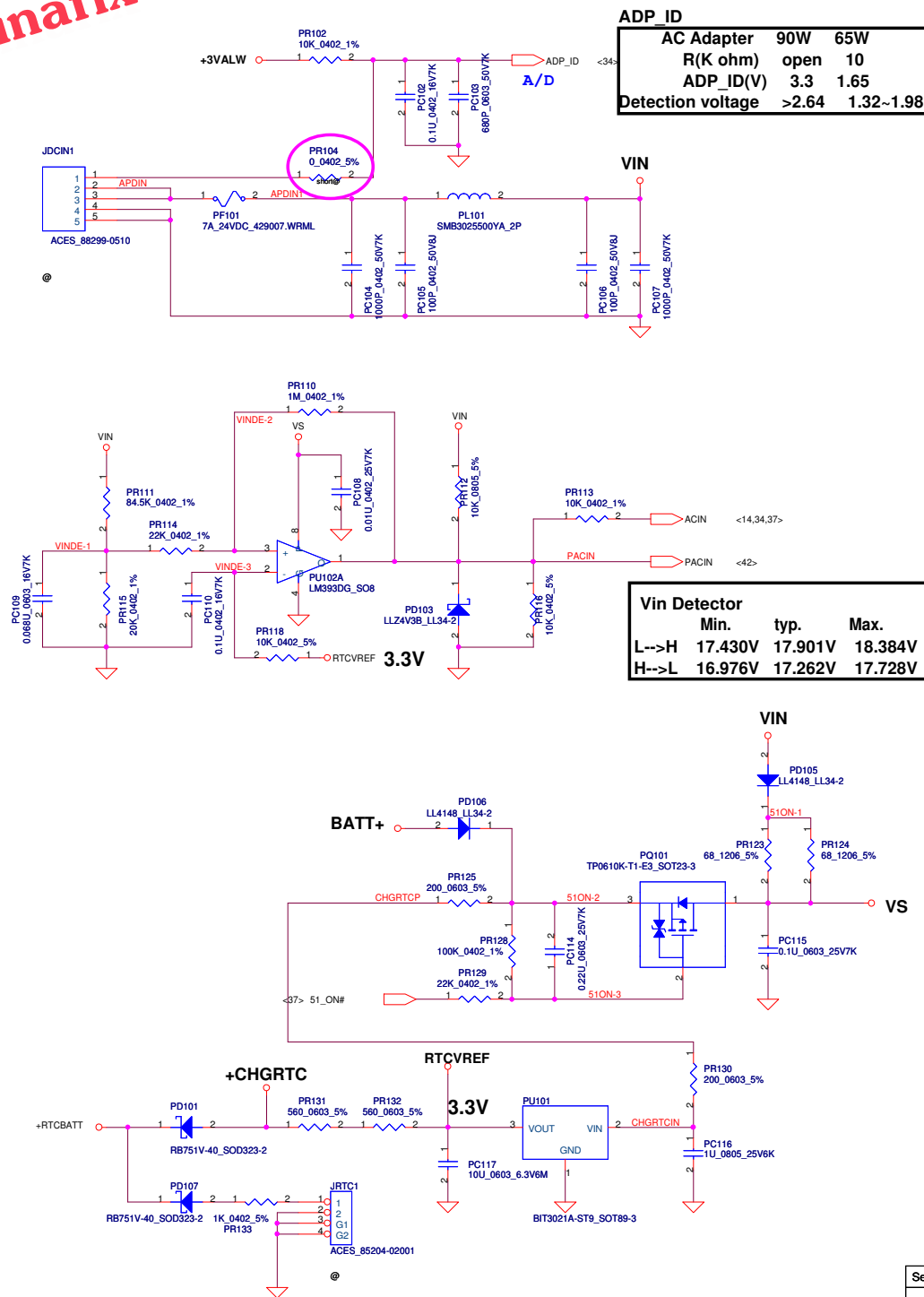
**LA-8671P\_SDV**

**Rev 1.0**

**http://www.vinafix.com**

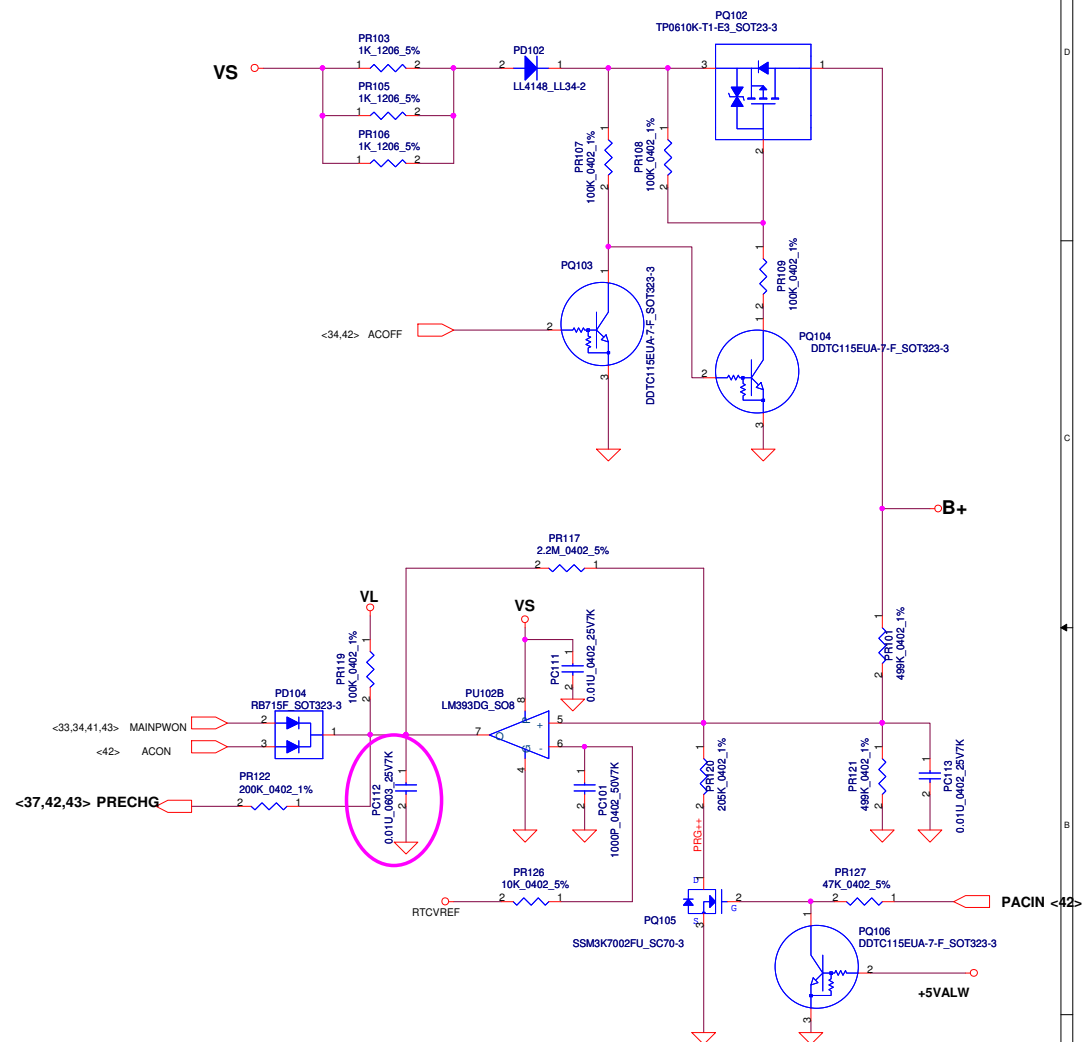


**Vinafix**



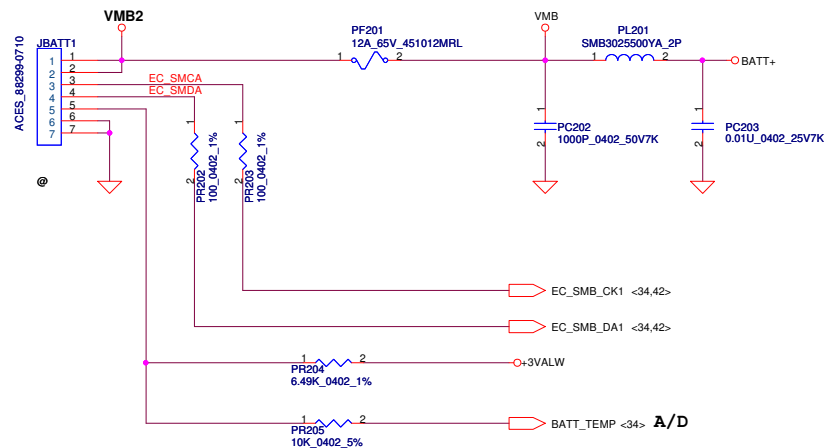
ADP_ID	AC Adapter	90W	65W
R(K ohm)	open	10	
ADP_ID(V)	3.3	1.65	
Detection voltage	>2.64	1.32~1.98	

Vin Detector			
	Min.	typ.	Max.
L-->H	17.430V	17.901V	18.384V
H-->L	16.976V	17.262V	17.728V



	Precharge detector		
	Min.	typ.	Max.
L-->H	14.991V	15.381V	15.782V
H-->L	13.860V	14.247V	14.621V

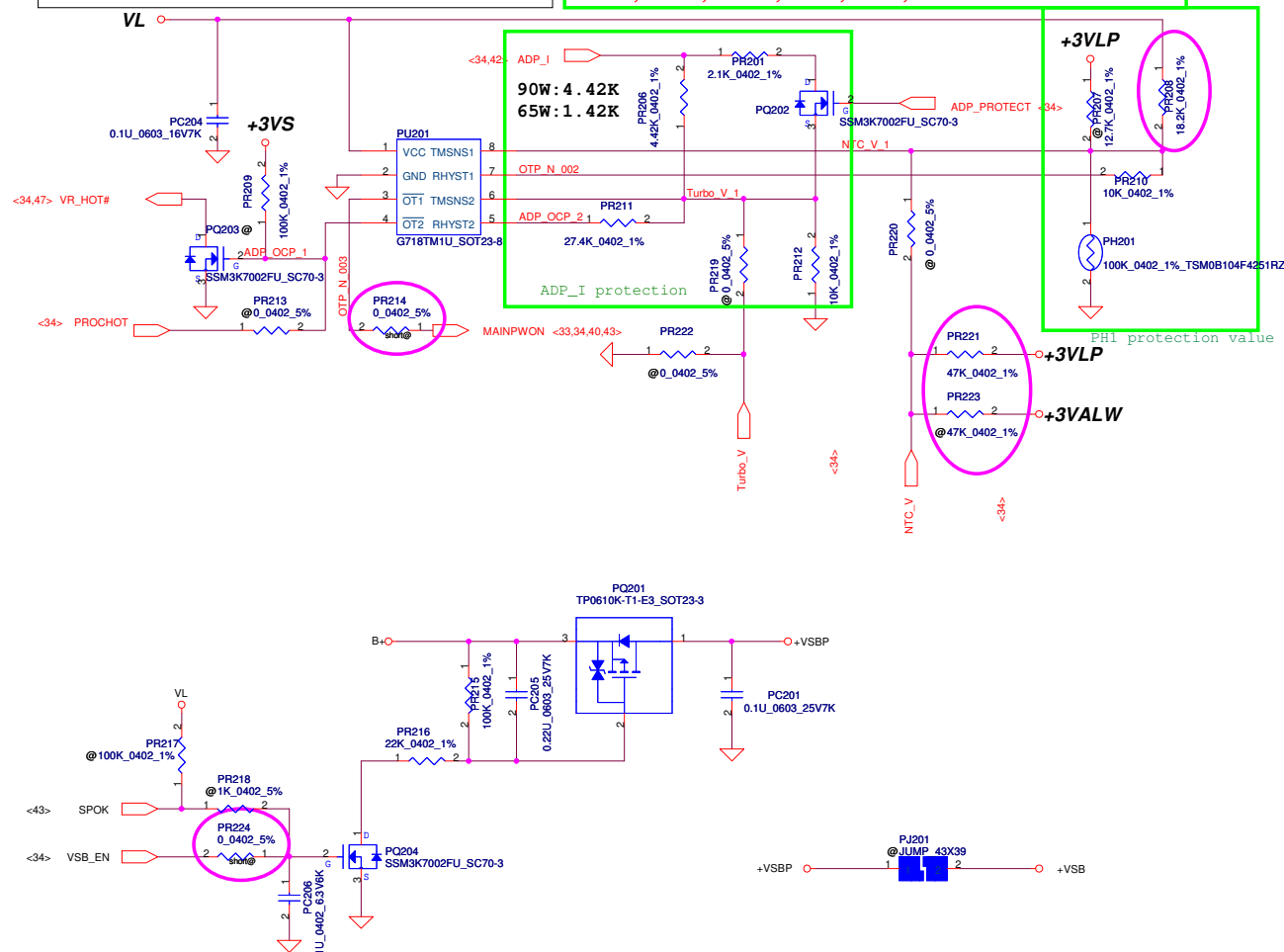
BATT ONLY			
	Precharge detector		
	Min.	typ.	Max.
L-->H	7.196V	7.349V	7.505V
H-->L	6.138V	6.214V	6.056V



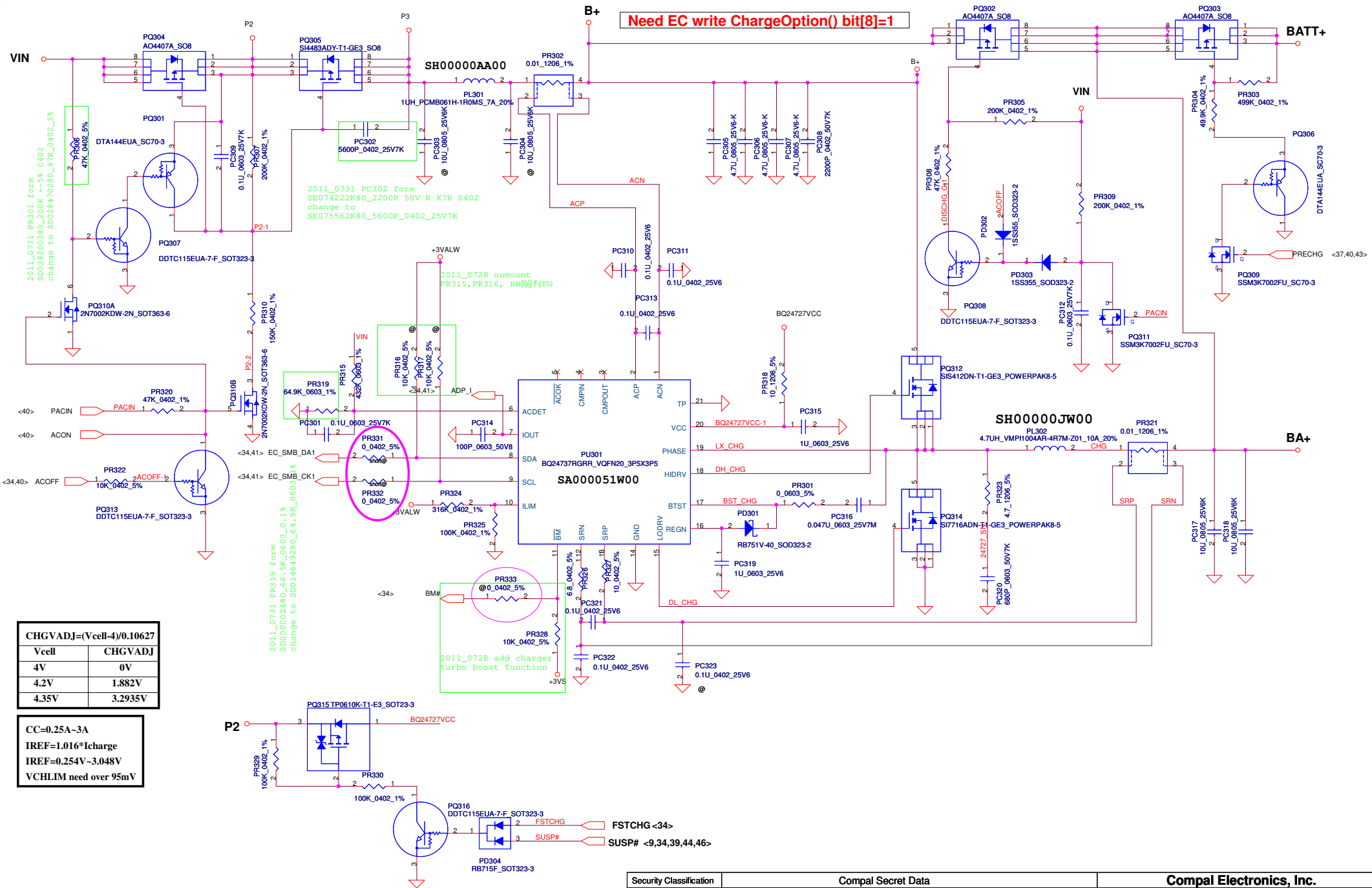
**PH1 under CPU botten side :**  
**CPU thermal protection at 93 +-3 degree C**  
**Recovery at 56 +-3 degree C**

**For KB930 --> Keep PU201 circuit  
( $V_{th} = 1.25V$ )**

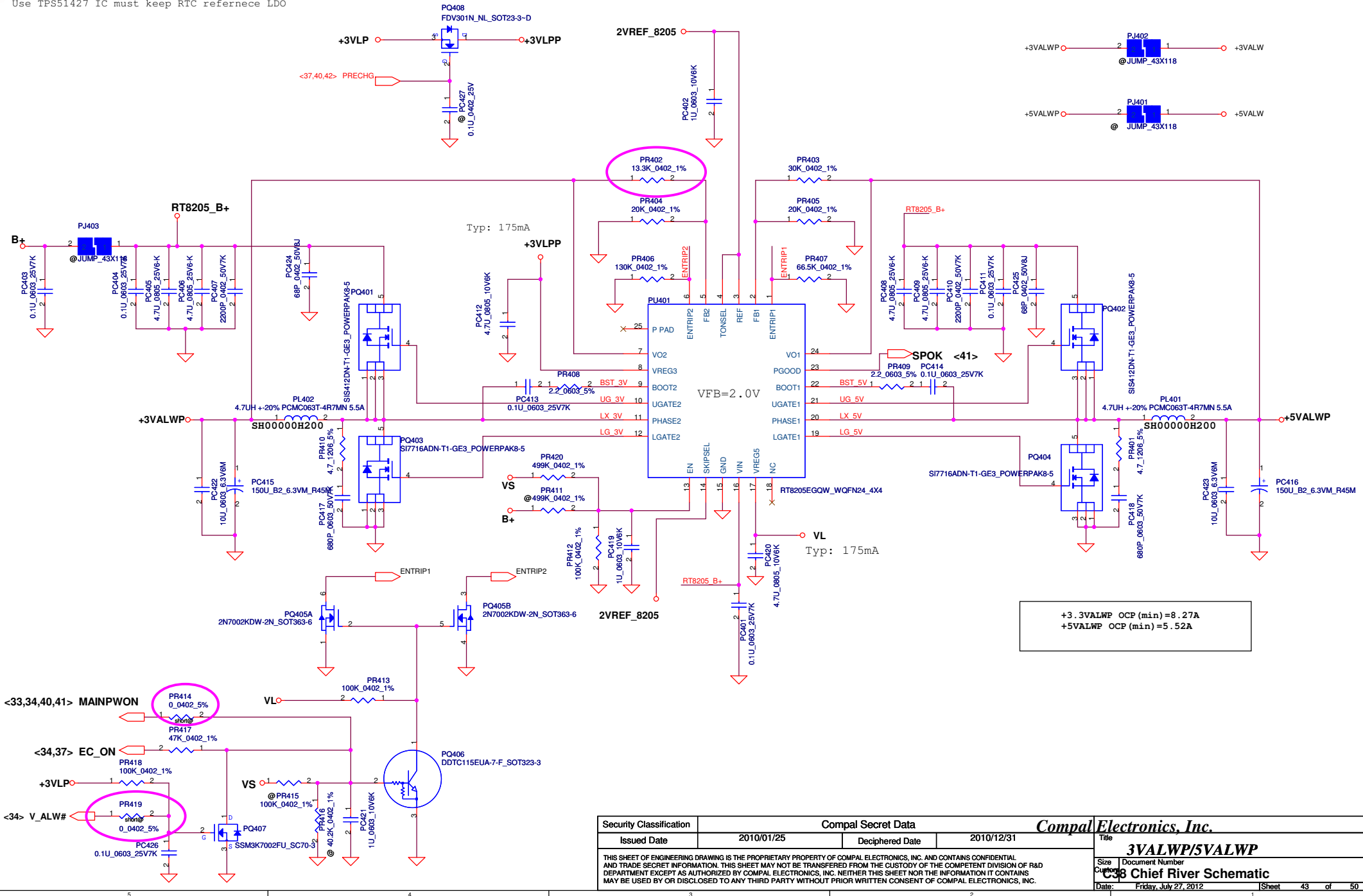
**For KB9012 (Red square) --> Remove PU201 circuit, but keep PR206  
PH201, PR205, PR211,PQ201,PR208,PR212**



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Notes:  
Use TPS51125 IC can remove RTC reference LDO  
Use TPS51427 IC must keep RTC reference LDO



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				Customer	C38 Chief River Schematic
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				Sheet	43 of 50
				Rev	0.4

<34,39> SYSON

2011\_0801 JP504 form  
43x118 change to 43x79

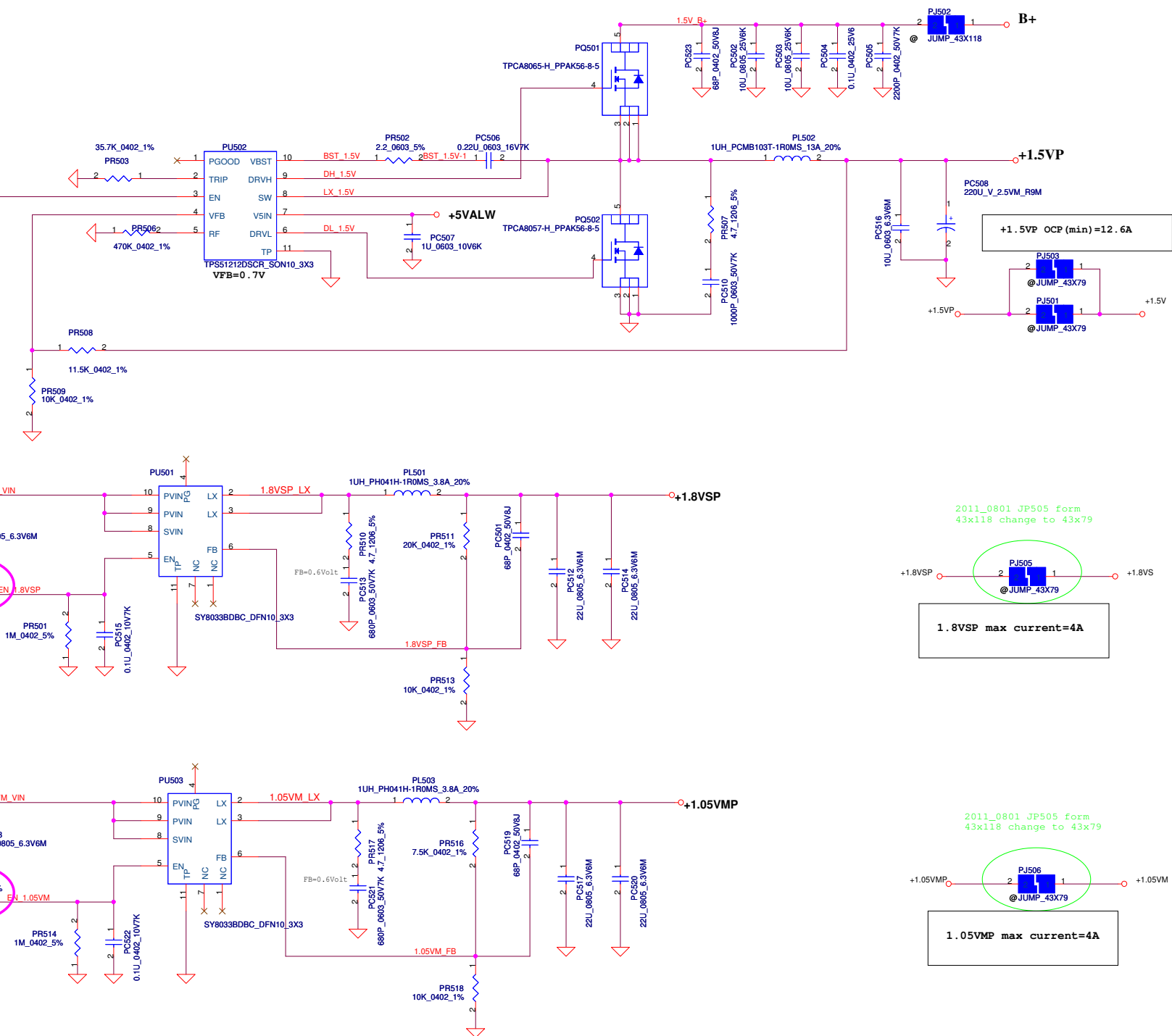
+5VALW

<9,34,39,42,46> SUSP#

2011\_0801 JP504 form  
43x118 change to 43x79

+5VALW

<19,34> M\_PWR\_ON



2011\_0801 JP505 form  
43x118 change to 43x79

+1.8VSP

1.8VSP max current=4A

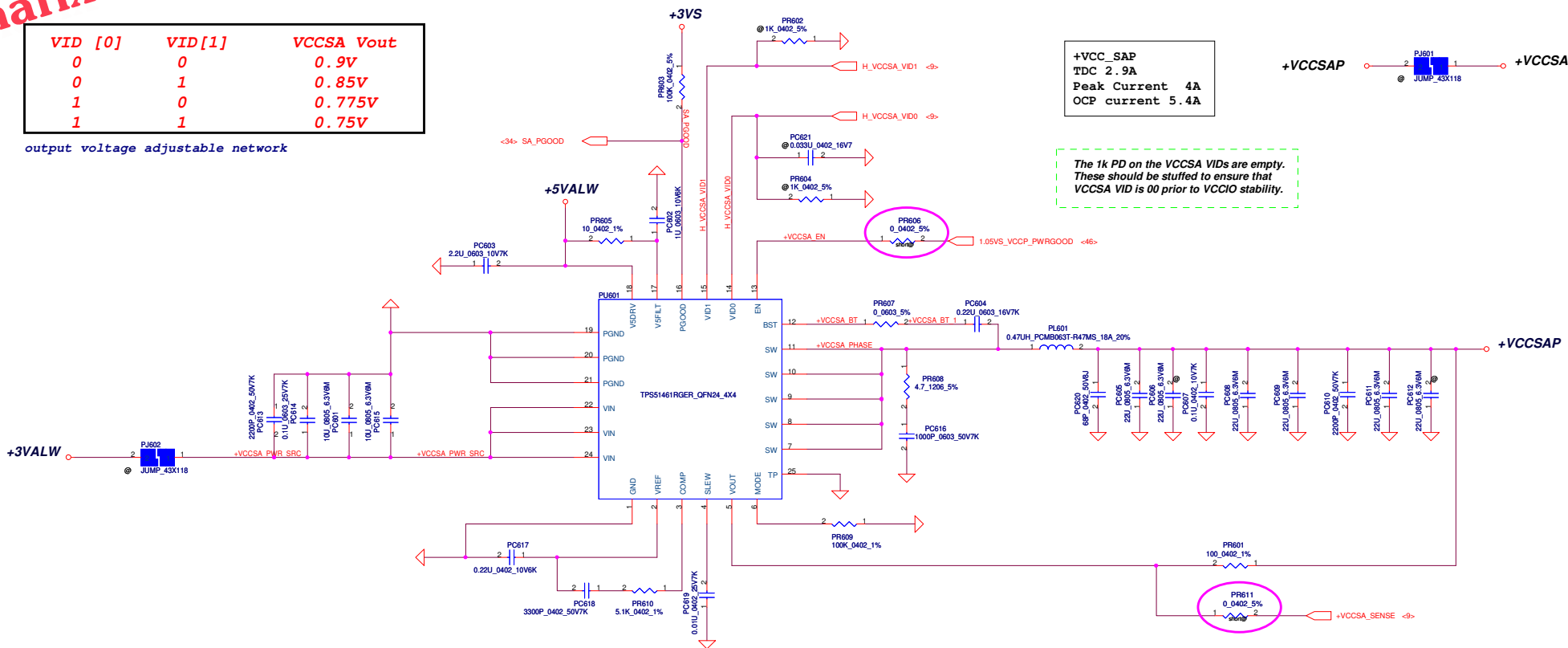
2011\_0801 JP505 form  
43x118 change to 43x79

+1.05VMP

1.05VMP max current=4A

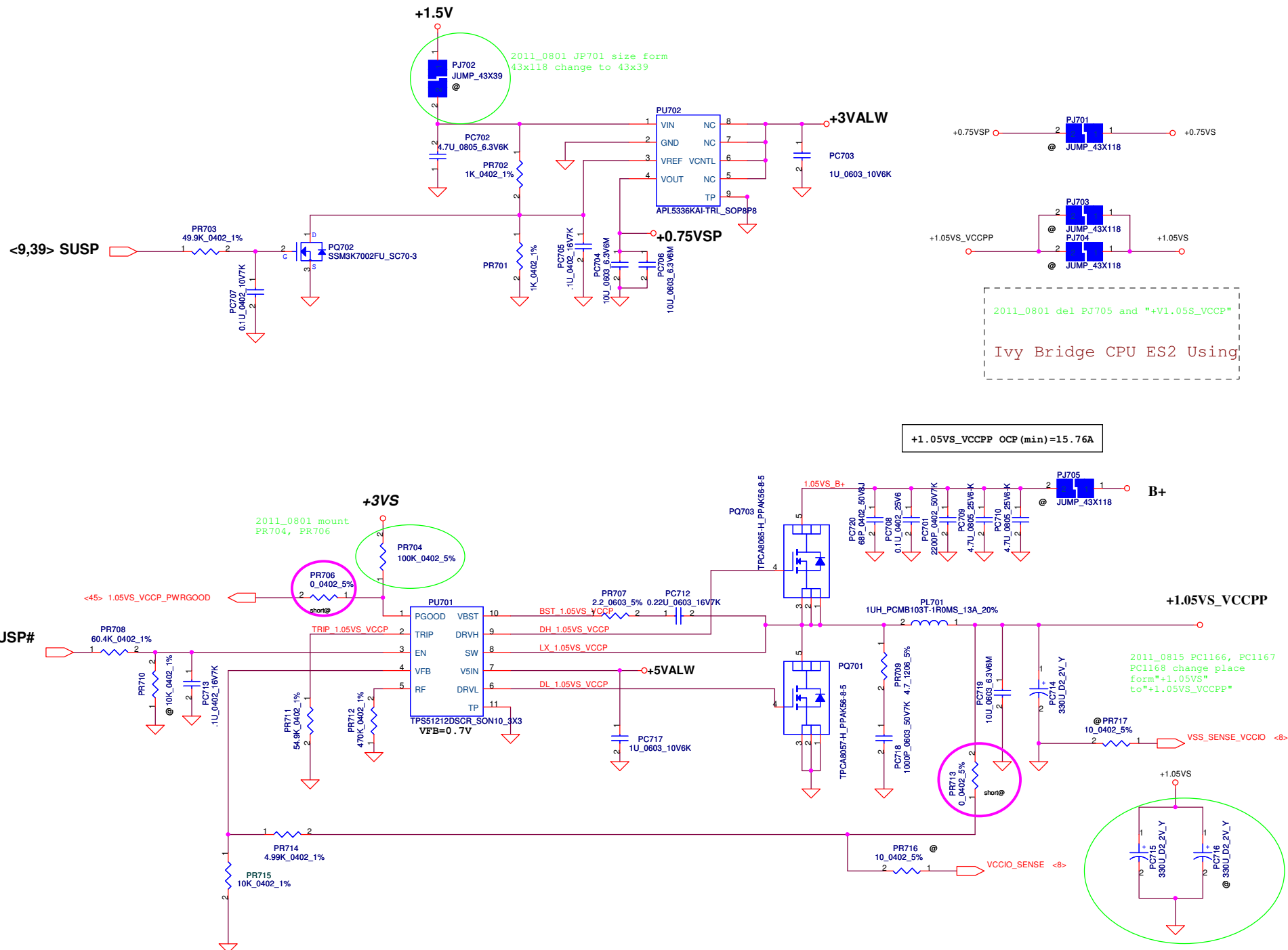
VID [0]	VID[1]	VCCSA Vout
0	0	0.9V
0	1	0.85V
1	0	0.775V
1	1	0.75V

output voltage adjustable network

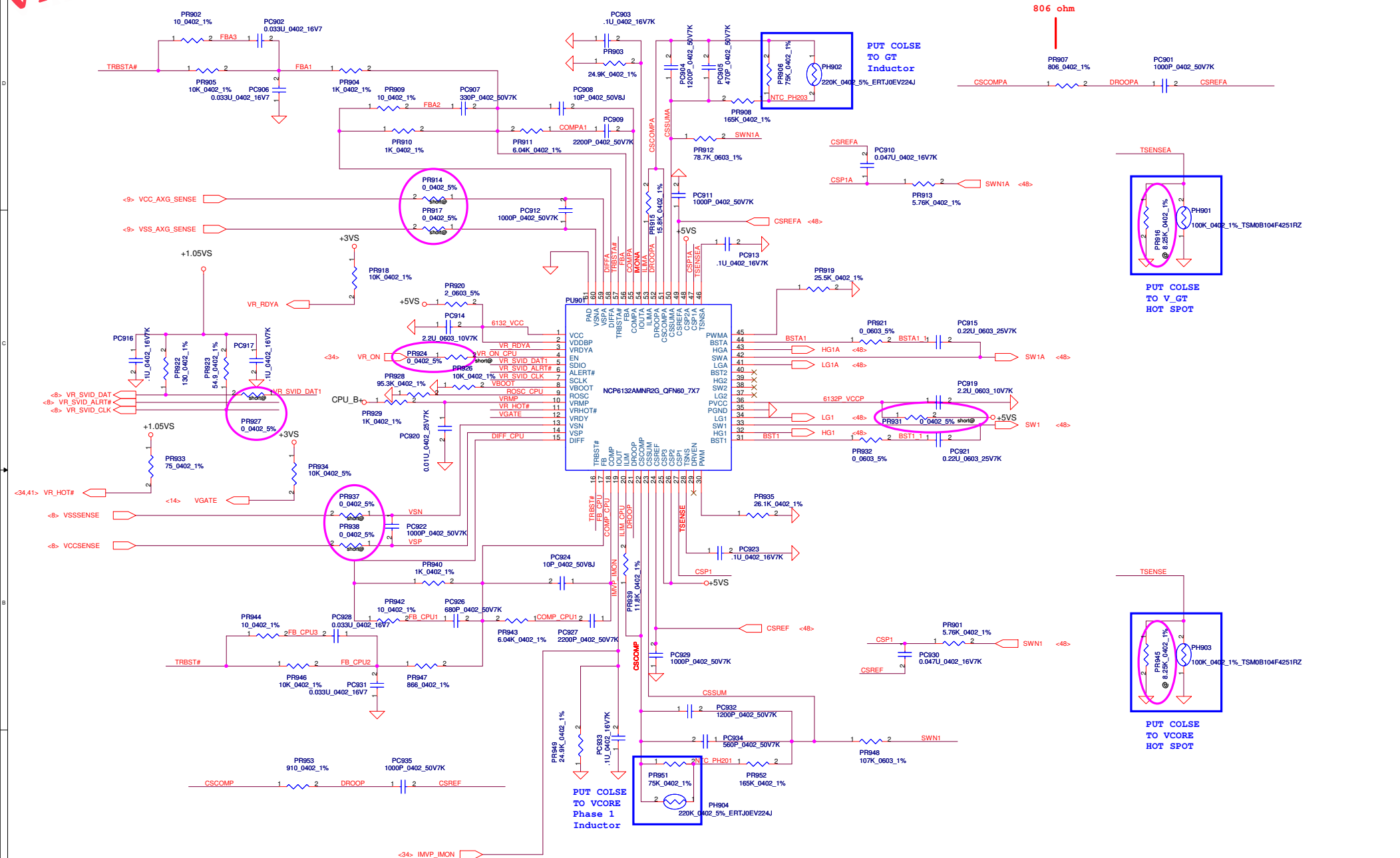


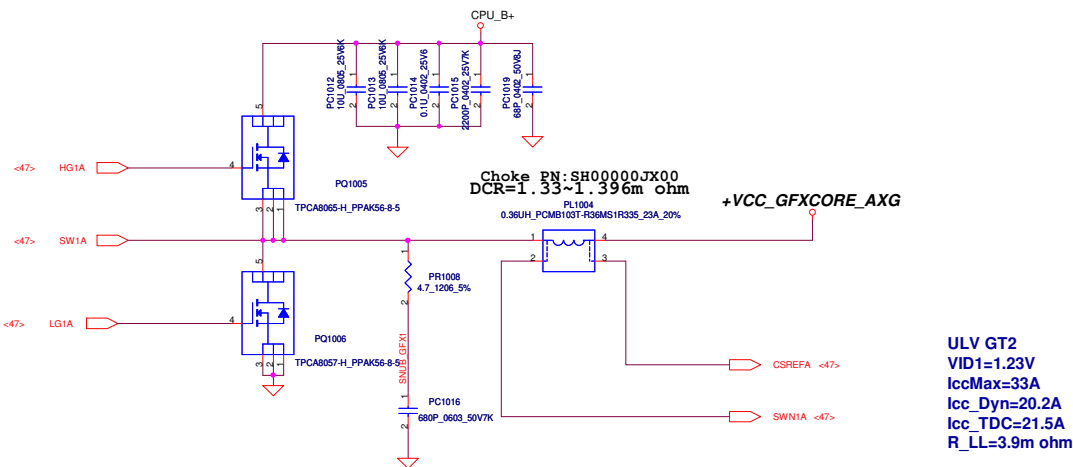
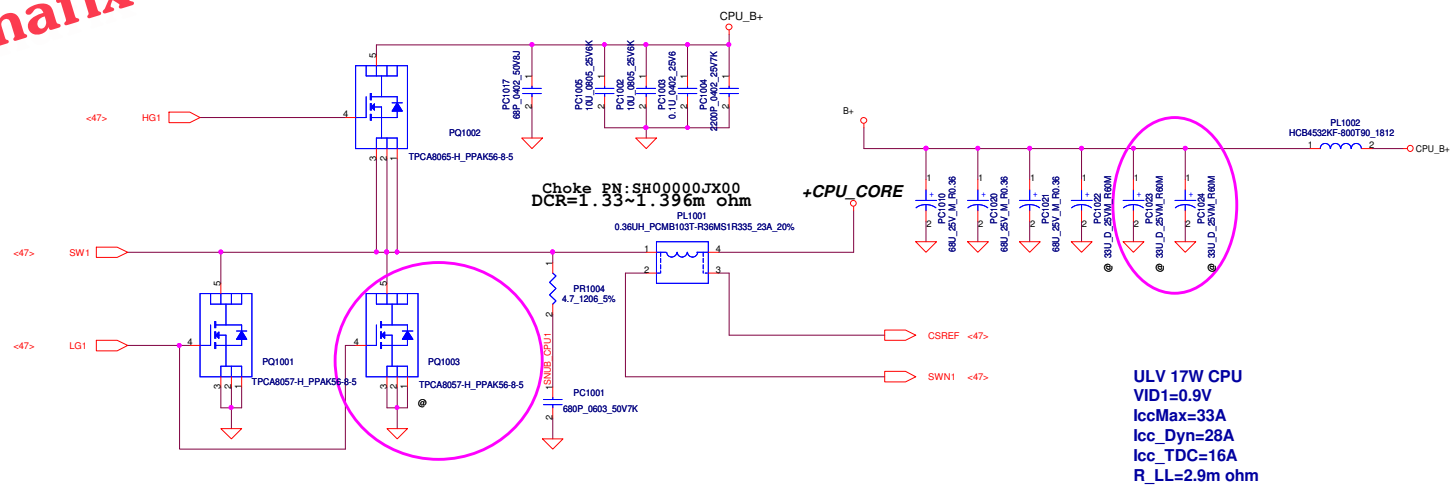
2011\_0801 del +V1.05S\_VCCPP circuit



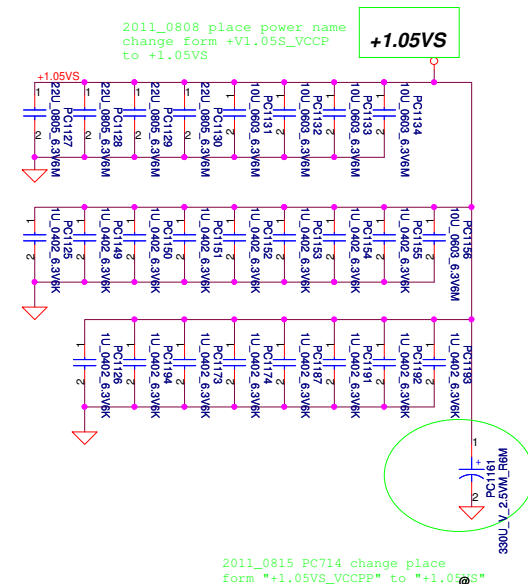
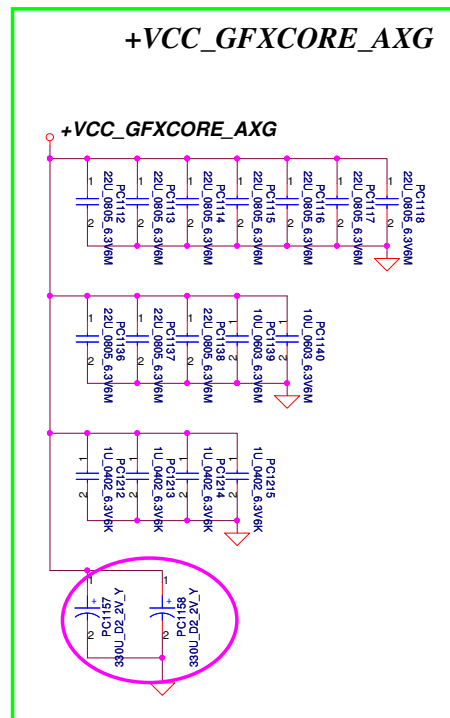
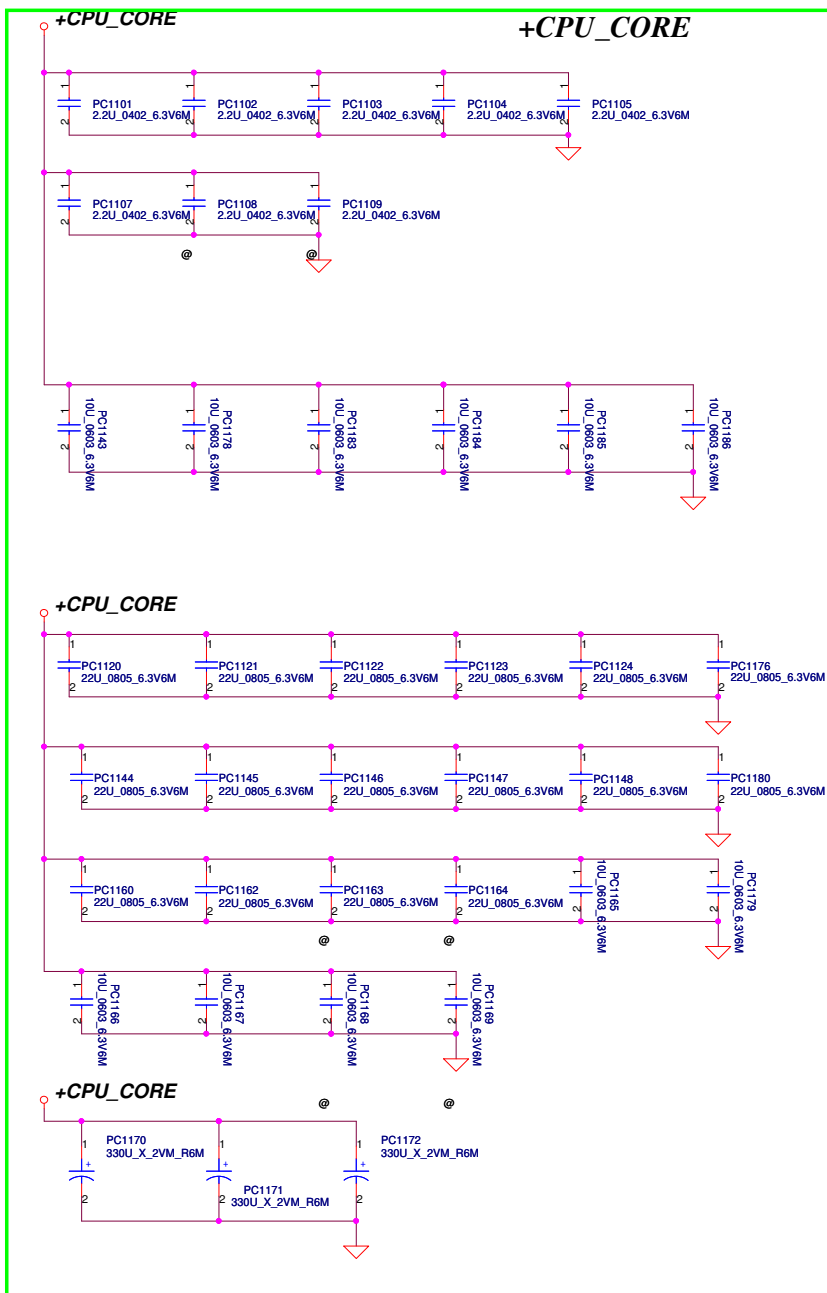


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Issued Date	2010/01/25	Deciphered Date	2010/12/31	Title	PWR +1.05VS VCCPP/+0.75VSP
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				PWR-CPU CORE	
Size	Custom	Document Number	C38 Chief River Schematic	Rev	0.4
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Security Classification		Compal Secret Data		Title	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	PWR - PROCESSOR DECOUPLING	
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				LA-8261P_SDV	
				Date:	Friday, July 27, 2012
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Item	Reason for change	PG#	Modify List	Date	Phase
1	NCP6132A update SPEC	P47	Remove PR916, PR945	2012/06/05	
2	Thermal verify PH1 setting point	P41	Change PR208 value to 18.2K	2012/06/08	
3	ME demand : Keyboard assembly issue	P44	Change PL401, PL402 to High=2.4mm (SH000000H200)	2012/06/08	
4	Noise is PASS, Don't need 33u CAP	P48	Remove PC1023, PC1024	2012/06/08	
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