

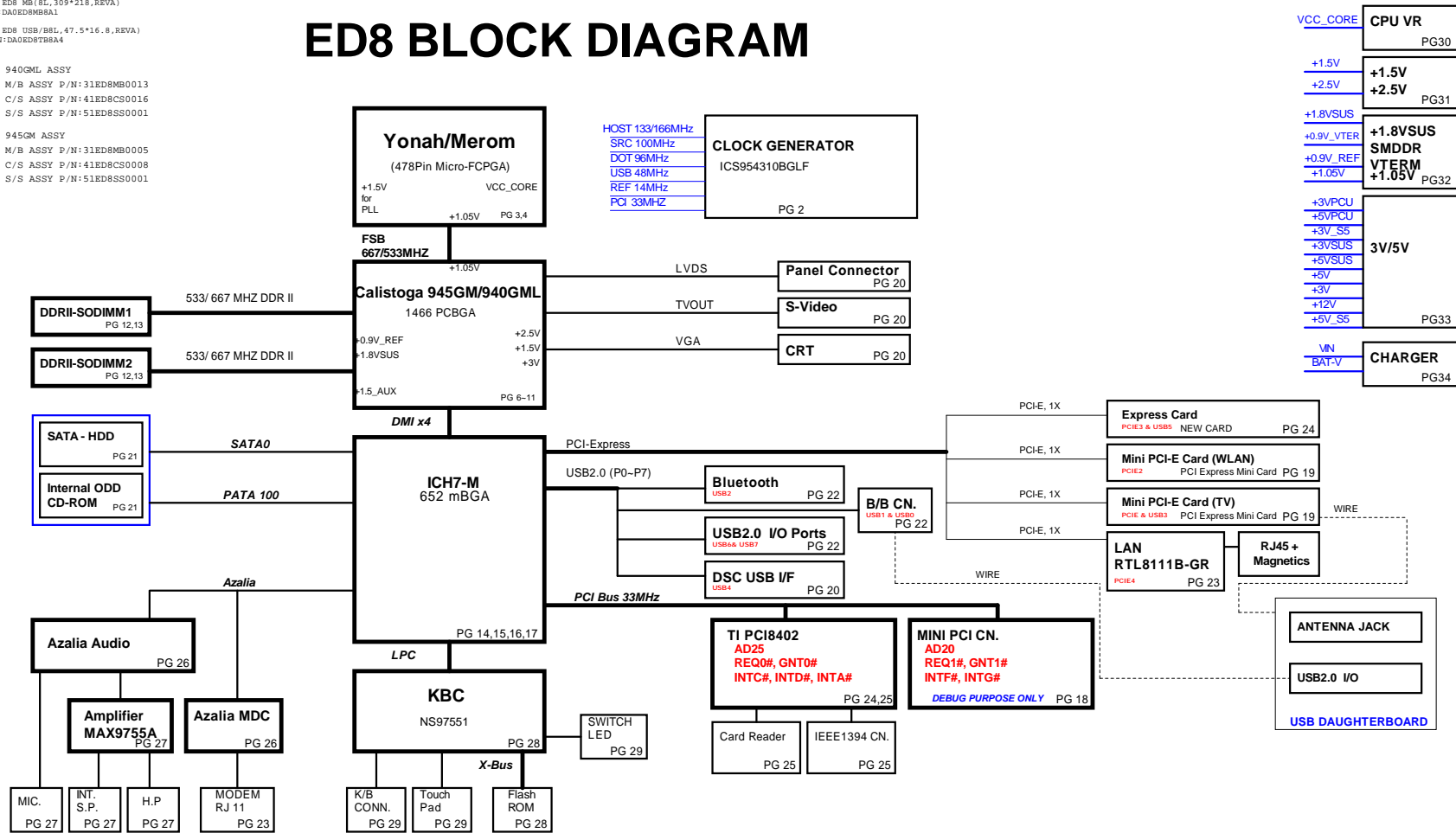
PCB ED8 MB (RL, 309\*218, REVA)  
P/N: DA0ED8MBRA1

PCB ED8 USB/B&L, 47, 5\*16, 8, REVA)  
P/N: DA0ED8TBRA4

ED8 940GML ASSY  
ED8 M/B ASSY P/N: 31ED8MB0013  
ED8 C/S ASSY P/N: 41ED8CS0016  
ED8 S/S ASSY P/N: 51ED8SS0001

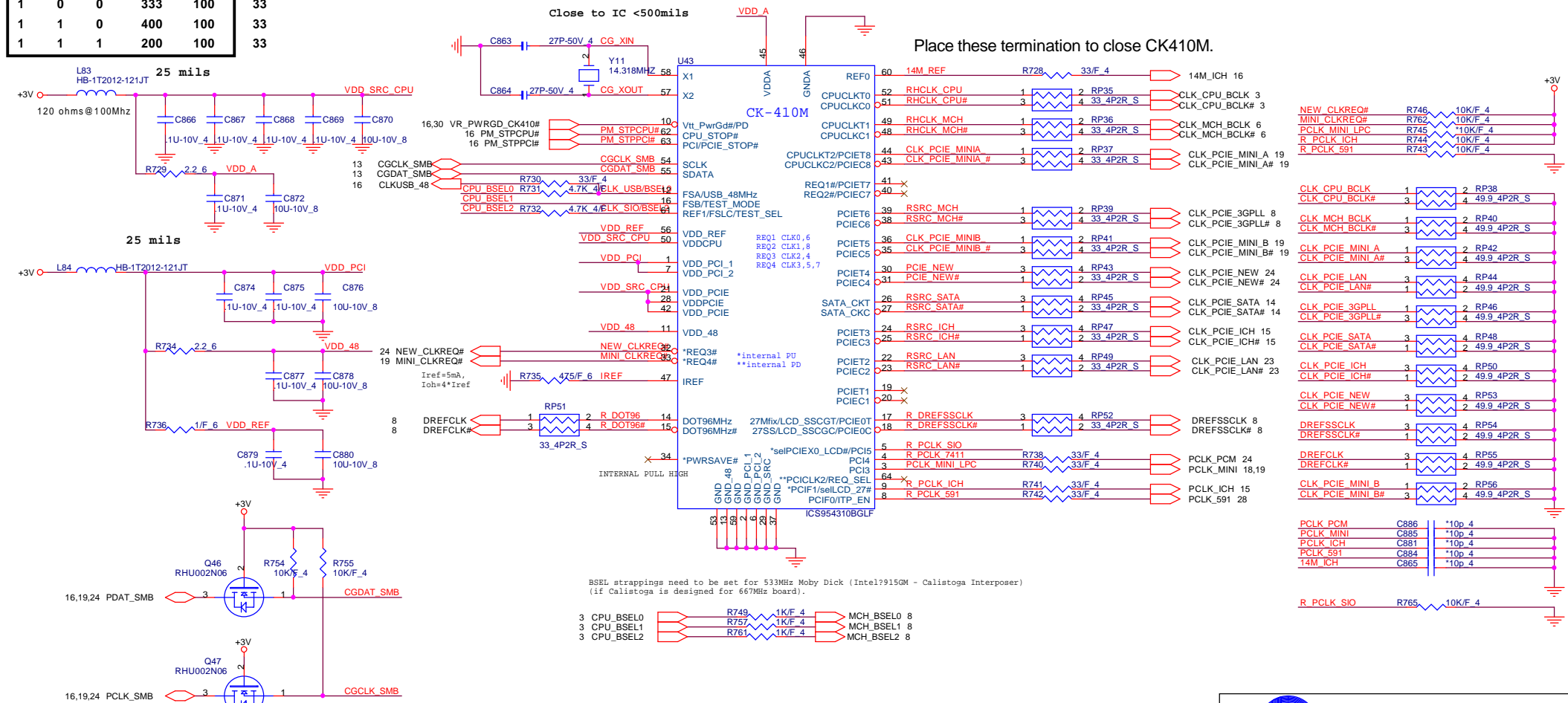
ED8 945GM ASSY  
ED8 M/B ASSY P/N: 31ED8MB0005  
ED8 C/S ASSY P/N: 41ED8CS0008  
ED8 S/S ASSY P/N: 51ED8SS0001

# ED8 BLOCK DIAGRAM



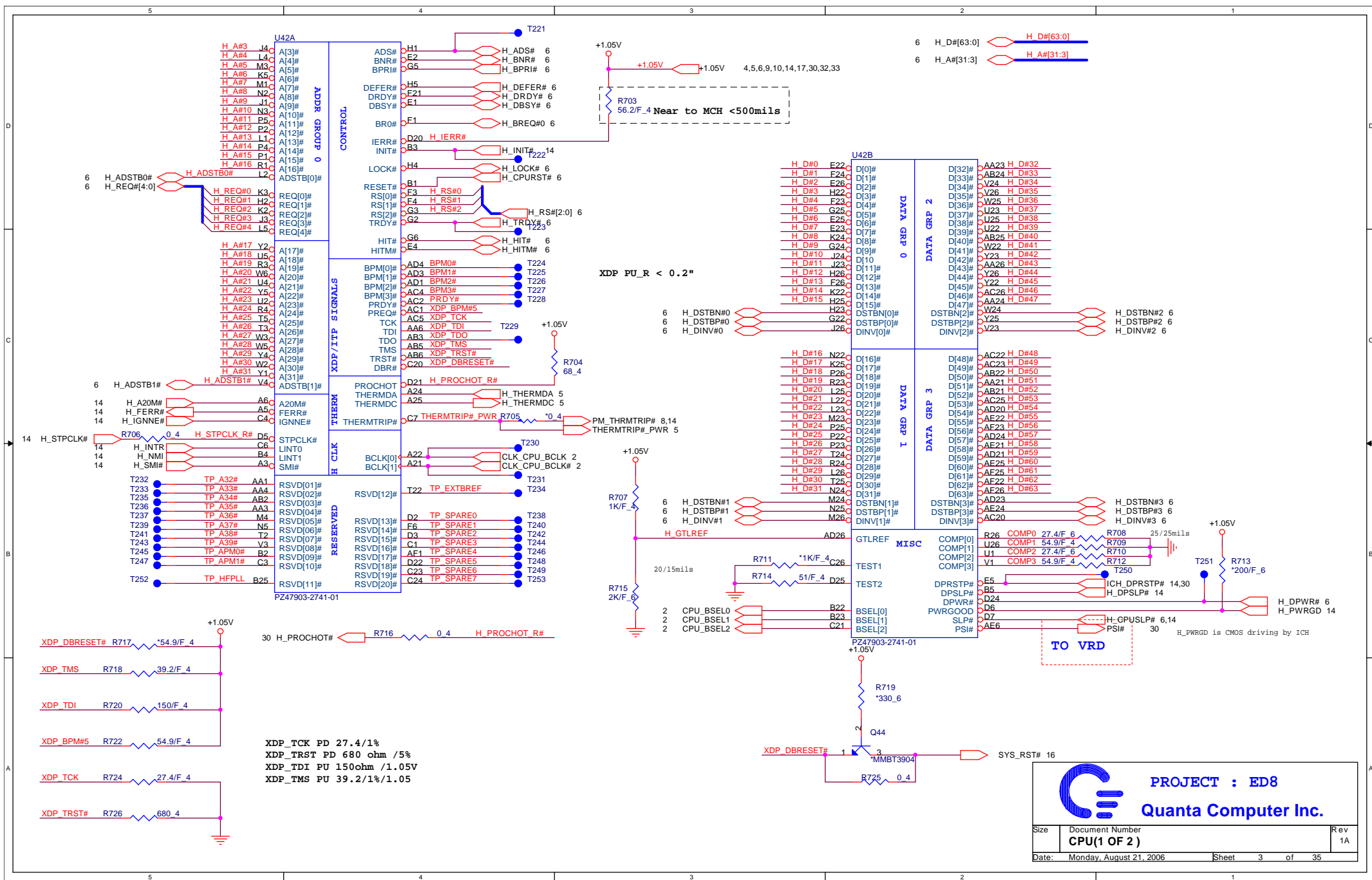
FSC	FSB	FSA	CPU	SRC
1	0	1	100	100
0	0	1	133	100
0	1	1	166	100
0	1	0	200	100
0	0	0	266	100
1	0	0	333	100
1	1	0	400	100
1	1	1	200	100

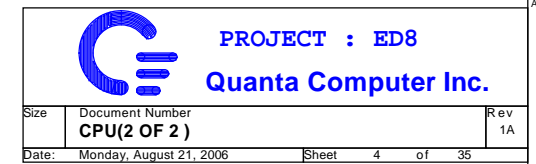
PCI
33
33 Default
33
33
33
33
33
33



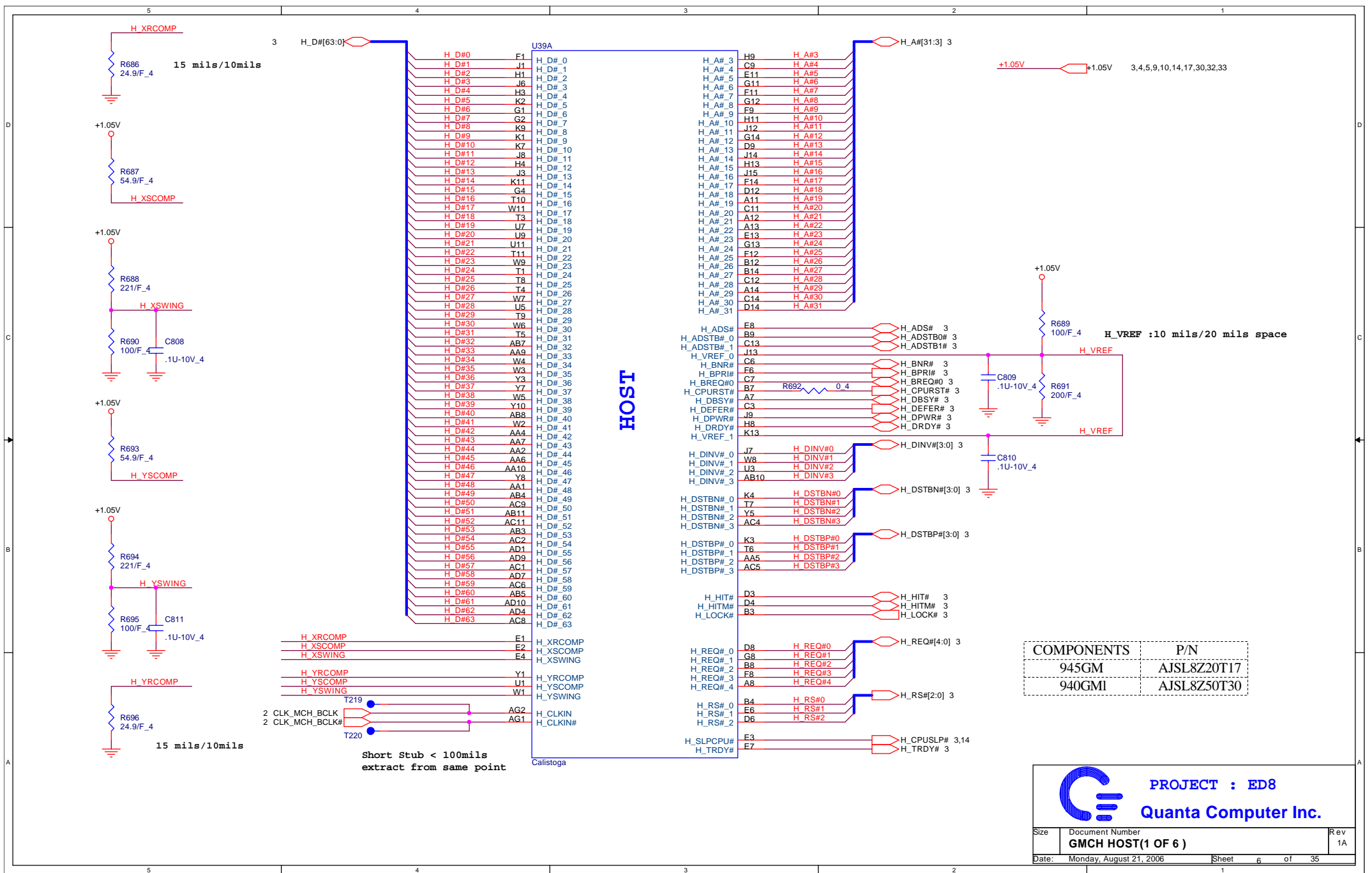
**PROJECT : ED8**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>CLOCK GENERATOR</b>	1A
Date:	Monday, August 21, 2006	Sheet 2 of 35



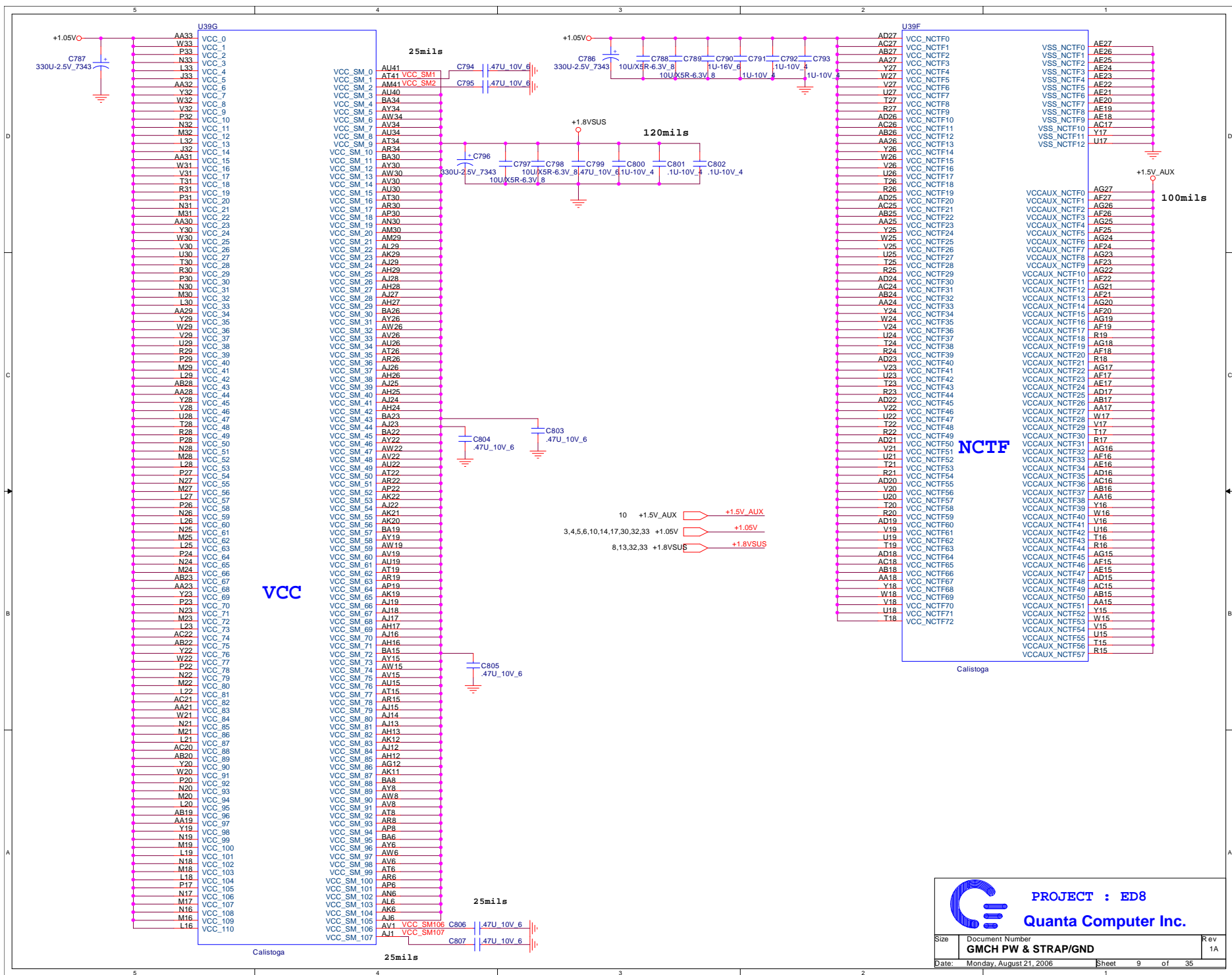




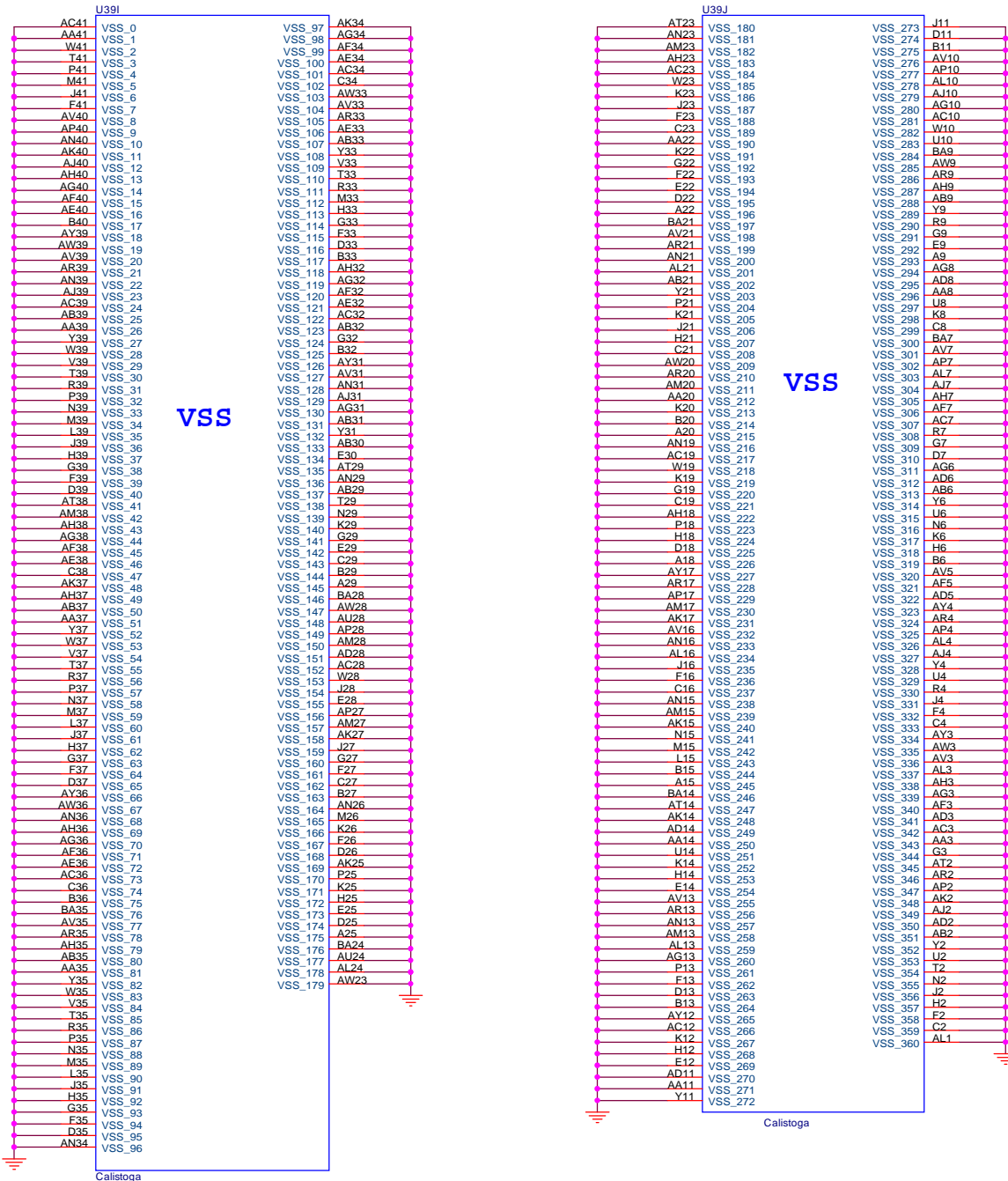






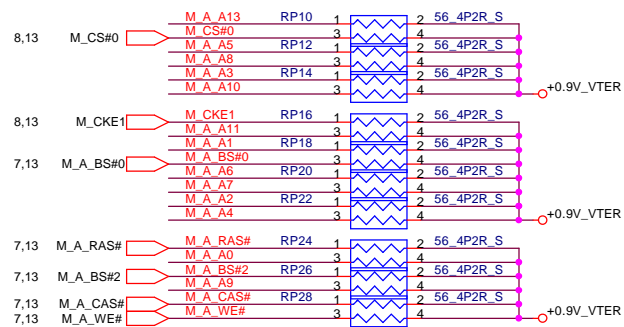
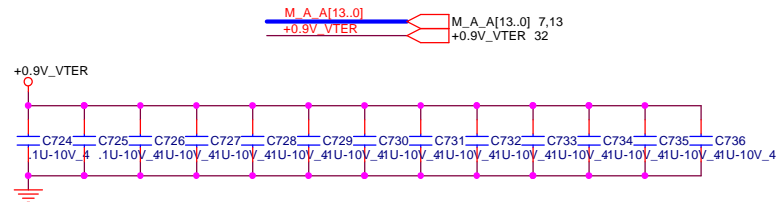






# DDRII DUAL CHANNEL A,B.

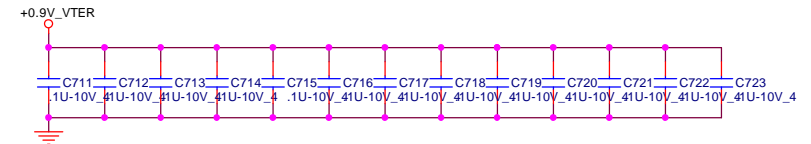
## DDRII A CHANNEL



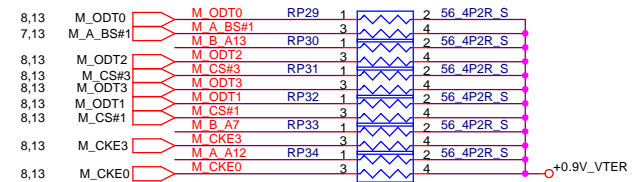
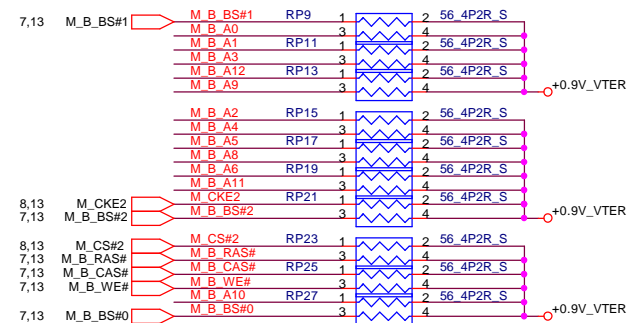
M\_B\_A[13..0]  
+1.8VSUS

M\_B\_A[13..0]  
+1.8VSUS

## DDRII B CHANNEL



Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR\_VTERM



PROJECT : ED8  
Quanta Computer Inc.

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	DDR RES. ARRAY	1A
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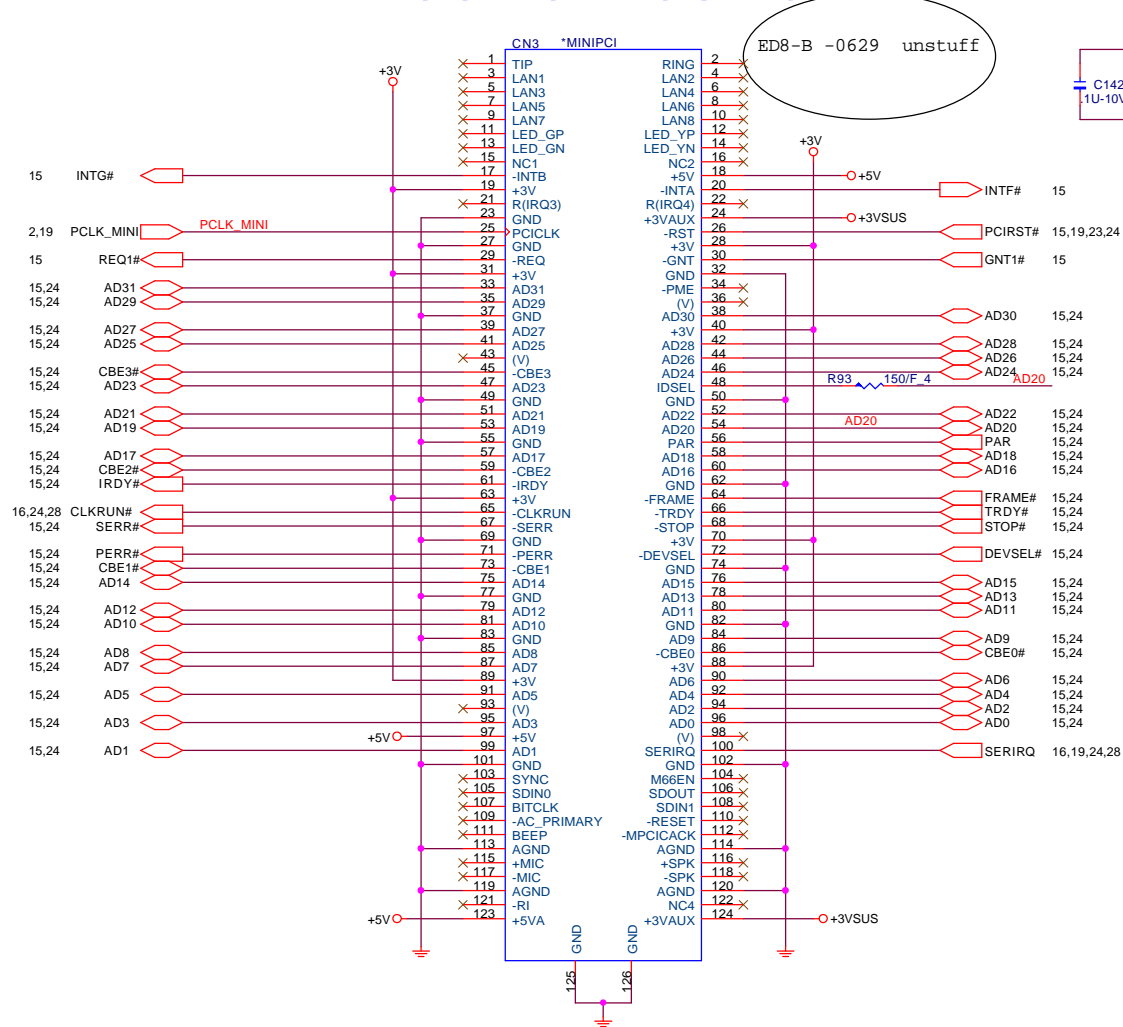






# DEBUG PURPOSE ONLY

ID Select : AD20  
Interrupt Pin : INTF# , INTG#  
Request Indicate : REQ1#  
Grant Indicate : GNT1#



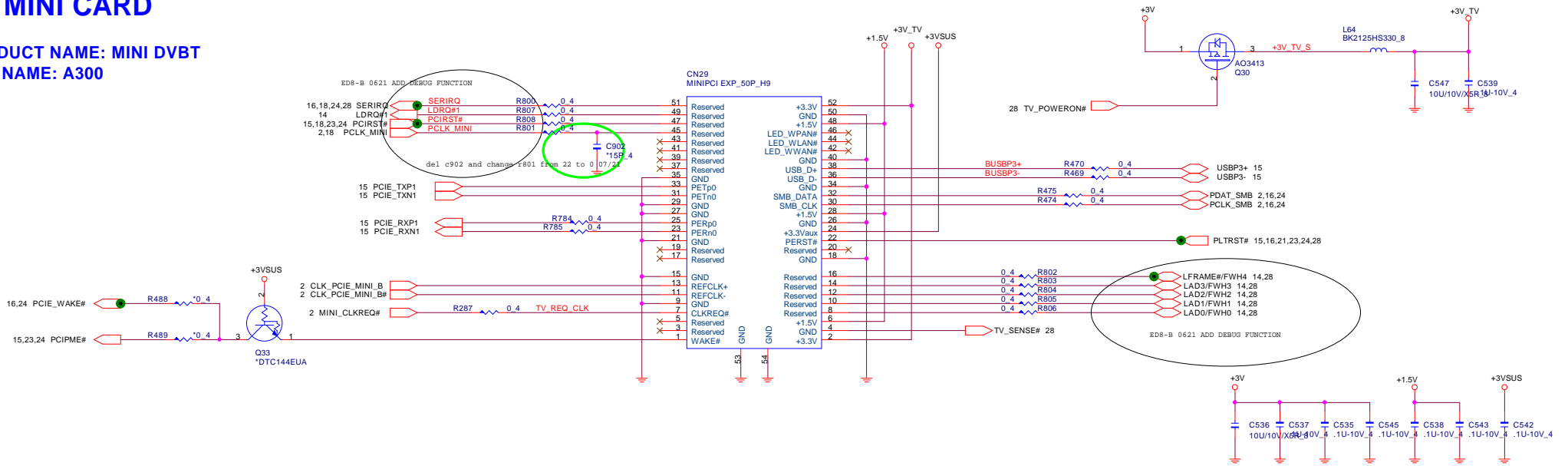
MPC

**PROJECT : ED8**  
**Quanta Computer Inc.**

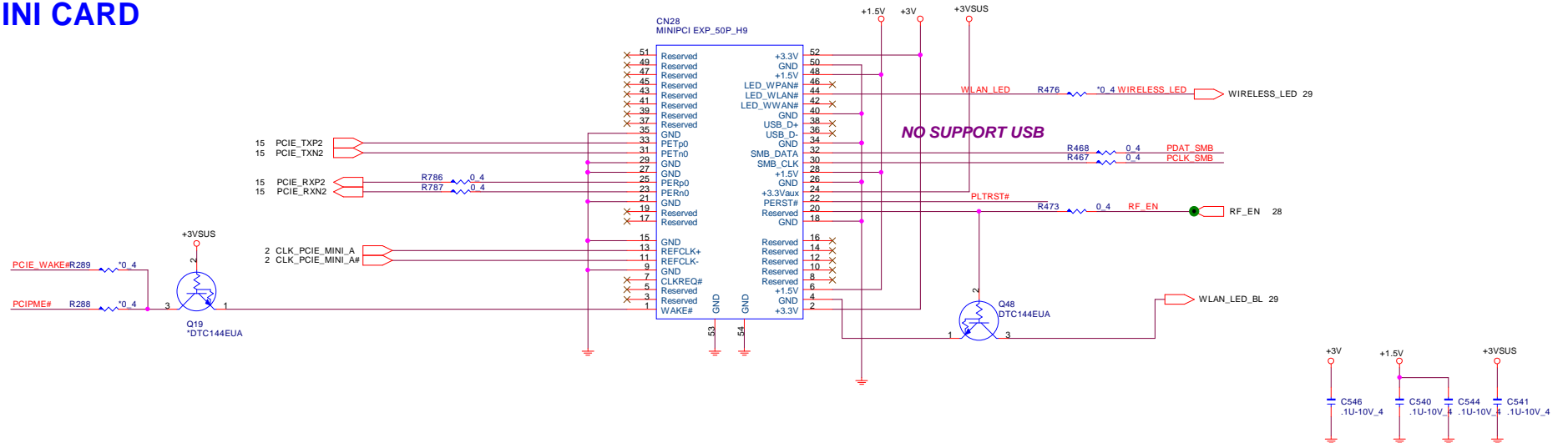
Size	Document Number	Rev
	<b>MINI PCI CONNECTOR (DEBUG ONLY)</b>	2A
Date:	Monday, August 21, 2006	Sheet 18 of 35

# TV MINI CARD

PRODUCT NAME: MINI DVBT  
MFR NAME: A300

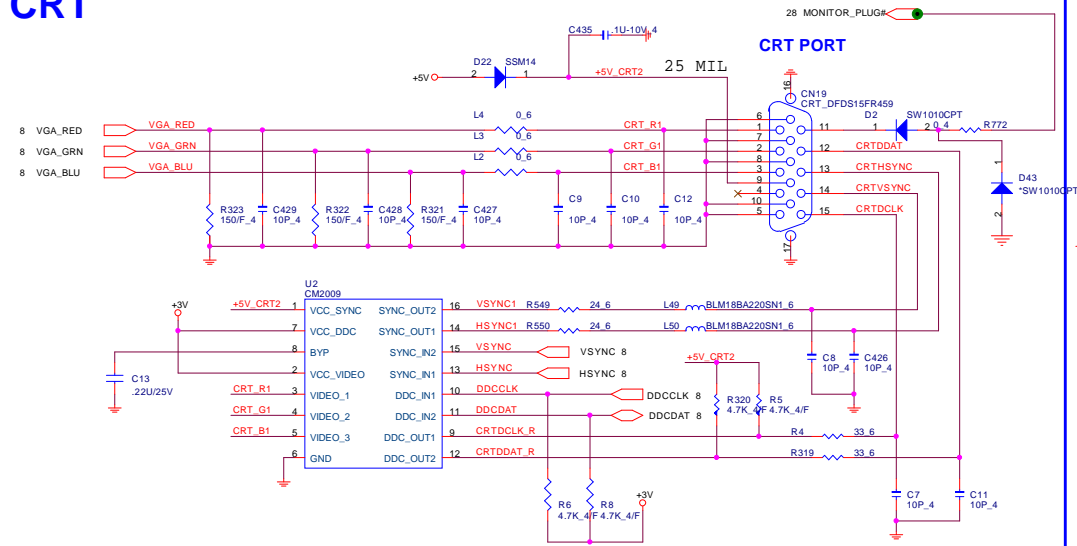


# WLAN MINI CARD

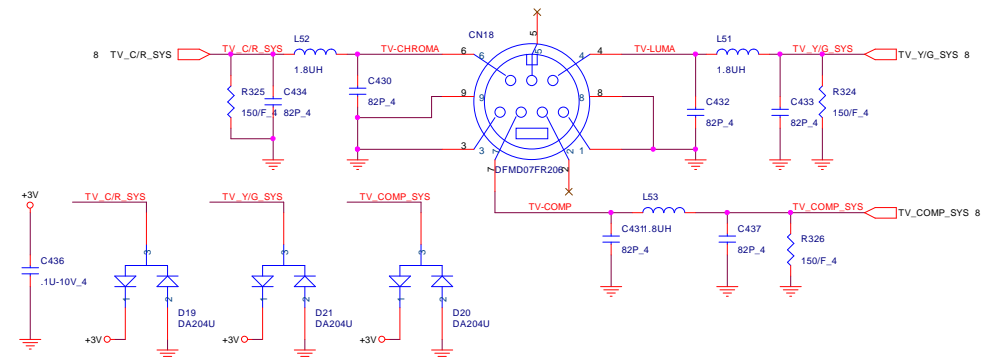


MINI CARD

# CRT

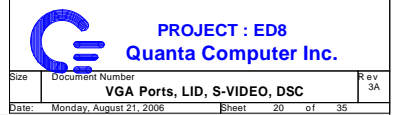
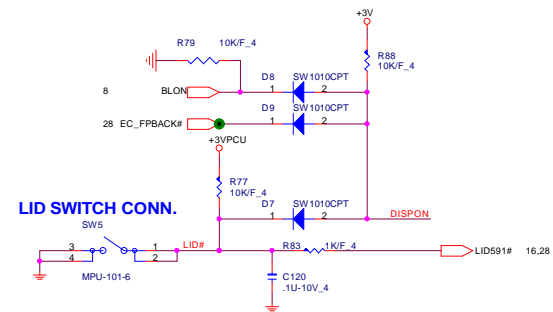
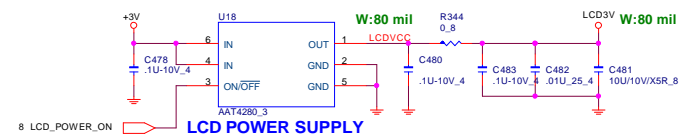
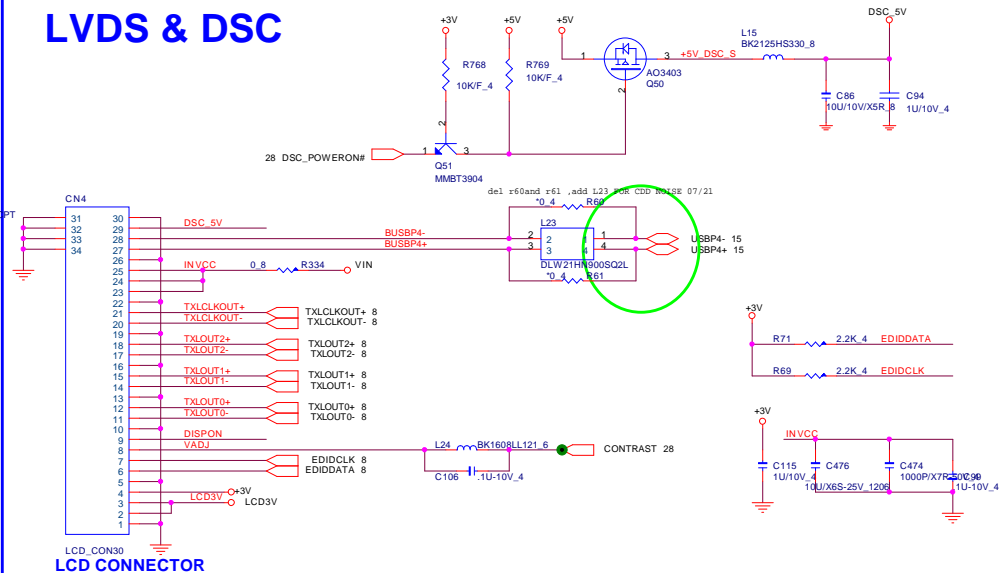


## TV

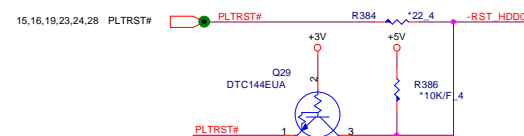
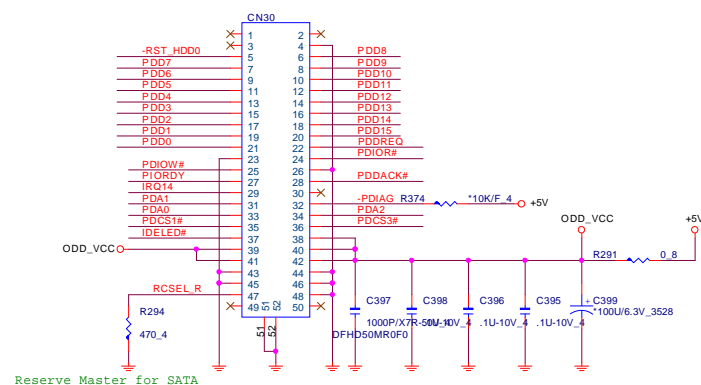
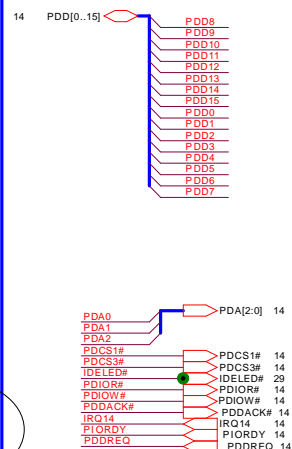


## CRT, LVDS, TV, DSC

## LVDS & DSC

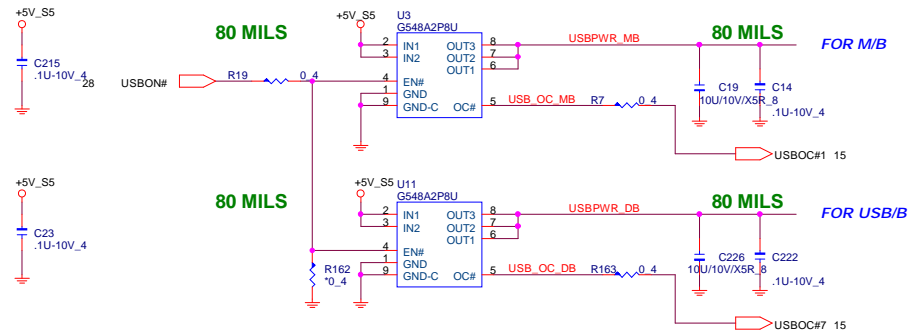


**ODD**

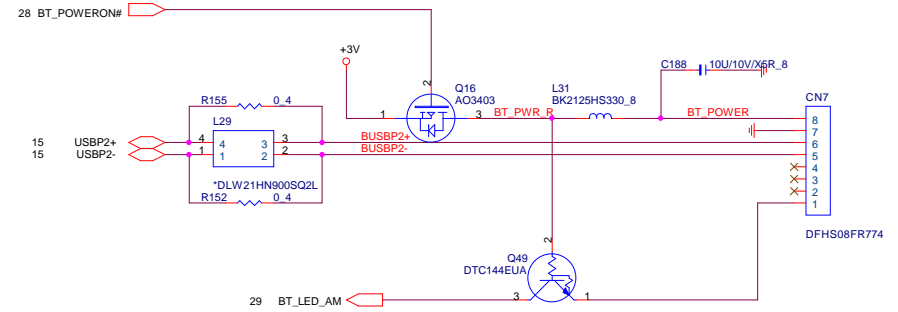


Size	Document Number <b>HDD &amp; CDROM , HOLES</b>	Rev 3
Date:	Monday, August 21, 2006	Sheet 21 of 35

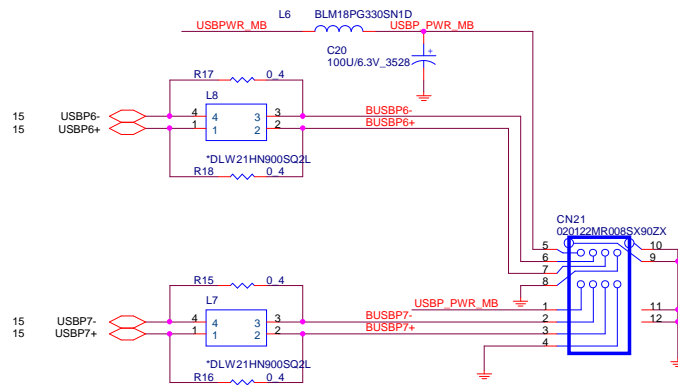
## USB POWER SUPPLY



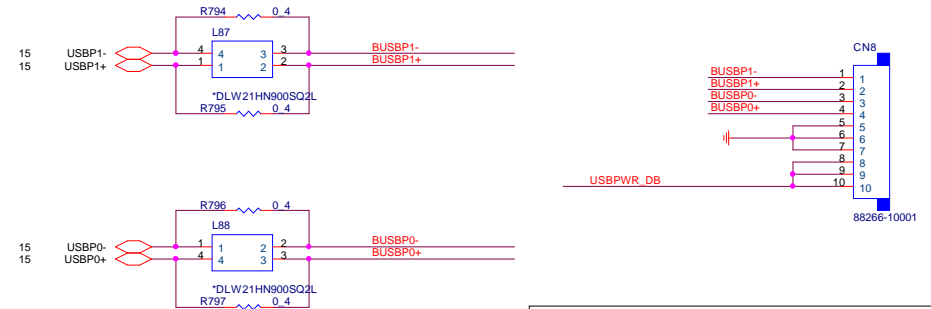
## BLUETOOTH




## USB I/O CONNECTORS



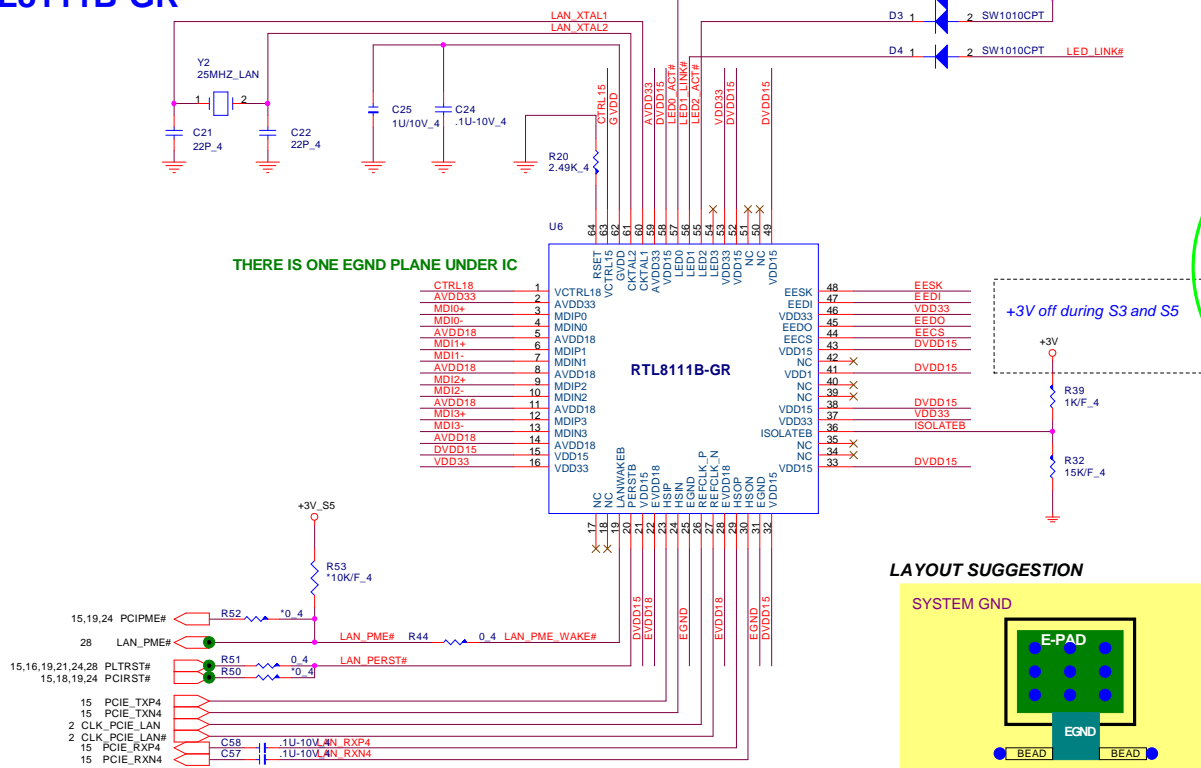
## USB BAORD WIRE CONNECTOR



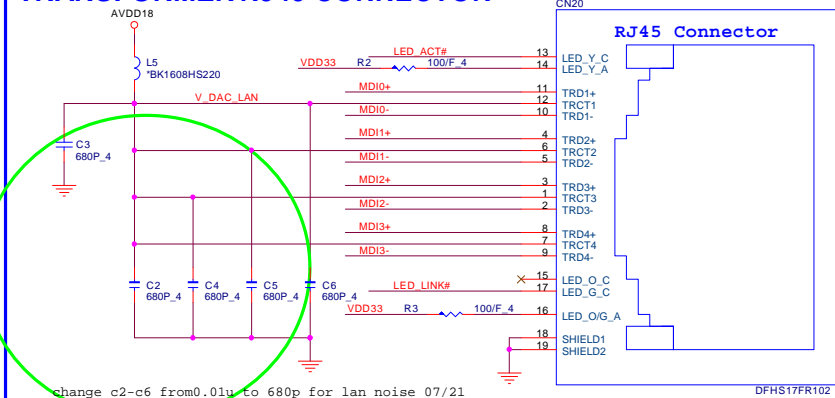
## USB, BT, USB/B CONN

	<p><b>PROJECT : ED8</b></p> <p><b>Quanta Computer Inc.</b></p>	
Size	Document Number <b>MINI PCI &amp; USB PORT &amp; BT</b>	Rev 1A
Date:	Monday, August 21, 2006	Sheet    22    of    35

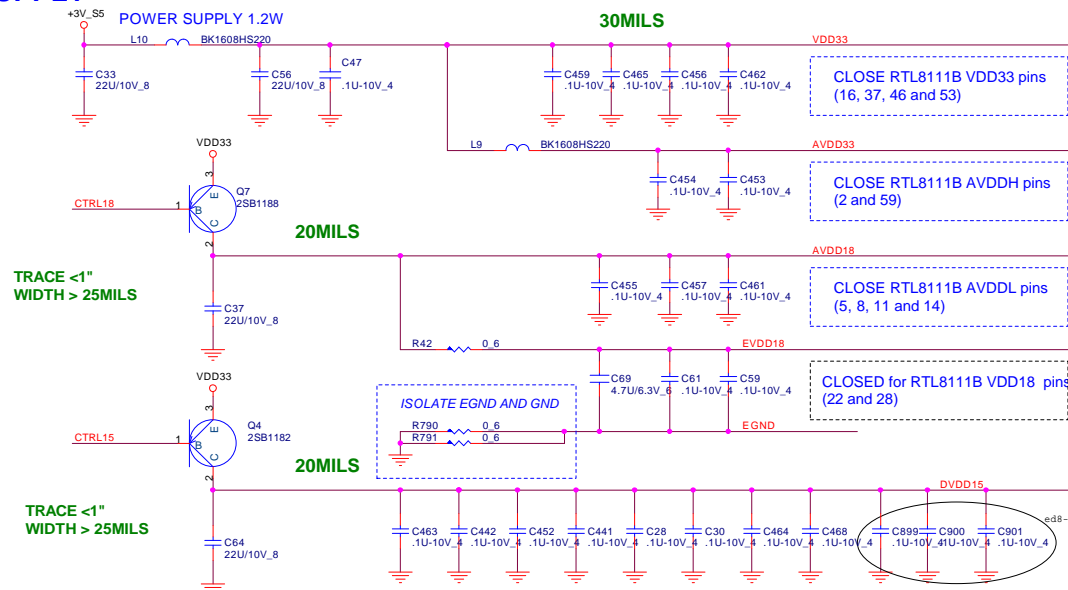
## RTL8111B-GR



## TRANSFORMER RJ45 CONNECTOR

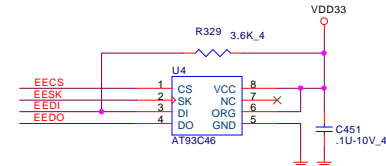


## POWER SUPPLY

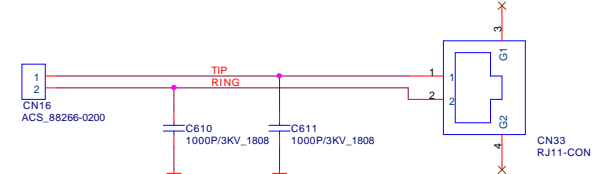


## LAN

## EEPROM



## RJ11



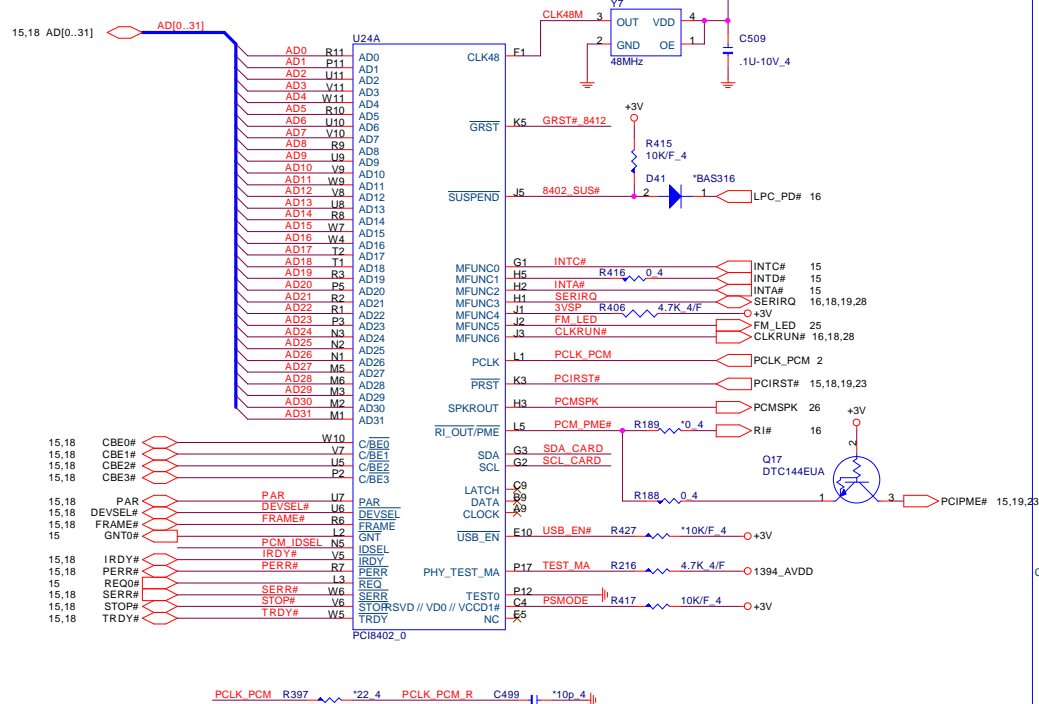
**PROJECT : ED8**  
**Quanta Computer Inc.**

Size Document Number  
**LAN RTL8111B-GR PCI-E & RJ11**  
Date: Monday, August 21, 2006 Sheet 23 of 35 Rev 3A

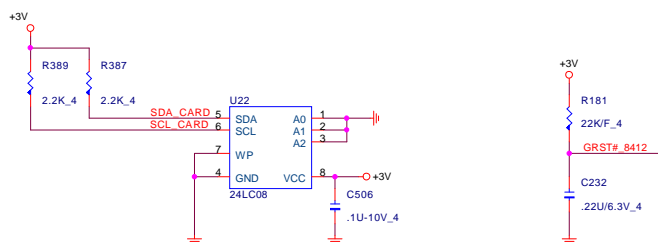
## PCI8402 PCI I/F

ID Select : AD25  
Interrupt Pin : INTA#, INTC#, INTD#  
Request Indicate : REQ0#  
Grant Indicate : GNT0#

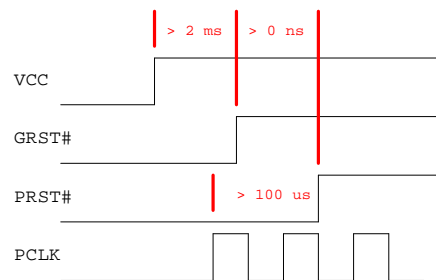
AD25 R405 150/F 4 PCM IDSEL



## IEEE1394 EEPROM

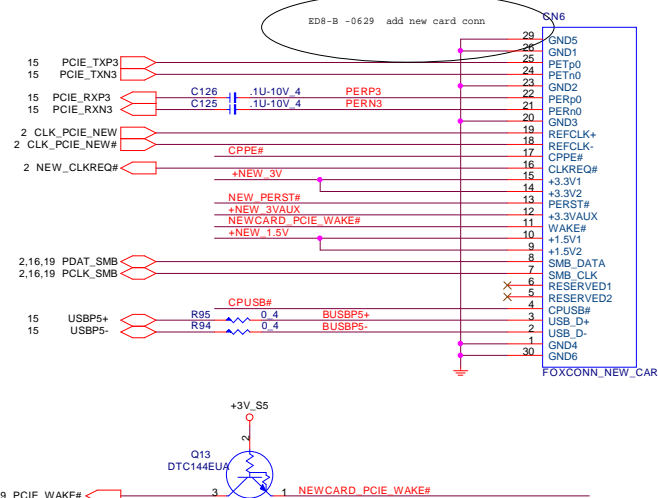


## IEEE1394 & NEW CARD

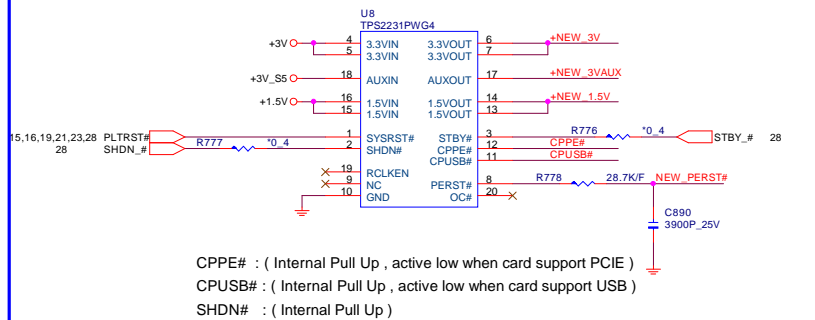


## NEW CARD

NEW CARD HEADER: DFHD26MR074 (13081-1)  
NEW CARD EJECTER: FBZF1026019 (130851-3)



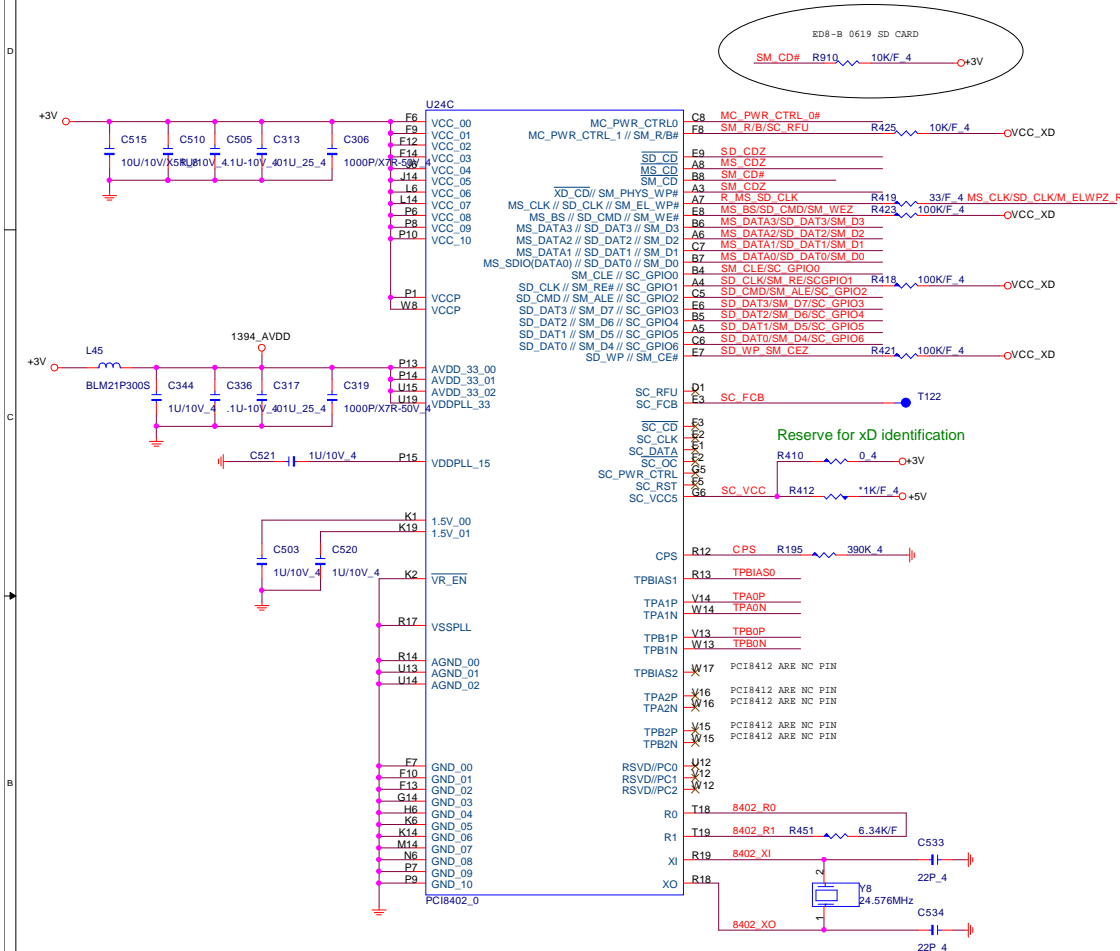
## NEW CARD'S POWER SWITCH



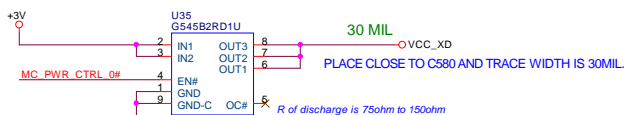
PROJECT : ED8  
Quanta Computer Inc.

Size Document Number  
PC18402-1 & NEW CARD  
Date: Monday, August 21, 2006 Sheet 24 of 35 Rev 2A

## PCI8402 I/F OF 1394 & CARD READER

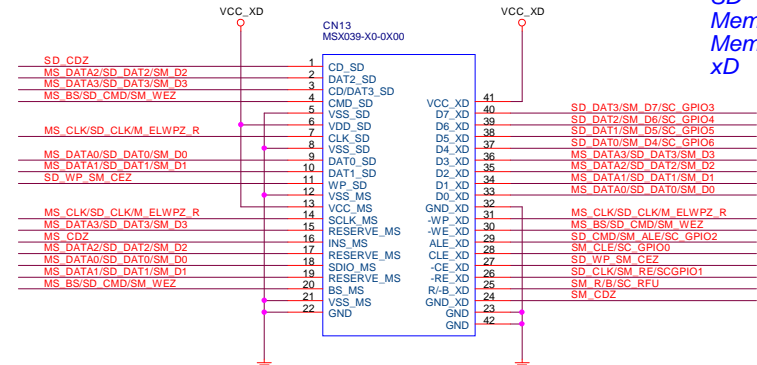


### MEMORY CARD READED POWER CONTROL (IC SOLUTION FOR C-TEST)



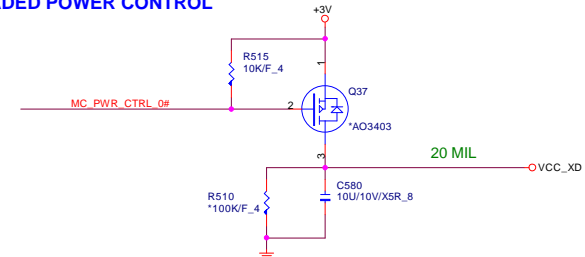
## IEEE1394 & CARD REDER

## 5 IN 1 MEMORY CARD READER

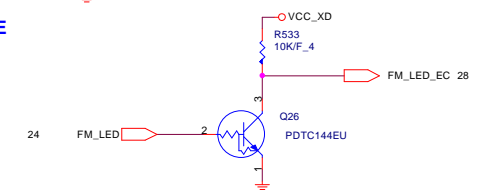


Support :  
MMC  
SD  
Memory Stick  
Memory Stick Pro  
xD

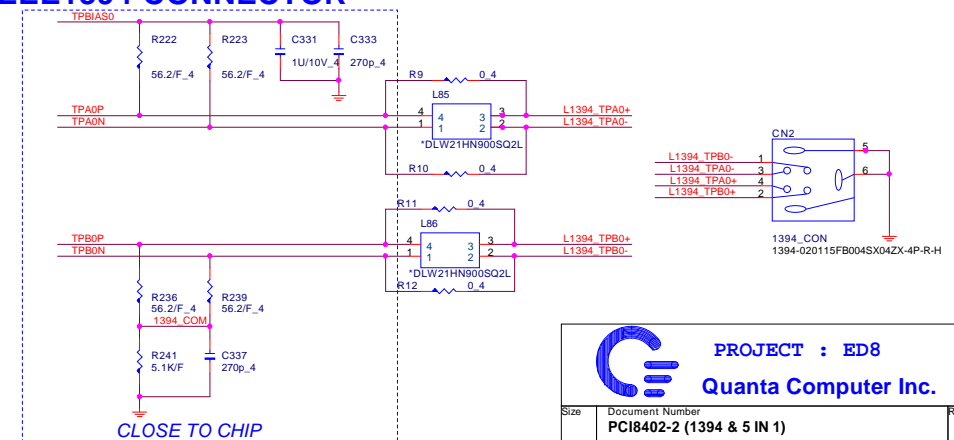
### MEMORY CARD READED POWER CONTROL



### MEMORY CARD READED LED INDICATE



## IEEE1394 CONNECTOR

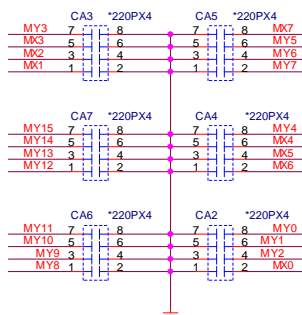
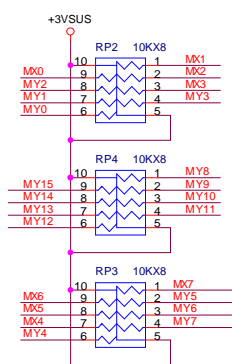
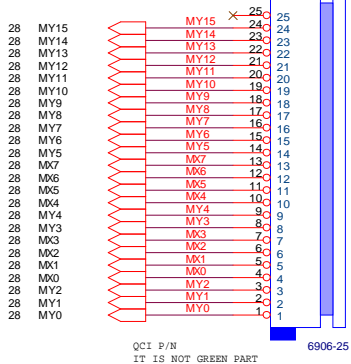




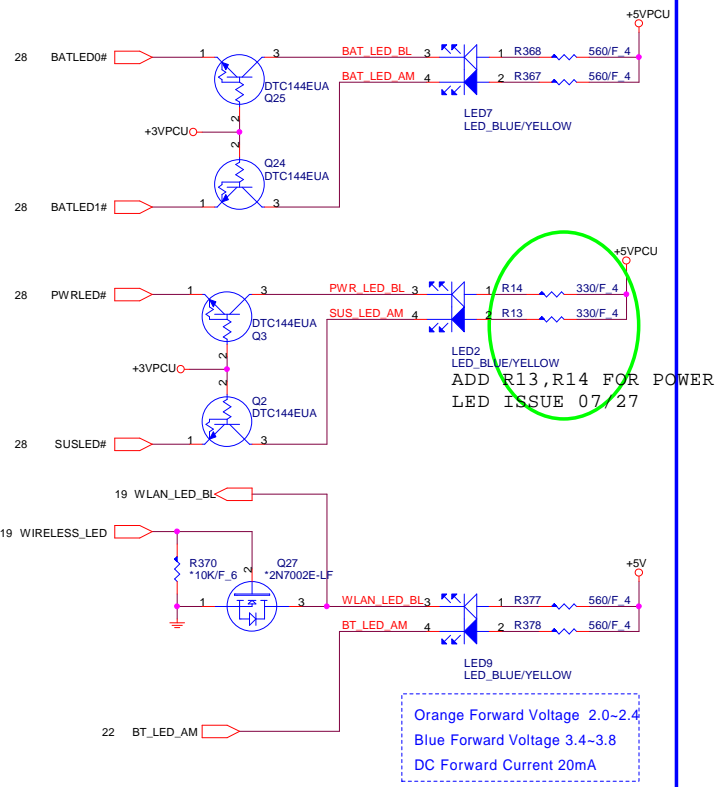
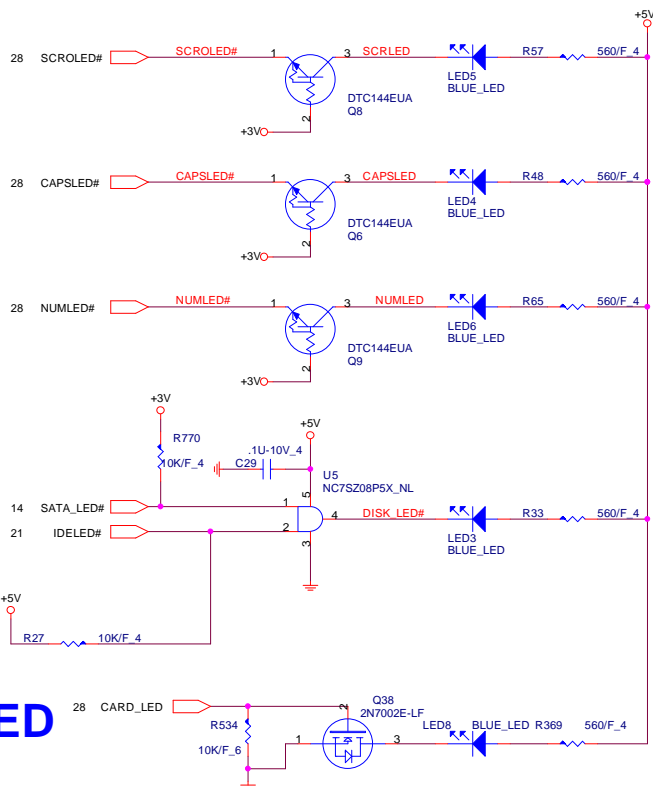
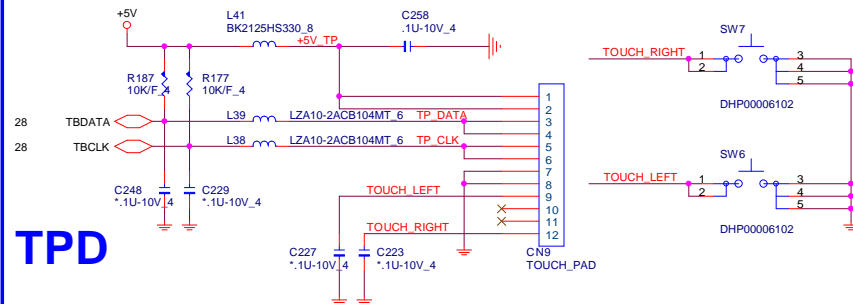




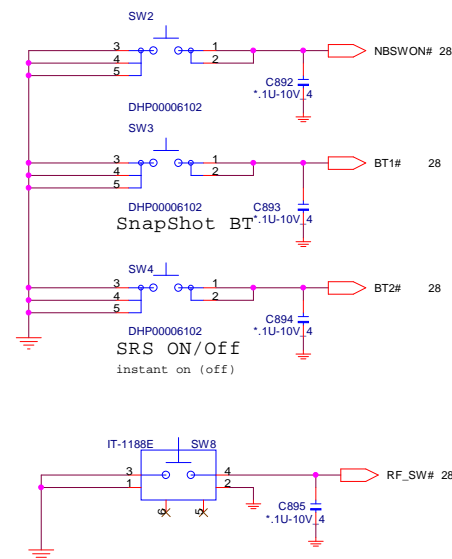
# KBC



# TPD

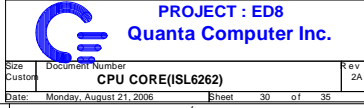


# SWITCH



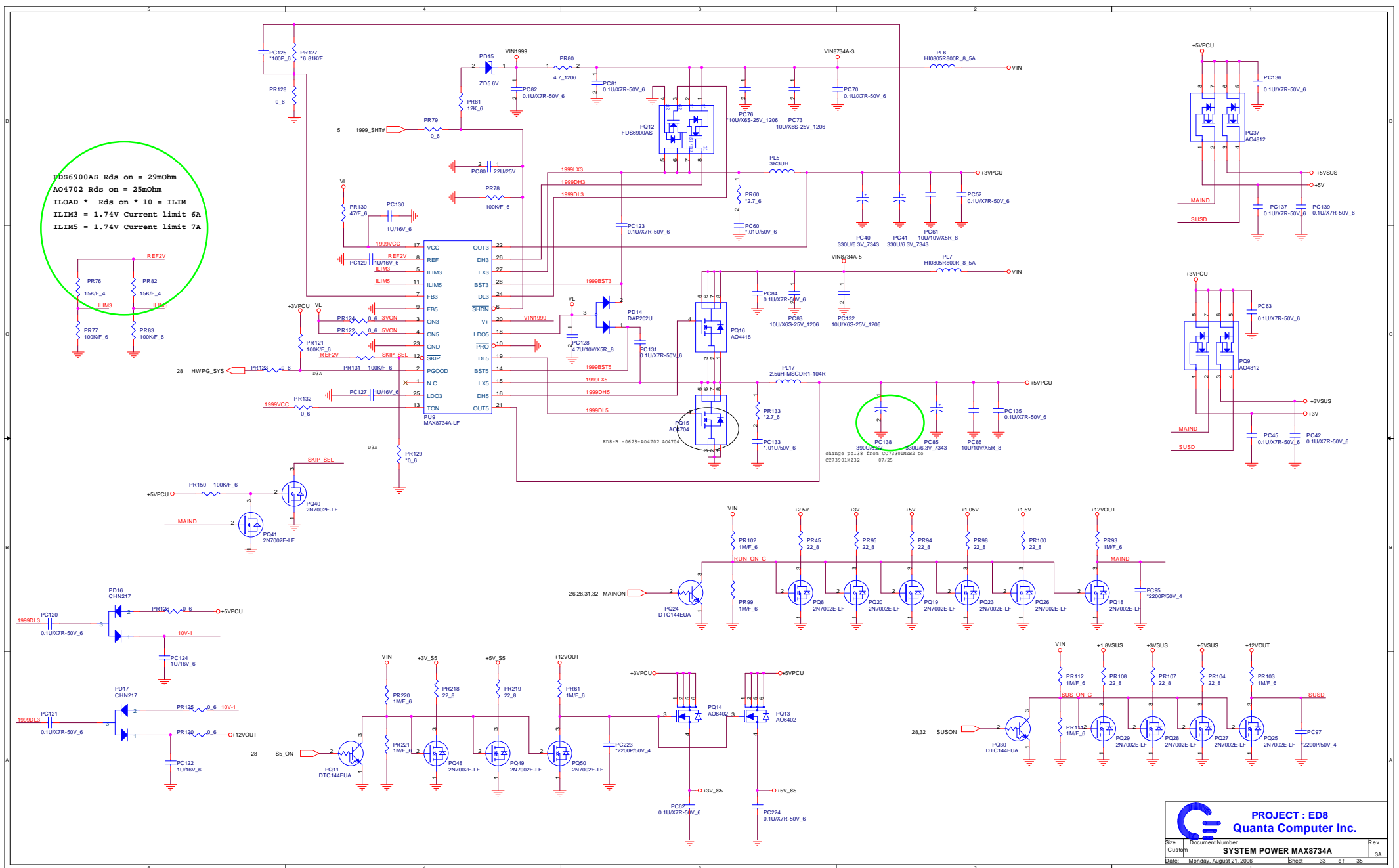
LED  
MSIC.

PROJECT : ED8		
Quanta Computer Inc.		
Size	Document Number	Rev
	T/P,FAN,SWITCH,LED,K/B	3A
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ADD PR224 AND PR225 FOR POWER REQUEST  
change pc3 footprint 0805 to 0603 07/27

