

**CLK-GEN**  
ICS954226

HOST 133/166MHz  
PCIE 100MHz  
VGA 96MHz  
USB 48MHz  
PCI 33MHz  
REF 14MHz

Page 2

**3V / 5V**

+3VPCU  
+3V\_S5  
+3VSUS  
+5VSUS  
+3V  
+5V  
+12V

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**1.8V / 1.05V**

+1.8VSUS  
SMDDR\_VTERM  
+1.05V

Page 24

**1.5V / 2.5V**

+1.5V  
+2.5V

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**CPU CORE**

+VCC\_CORE

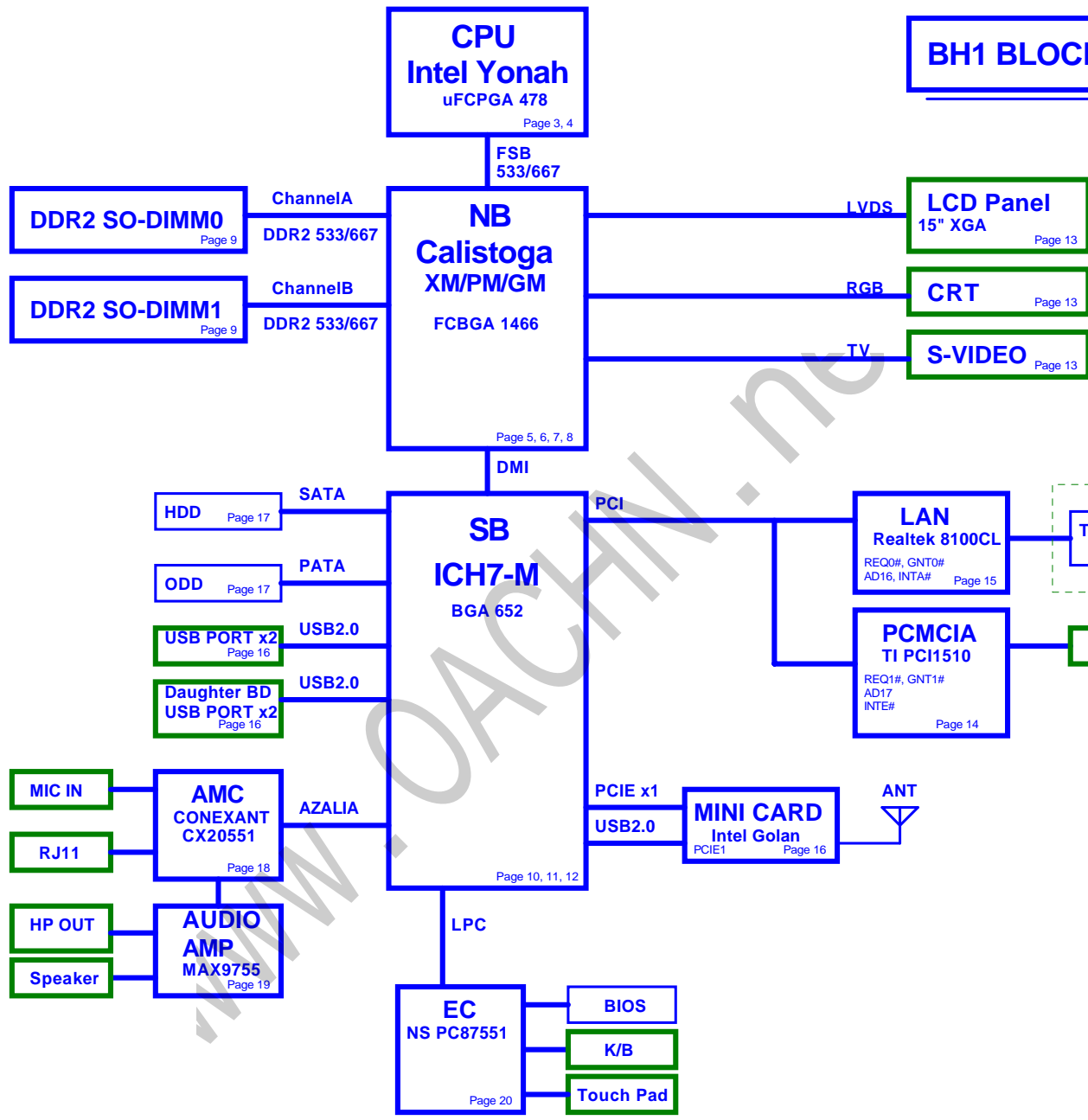
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**BATTERY CHARGER**

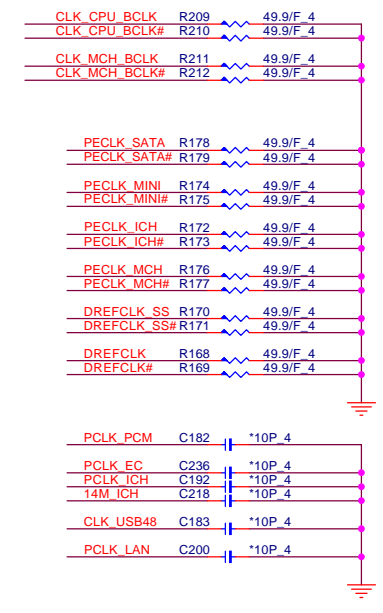
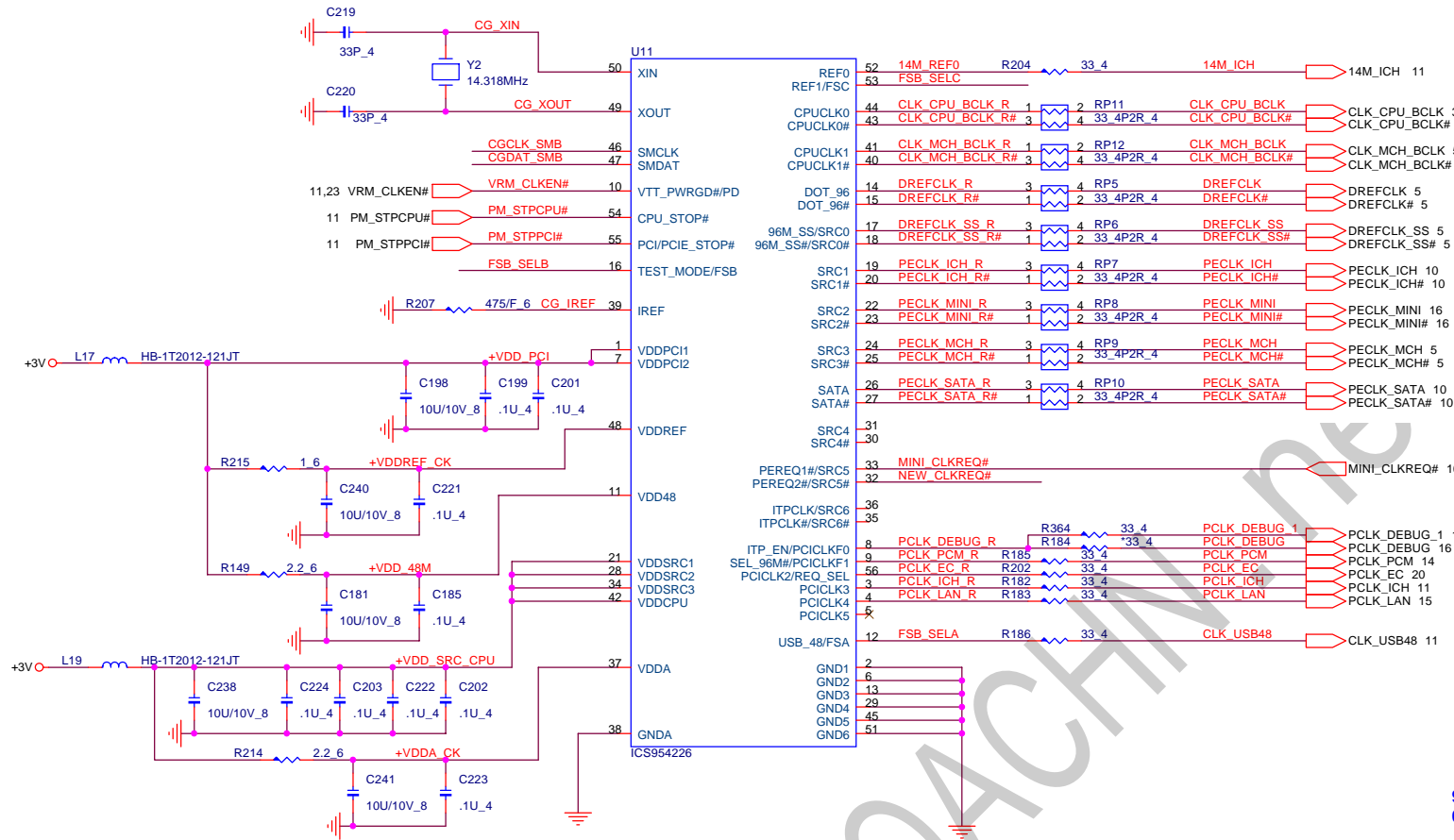
VIN

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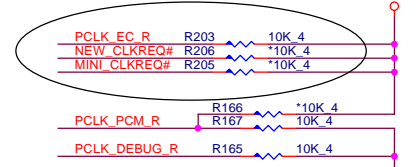
Power State Table		
Power Name	Control Signal	Power State
+3VPCU	N/A	ALWAYS
+3V_S5	S5_ON	S0 - S5
+3VSUS	SUSON	S0 - S3
+3V	MAINON	S0
+5VSUS	SUSON	S0 - S3
+5V	MAINON	S0
+12V	MAINON	S0
+1.8VSUS	SUSON	S0 - S3
+SMDDR_VTERM	MAINON	S0
+1.05V	MAINON	S0
+1.5V	MAINON	S0
+2.5V	MAINON	S0
+VCC_CORE	VRON	S0



# **BH1 BLOCK DIAGRAM**

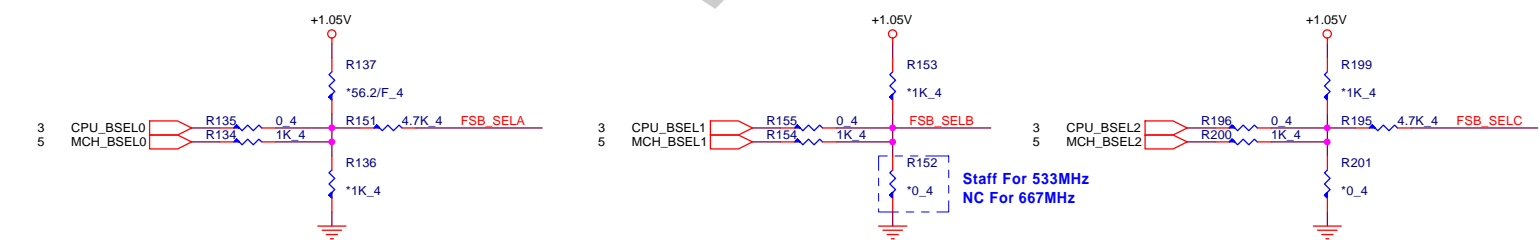
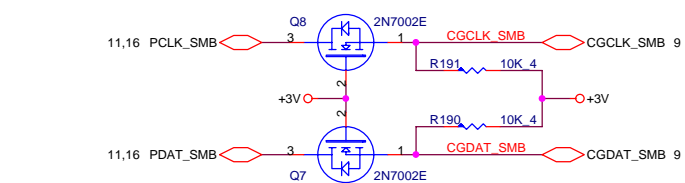


96M/SRC0 SELECT  
0: 96M 1: SRC0



PEREQ Control Pair	
PEREQ1	SATA,PCIEX0,2
PEREQ2	PCIEX1,3,4

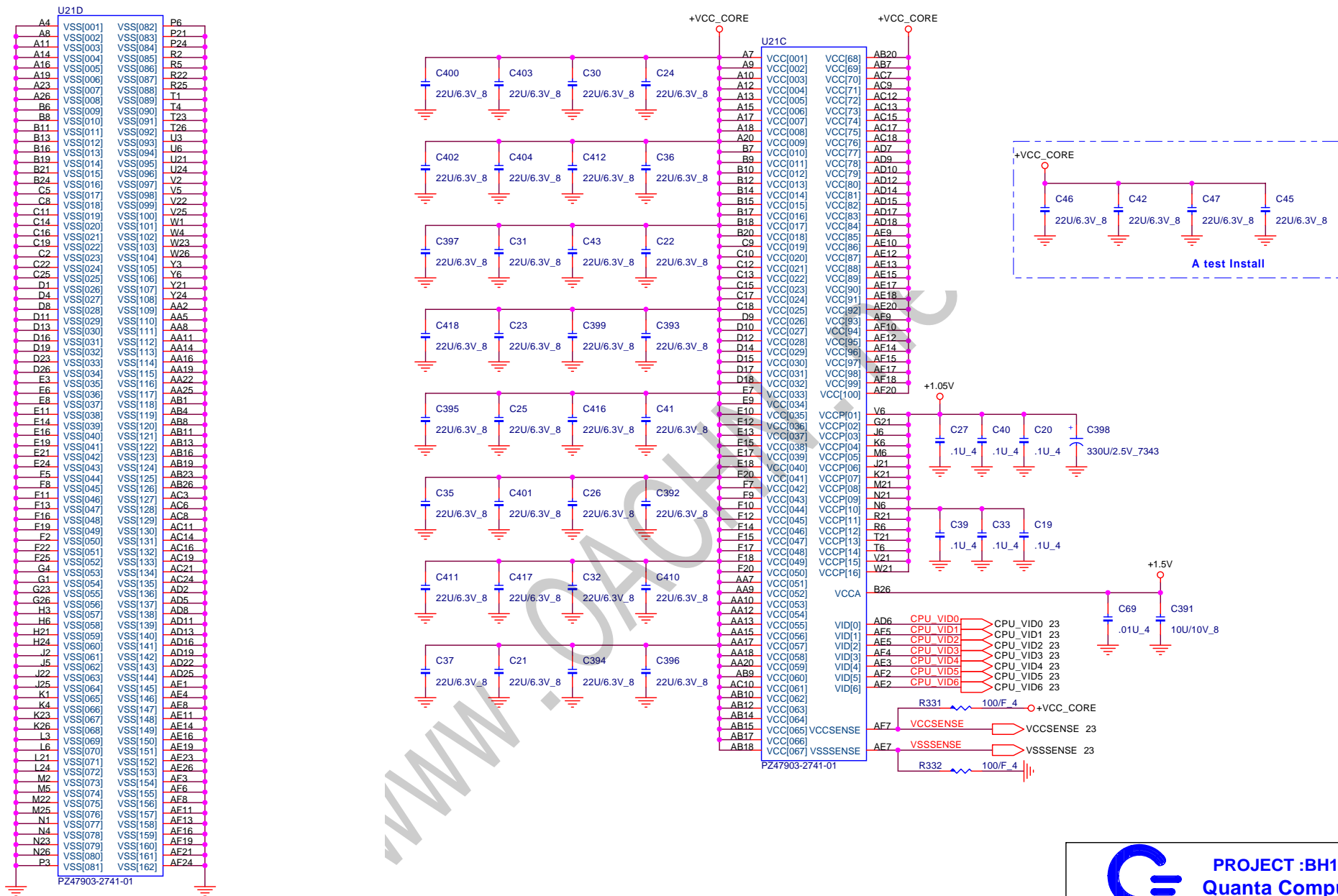
FSC	FSB	FSA	CPU	SRC	PCI	FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33	0	1	0	200	100	33
0	0	1	133	100	33	0	0	0	266	100	33
0	1	1	166	100	33	1	0	0	333	100	33
1	1	1	200	100	33	1	1	0	400	100	33



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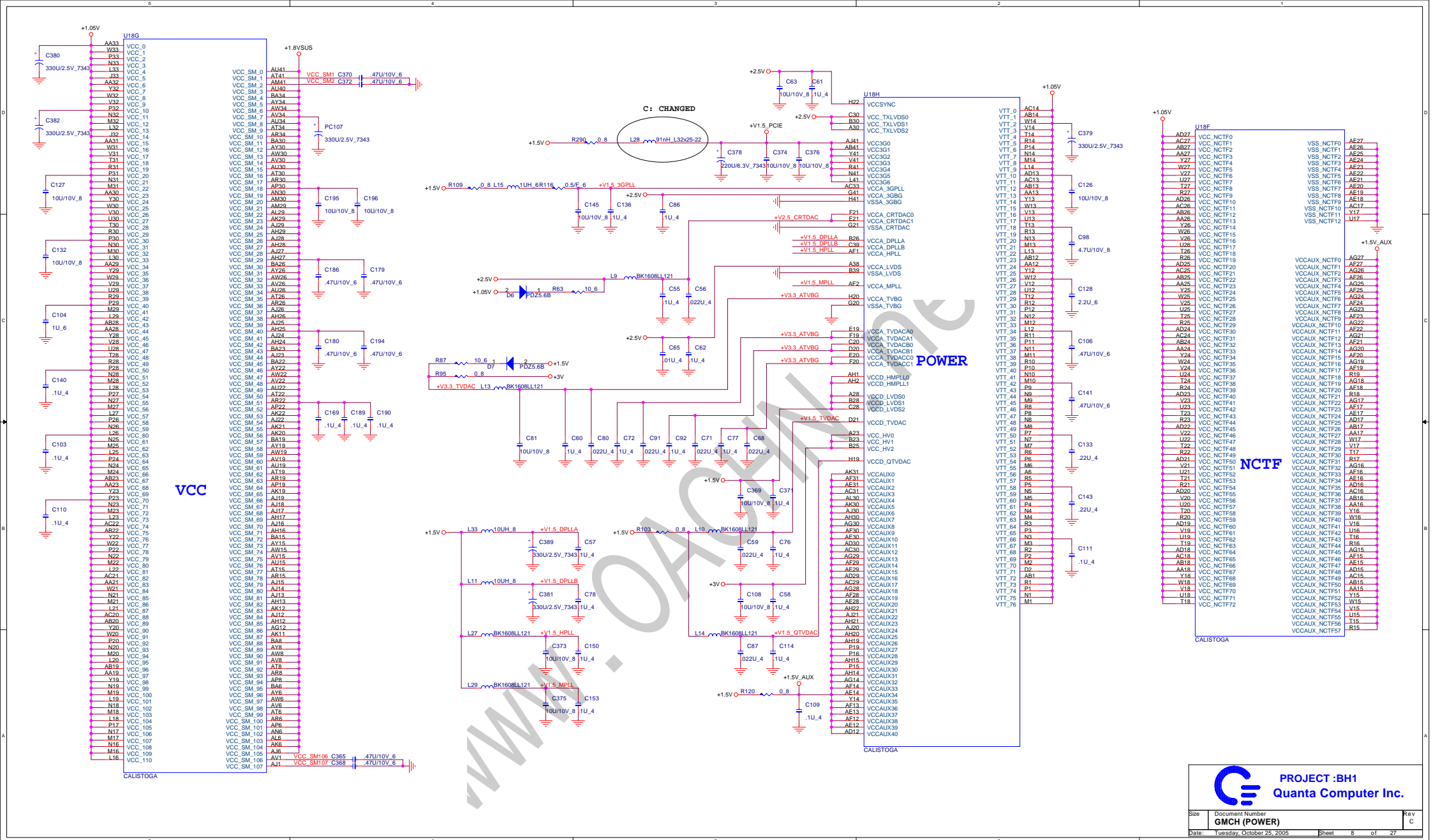
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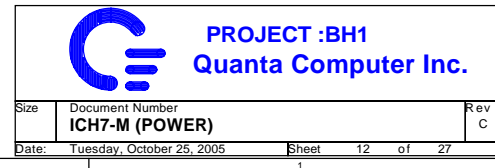






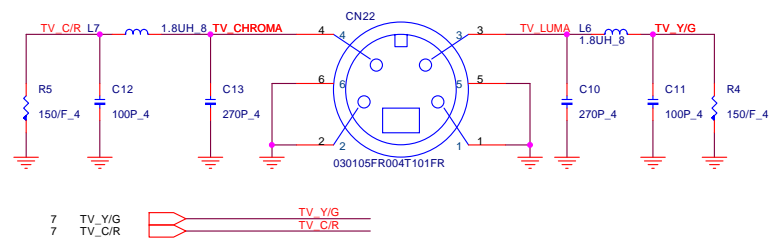




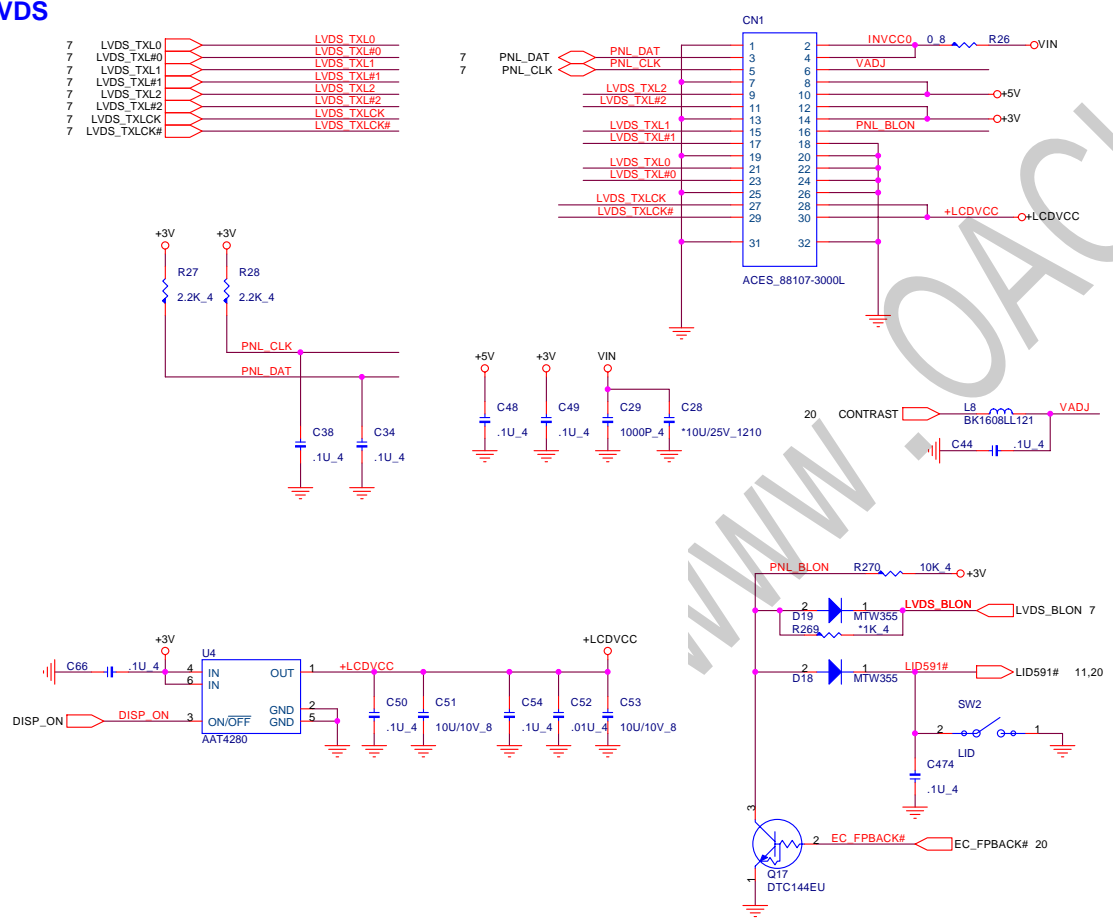




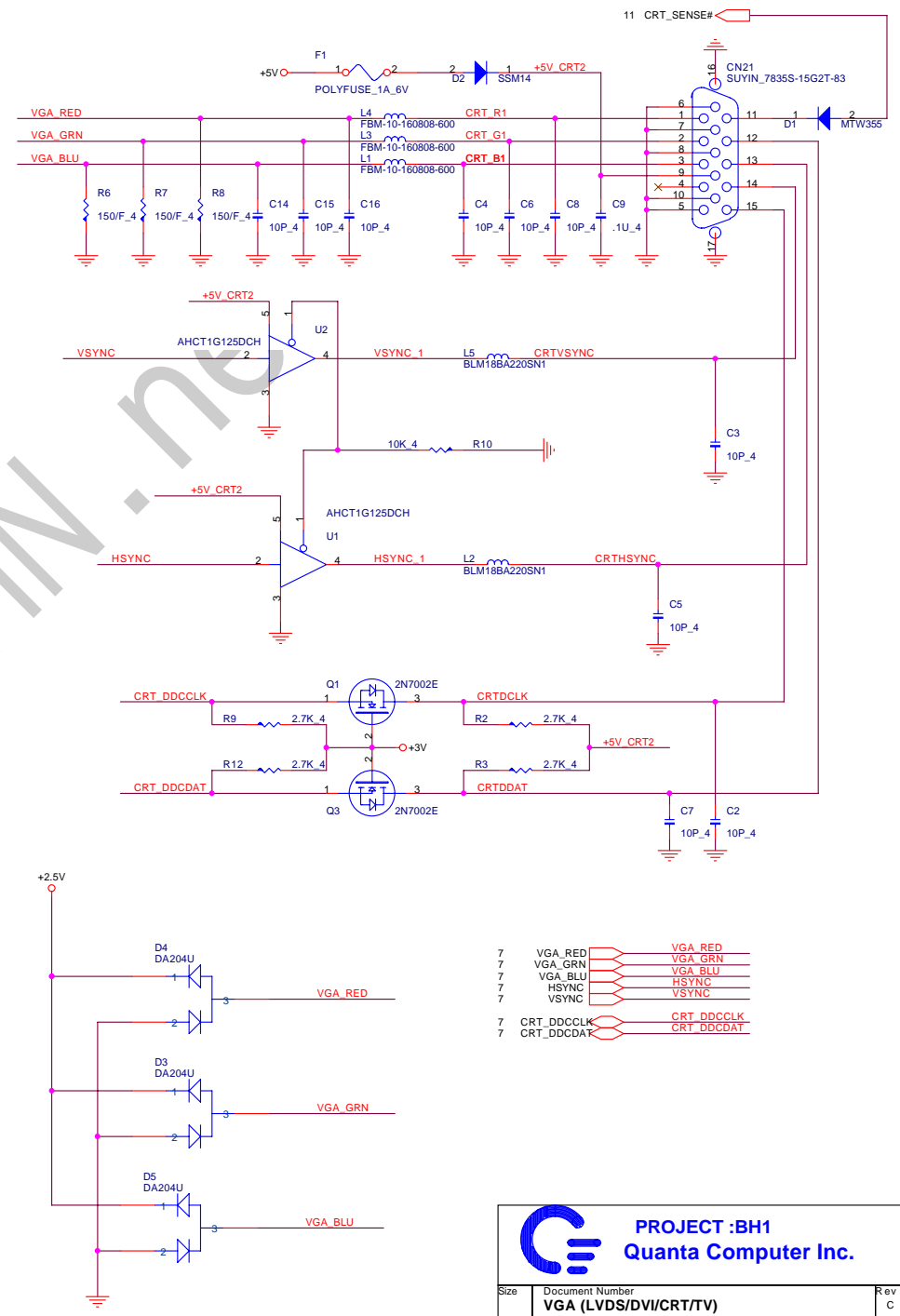
S-VIDEO




LVDS



CRT

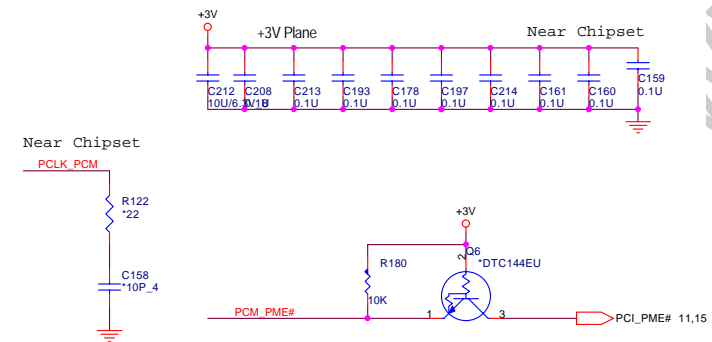
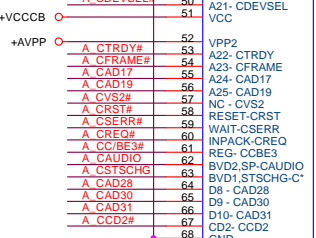
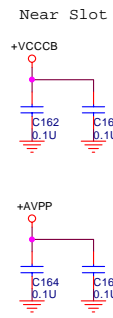
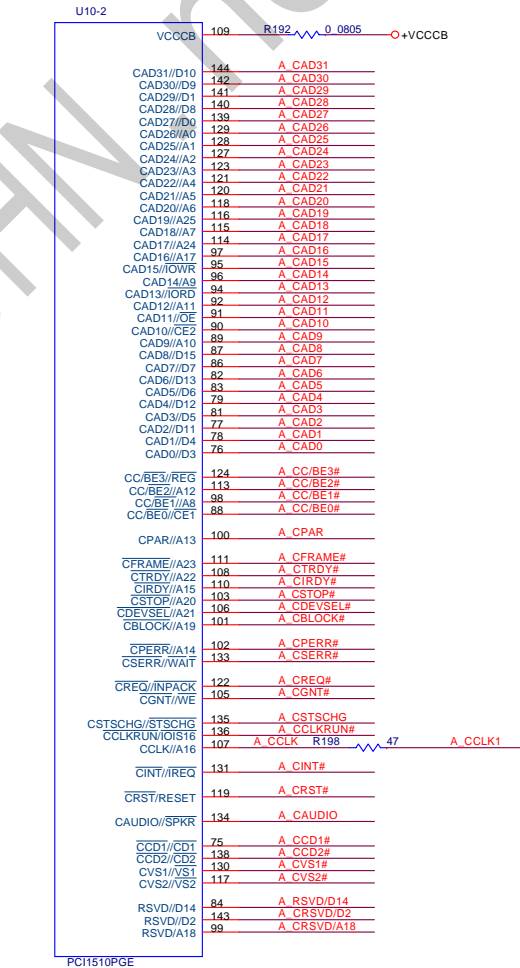
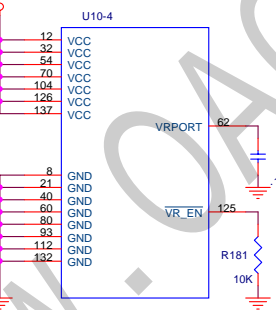
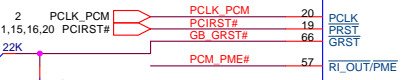
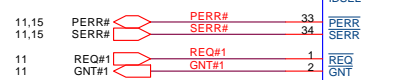
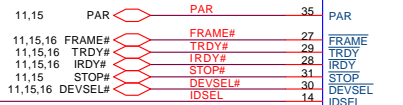
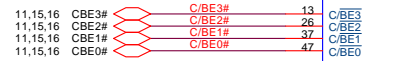
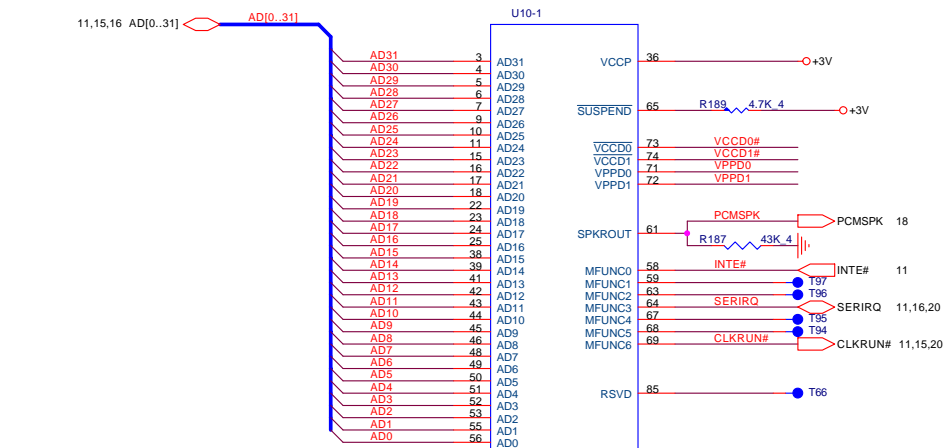
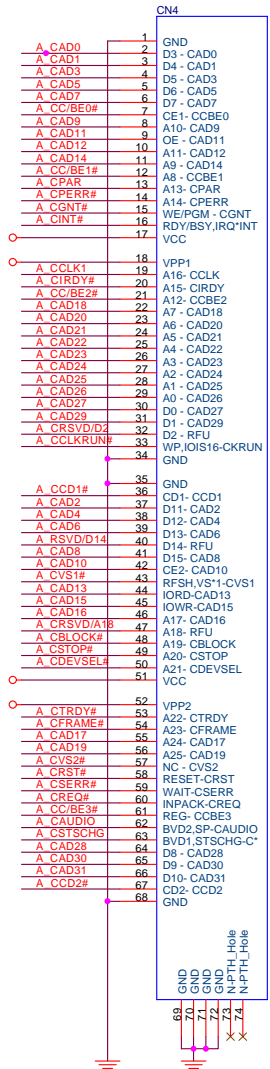
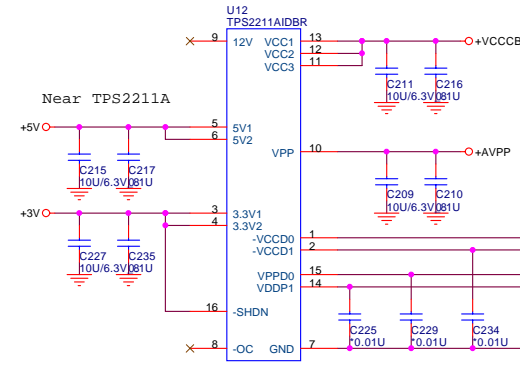
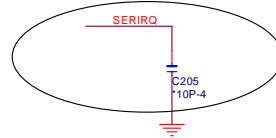




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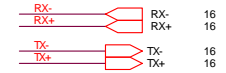
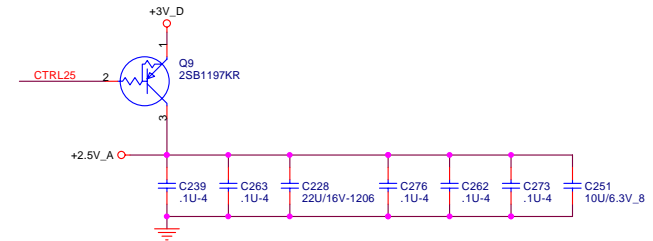
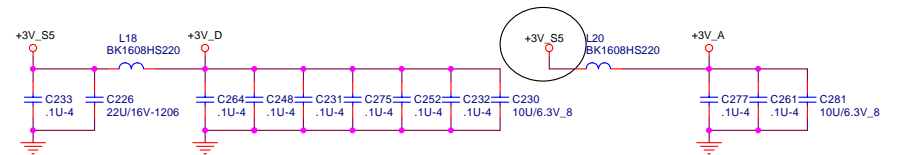
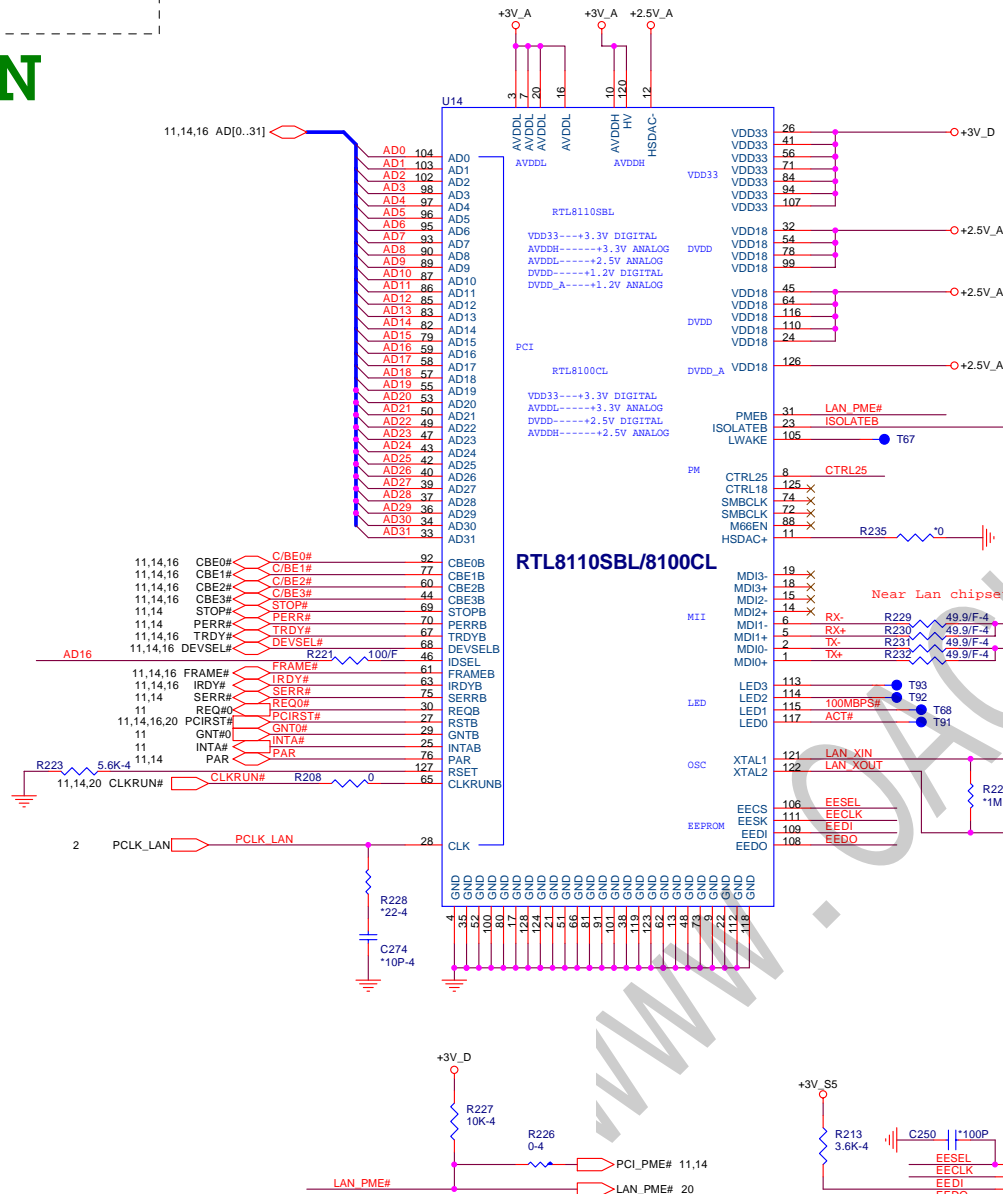
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**CBS**

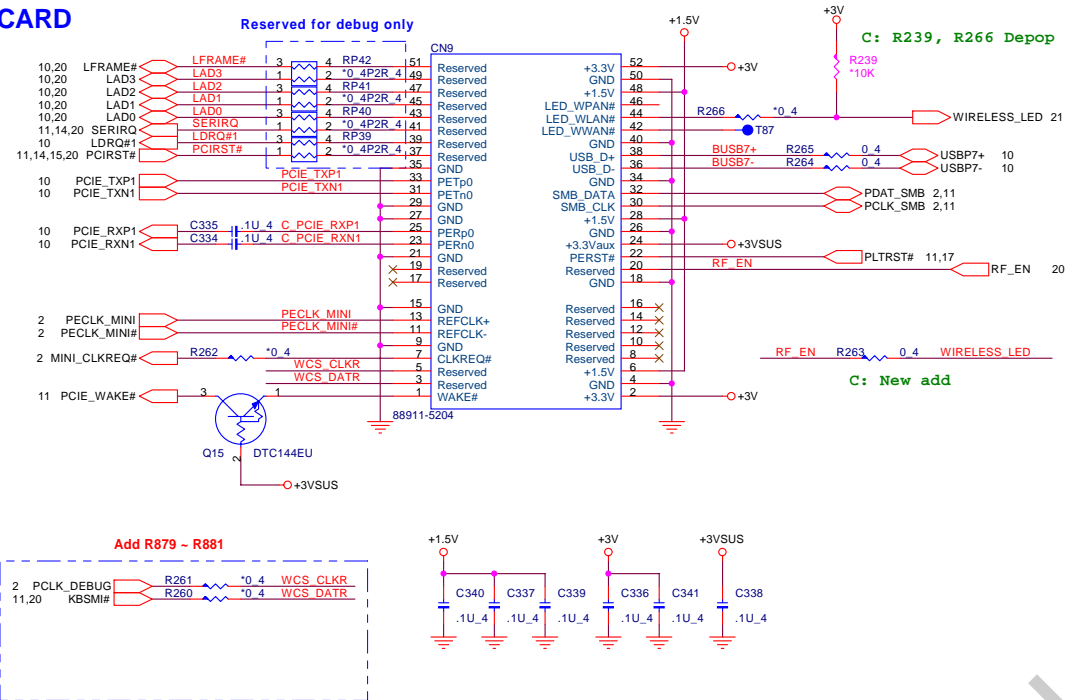


ID Select : AD16  
Interrupt Pin : INTA#  
Request Indicate : REQ0#  
Grant Indicate : GNT0#

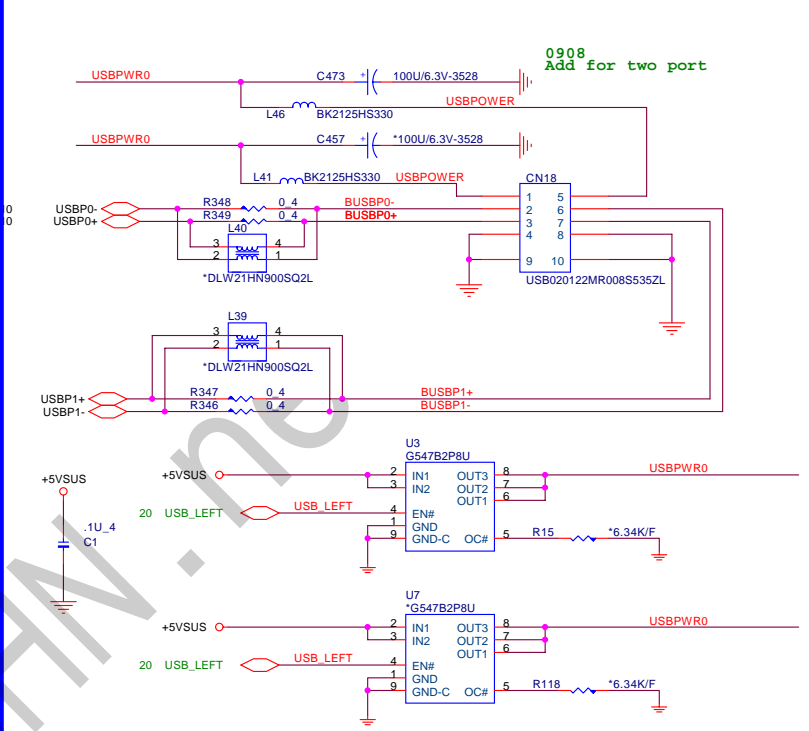
LAN



MINI CARD

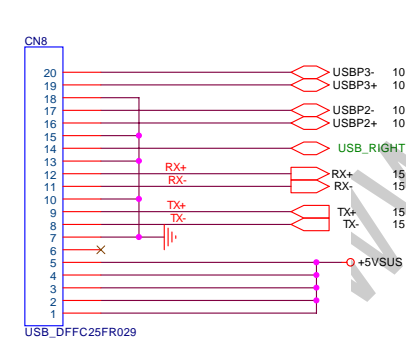


USB PORT x2



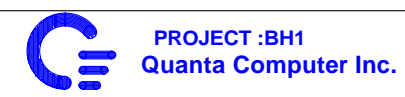
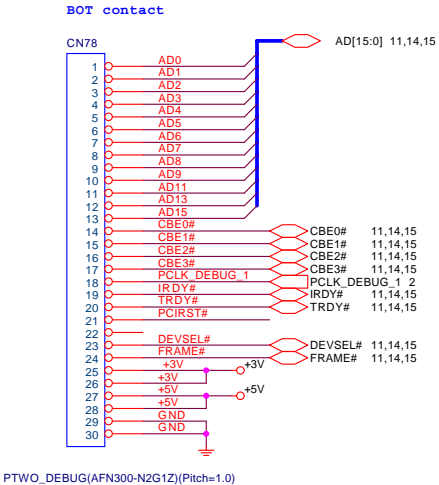
USB/LAN BOARD

1018 change to FPC type



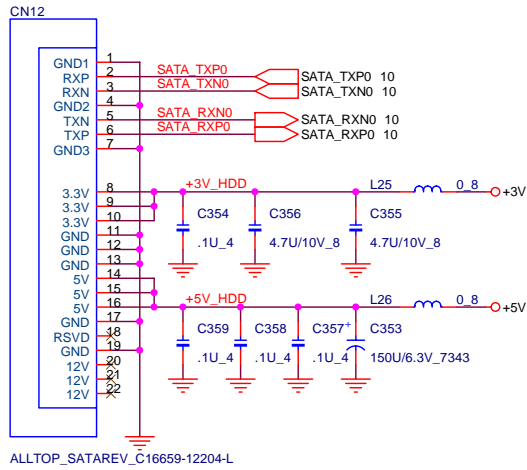
Debug card interface

Add for debug 0907

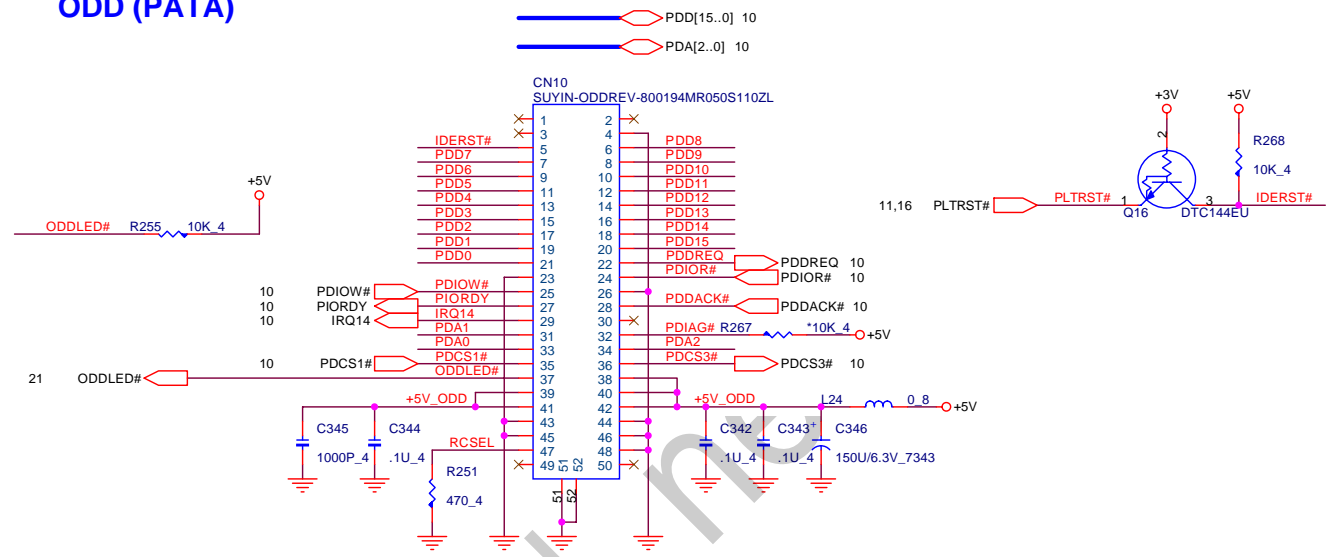




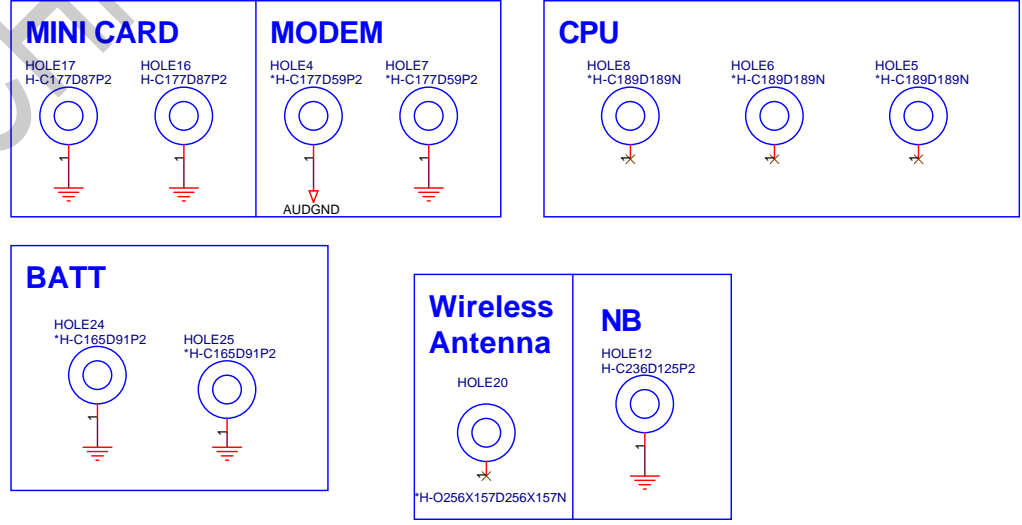
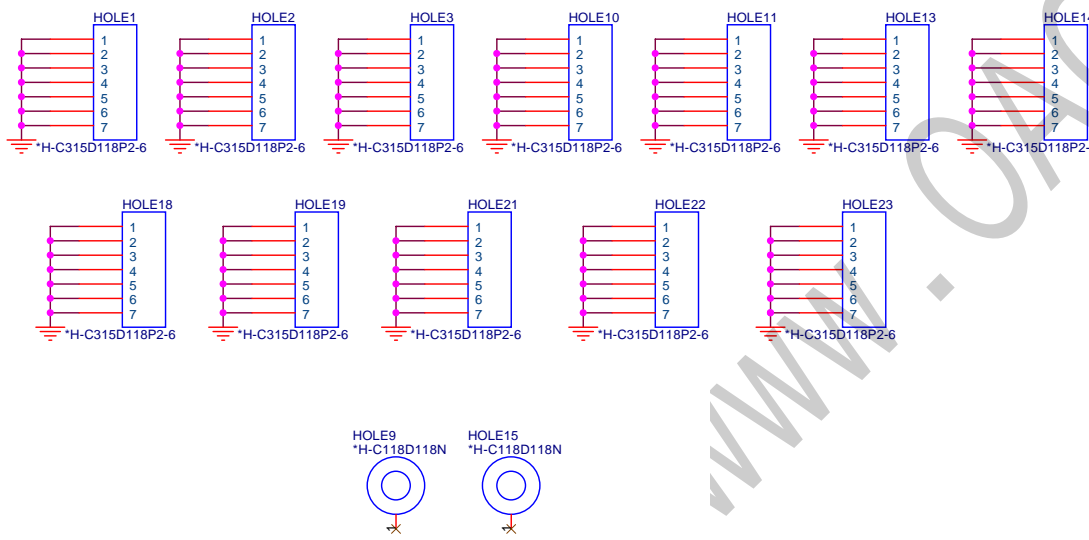
## HDD (SATA)




## ODD (PATA)



## SCREW HOLE

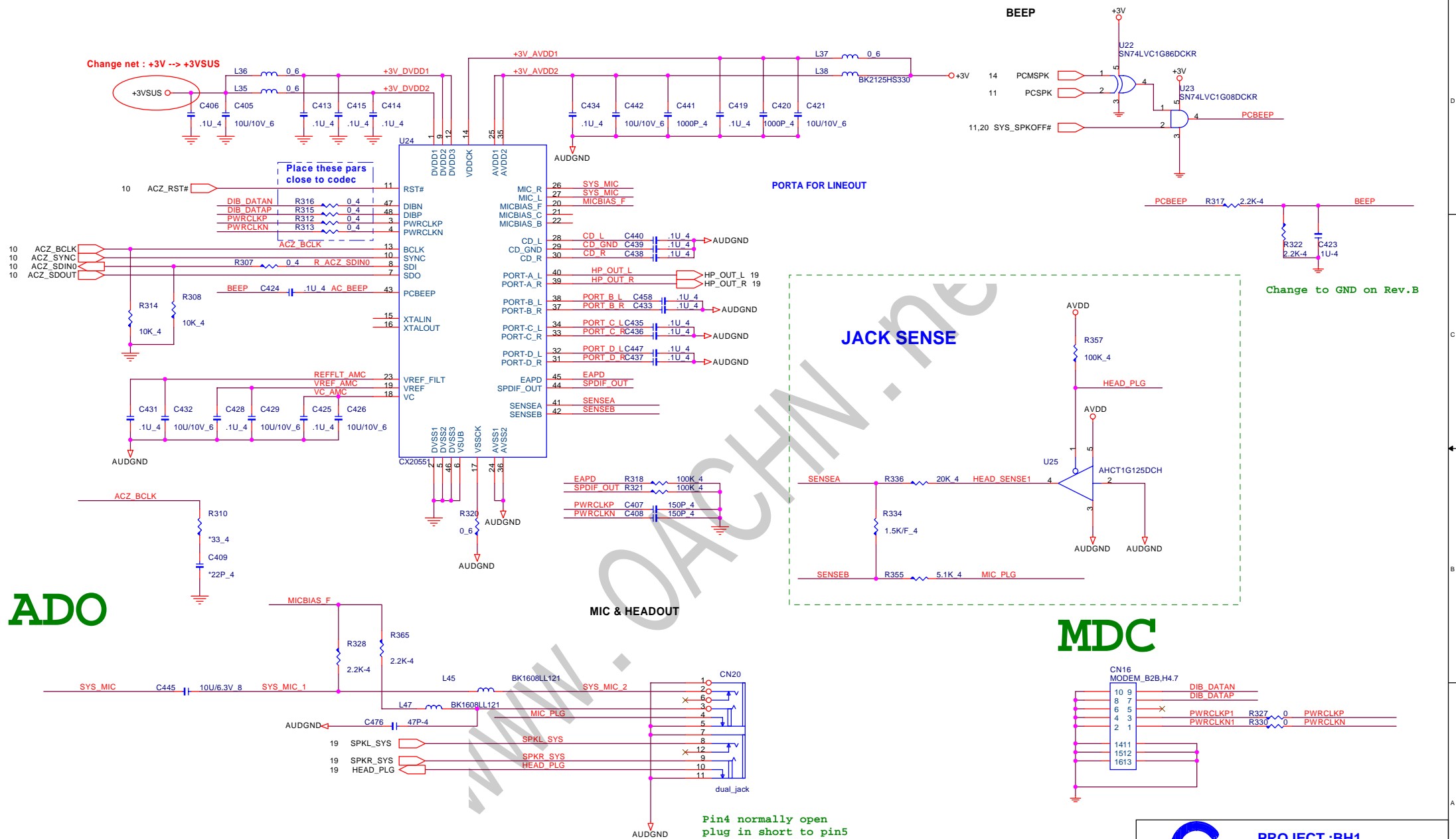




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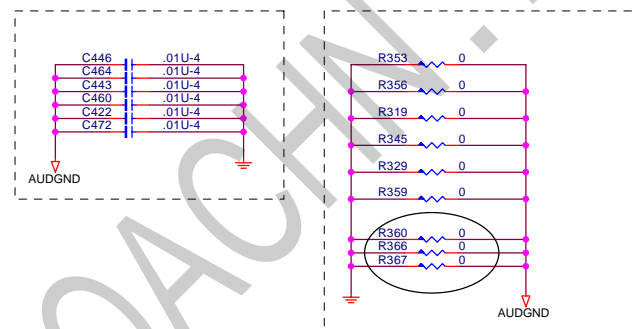
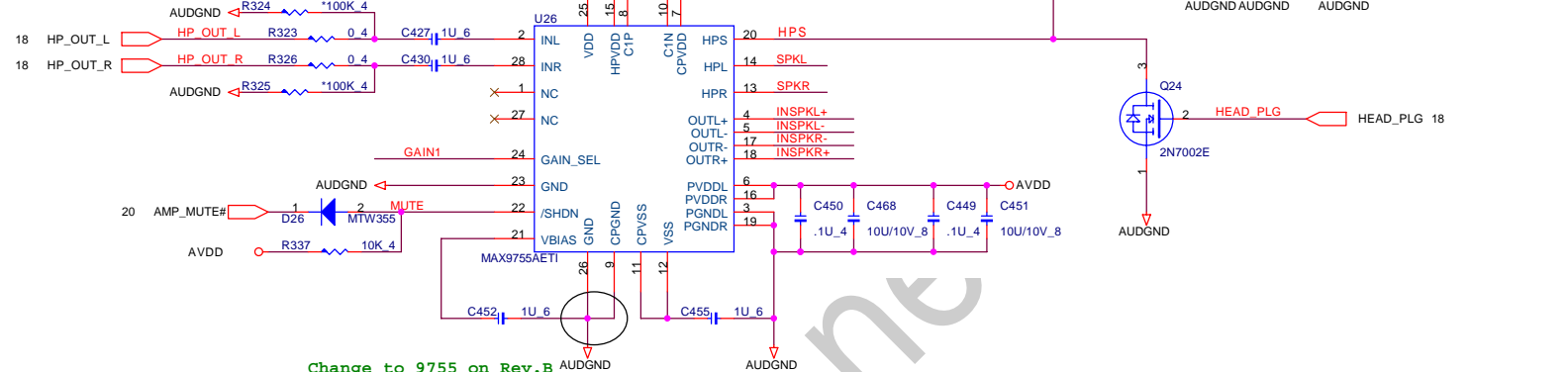
ADO



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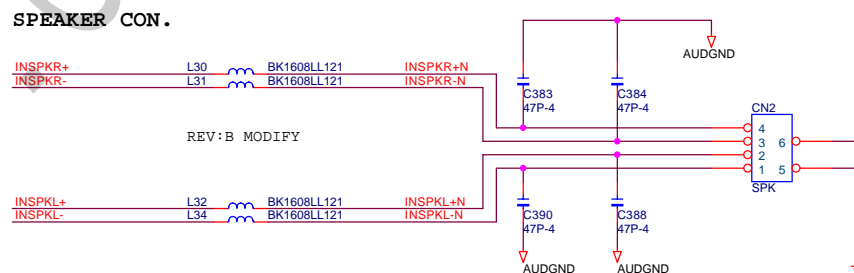
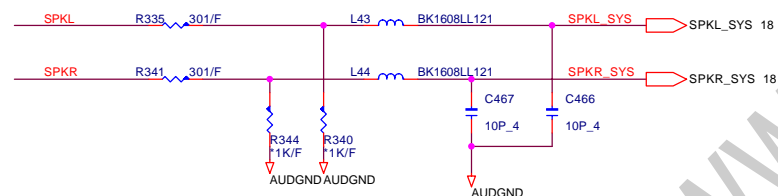
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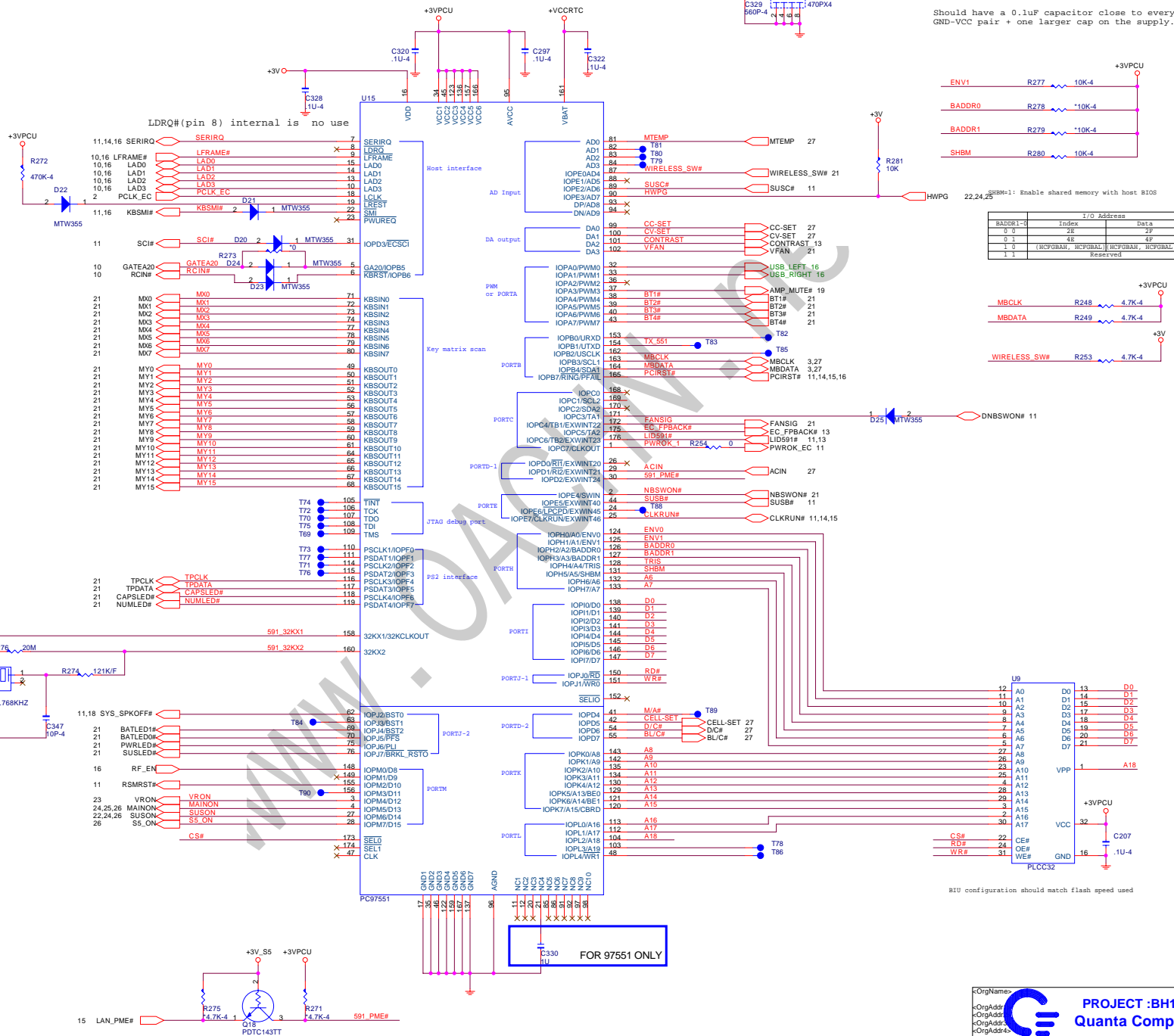
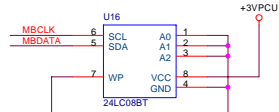
0	10.5	3
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B: add on connector side


SPEAKER CON.





I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)(HCFGBAH, HCFGBAL)	
1 1	Reserved	

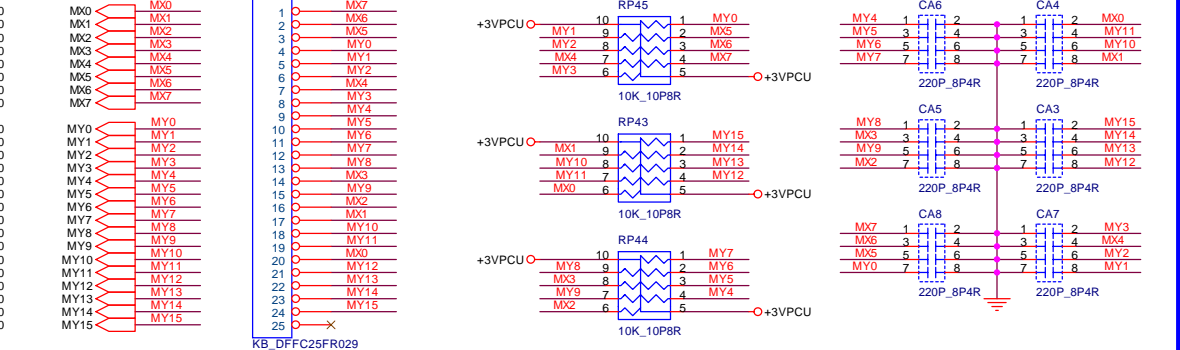
```
SHBM=1: Enable shared memory with host BIOS
```



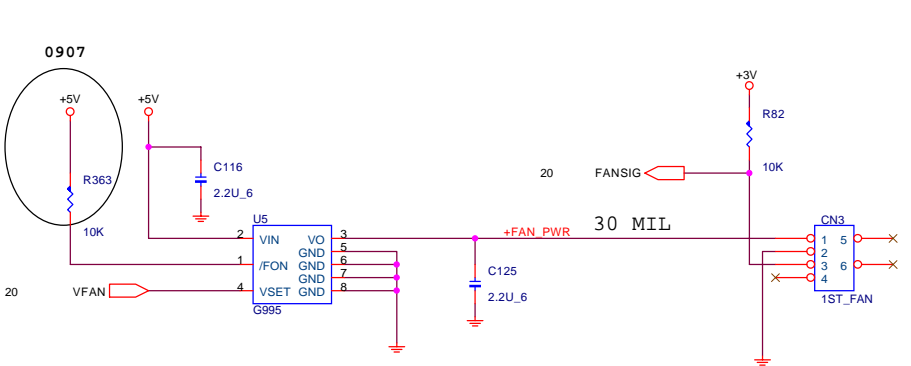
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INT K/B

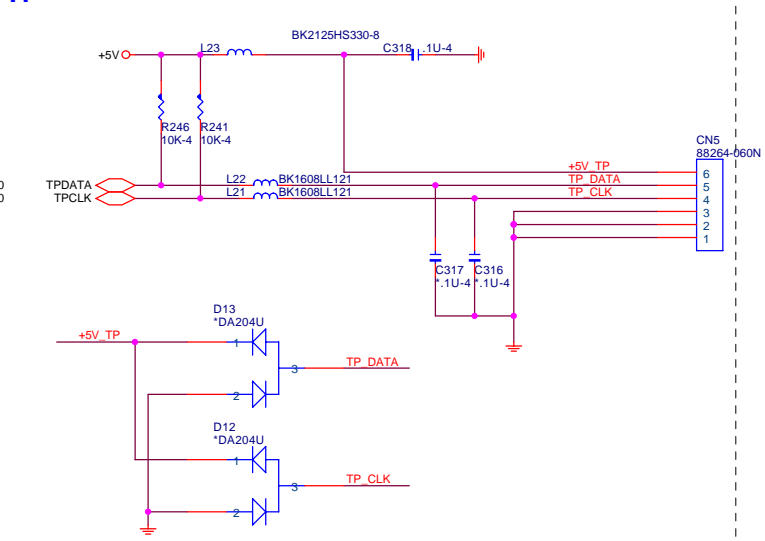


FAN CONTROL

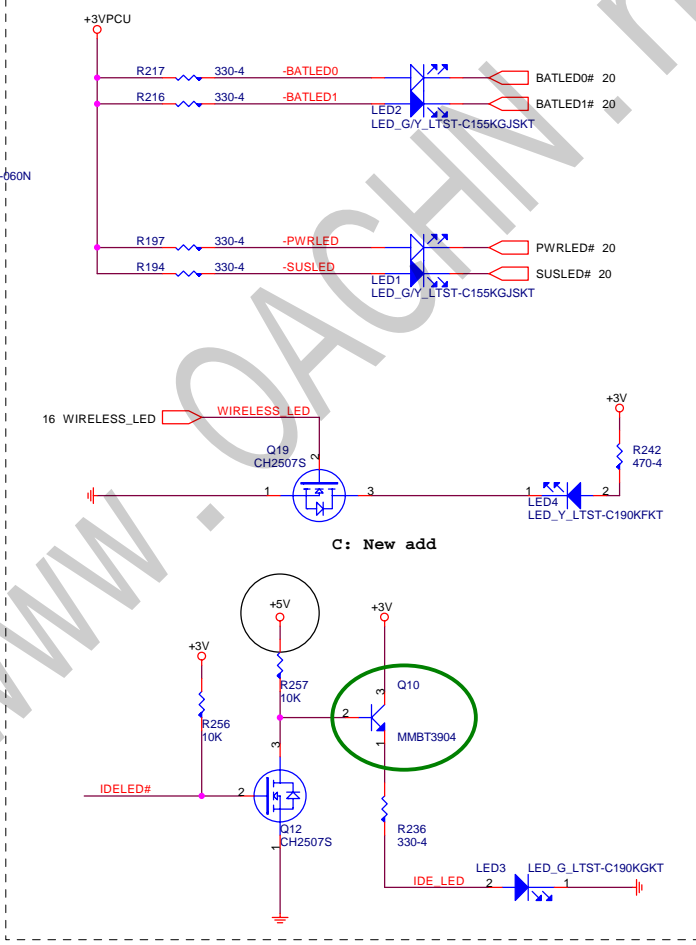


TP

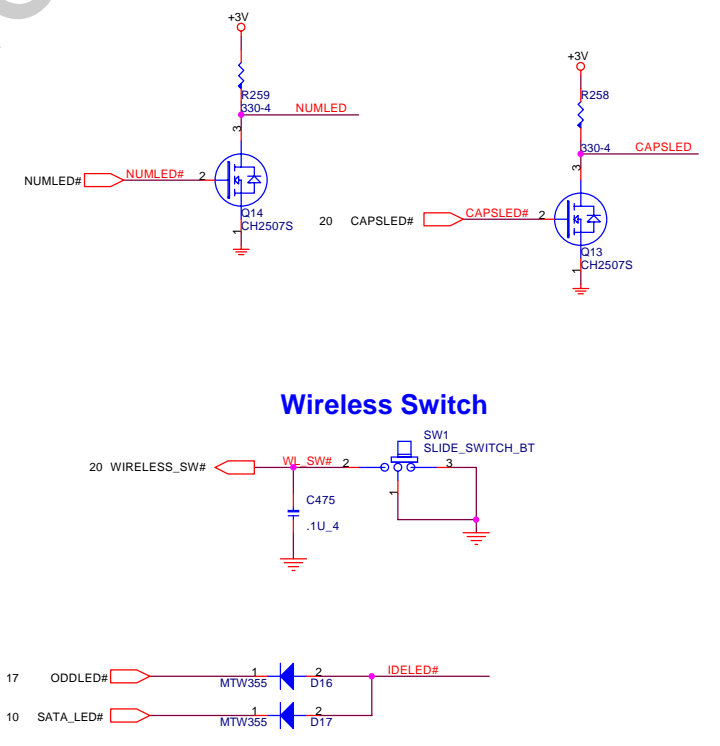
20 MIL



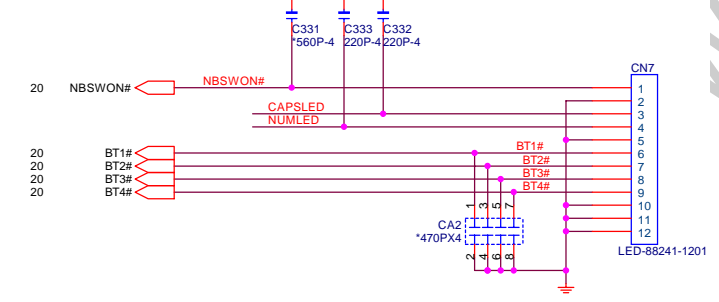
LED




Wireless Switch



LED BD

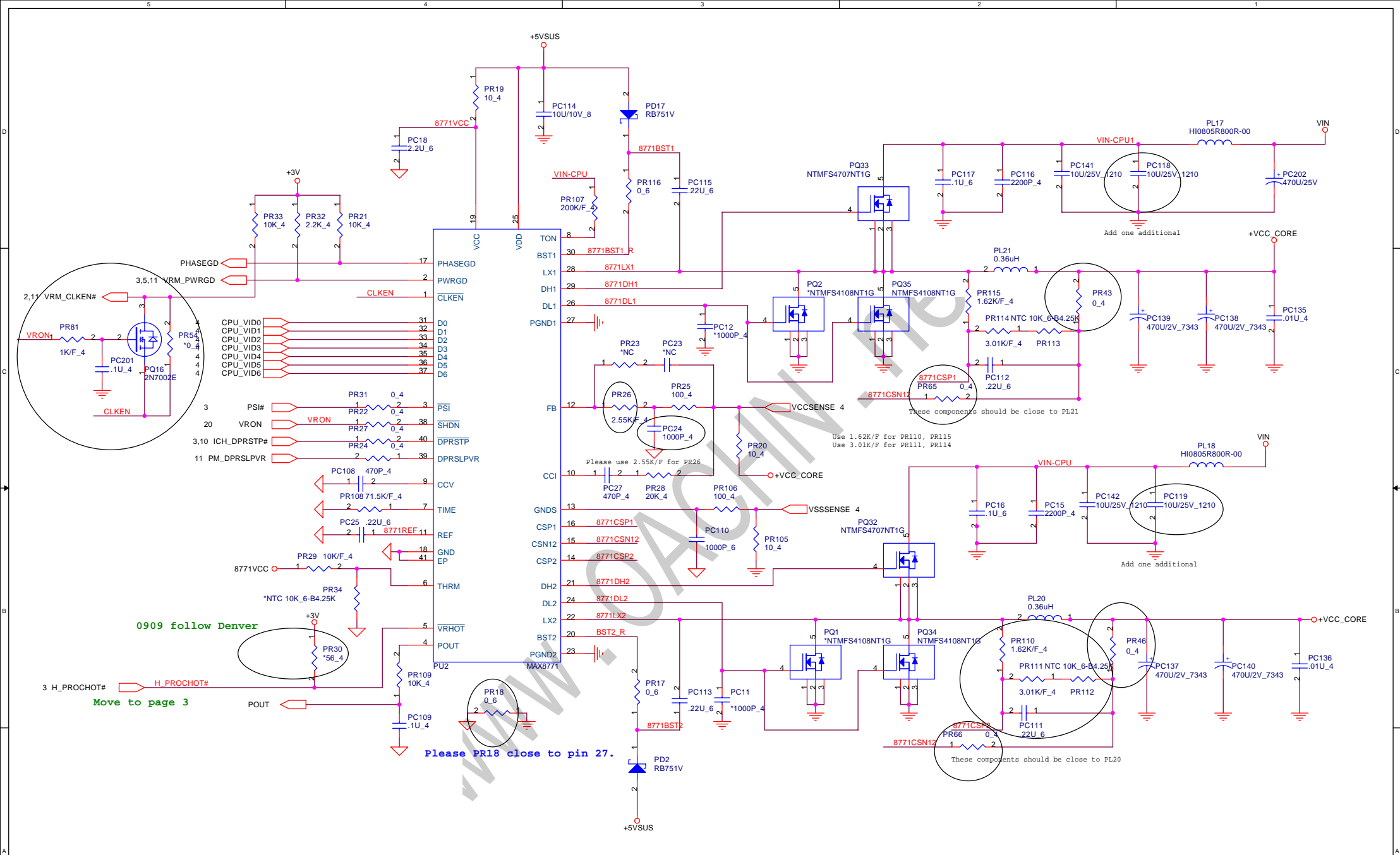




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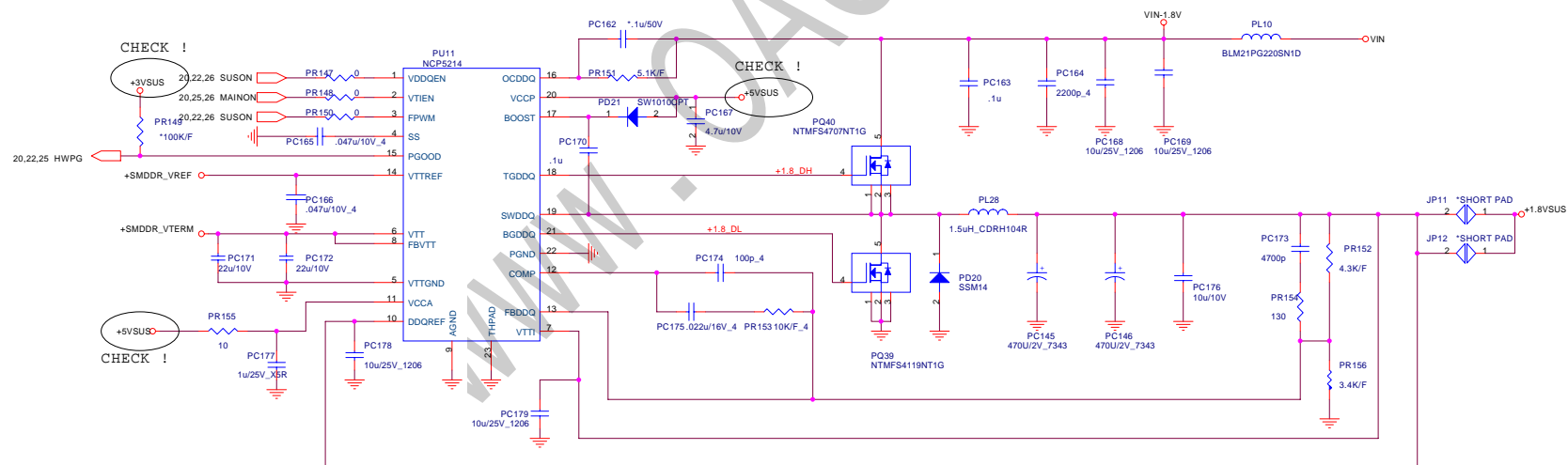
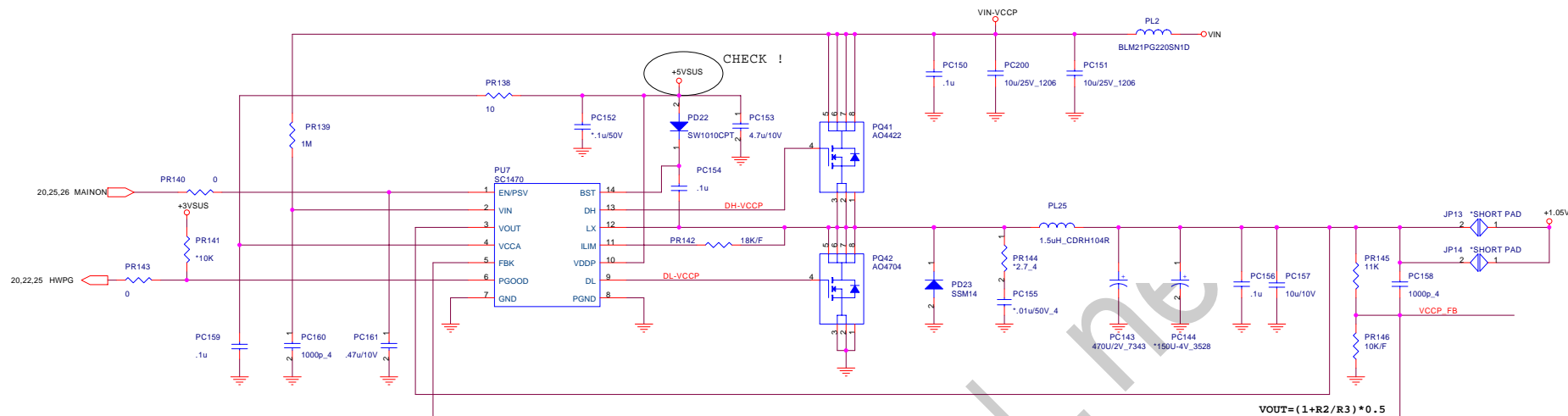
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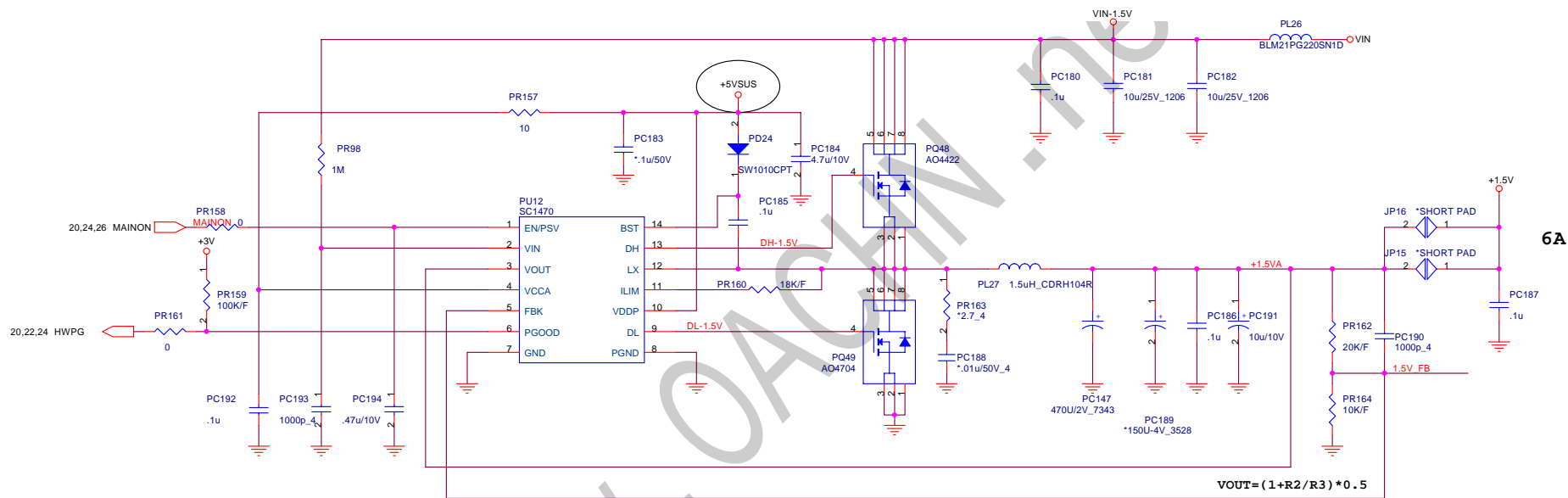


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	CPU CORE (MAX8771)	C
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	+1.5V	C
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