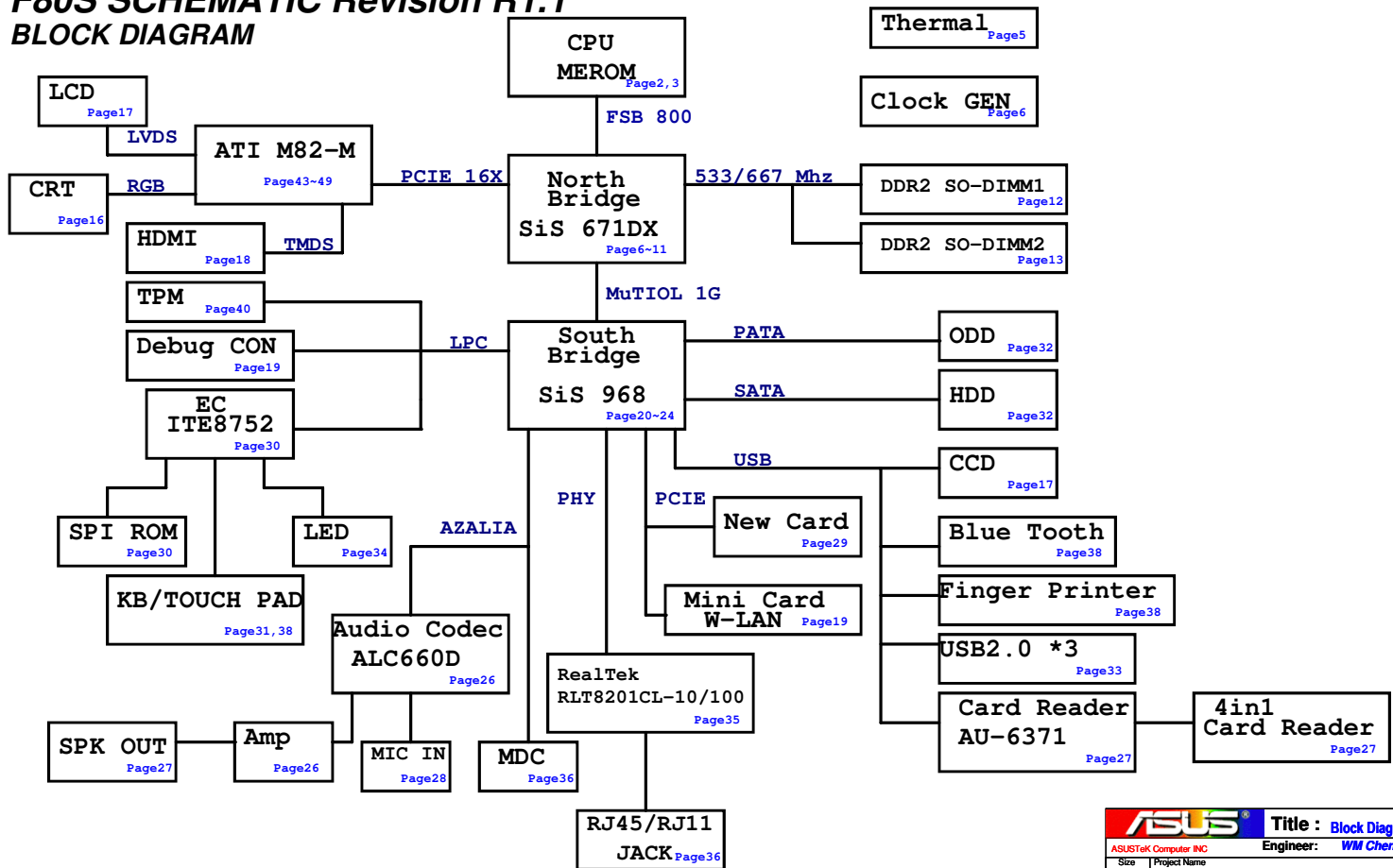
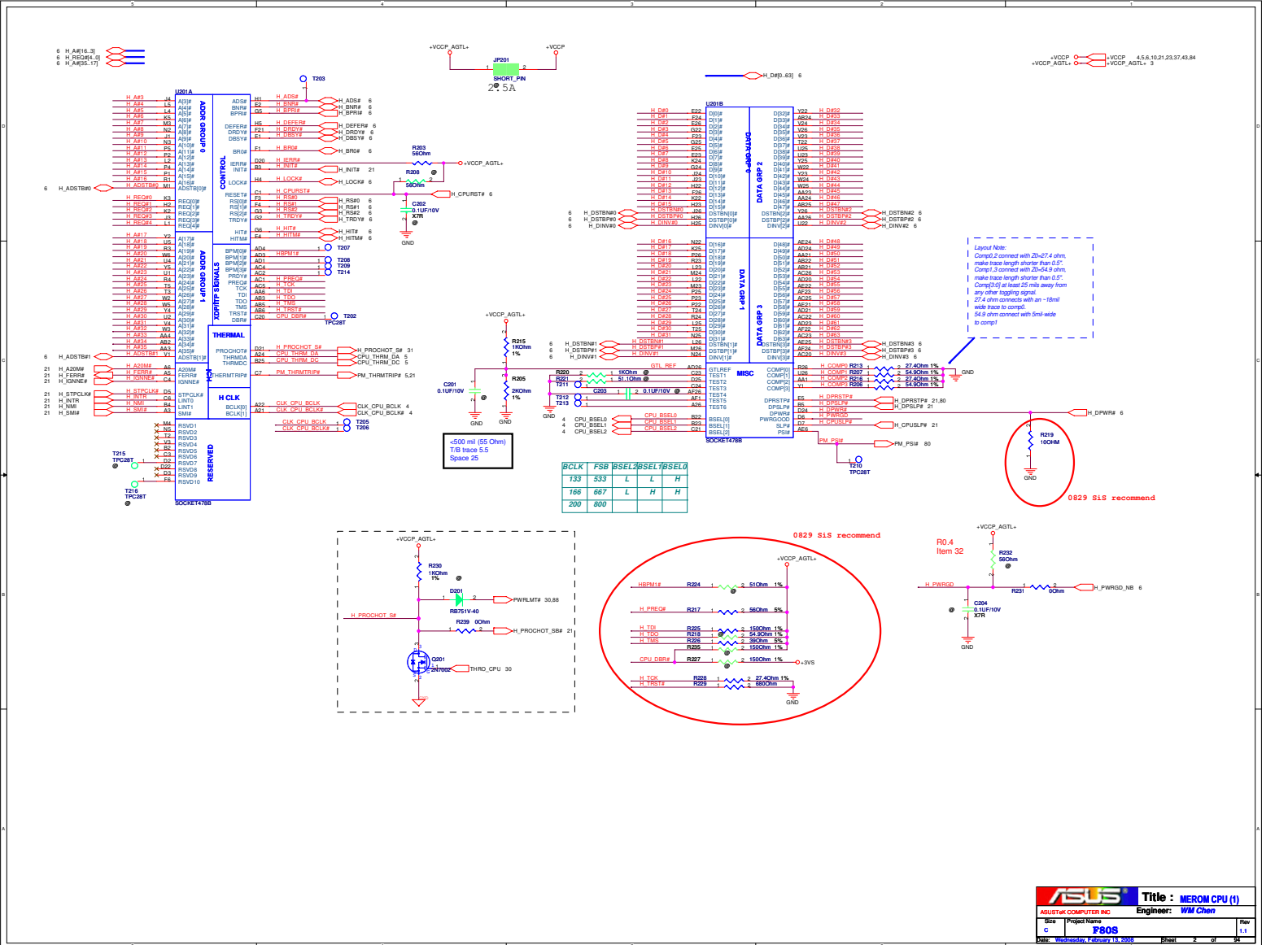
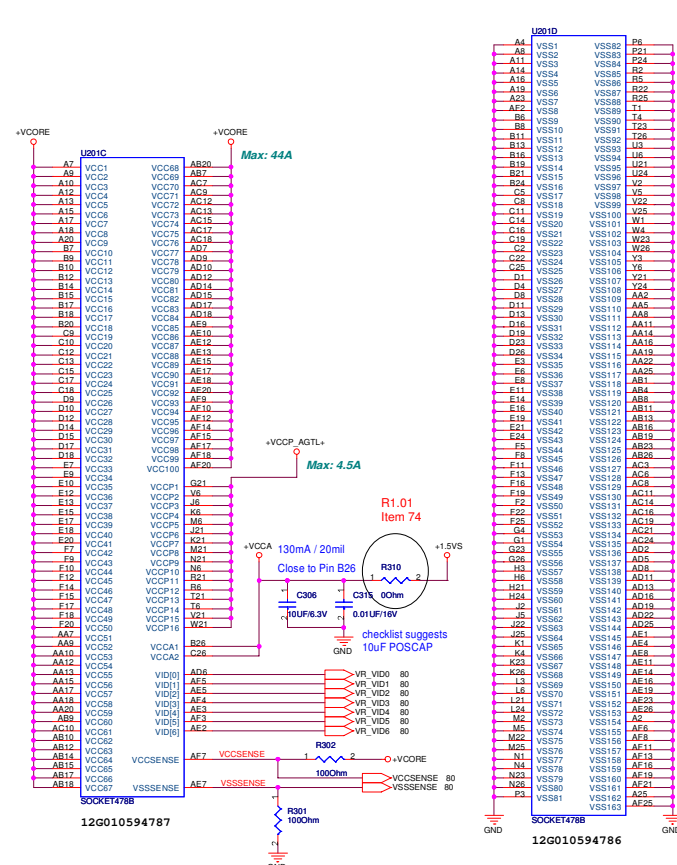


F80S SCHEMATIC Revision R1.1

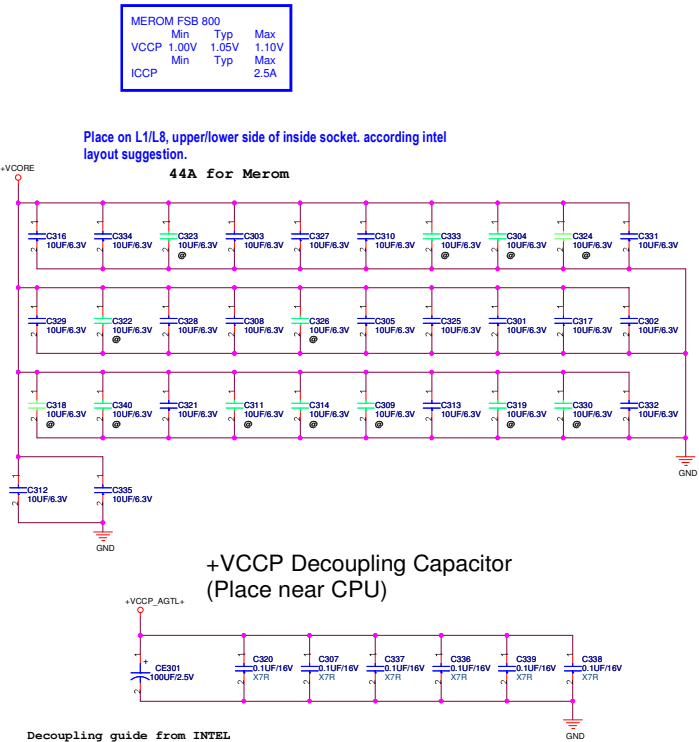
BLOCK DIAGRAM





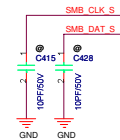


Layout Note:
VCCSENSE/VSSSENSE lines between the CPU and the VR should have a trace width of 18 mils on 7 mils spacing, with trace impedance of Zo=27.4 Ohm.
The VCCSENSE/VSSSENSE should be length matched to within 25 mils.
These resistors should be placed within 2 inch of the CPU.



Decoupling guide from INTEL

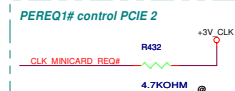
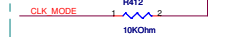
VCCORE	22uF/10V	* 32pcs
VCCP	330uF/2V	* 6pcs
VCCP	0.1uF	* 6pcs for CPU
VCCP	150uF	* 1pcs for CPU



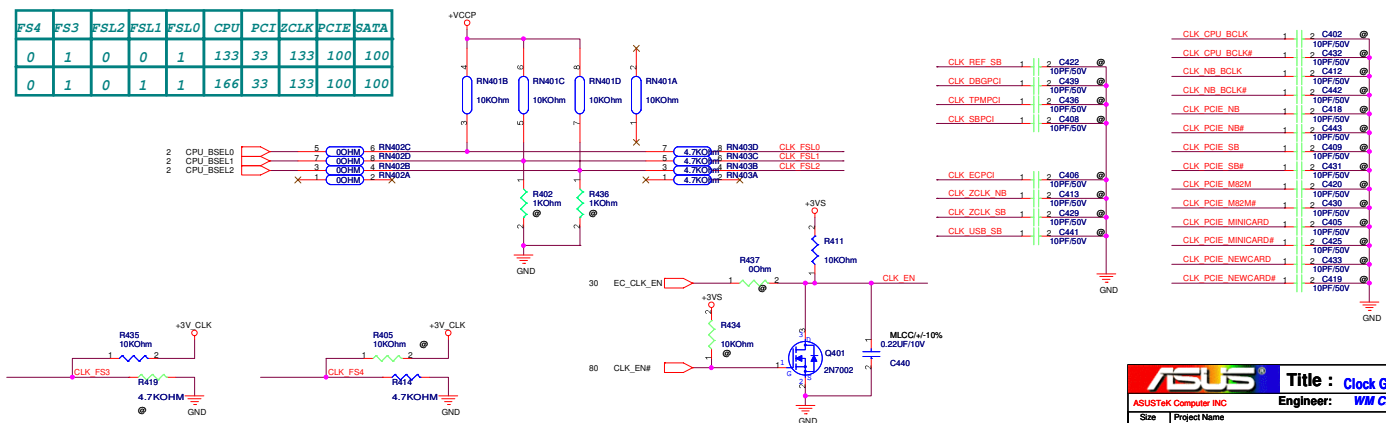
CLK_MODE

0 = Desktop Mode

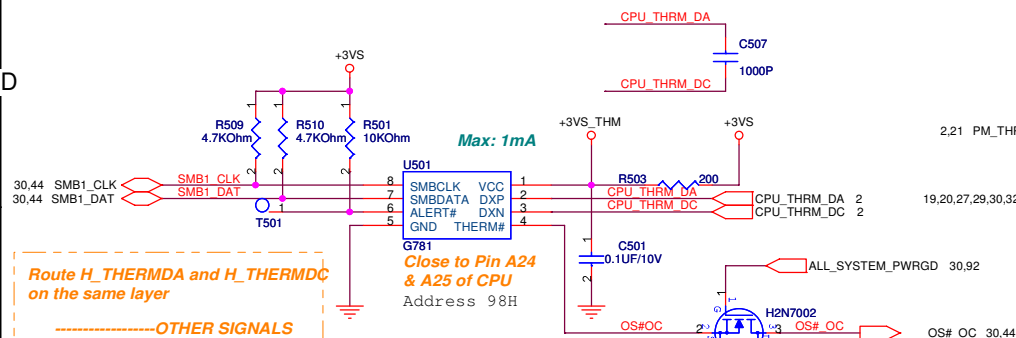
1 = Mobile Mode



FS4	FS3	FSL2	FSL1	FSL0	CPU	PCI	ZCLK	PCIE	SATA
0	1	0	0	1	133	33	133	100	100
0	1	0	1	1	166	33	133	100	100



4 CPU Thermal Sensor



Route H_THERMDA and H_THERMDC on the same layer

Max: 1mA

U501

SMBCLK	VCC	1
SMBDATA	DXP	2
ALERT#	DXN	3
GND	THERM#	4

G781

*Close to Pin A24
& A25 of CPU*

Address 98H

-----OTHER SIGNALS

20 mils

=====GND

10 mils

=====H_THERMDA(10 mils)

10 mils

=====H_THERMDC(10 mils)
10 mils

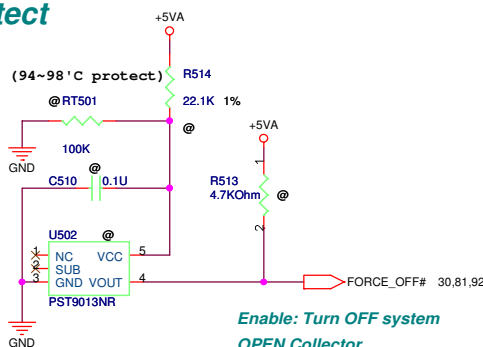
10 mils

=====GND
20 mile

20 mils

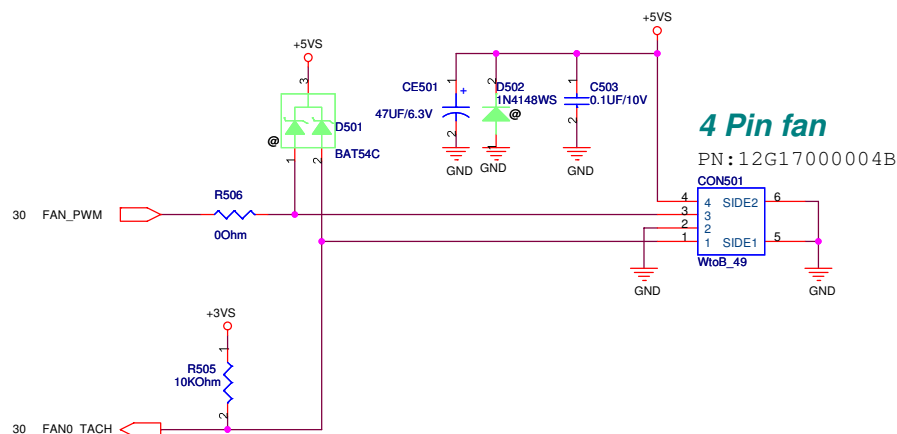
07

H/W Thermal Protect



**Enable: Turn OFF system
OPEN Collector**

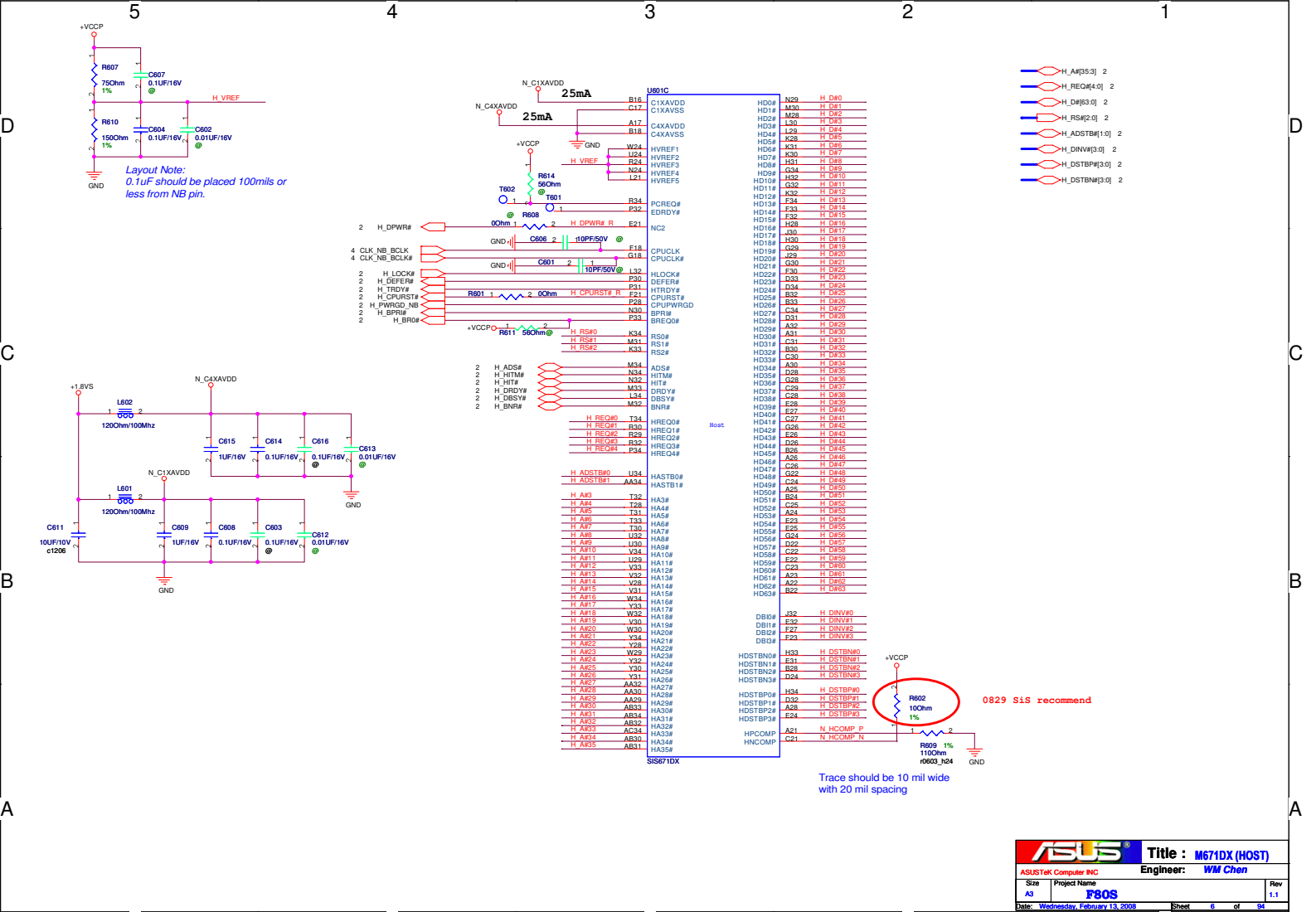
DC FAN Control

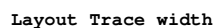


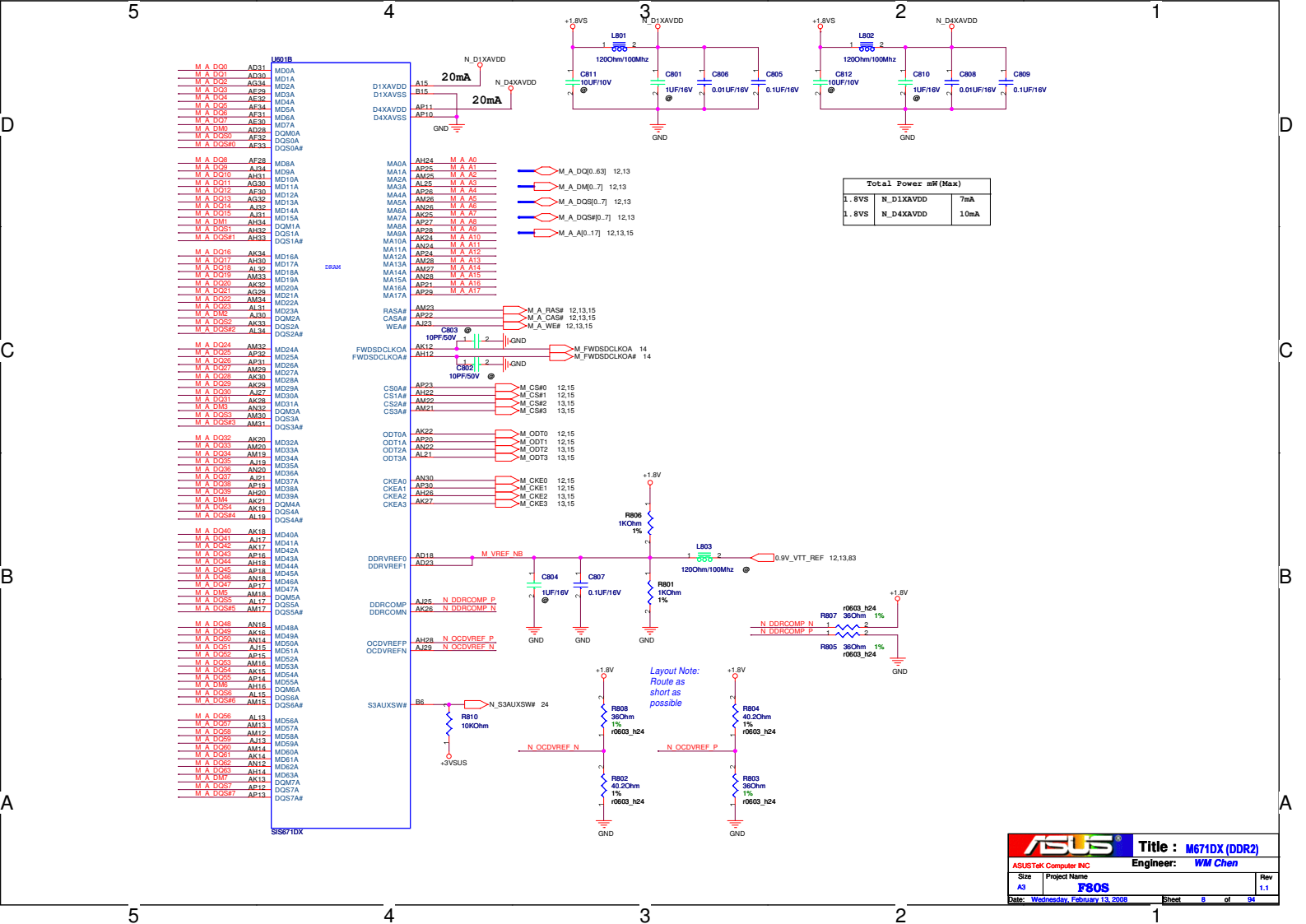
4 Pin fan

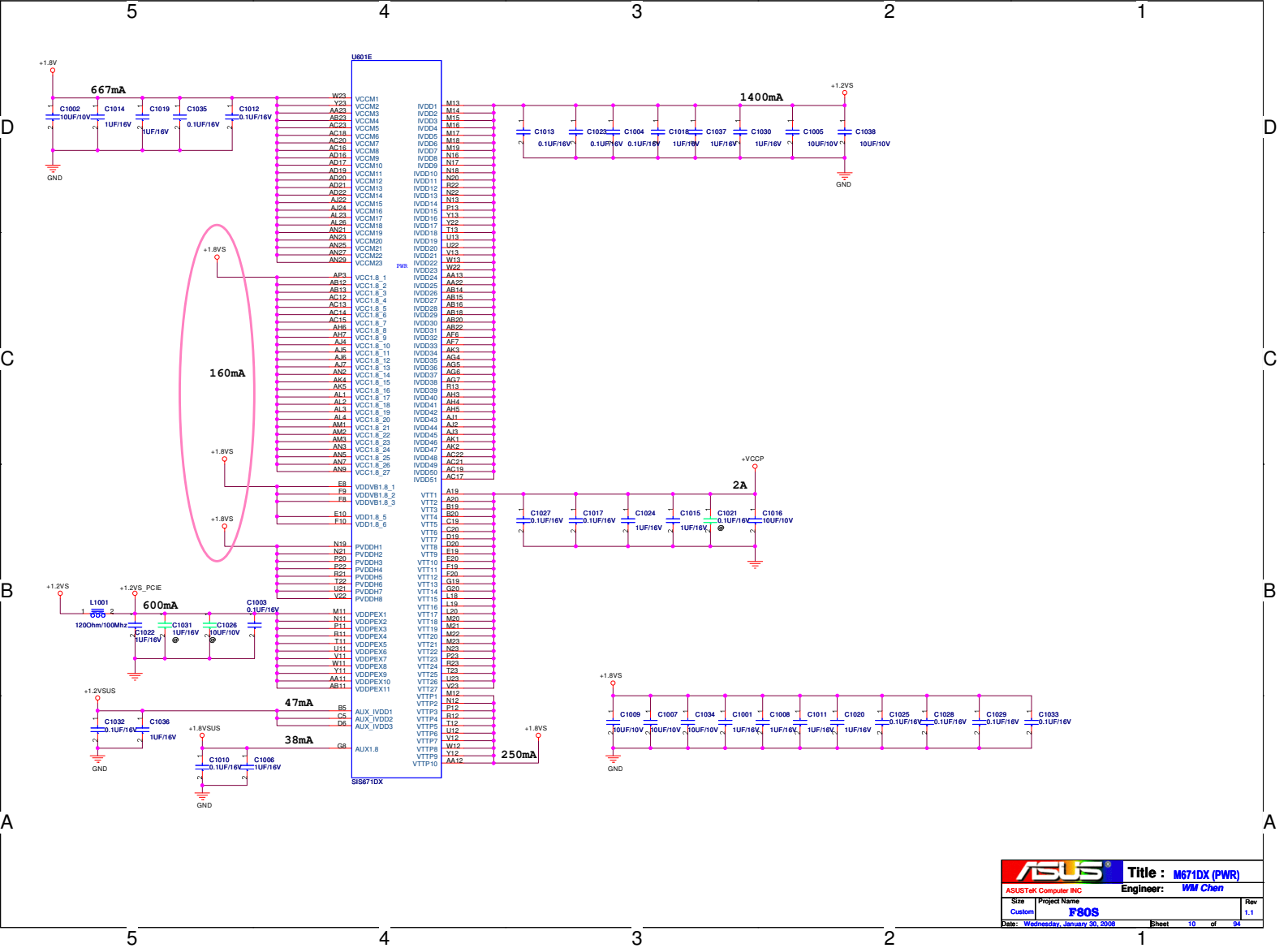
PN:12G17000004B

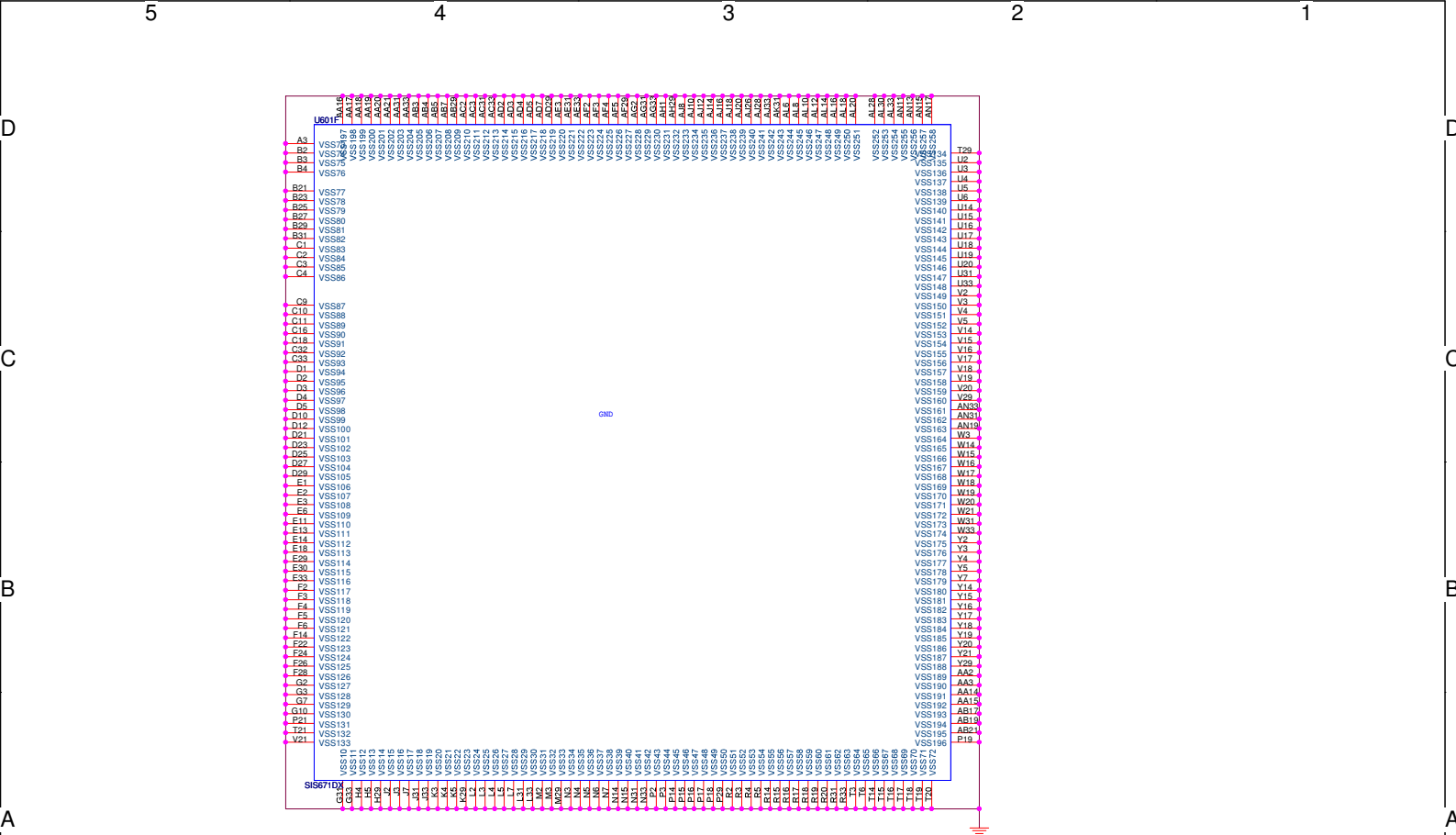
		Title : Thermal Sensor	
ASUSTeK Computer INC		Engineer: WM Chen	
Size A4	Project Name F80S	Rev 1.1	
Date: Wednesday, February 13, 2008		Sheet 5	of 94





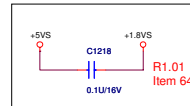
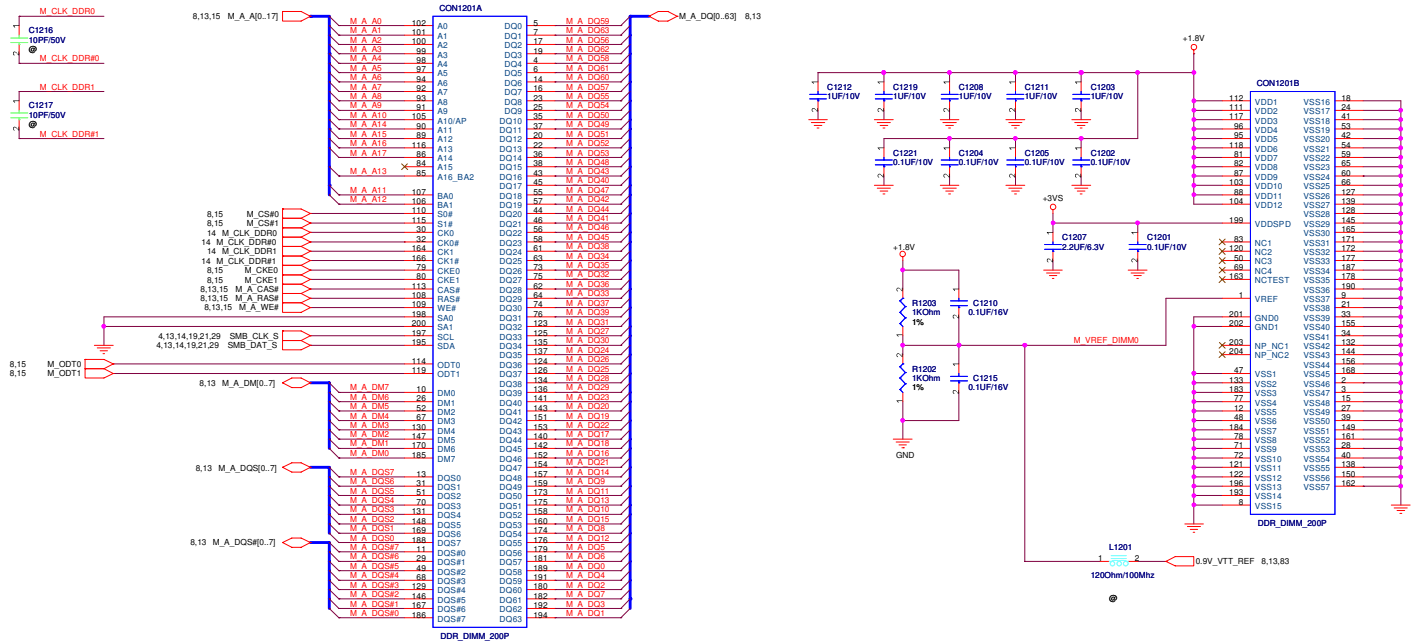




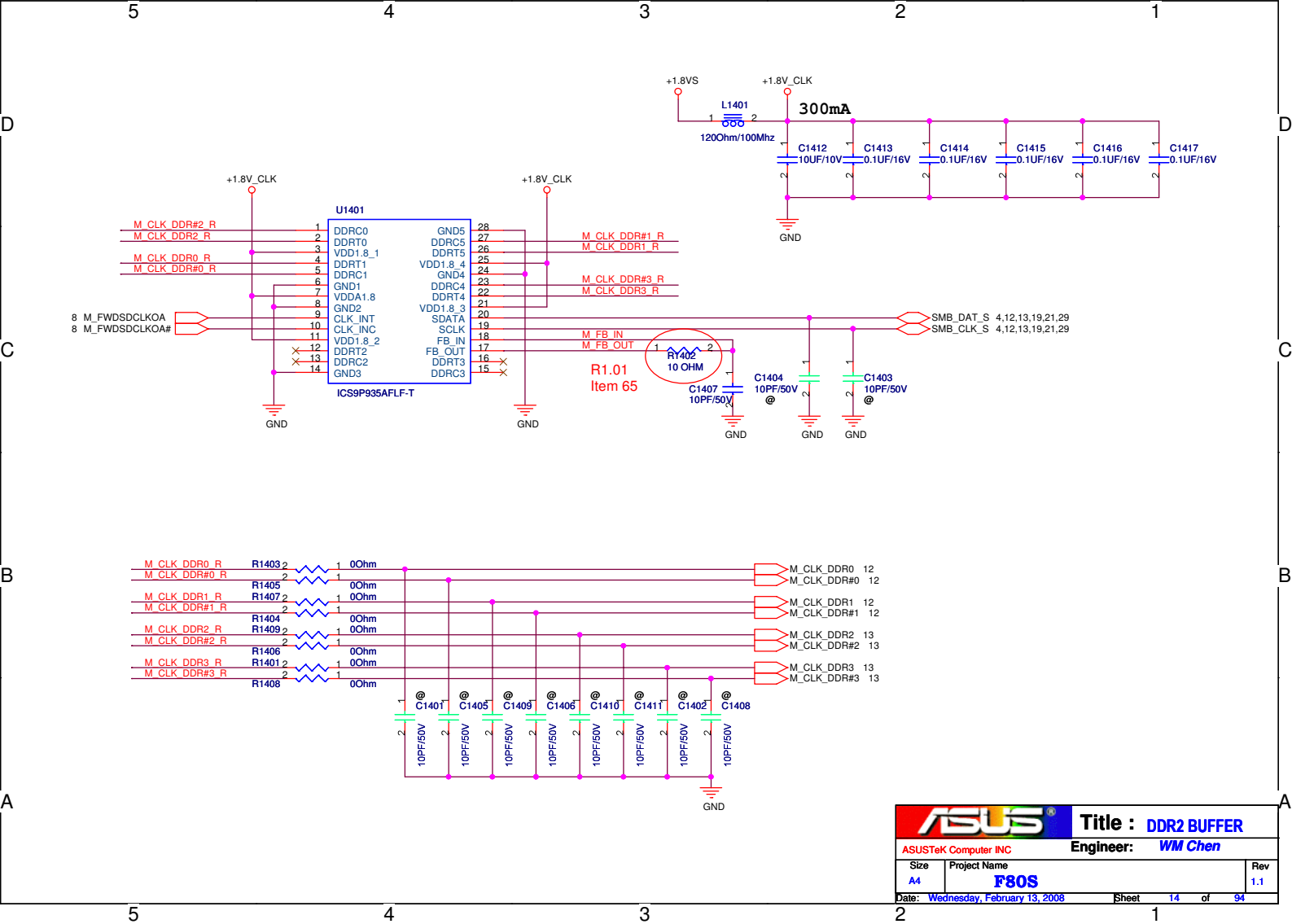


Reverse Type

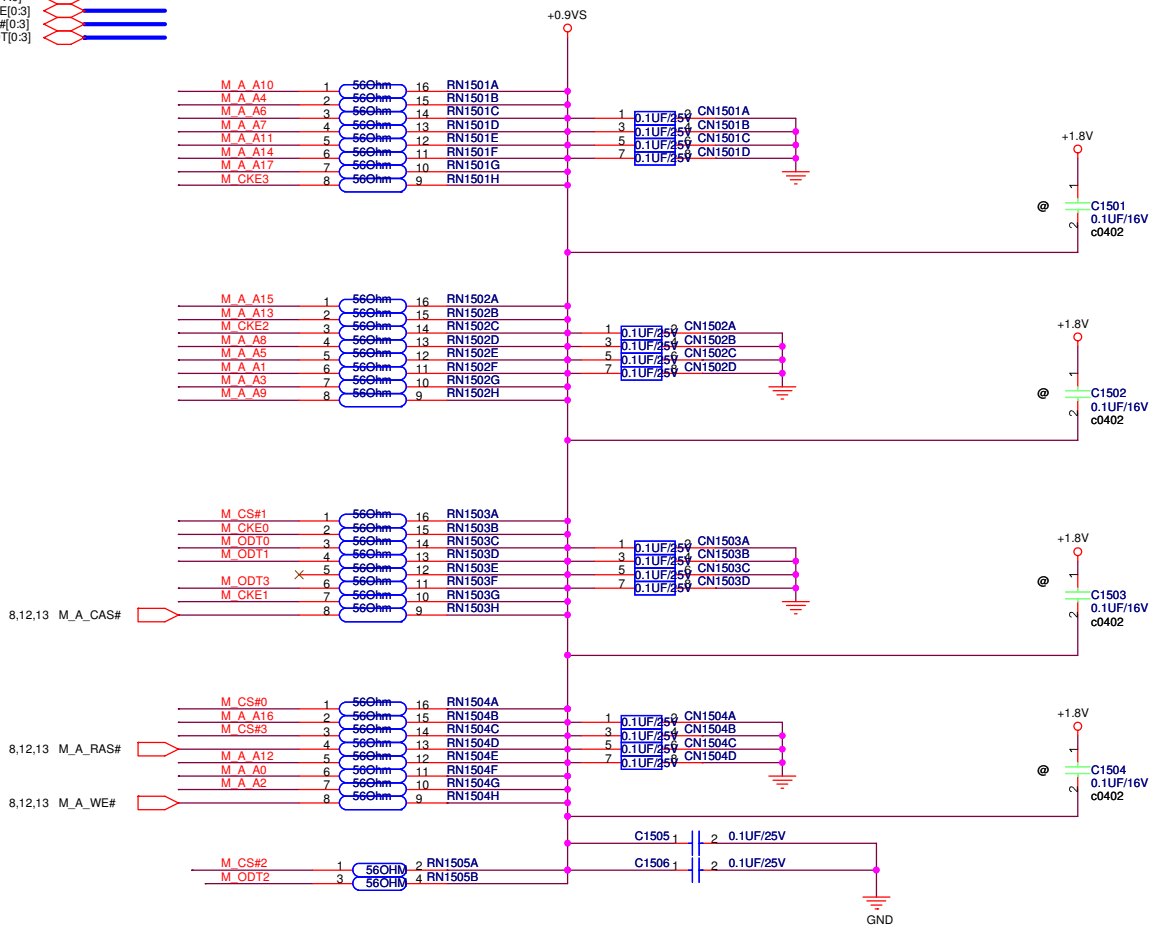
R0.4
Item 30
: 12G025122006 H:5.2mm







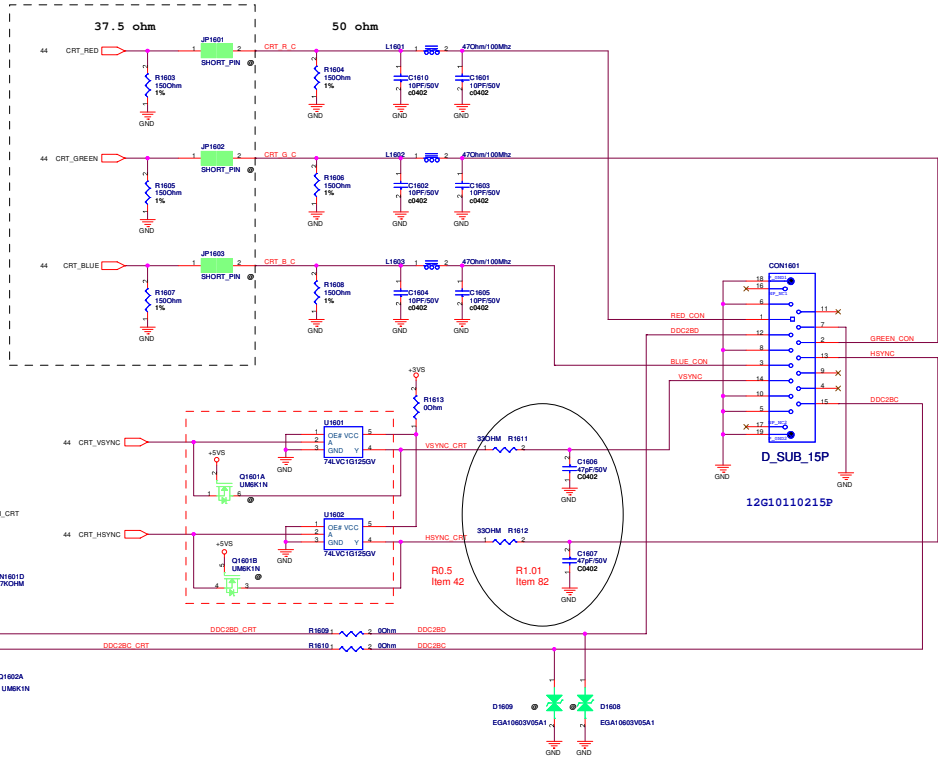
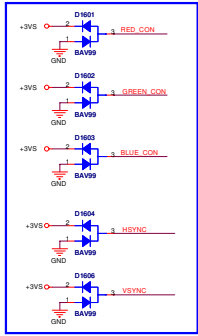
8,12,13 M_A_A[17:0]
8,12,13 M_CKE[0:3]
8,12,13 M_CS#[0:3]
8,12,13 M_ODT[0:3]



Layout note:
Place one cap close to every 2 pull-up resistors terminated to +0.9VS

Layout note: Near U4401

PLACE ESD Diodes near connector



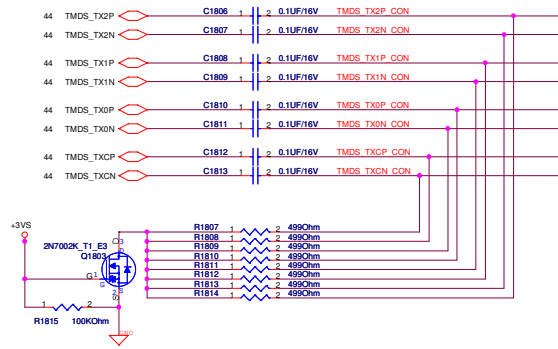
When AC in, plug cable
varify hi Voltage?

Cable Requirement:
Impedence: 100 ohm +/- 10%
Length Mismatch <= 10 mils
Twisted Pair(Not Ribbon)
Maximum Length <= 16"

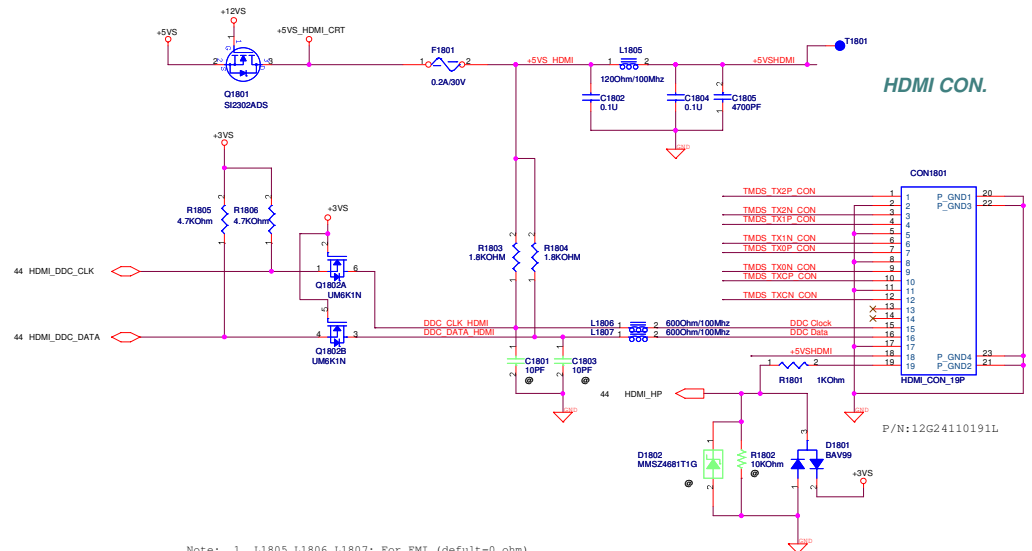
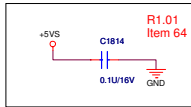
		Title : LVDS & INVERTER	
ASUSTek COMPUTER INC		Engineer: WM Chen	
Size Custom	Project Name F80S	Rev 1.1	
Date: Wednesday, February 13, 2008		Sheet 17	of 94

HDMI

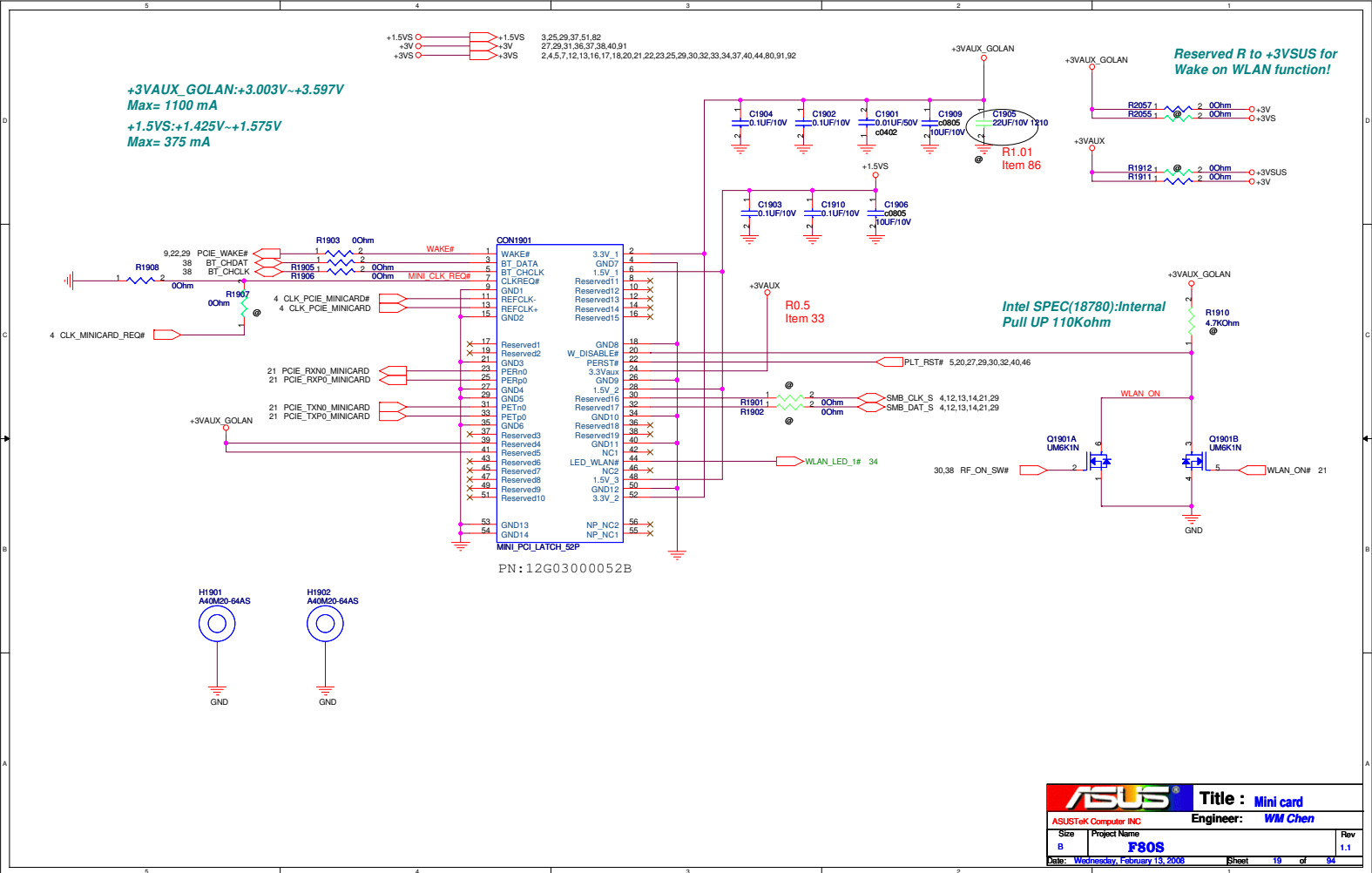
near the HDMI connector

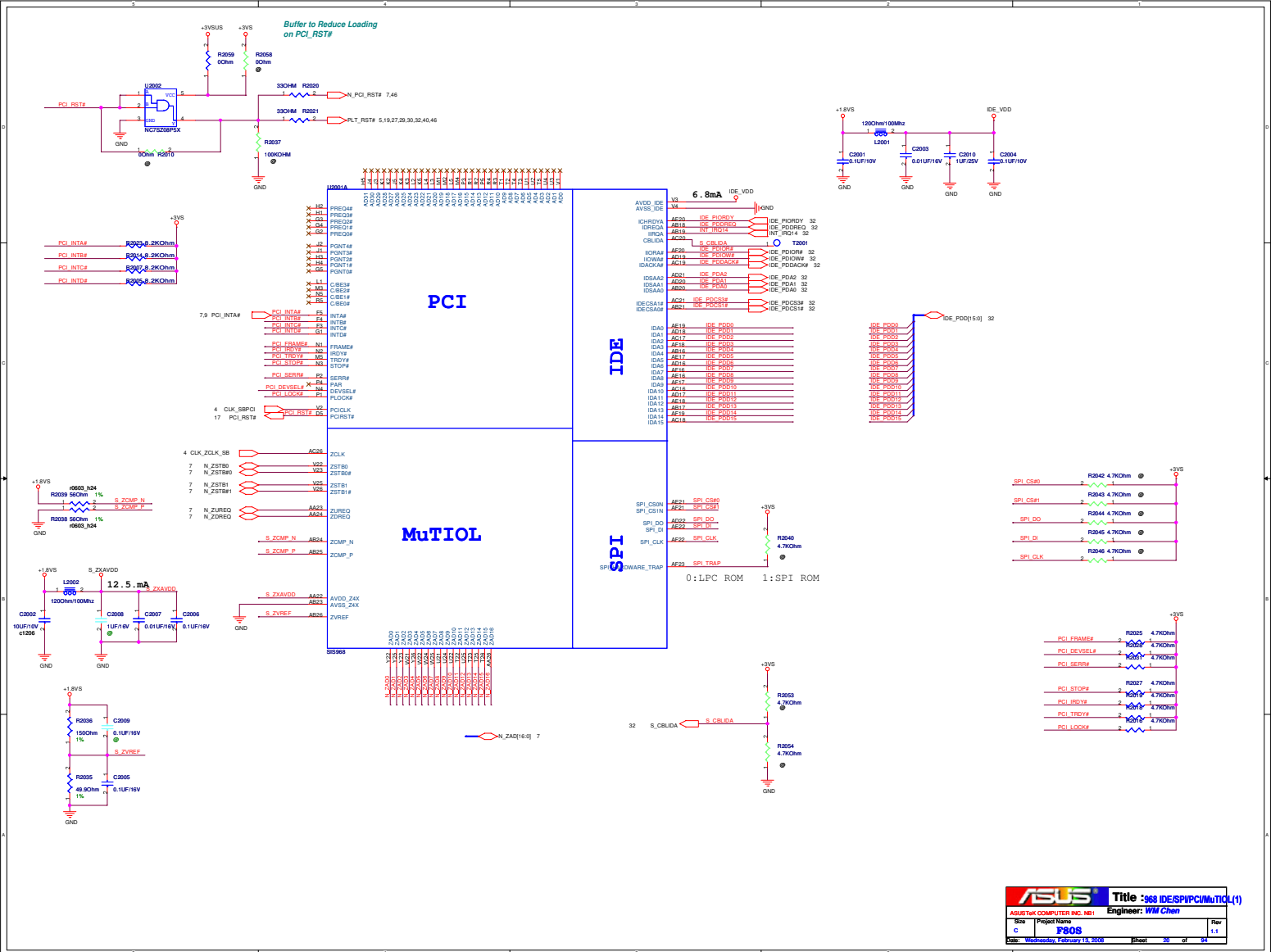


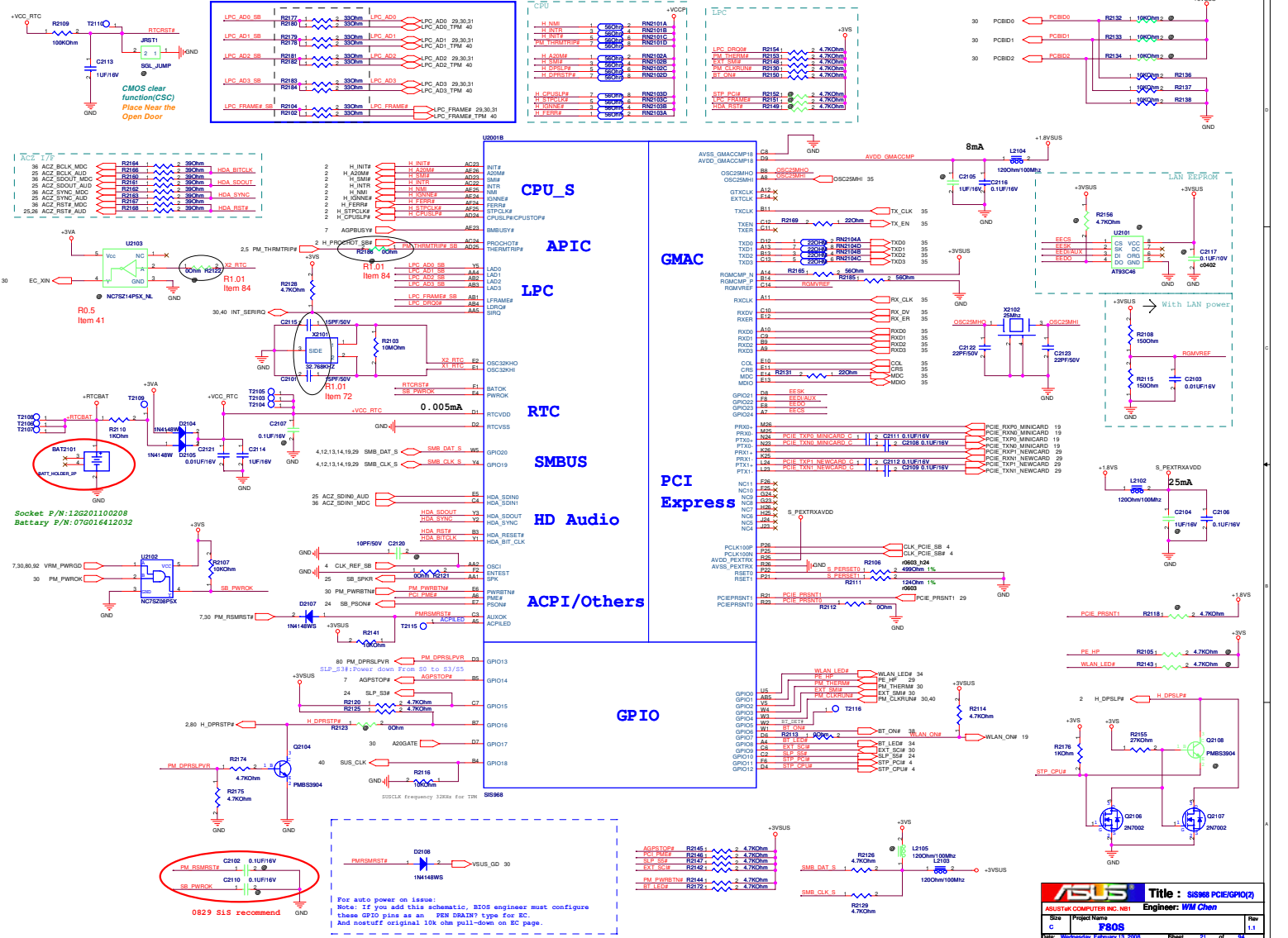
reference should be +3V5, but Av1 answer that +3V5 is fine. As long as it can turn the MOSFET on.

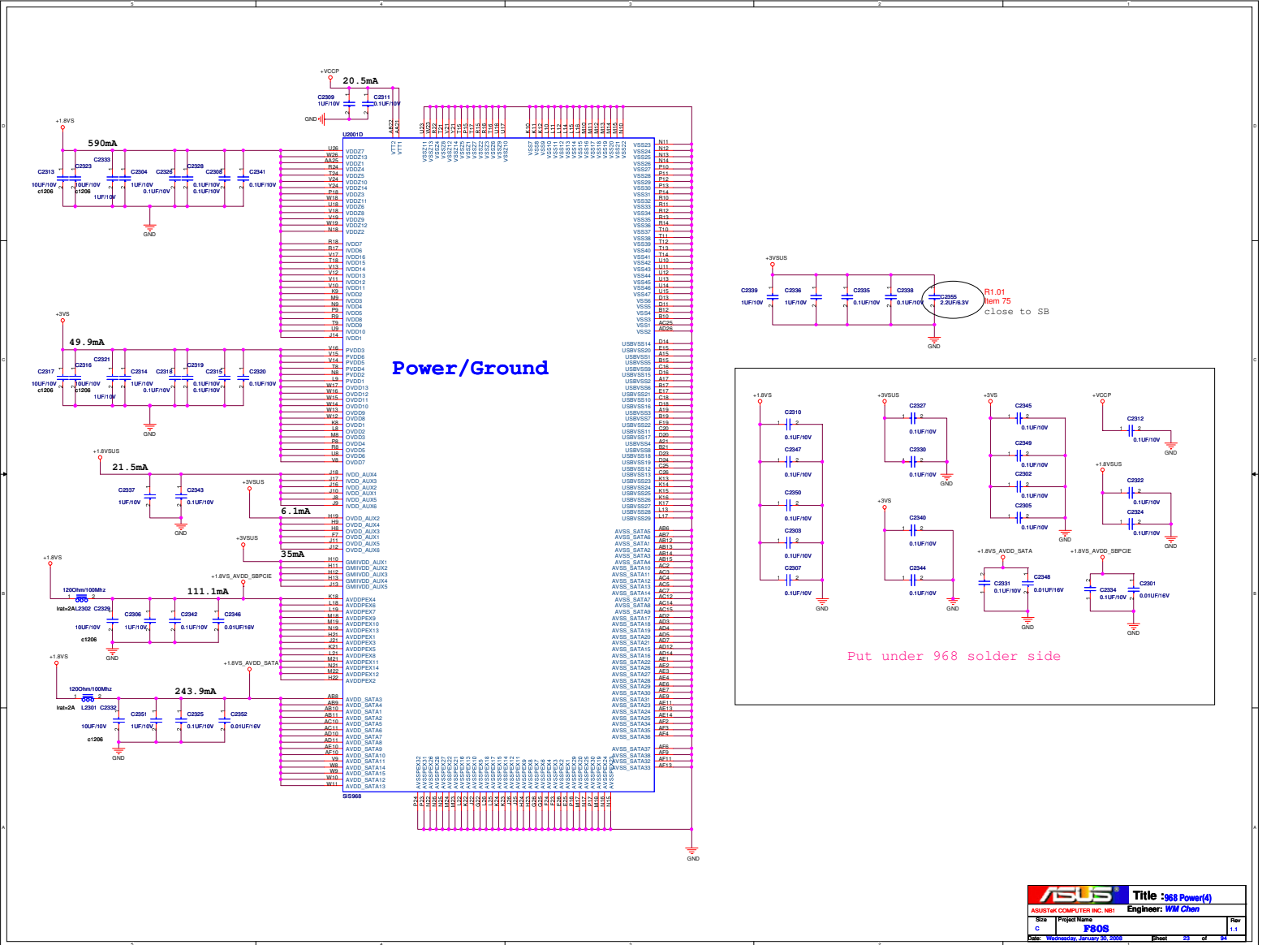


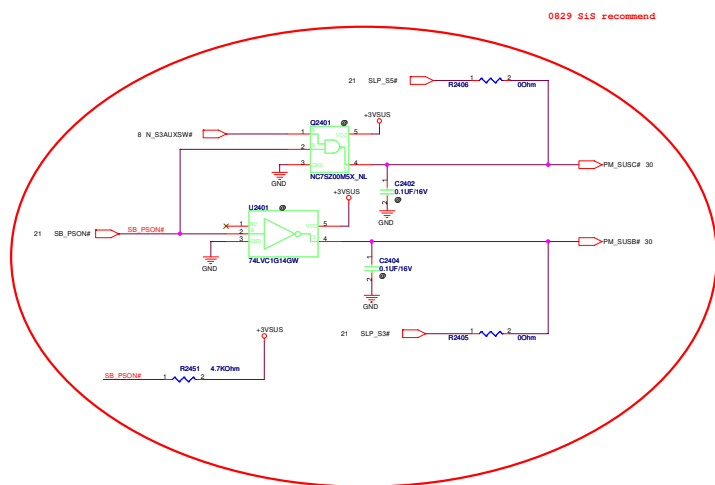
Note: 1. L1805, L1806, L1807: For EMI. (default=0 ohm)
2. DDC_CLK_HDMI, DDC_DATA_HDMI: +5V tolerant

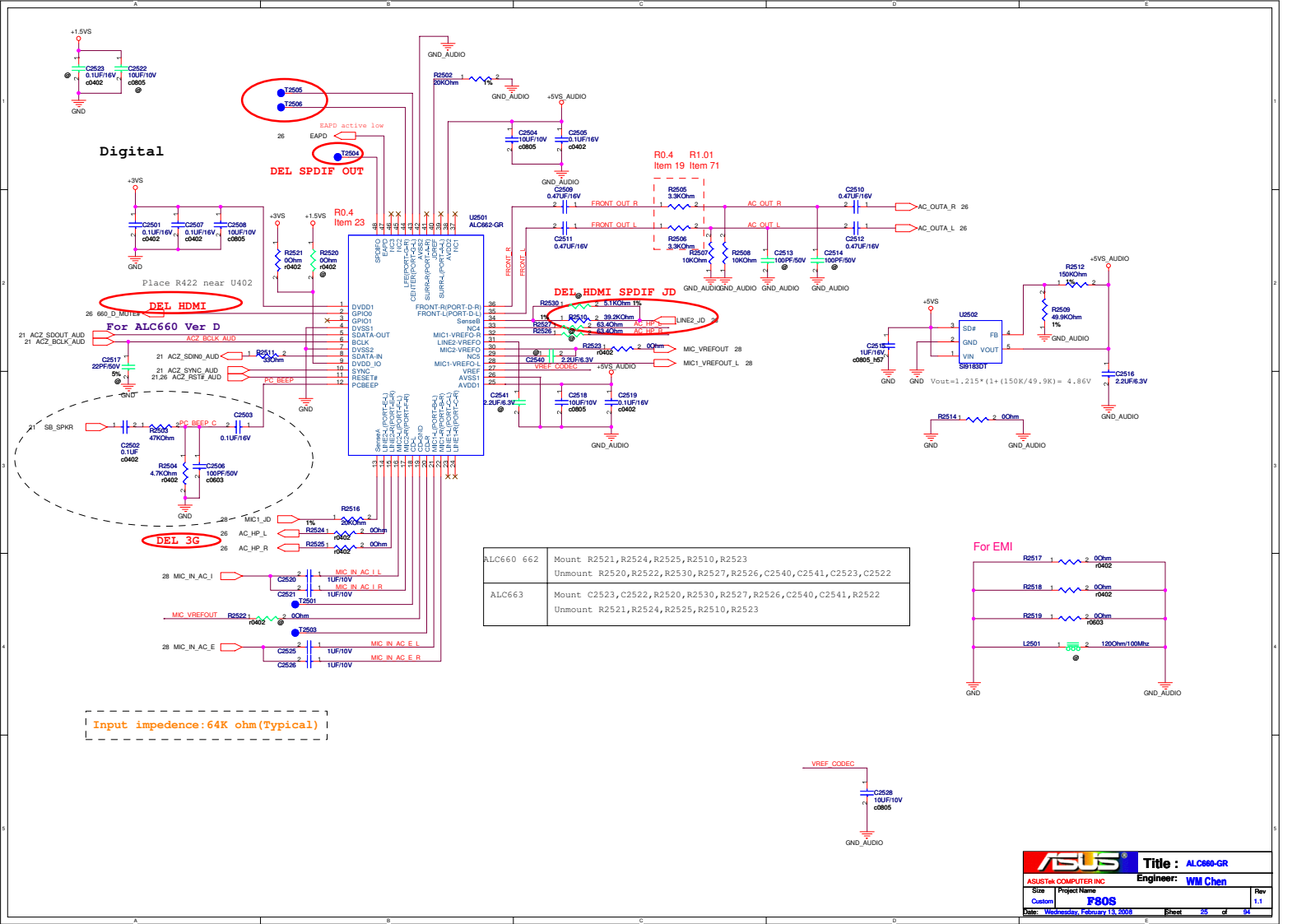




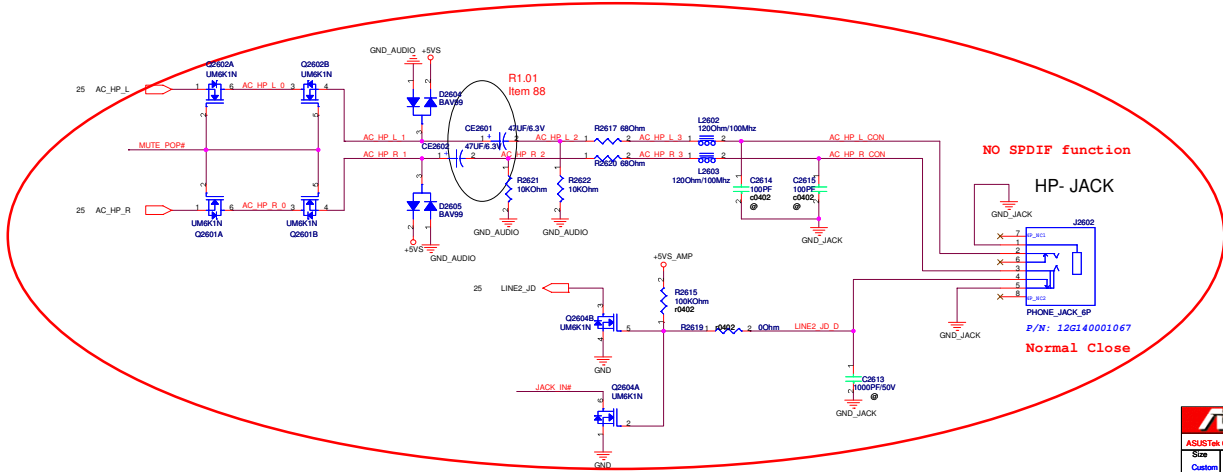
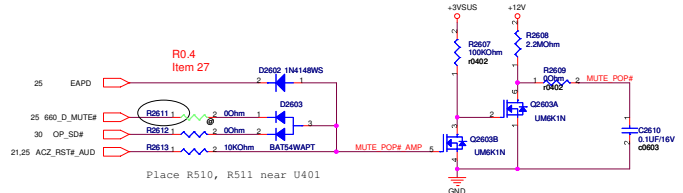
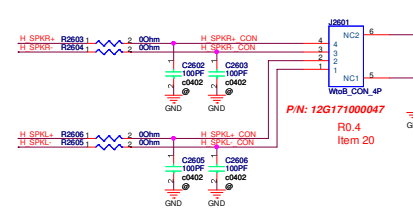
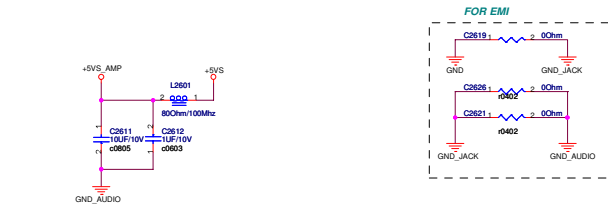
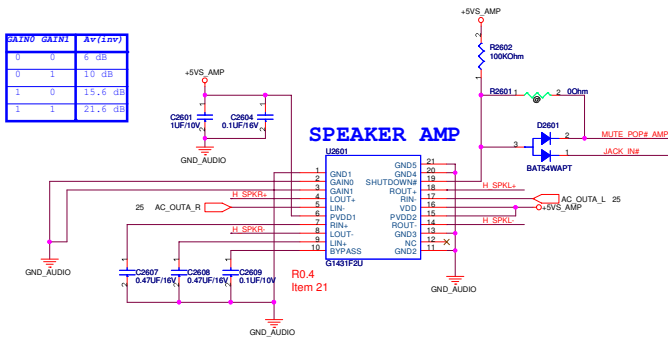


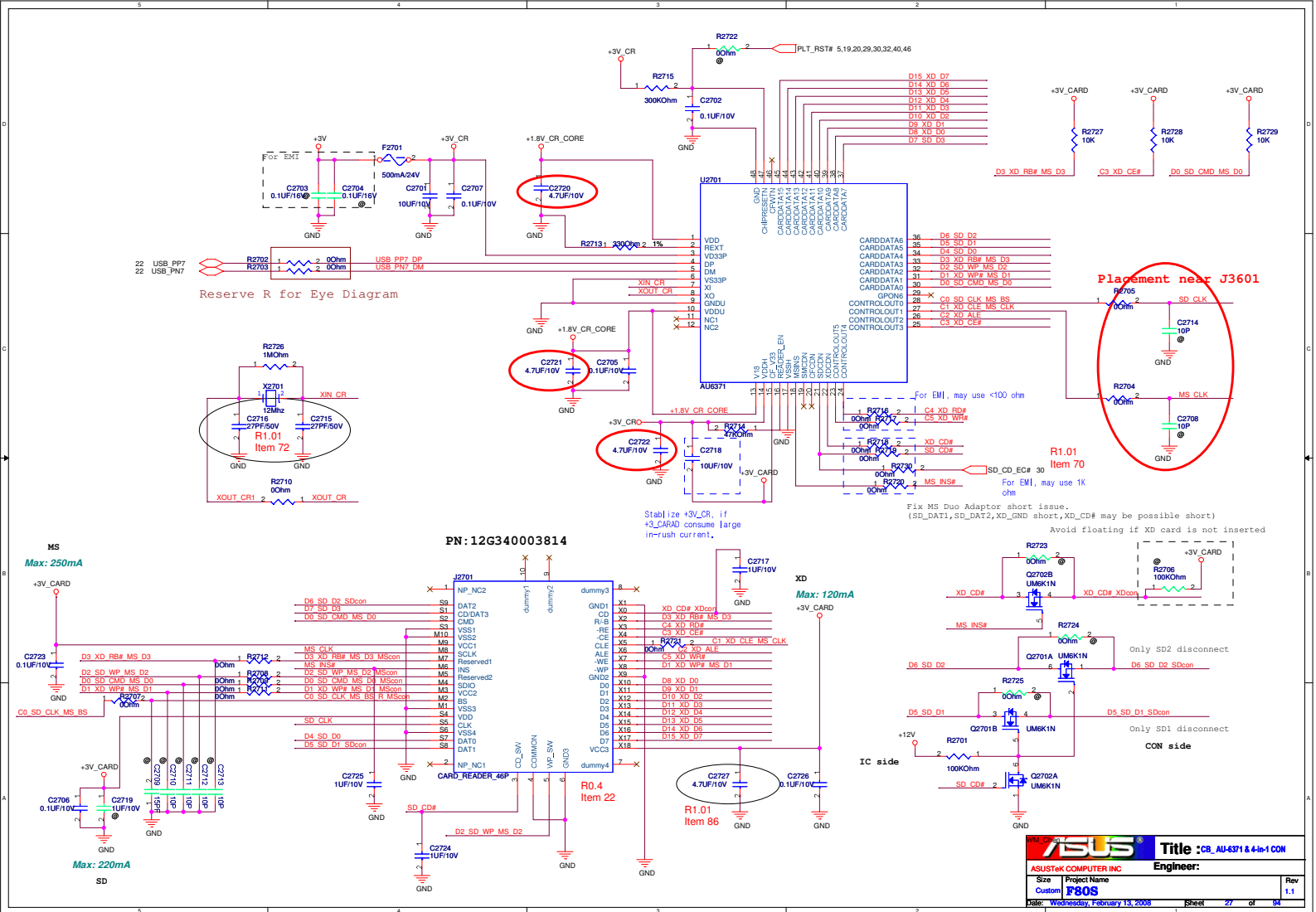




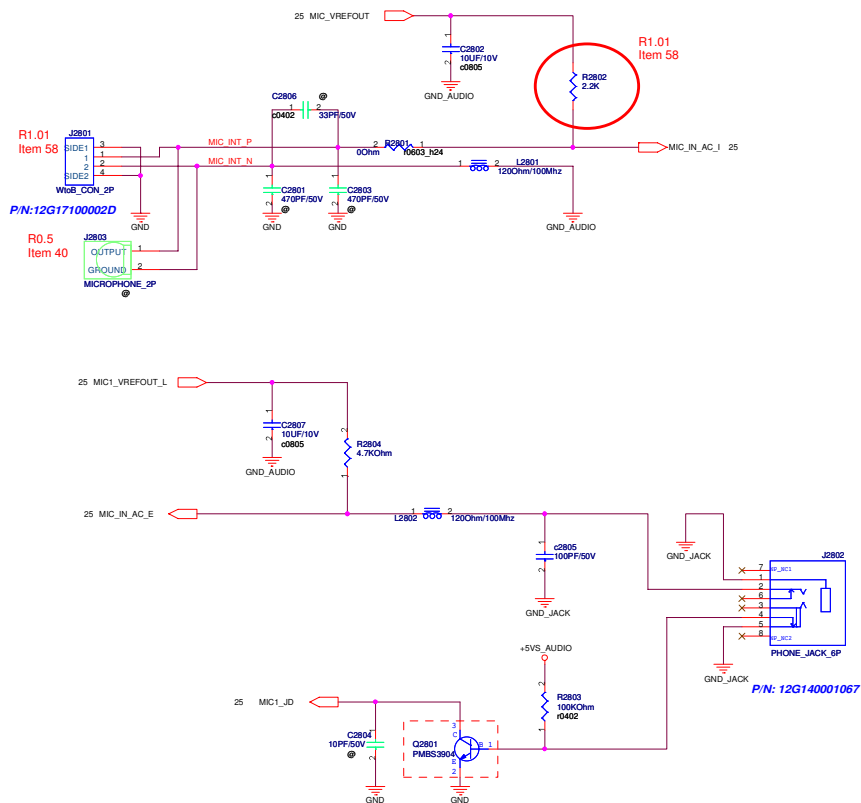


Gain	Gain	Av (dB)
0	0	5.0 dB
0	1	10.0 dB
1	0	15.6 dB
1	1	21.6 dB





Internal MIC Pre-Amplifier



<Variant Name>

ASUS		Title : MICROPHONE	
ASUSTEK COMPUTER INC		Engineer: WM Chen	
Size	Project Name	Rev	
Custom	F80S	1.1	
Date: Wednesday, February 13, 2008		Sheet 28 of 54	

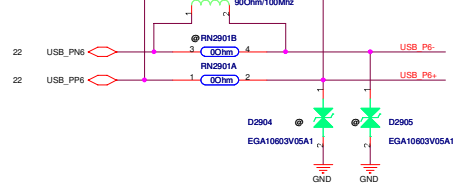
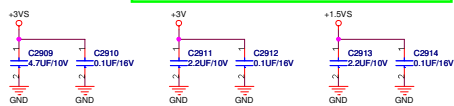
0,37,40,91,92 SUSB_EC1# PERST#2
R2901
+3VS
+1.5VS
+3V
19,20,27,30,32,40,46 PLT_RST#

The diagram shows the J2904 connector with the following connections:

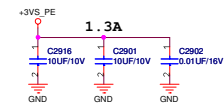
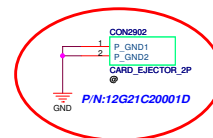
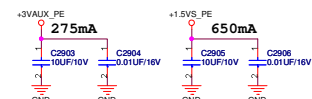
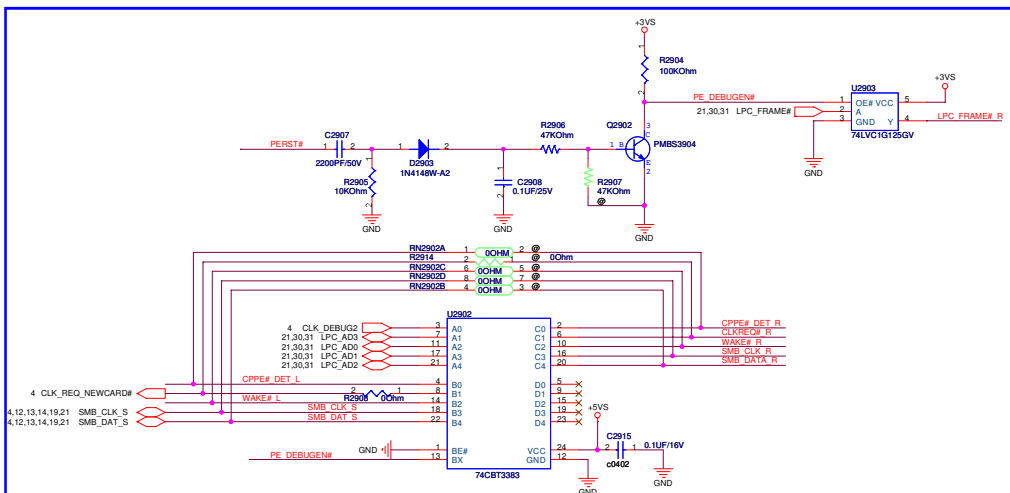
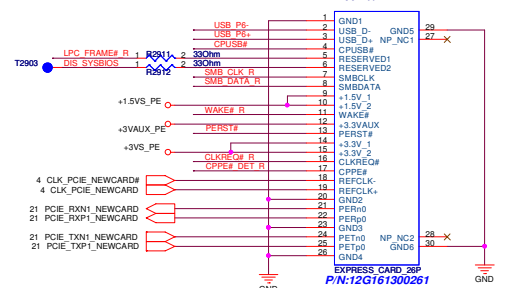
- PERST#** (pin 20) to **PERST#** (pin 8) via a **10kΩ** resistor.
- RST#** (pin 21) to **RST#** (pin 6).
- +3V** (pin 9) to **3.3V1N_1** (pin 3).
- +1.5V** (pin 14) to **3.3V1V_1** (pin 14).
- +3V** (pin 17) to **3.3V2V_1** (pin 5).
- GND** (pin 21) to **GND** (pin 21).
- PE_HP** (pin 21) to **PE_HP** (pin 1) via a **10kΩ** resistor.
- PCIE_PRSNT1** (pin 21) to **PCIE_PRSNT1** (pin 21).

The diagram also shows the following components and labels:

- J2904** connector.
- GTBY#** (pin 19) to **GTBY#** (pin 19).
- SHDN#** (pin 18) to **SHDN#** (pin 18).
- PERST#** (pin 13) to **PERST#** (pin 13).
- 3.3V1N_1** (pin 3) to **3.3V1N_1** (pin 3).
- 3.3V1V_1** (pin 14) to **3.3V1V_1** (pin 14).
- 3.3V2V_1** (pin 5) to **3.3V2V_1** (pin 5).
- AUXIN** (pin 16) to **AUXIN** (pin 16).
- GSYSRST#** (pin 6) to **GSYSRST#** (pin 6).
- GN1** (pin 21) to **GN1** (pin 21).
- GN2** (pin 21) to **GN2** (pin 21).
- WEL351YG** (pin 21) to **WEL351YG** (pin 21).
- CRUSB#** (pin 11) to **CRUSB#** (pin 11).
- CPPE#** (pin 10) to **CPPE#** (pin 10).
- CPUSB#** (pin 10) to **CPUSB#** (pin 10).
- RCLKEN** (pin 10) to **RCLKEN** (pin 10).
- NC** (pin 10) to **NC** (pin 10).
- Q_CPE** (pin 10) to **Q_CPE** (pin 10).
- Q_CPE#** (pin 10) to **Q_CPE#** (pin 10).
- CPPE# DET L** (pin 10) to **CPPE# DET L** (pin 10).

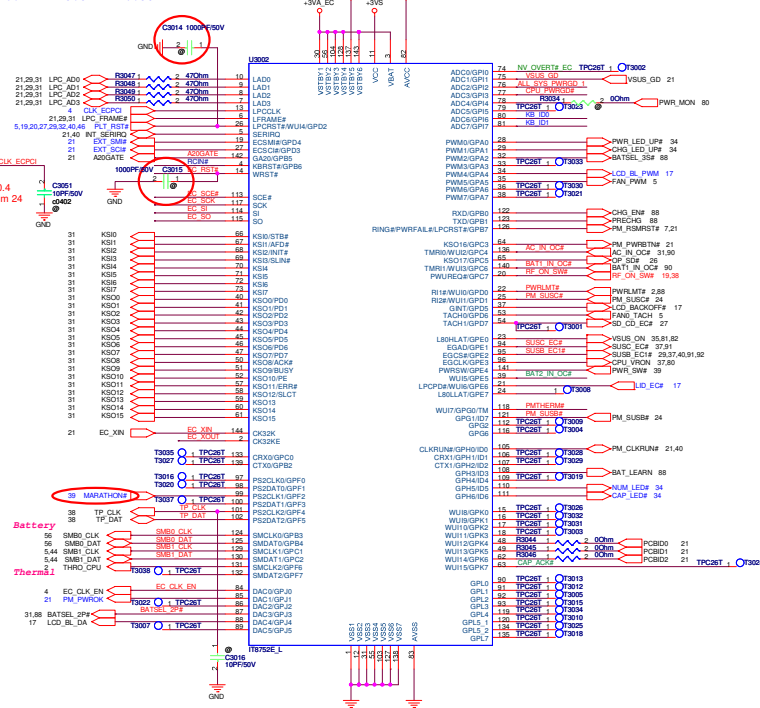


CON2901

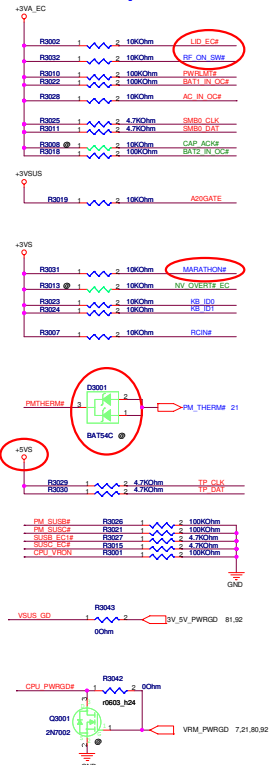


IT8752 Core Chip

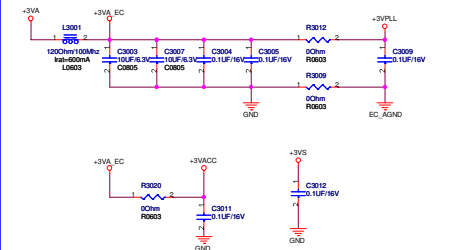
Standby (Sleep) Power Consumption:
0.1mA * 3.3V = 0.33mW



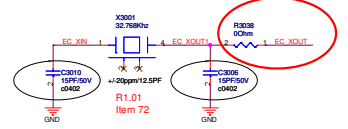
EC Pull-Up/Down



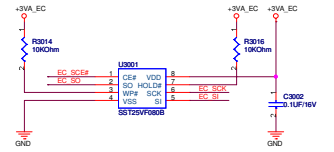
EC Power



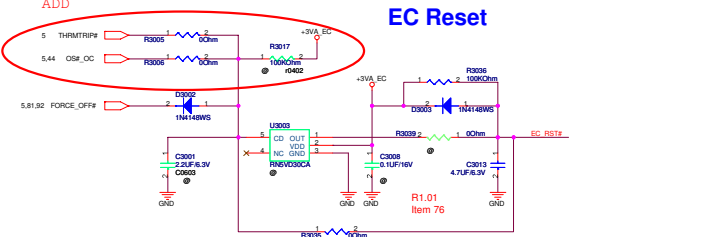
EC XTAL



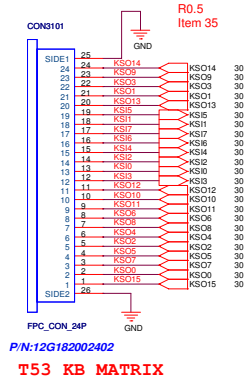
SPI ROM



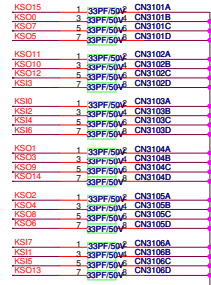
EC Reset



For Keyboard



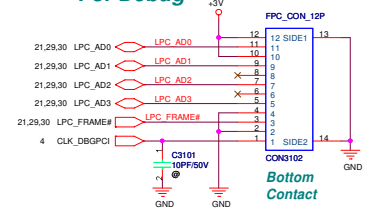
R0.5
Item 35



R0.5
Item 38

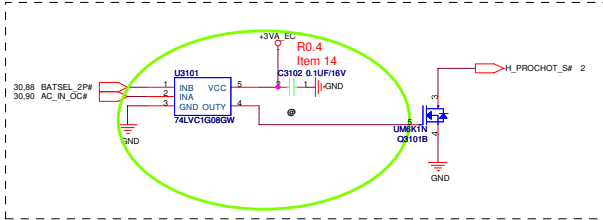
Reserve for EMI

For Debug



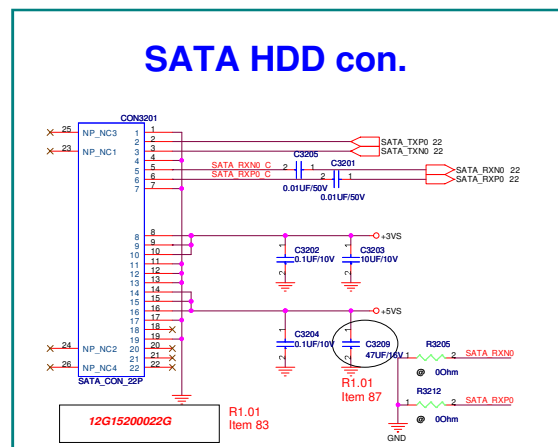
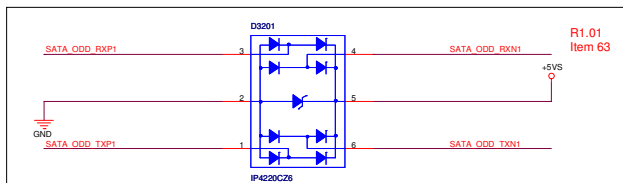
Bottom Contact

PWRLMT Circuit: For 65W adaptor.

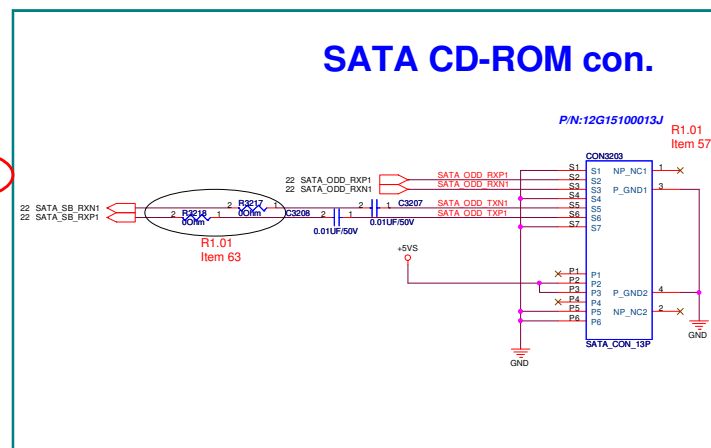
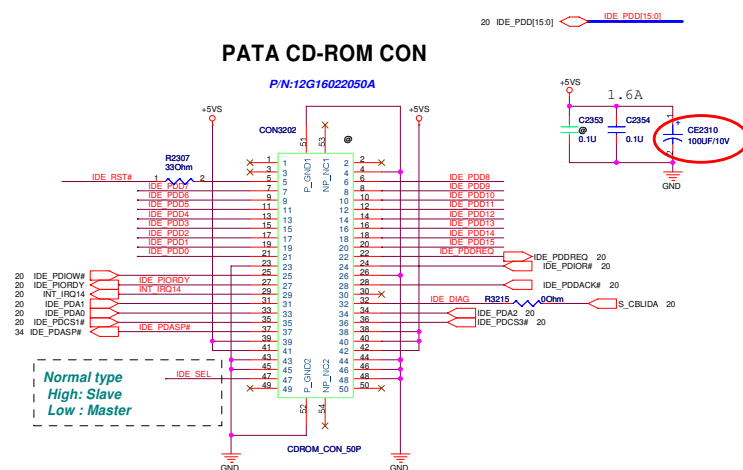


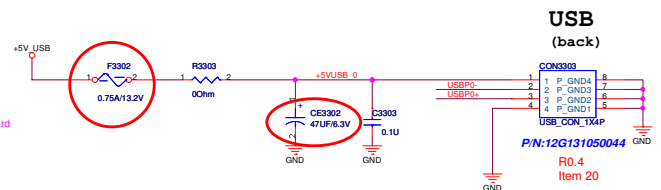
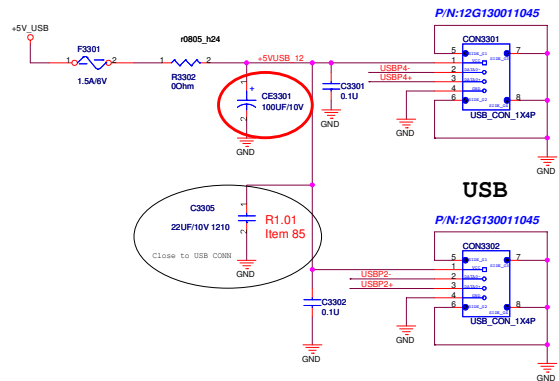
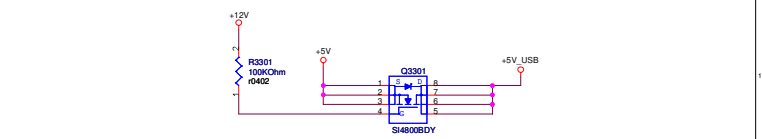
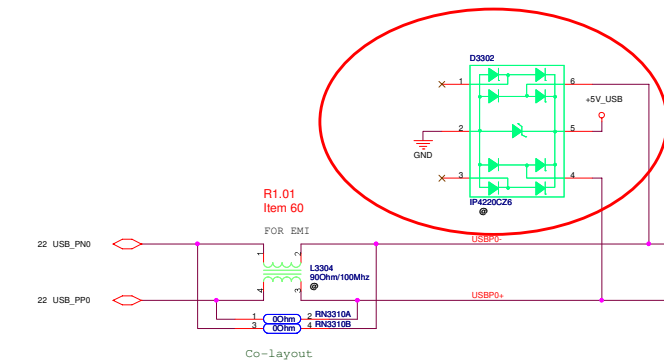
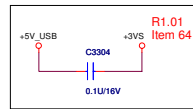
<Variant Name>

ASUS		Title : KB conn	
ASUSTek COMPUTER INC		Engineer: WM Chen	
Size	Project Name	Rev	
Custom	F80S	1.1	
Date: Wednesday, February 13, 2008		Sheet	31 of 34

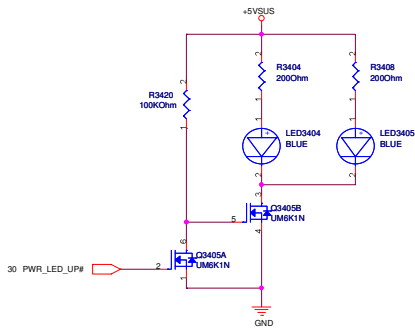


PATA CD-ROM CON

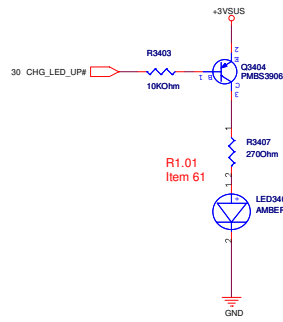




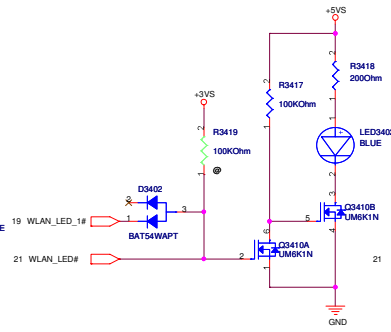
PWR LED



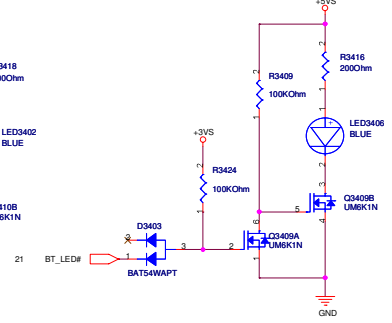
For BATTERY LED



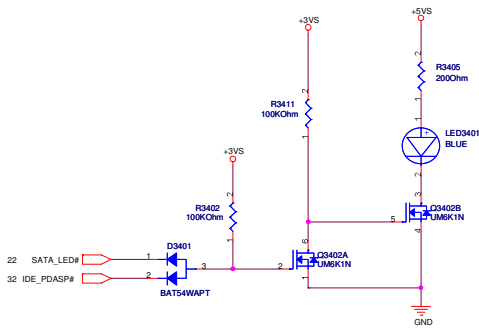
WireLess LED



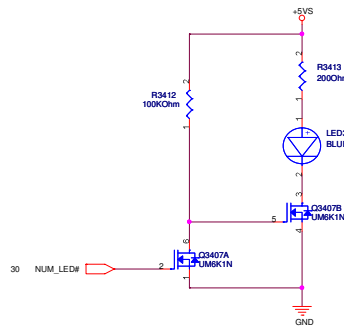
BT LED



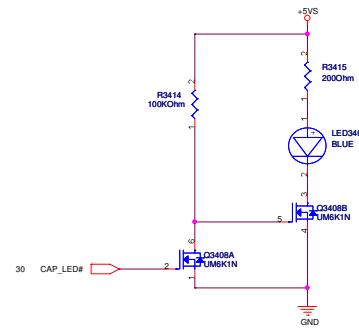
SATA/IDE LED



Num Lock



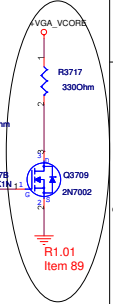
Cap. Lock



<Variant Name>

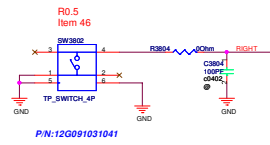
ASUS		Title : LED	
ASUSTeK COMPUTER INC		Engineer: WM Chen	
Size	Project Name	Rev	
Custom	F80S	1.1	
Date: Wednesday, February 13, 2008		Sheet 34 of 34	

System Power Sequence
+VCCRSTC -> RTCRST# -> V5REFSUS -> 3.3/1.5VSUS<
RSMRST# -> SUSC# -> USB# -> VCCLAN -> LANPWROK
-> V5REF -> PWROK -> GMCH -> VCCP -> VCCORE
SUSSTAT# -> PCIRST#
CPU : +VCCORE, +VCCP, +1.05VS
NB : +1.05VS, +1.2VS, +2.5V, +VCCP
SB : +1.2VSUS, +3.3VSUS, +VCCP, +1.5VS, +3.3VS
DDR : +1.8V, +0.9VS



<Variant Name>

ASUS		Title : DISCHARGE CKT	
ASUSTek COMPUTER INC		Engineer: WM Chen	
Size	Project Name		Rev
Custom	F80S		1.1
Date: Wednesday, February 13, 2008		Sheet	37 of 54



Co-layout FOR EMI

For Intel Wireless CoExistence System

Bluetooth Conn.

R1.1
Item 60

L3801
90Ohm1.00MHz

CON3802

PN:12G170010083

WOL_CON_8P

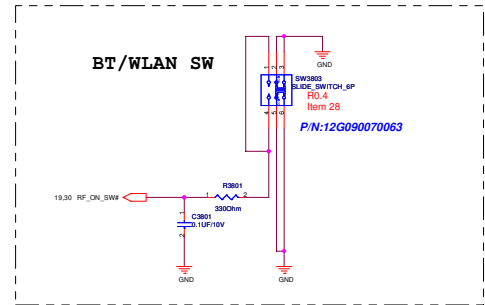
max. 60mA

BT_ON

BT_ON#

BT_ON# 21

Low -> Disable
High -> Enable



Finger Printer Conn.

R0.4 Item 28

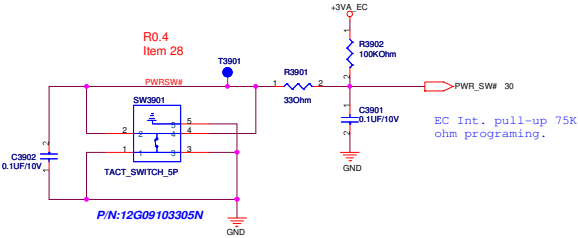
R1.1 Item 60

Components and Connections:

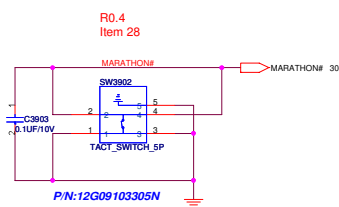
- Capacitors:** C3803 (100nF), C3807 (100nF), C3806 (100nF), C3808 (100nF).
- Resistors:** R3801 (100k), R3802A (100k), R3802B (100k).
- ICs:** C3803 (FPC CON 4P), C3807 (FPC CON 4P), C3806 (FPC CON 4P), C3808 (FPC CON 4P).
- USB Hub:** D3801 (F425CZ2).
- Power:** +3V, GND.
- Labels:** USB_P1, USB_P3, USB_P3+, USB_P3-.

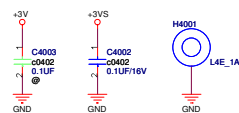
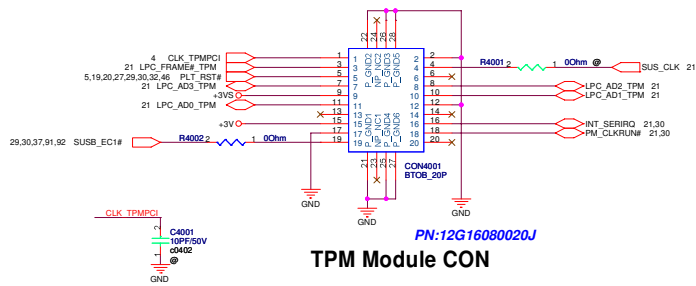
 Title : BT&TP&FP		
ASUSTek COMPUTER INC. NB1 Engineer: WM Chen		
Size C	Project Name F80S	Rev 1.1
Date: Wednesday, February 15, 2006		
Sheet 38 of 94		

Power Button



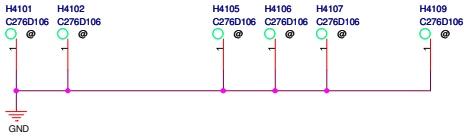
MARATHON#



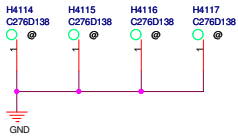


<Variant Name>			
ASUS		Title : TPM	
ASUSTeK COMPUTER INC. NE1		Engineer: WM Chen	
Size	Project Name		Rev
Custom	F80S		1.1
Date: Wednesday, February 13, 2008		Sheet	40 of 54

R0.5
Item 36



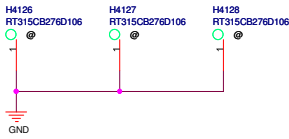
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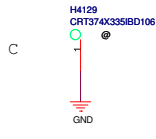
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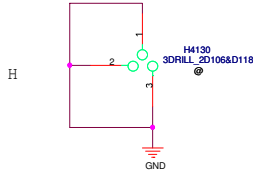
E



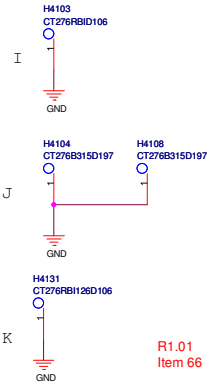
G



C



H



I

J

K

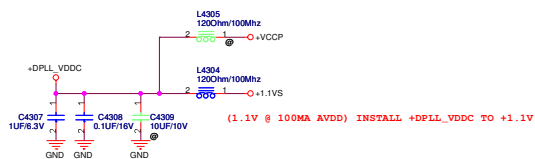
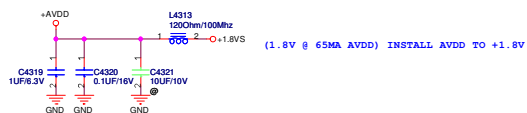
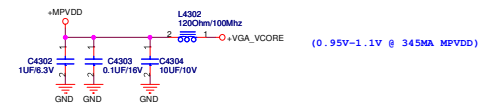
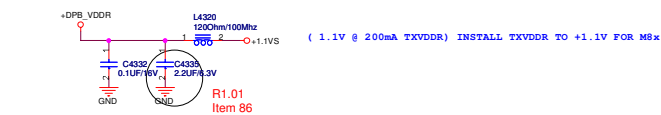
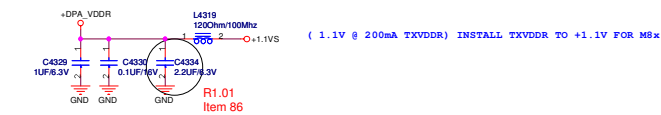
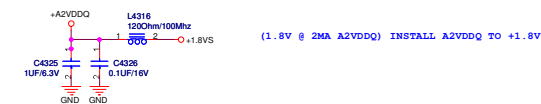
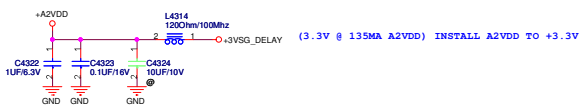
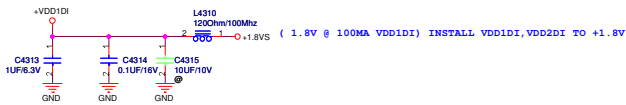
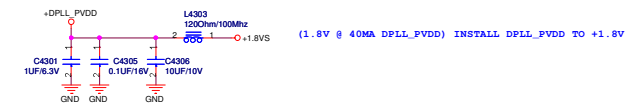
R1.01
Item 66

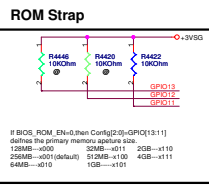
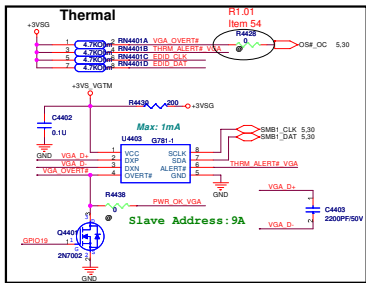
ASUS		Title : Screw hole	
ASUSTeK COMPUTER INC. NB1		Engineer: WM Chen	
Size B	Project Name F80S	Date: Wednesday, January 30, 2008	Rev 1.1
Sheet 41 of 94			

		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer: WM Chen	
Size Custom	Project Name F80S		Rev 1.1
Date: Wednesday, February 13, 2008		Sheet 42	of 94

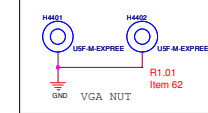
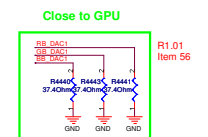
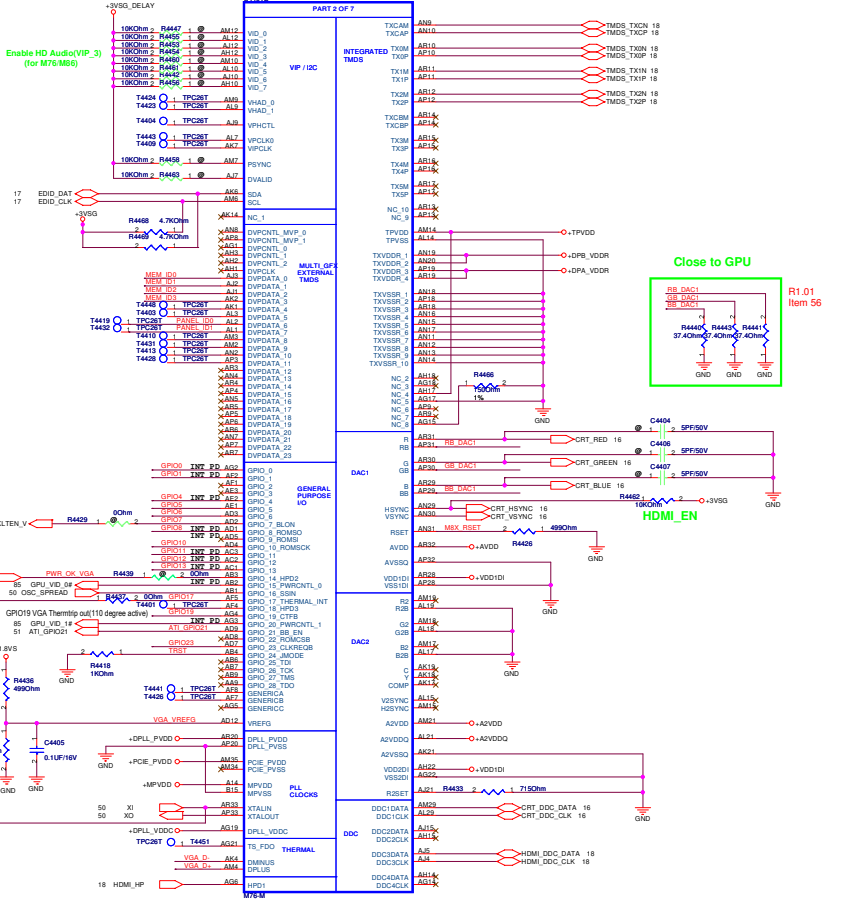
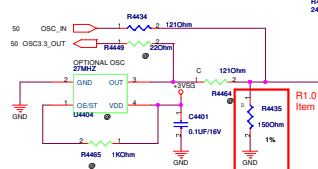
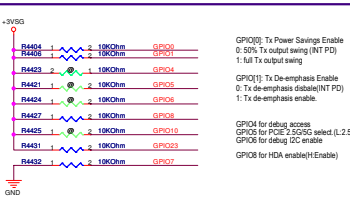
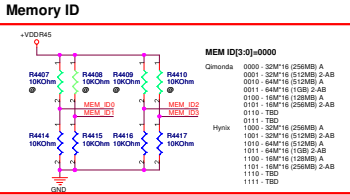
M8x Power

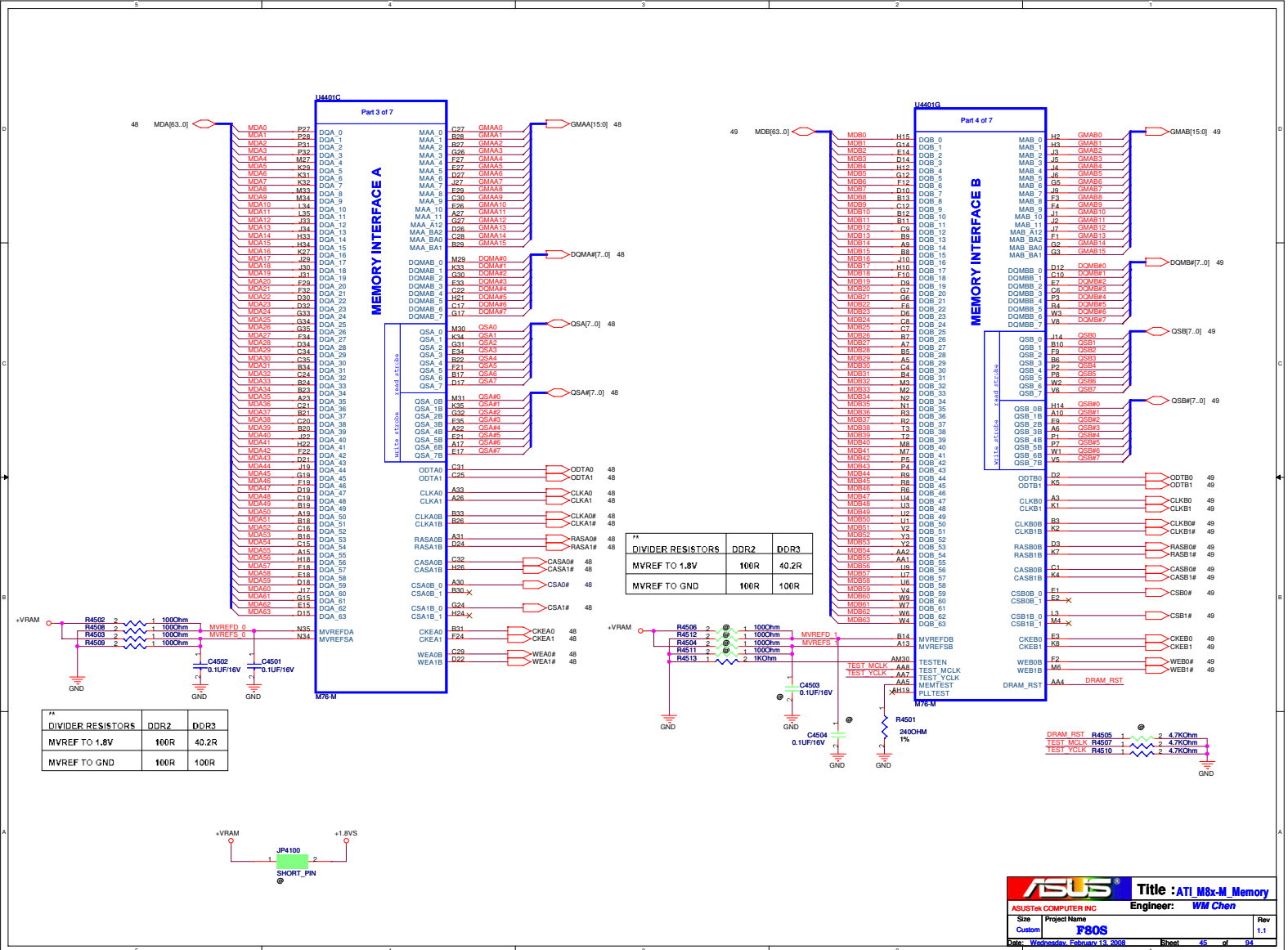
PLACE ALL DECOUPLING AS CLOSE TO ASIC AS POSSIBLE

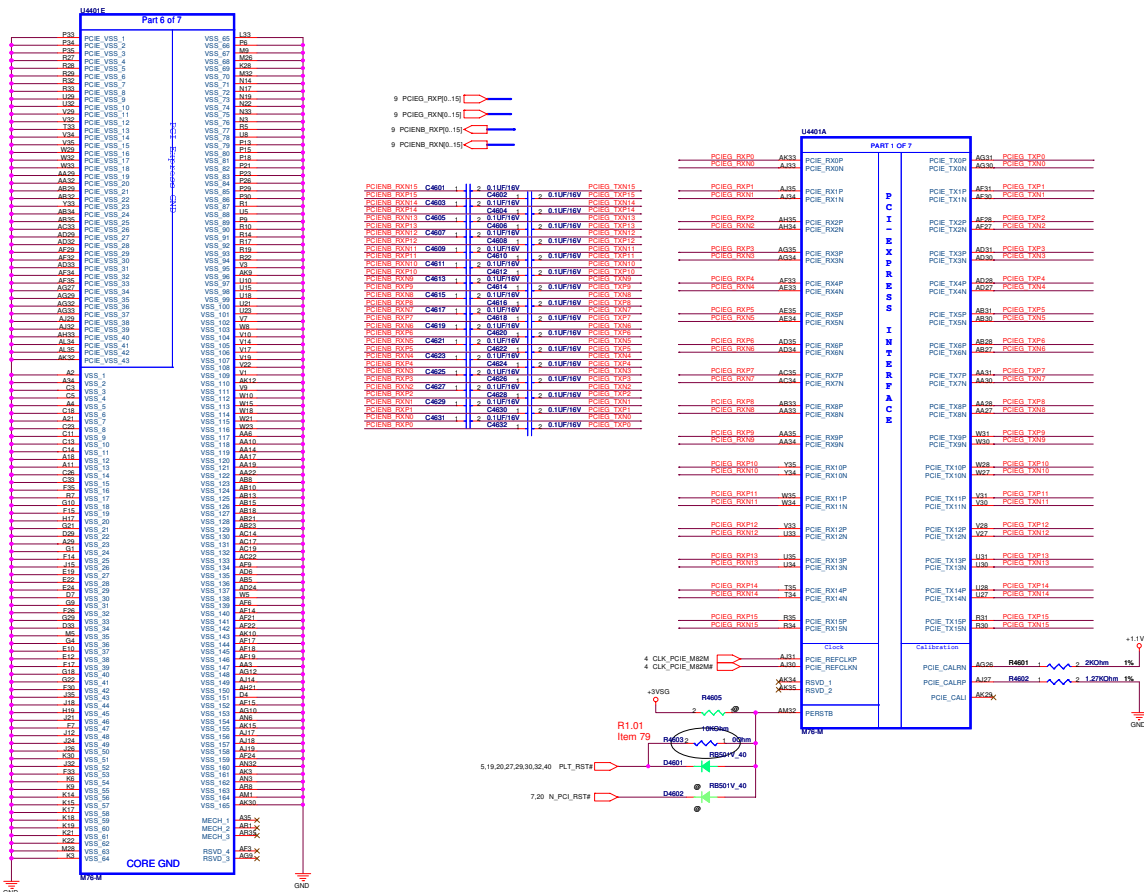


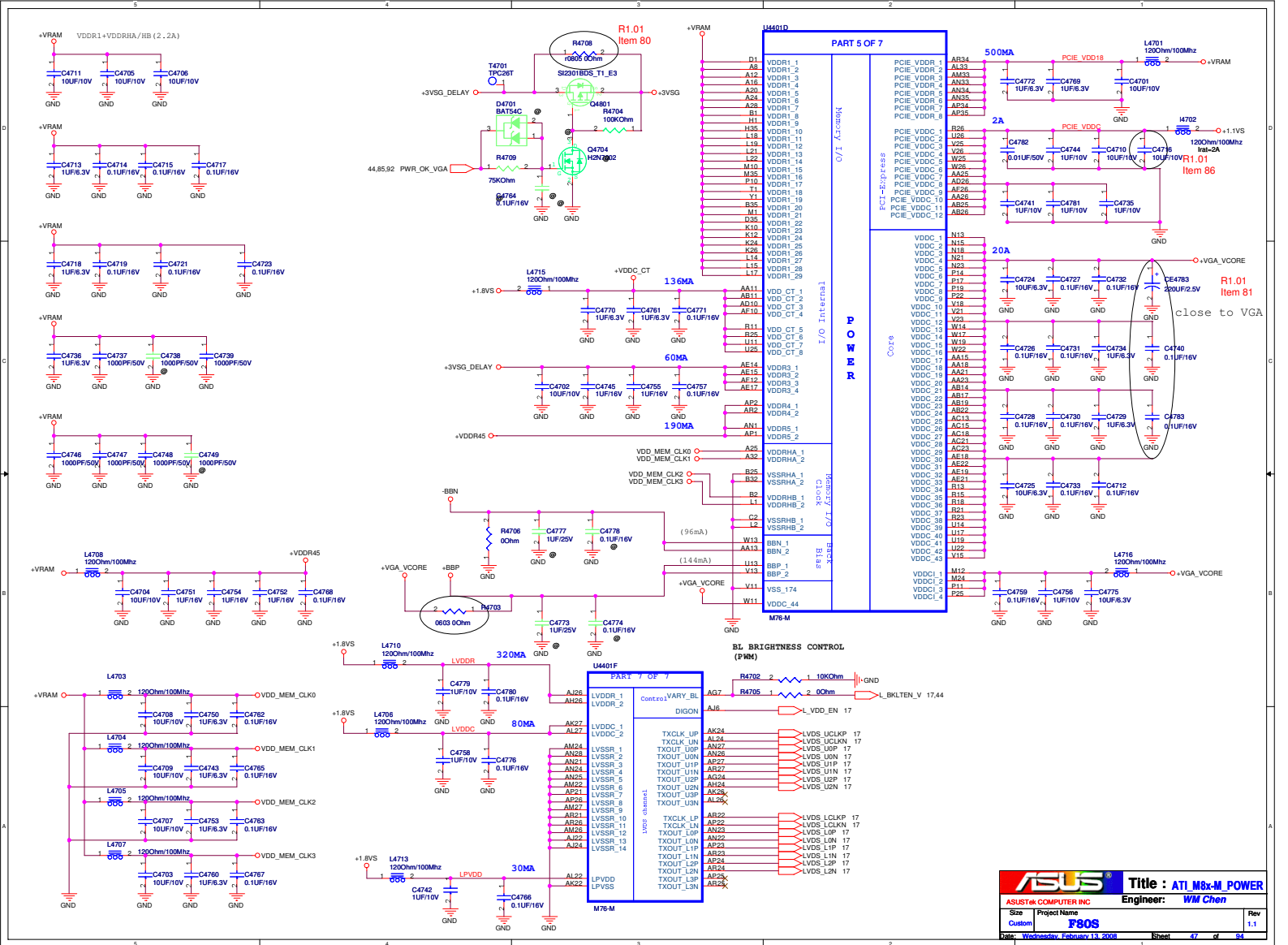


Enable HD Audio (VIP_3)
(for M76M86)

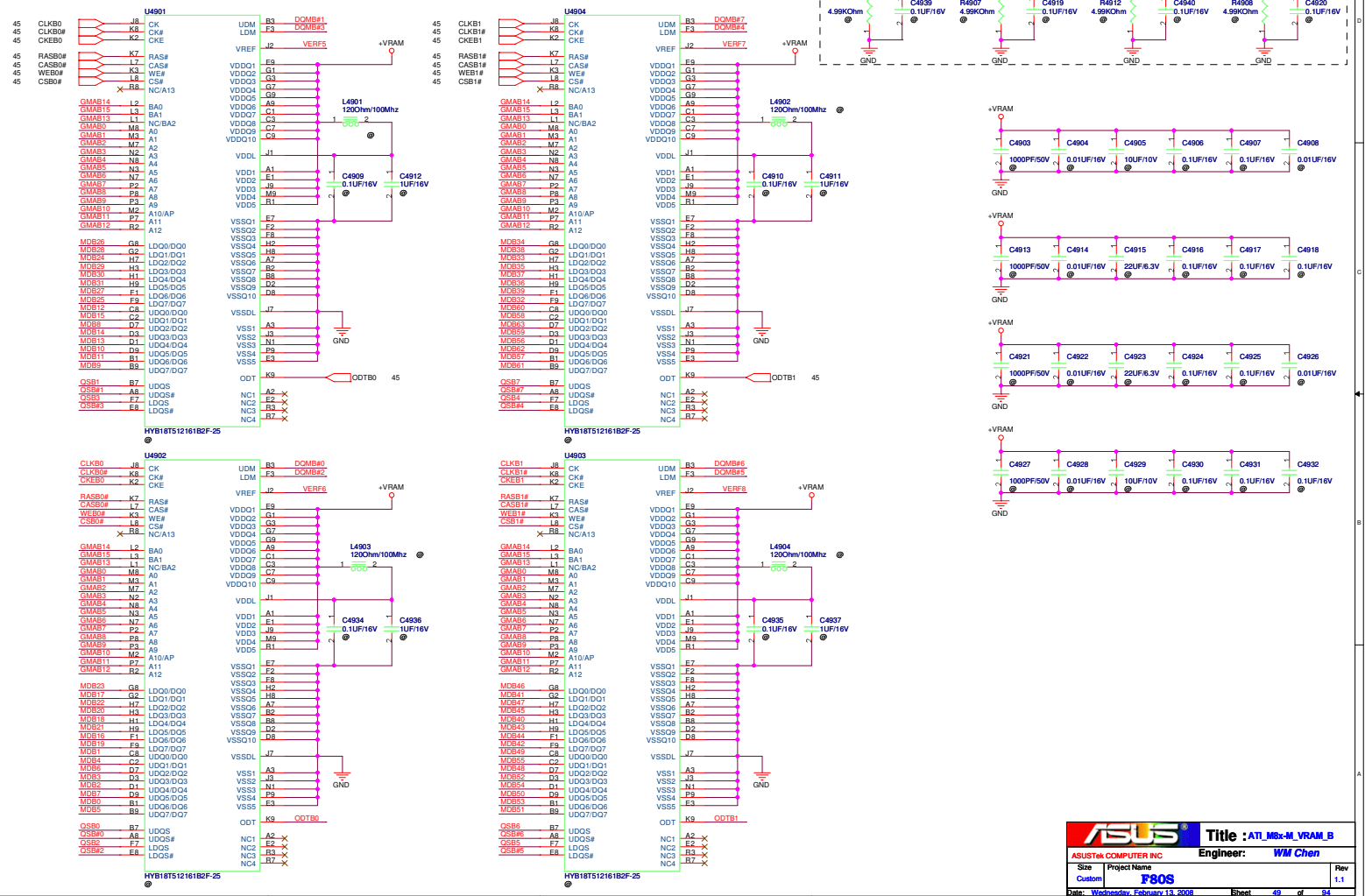


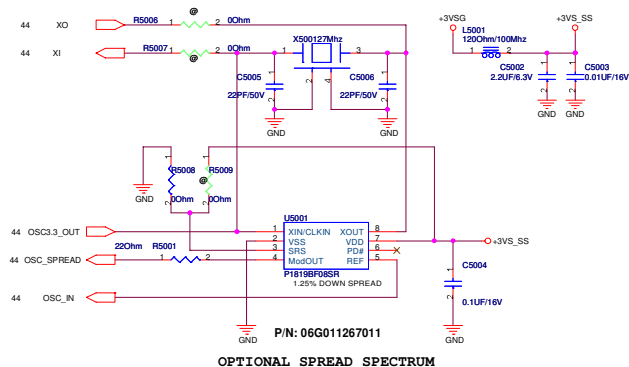






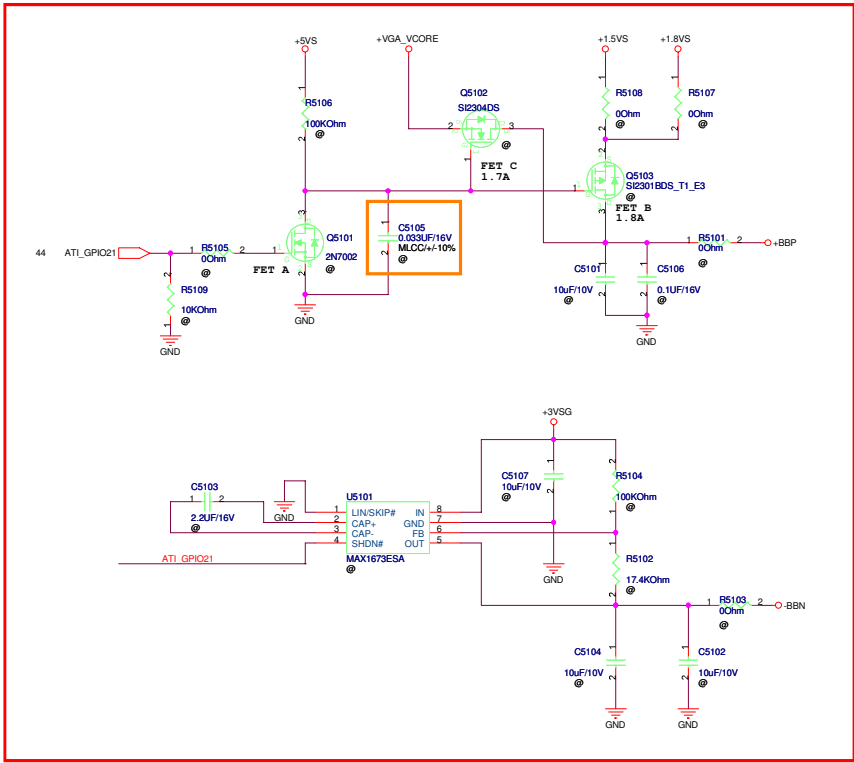
DO NOT INSTALL CHANNEL B
WITH M82M





COMPONENTS SHOWN ARE EXAMPLES ONLY
AND NOT NECESSARILY QUALIFIED

R0.4
Item 30

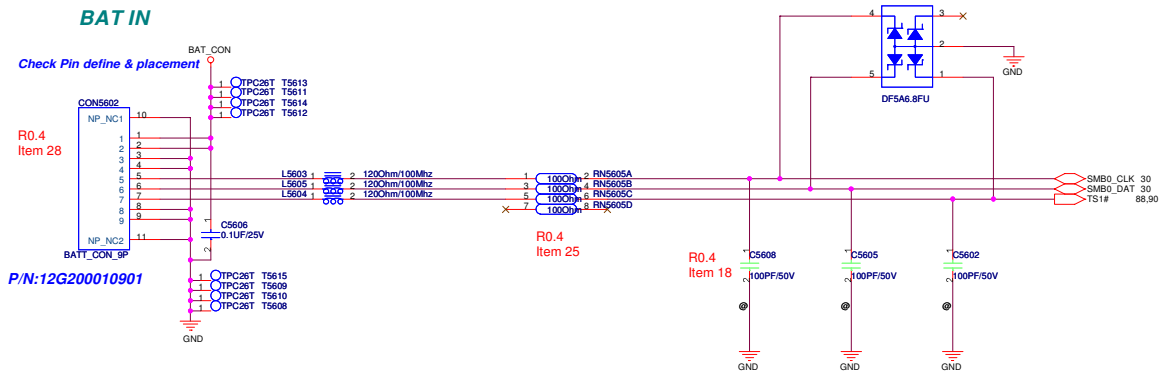
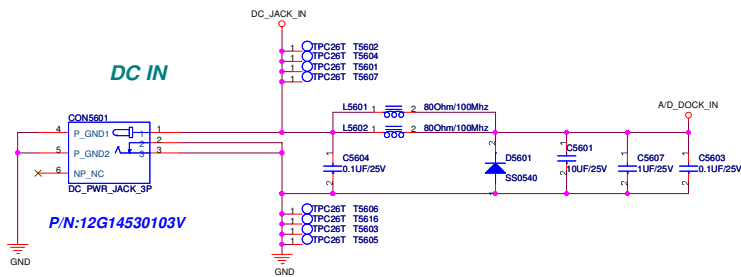


BB_ENA = 0V FOR BACK BIASING DISABLED
MAX1673 SHUTDOWN
-BBN = 0V VIA MAX1673 INTERNAL 1 OHM TO GROUND
N FET A = OFF, P FET B = OFF, N FET C = ON
+BBP = +VGA_CORE


BB_ENA = +3.3V FOR BACK BIASING ENABLED
MAX1673 ENABLED
-BBN = -.5V
N FET A = ON, P FET B = ON, N FET C = OFF
+BBP = +1.5V

GPIO_21_BB_EN	+BBP
0	1.1V
1	1.5V

GPIO_21_BB_EN	-BBN
0	GND
1	-0.5V



R0.4
Item 31

		Title : Empty	
ASUSTeK COMPUTER INC. NB1		Engineer: WM Chen	
Size	Project Name		Rev
A	F80S		1.1
Date: Wednesday, January 30, 2008		Sheet	57 of 94



Title : BLANK

ASUSTeK COMPUTER INC. NB1

Engineer: *WM Chen*

Size

A

Project Name	
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F80S

Rev

1.1

Date: Wednesday, January 30, 2008

Sheet 58 of 94

R0.4 revision history:

1. Delete R511,R515,D503,C503,C509,Q505,R506 from 100K change to 0ohm for better signal quality .
2. Change D3302,D3303,D2107 from 1SS355 to 1N4148WS-L for cost down.
3. Change D3401,D3402,D2601,D2603 to BAT54WAPT for cost down and common part.
4. Change D2106 to BAT54C for cost down.
5. Change U1701 to AH180-WG-7 for cost down.
6. Delete RN301,RN302 for not use on board CPU cost down.
7. Add R3047-R3050 for newcard debug card.
8. Change CAP_ACK# from pin24 to pin63 for EC(page30) pin assignment R0.06.
9. Add R3028 for net:AC_IN_OC# pull hi.
10. Change C3002 from 1uF to 0.1uF for cost down.
11. Change Q4203 from 2N7002 to PMBS3904 for cost down and MIC jack.
12. Unmount R1727 for pull hi in p34.Change L1716 to R1710 for U1701 on MB.
13. Change USB connector J2401~ J2403.
14. Unmount C3102 for cost down.
15. Reserve R3810 for prevent power short cause large current.
16. Change R3404,R3403,R3412,R3414 from 4.7K to 10K for power saving.
17. Change L3601,L3605 to R3622,R3623 for EMI fine tune cost down.
18. Unmount C5608,C5605,C5602 for EMI fine tune cost down.
19. Change R2505,R2506 from 680ohm to 2.7Kohm for 1W speaker.
20. Change CON3303,J2601 part number for ME request.
21. Change U2601 from TI to GMT,C2609 to 0.1uF for cost down.
22. Change card reader connector J2701 part number for connector in bottom side.
23. Change U2501 from 660 to 662 for the logo request codec need 2-ADC after 2008/6.
24. Reserve C3051 for fine tune CLK_ECPCI.
25. Add RN5605 for protect EC to prevent voltage damage.
26. Change CON3802 pin assignment for use W7 BT cable.
27. Unmount R2611 for ALC662 not need.
28. Change SW3901,SW3902,SW3801,SW3802,SW3803,J2801,CON5602,BAT2101,CON3801,CON3803 part number for ME request.
29. Delete D3301 for cost down.
30. Add VGA Back Bias on page51 for power play function
31. +1.2VSUS from page57 change to page82 for power circuit
32. Delete H_PWRGD_EC (Q202,R233) because it's not necessary.


R0.5 revision history:

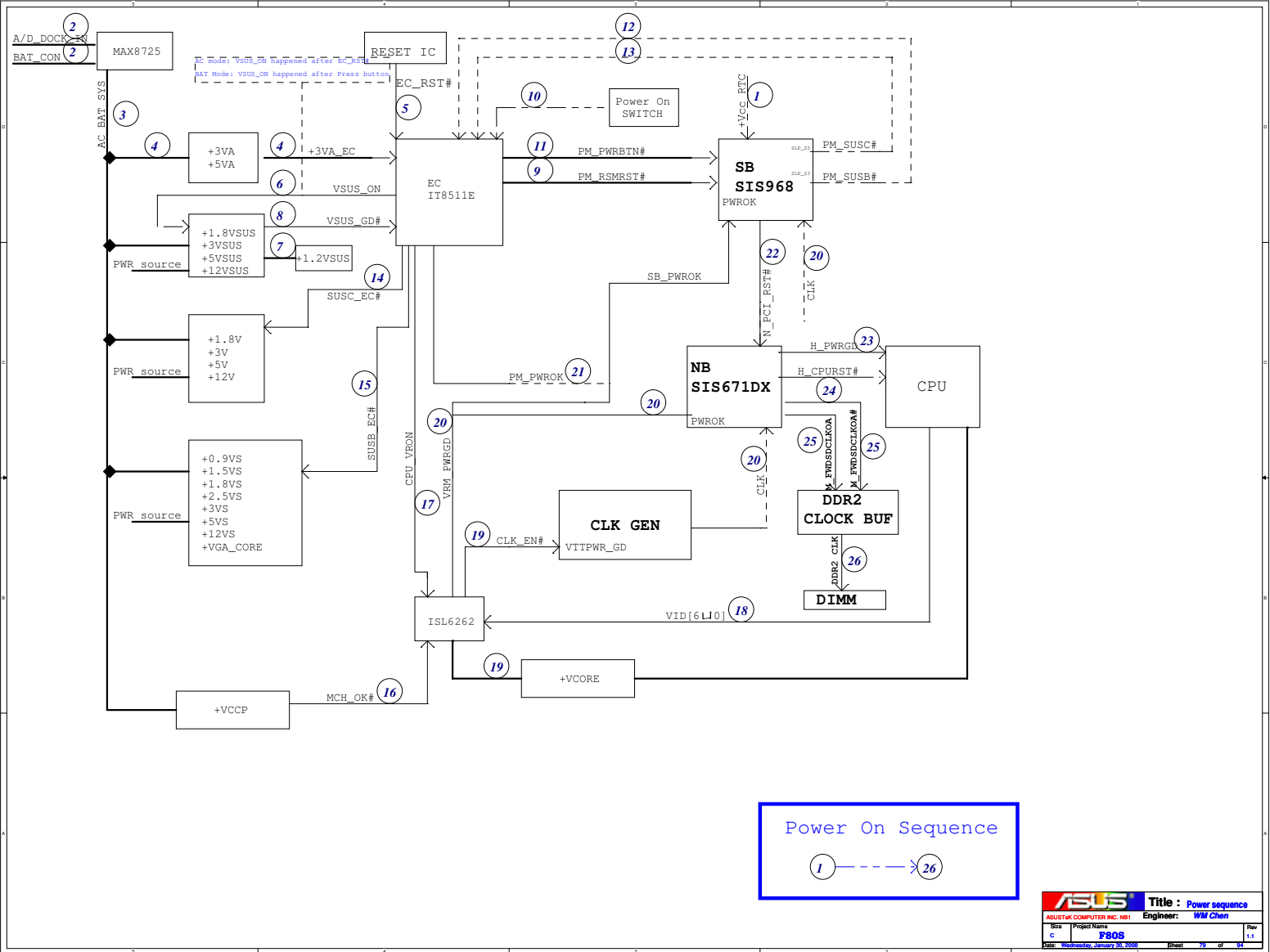
33. Reserve CON1901 pin24 to +3VAUX for Azurewave's Wireless-Lan Card.
34. Modify p25,p26 for colay ALC662 and ALC663 for sales recomment.
35. Change KB connector CON3101 part number for ME request.
36. Change Screw hole for ME request.
37. Change CON3602 part number for ME request.
38. Cap. Array CN3101-CN3106 from 0805 change to 1206 size.
39. Add H4401,H4402 VGA NUT
40. Reserve internal mic J2803 for experiment.
41. Reserve U3502,U2103 circuit for experiment.
42. Add U1601,U1602 for NB output 3.3V level cost down.
43. Change page34 LED schematic for use Blue LED.
44. Reserve C3812,C3813 for EMI request TP_GND.
45. Modify Page 22,32 for sales require SATA ODD.
46. Change SW3801,SW3802 part number for ME request.
47. Change U3001 part number for cost down.
48. Reserve R208,C202 for H_CPURST#.
49. Add U3801 for EMI request.
50. Add RN1801-RN1804 for EMI request.
51. Delete VRAM termination
52. Add Q4405,R4411,C4466 for +3VSG
53. Change Hall-sensor U1701 part number for ME request.

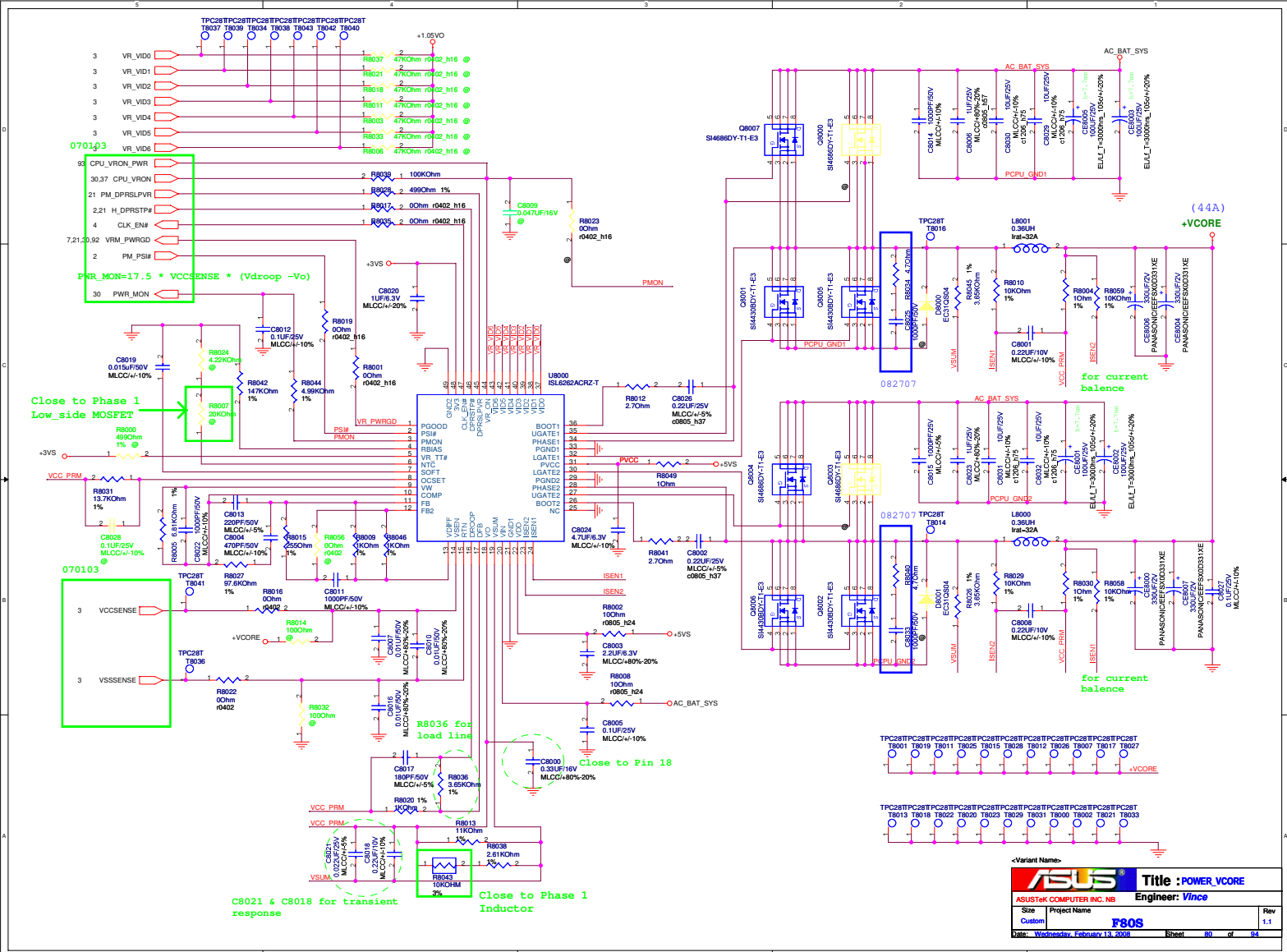
R1.01 revision history:

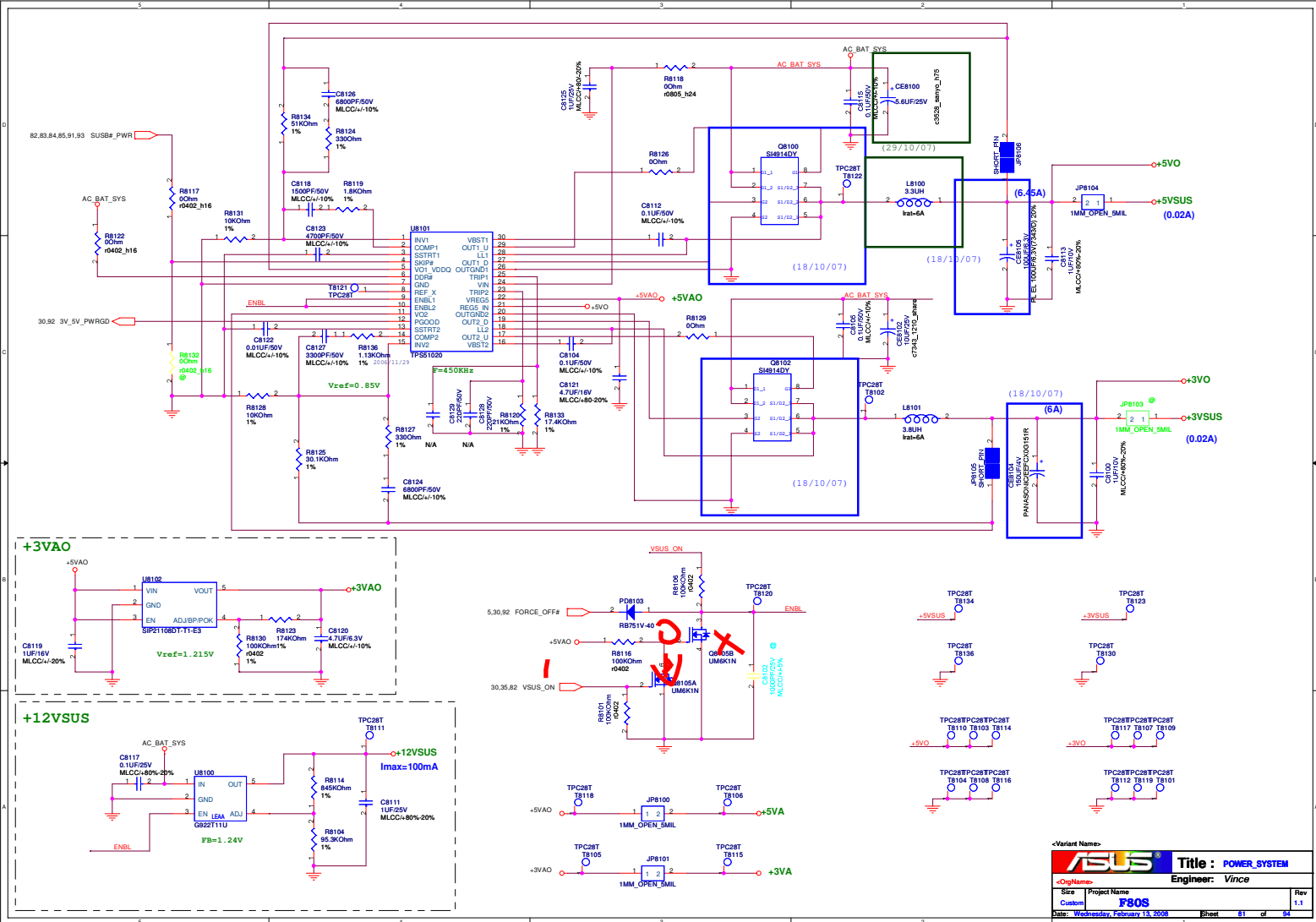
54. Unmount R4428 for leakage current issue.
55. Change R4435 from 71.5 to 150 for SIS request.
56. Mount R4440,R4441,R4443 37.4 ohm for VGA issue.
57. Change SATA ODD Connector CON3203 part number for ME request.
58. Change J2801 connector for Wire-MIC,change R2802 from 4.7K to 2.2K after test internal mic performance.
59. ADD D3603,C3609 for LAN ESD solution.
60. Change L1709,L3801,L3804,L3301,L3302,L3304,L2901 part number for colay footprint.
61. Change R3407,LED3403 value for meet factory LED spec.
62. Change VGA NUT H4401,H4402 to 13G021036001 for Thermal request.
63. Add D3201 IP4220CZ6 and R2225, R2226, R3217, R3218 0 ohm for EMI request.
64. Add C1218, C1707, C1814, C3304 0.1uF for EMI request.
65. Change R1402 from 22 to 10 for DDR Feedback quality.
66. Change Screw hole H4103, H4104, H4108 and Add H4131 for ME request.
67. Change USB external ports for controller.
68. Change CON3602 RJ11+45 part number for ME request.
69. Change R1705 from 100ohm to 330ohm for meet panel spec.
70. Add R2730 for factory recovery AU6371 driver CD.
71. Change R2505,R2506 from 2.7K to 3.3K for HDD-Speaker resonance issue.
72. Change C410,C417,C2101,C2115,C2715,C2716,C3006,C3010 value for TXC report suggest.
73. Del R1816,R1817 for SMT colay request.
74. Add R310 for +VCCA_CPU voltage ripple.
75. Add C2355 2.2uf for SB issue.
76. Unmount R3017,C3001,U3003,C3008,mount R3036,D3003,C3013,R3035,Add R3039 for cost down.
77. Add C3514 for Lan issue.
78. Mount R4459 and unmount Q4405, R4411,C4466 for cost down.
79. Add R4603 and unmount D4601 for cost down.
80. Mount R4708 and unmount Q4801,Q4704,R4709,R4704 for cost down.
81. ADD CE4783 220UF,C4740,C4783 0.1uF for VGA issue.
82. Mount R1611,R1612 from 0 ohm to 33 ohm and mount C1606,C1607 for meet spec.
83. Change CON3201 part number for ME request.
84. Add R2122,R2186 for SB issue.
85. Add C3305 22UF for meet spec.
86. Add C1905,C2727,C4333,C4334,C4335,C4716,Del C4331 for meet spec.
87. Del CE3202 100UF and Add C3209 47UF for ME request.
88. Change CE2601,CE2602 from 27uF to 47uF for better Low frequency response.
89. Add D4401,R3717,Q3709 for meet VGA spec.

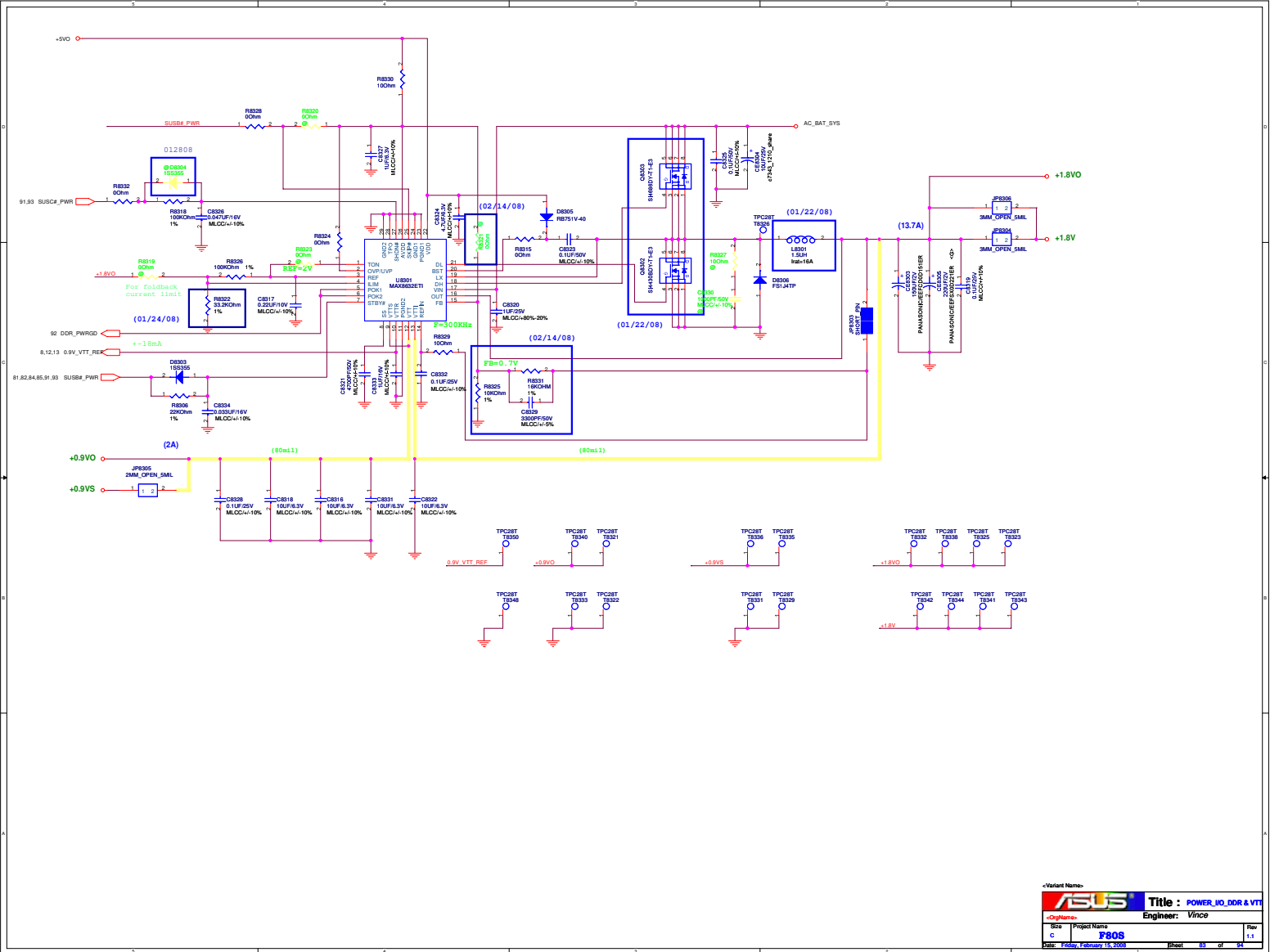
		Title :	
ASUSTeK COMPUTER INC. NE1		Engineer: WM Chen	
Size	Project Name		Rev
Custom	F80S		1.1
Date: Wednesday, January 30, 2008		Sheet	58 of 84

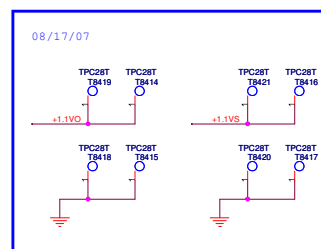
S	A	S	2	1									
D													
C													
B													
A													
<div><div>Title : BLANK</div><div>ASUSTEK COMPUTER INC. NE1Engineer: WM Chen</div><table><tr><td>Size</td><td>Project Name</td><td>Rev</td></tr><tr><td>Custom</td><td>F80S</td><td>1.1</td></tr><tr><td colspan="2">Date: Wednesday, January 30, 2008</td><td>Sheet 80 of 84</td></tr></table></div>					Size	Project Name	Rev	Custom	F80S	1.1	Date: Wednesday, January 30, 2008		Sheet 80 of 84
Size	Project Name	Rev											
Custom	F80S	1.1											
Date: Wednesday, January 30, 2008		Sheet 80 of 84											
S	A	S	2	1									



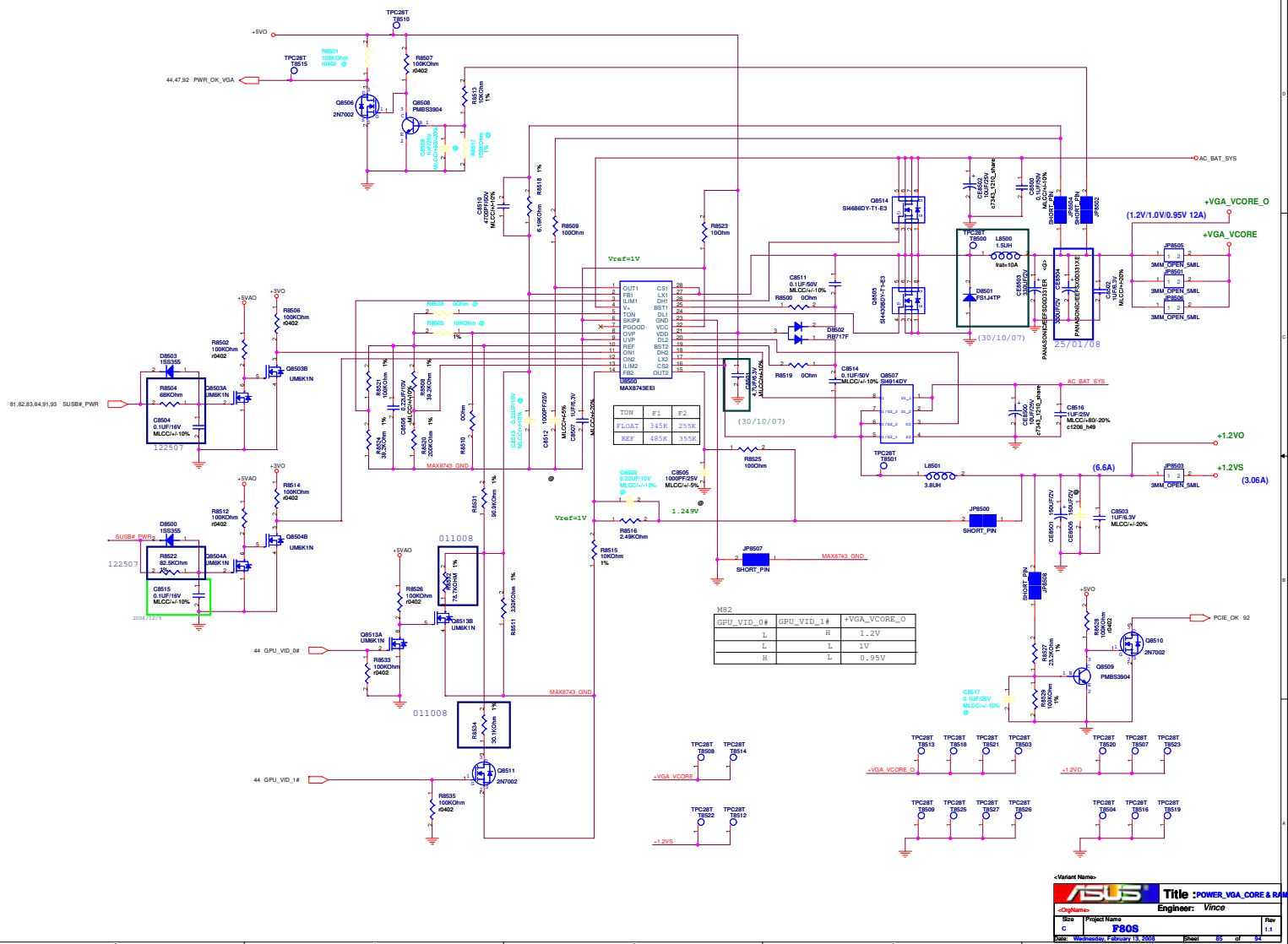








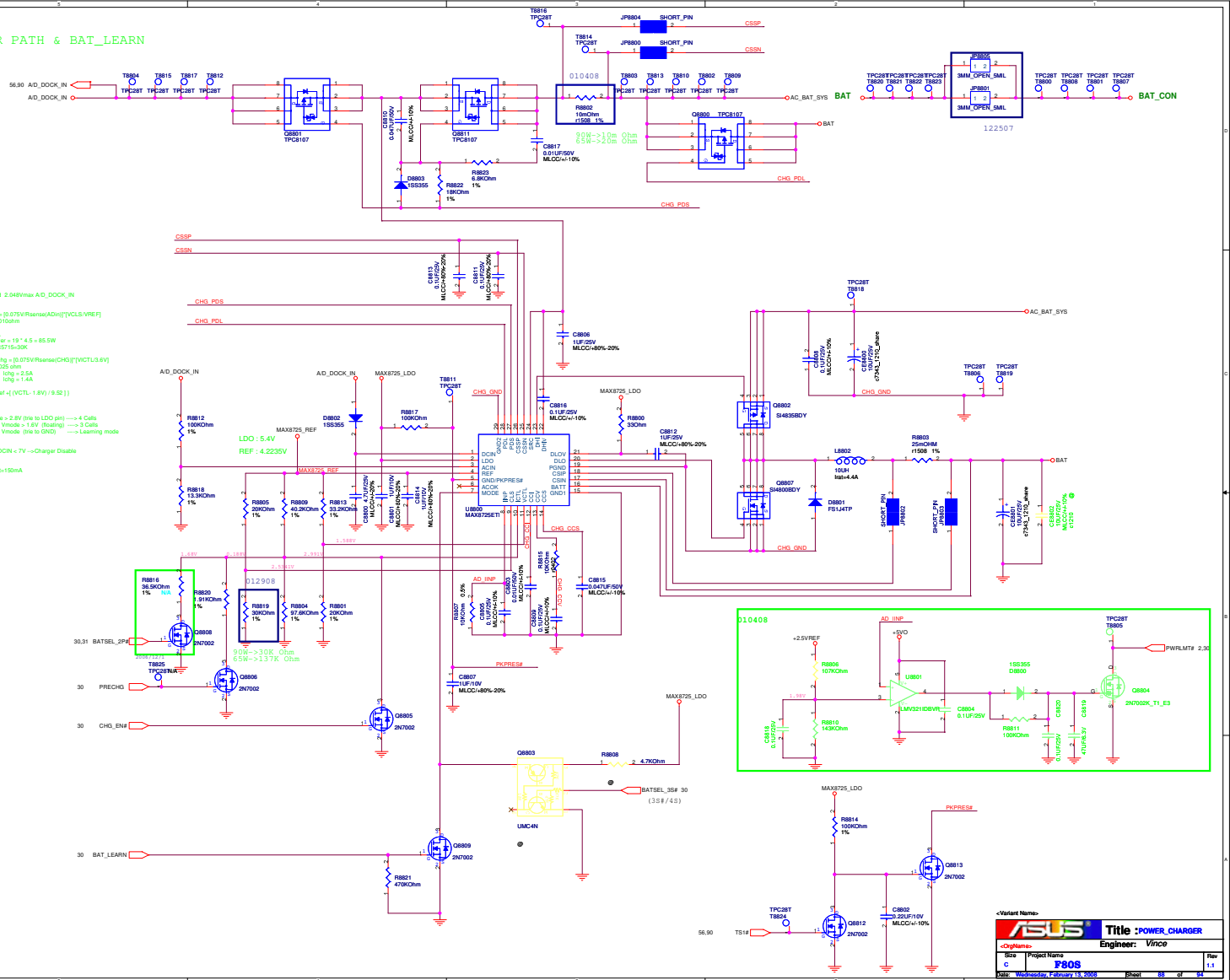
		Title : POWER_I/O_VCCP & 1.IVS	
<OrigName>		Engineer: Vince	
Size Custom	Project Name F80S		Rev 1.1
Date: Wednesday, February 13 2008		Sheet	94 of 94




<Variant Name>

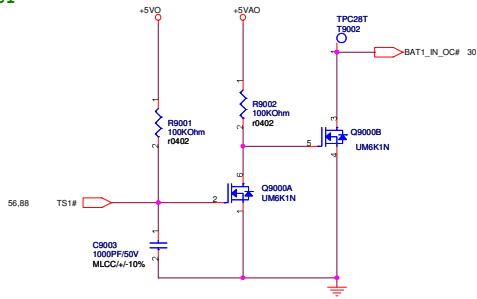
		Title : NA	
<p><OrgName></p>		Engineer: Vince	
Size Custom	Project Name F80S	Rev 1.1	
Date: Wednesday, January 30, 2008		Sheet 67 of 94	

POWER PATH & BAT_LEARN

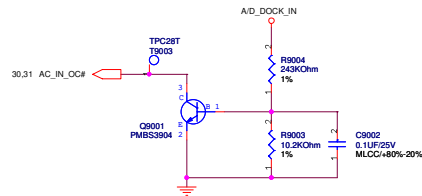


S	A	S	2	1
D				D
C				C
B				B
A				A
<div><Variant Name>  Title : N/A <OrigName> Engineer: Vince Size Project Name Rev Custom F80S 1.1 Date: Wednesday, January 30, 2008 Sheet 89 of 94</div>				
S	A	S	2	1

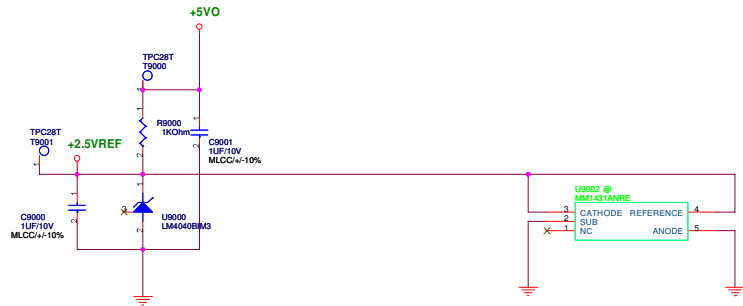
BATTERY IN DETECT



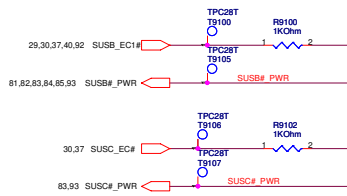
ADAPTER IN DETECT



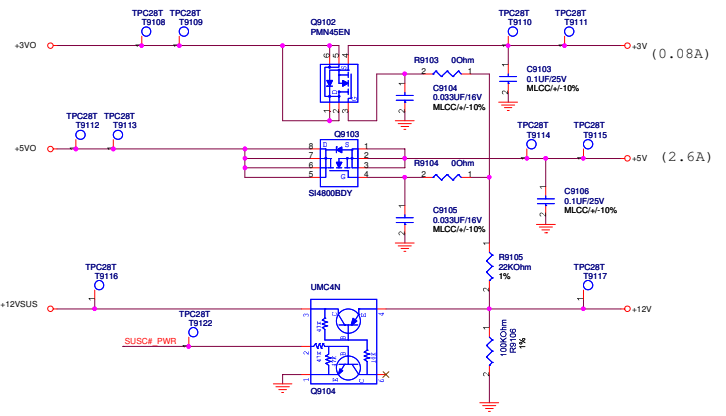
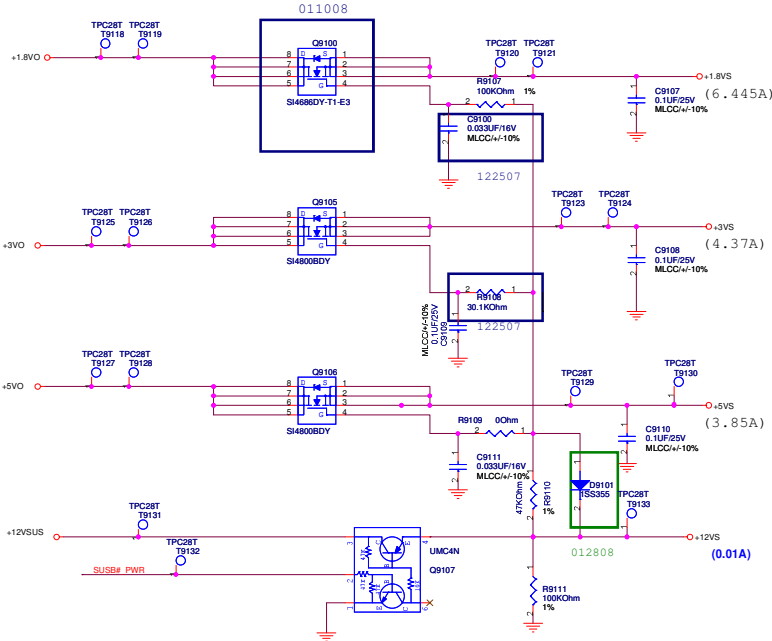
+2.5VREF



SUSC#_STAGE POWER

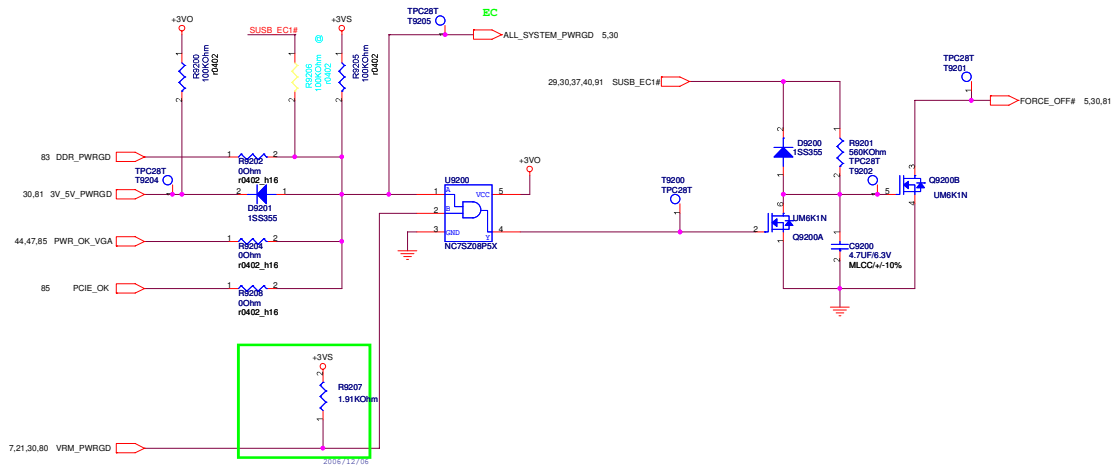


SUSB#_PWR POWER



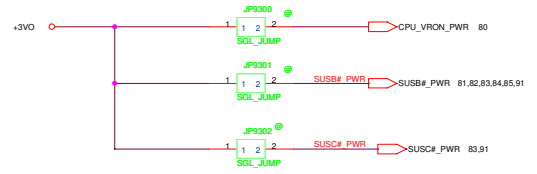
ASUS		Title : POWER_LOAD SWITCH	
Engineer: Vince		Rev	
Size	Project Name	F80S	
Custom	Project Name	F80S	
Date: Wednesday, February 13, 2008	Sheet	81	of 84

POWER GOOD DETECTOR



AC_BAT_SYS	AC_BAT_SYS	17,80,81,83,85,88
+3VA	+3VA	17,21,30,37,81
+5VA	+5VA	5,81
+5VO	+5VO	81,83,85,88,90,91
+3VO	+3VO	81,85,91,92
+3VSUS	+3VSUS	8,17,19,20,21,22,23,24,26,30,34,35,36,81,82
+5VSUS	+5VSUS	34,81
+3V	+3V	19,27,29,31,36,37,38,40,91
+3VS	+3VS	2,4,5,7,12,13,16,17,18,19,20,21,22,23,25,29,30,32,33,34,37,40,44,80,91,92
+12VSUS	+12VSUS	81,82,91
+12V	+12V	26,27,30,37,84,91
+12VS	+12VS	17,18,37,44,91
+5V	+5V	17,33,37,91
+5VS	+5VS	5,12,16,18,25,26,29,30,32,34,37,38,51,80,91
+1.8VO	+1.8VO	82,83,91
+1.8VSUS	+1.8VSUS	10,21,22,23,82
+1.8V	+1.8V	8,10,12,13,15,37,83
+1.8VS	+1.8VS	6,7,8,10,12,14,20,21,22,23,37,43,44,45,47,51,91
+1.5VS	+1.5VS	3,19,25,29,37,51,82
+VCCP	+VCCP	2,4,5,6,10,21,23,37,43,84
+0.9VS	+0.9VS	15,37,83
BAT	BAT	88
+2.5VREF	+2.5VREF	82,84,88,90
+VCORE	+VCORE	3,37,80
+VGA_VCORE	+VGA_VCORE	37,43,47,51,85
+1.2VO	+1.2VO	84,85
+1.2VS	+1.2VS	9,10,37,85
BAT_CON	BAT_CON	56,88

FOR POWER TEST



ASUS		Title : POWER_SIGNAL	
Engineer: Vince			
Size	Project Name	Rev	
Custom	F80S	1.1	
Date: Wednesday, February 13, 2008	Sheet	93	of 94

