

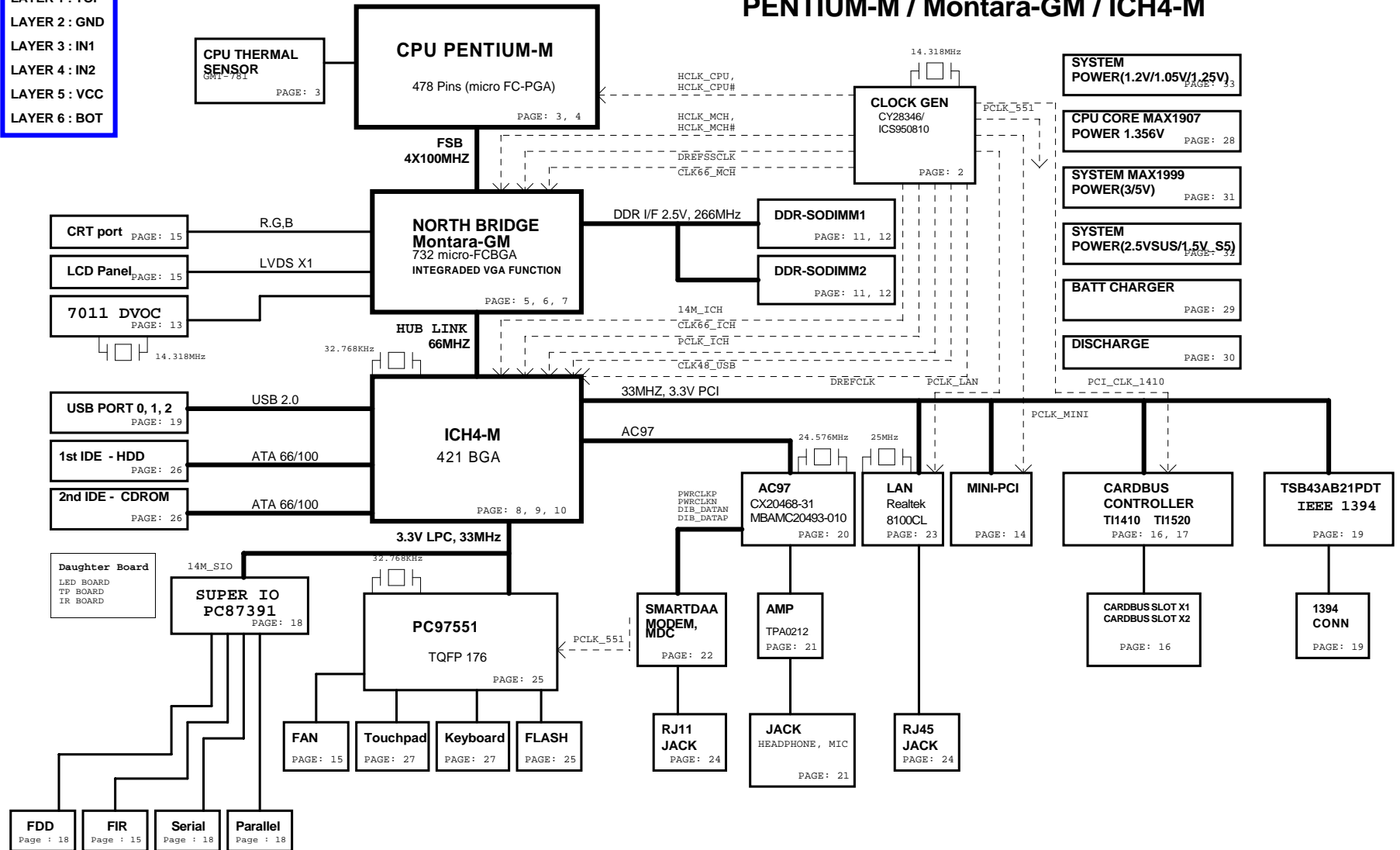
# KT2 BLOCK DIAGRAM (KT2 APJ)

## PENTIUM-M / Montara-GM / ICH4-M

### PCB STACK UP

- LAYER 1 : TOP
- LAYER 2 : GND
- LAYER 3 : IN1
- LAYER 4 : IN2
- LAYER 5 : VCC
- LAYER 6 : BOT

31KT2MB0018



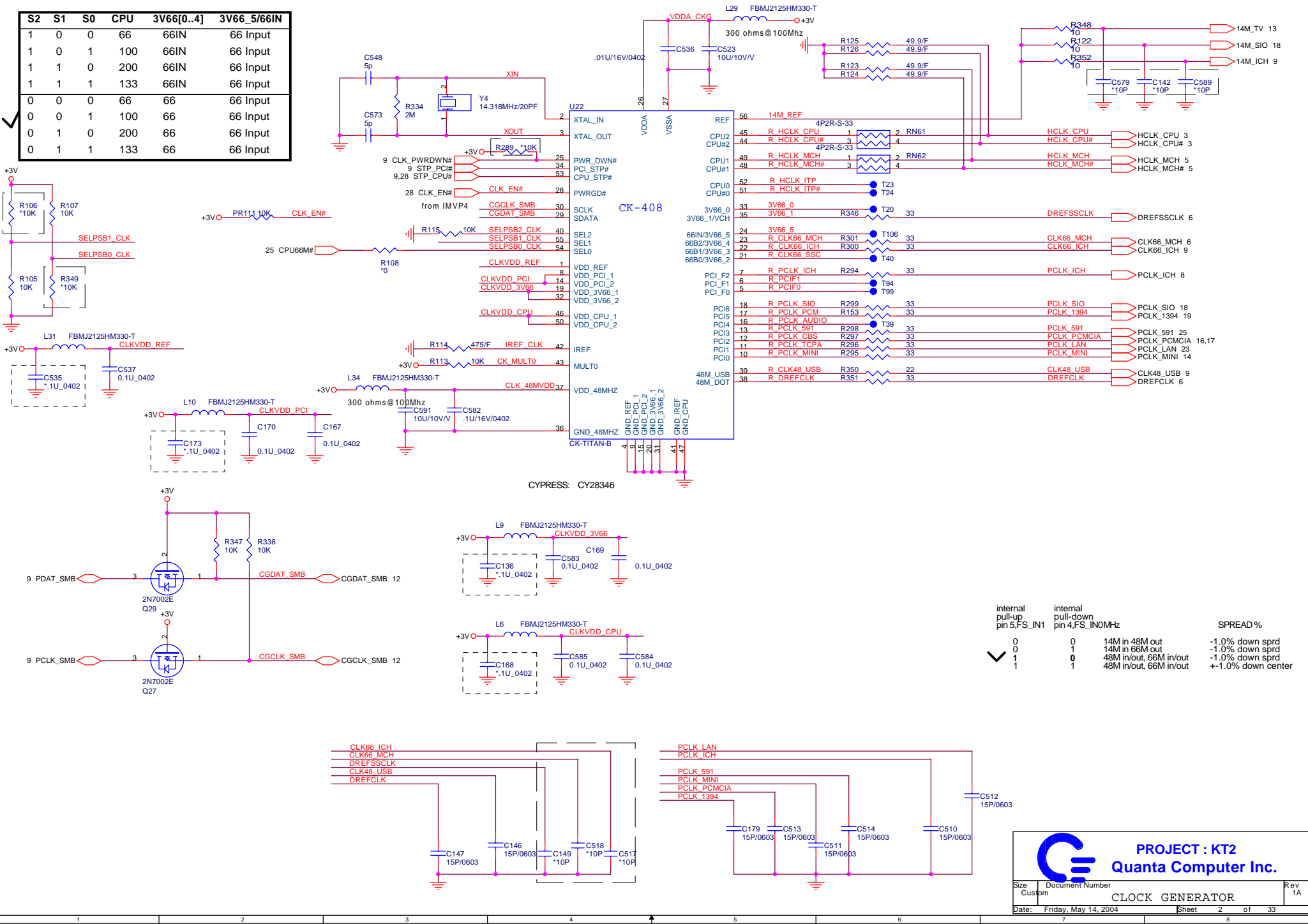
REQ0#, GNT0# : PCMCIA  
REQ1#, GNT1# : MINIPCI  
REQ2#, GNT2# : LAN  
REQ3#, GNT3# : IEEE1394  
REQ4#, GNT4# : N/A

INTA# : LAN  
INTB# : CARDBUS 1520  
INTC# : MINIPCI  
INTD# : MINIPCI  
INTE# : CARDBUS  
INTF# : IEEE1394  
INTG# : NC  
INTH# : Internal USB


AD16 : RTL8100CL  
AD17 : PCI1520  
AD21 : PCI1410  
AD22 : MINIPCI  
AD23 : IEEE1394

CLK GEN

S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

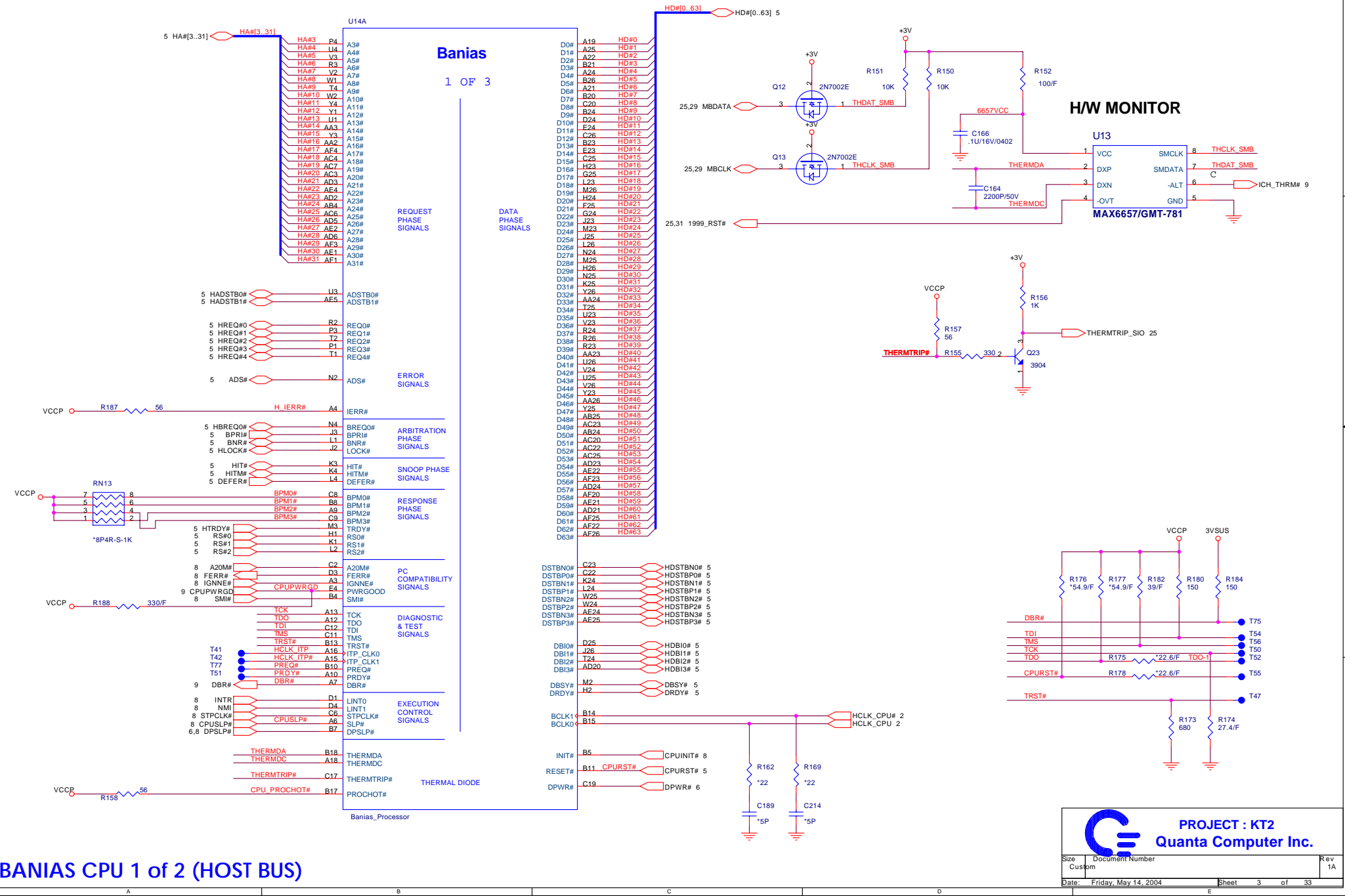


internal pull-up pin 5,FS_IN1	internal pull-down pin 4,FS_IN0MHz	SPREAD %
0	0	14M in 48M out
0	1	14M in 66M out
1	0	48M in/out, 66M in/out
1	1	48M in/out, 66M in/out



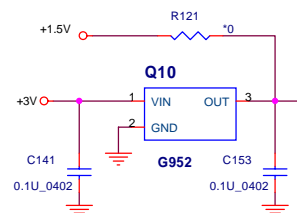
**PROJECT : KT2**  
**Quanta Computer Inc.**

Size	Document Number	Rev
Custom	CLOCK GENERATOR	1A
Date:	Friday, May 14, 2004	Sheet 2 of 33



COMP0 & 2=> Trace Z=27.4ohm  
COMP1 & 3=> Tracer Z=55ohm

CHANGE TO 0805

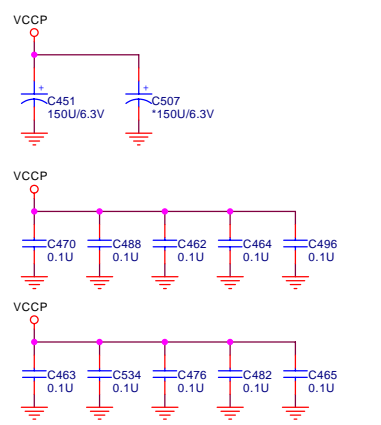


WITHIN 0.5" &  
SPACE 25 mil

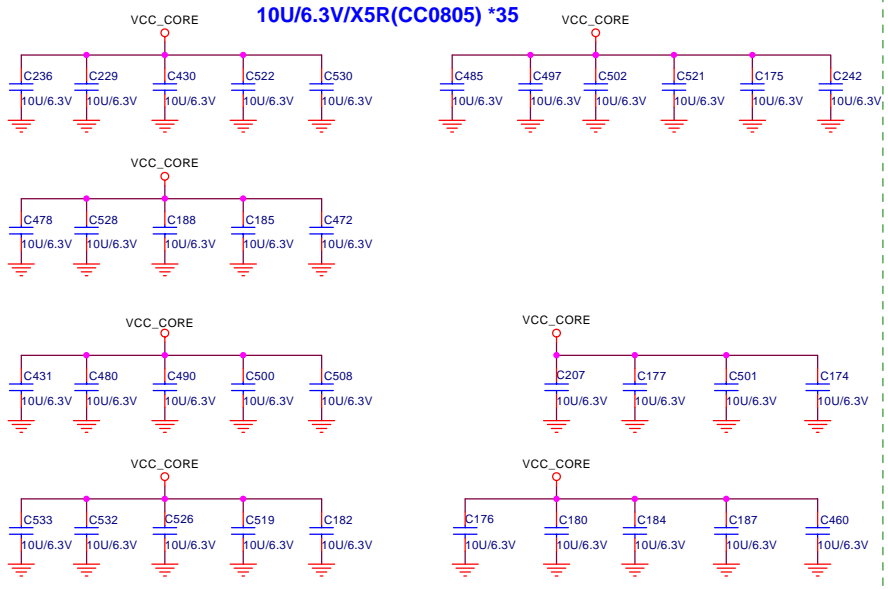
A0 : STUFF  
A1 : NC

CC0402

FOR VT7/ 0.1UF X10



100U/6.3V/X5R(CC0805) \*35

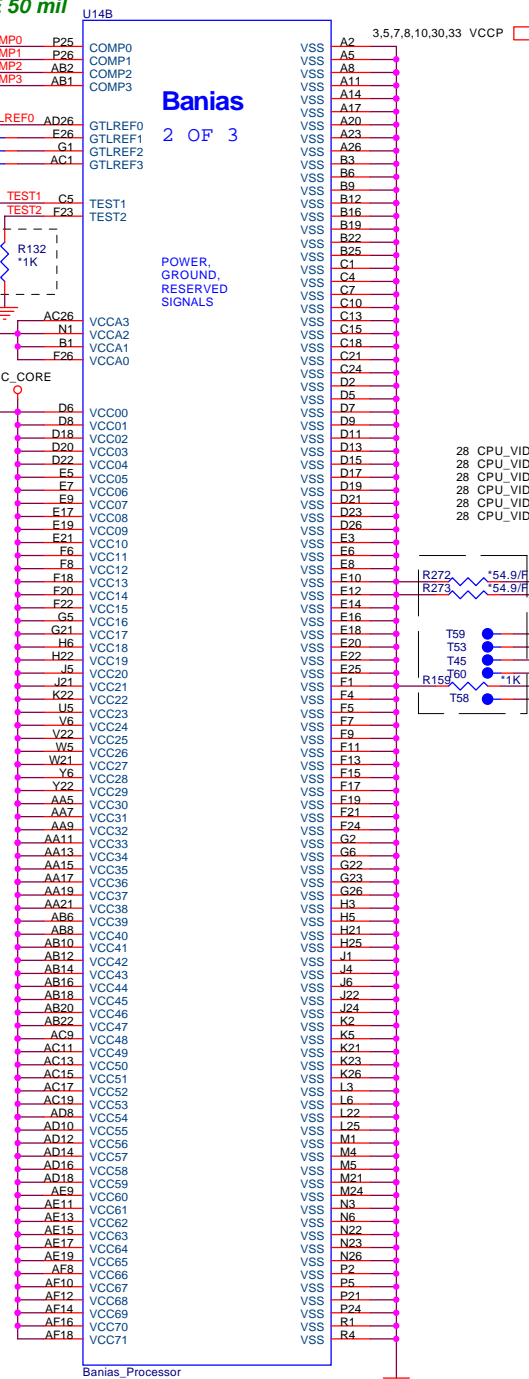


WITHIN 0.5" & SPACE 50 mil



Banias  
2 OF 3

POWER, GROUND, RESERVED SIGNALS



Banias  
3 OF 3

POWER, GROUND AND NC

VID

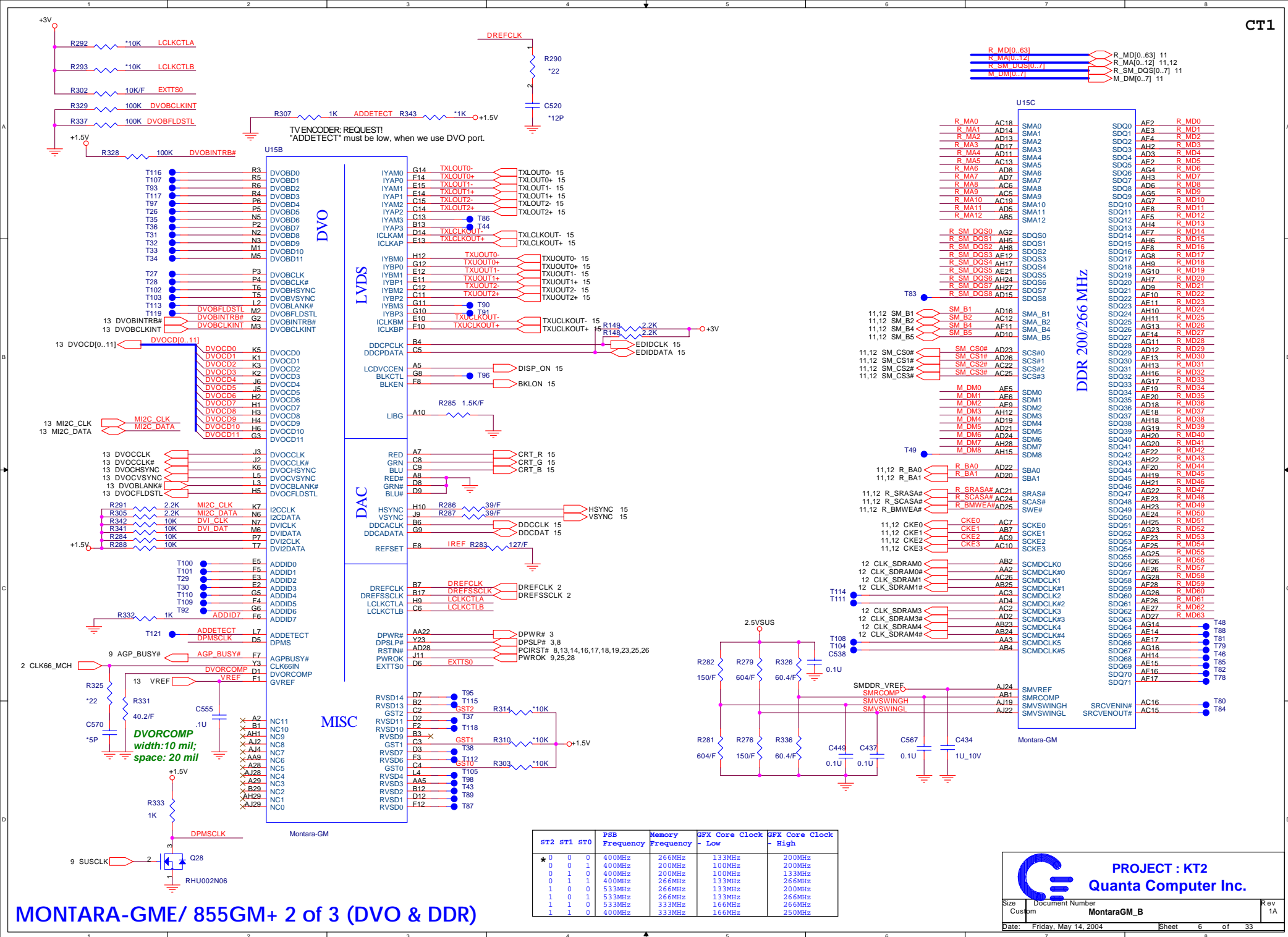


PROJECT : KT2  
Quanta Computer Inc.

Size	Document Number	Rev
B		1A
CPU POWER		
Date:	Friday, May 14, 2004	Sheet 4 of 33

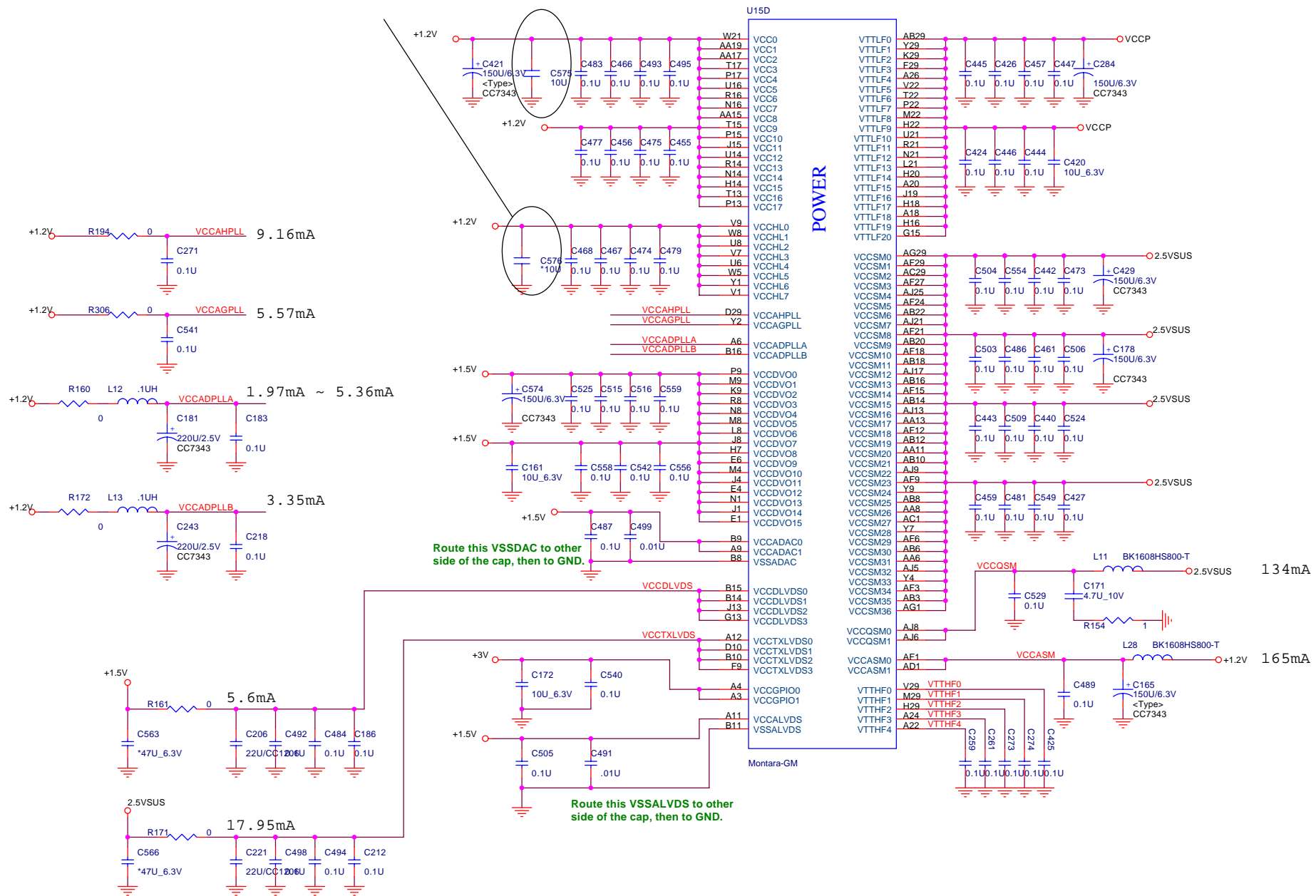
BANIAS CPU 2 of 2 (PWR)







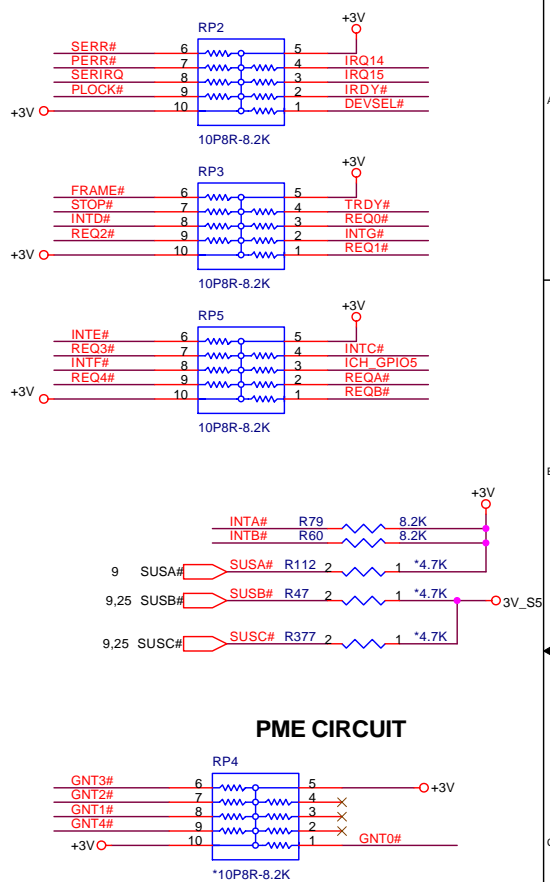
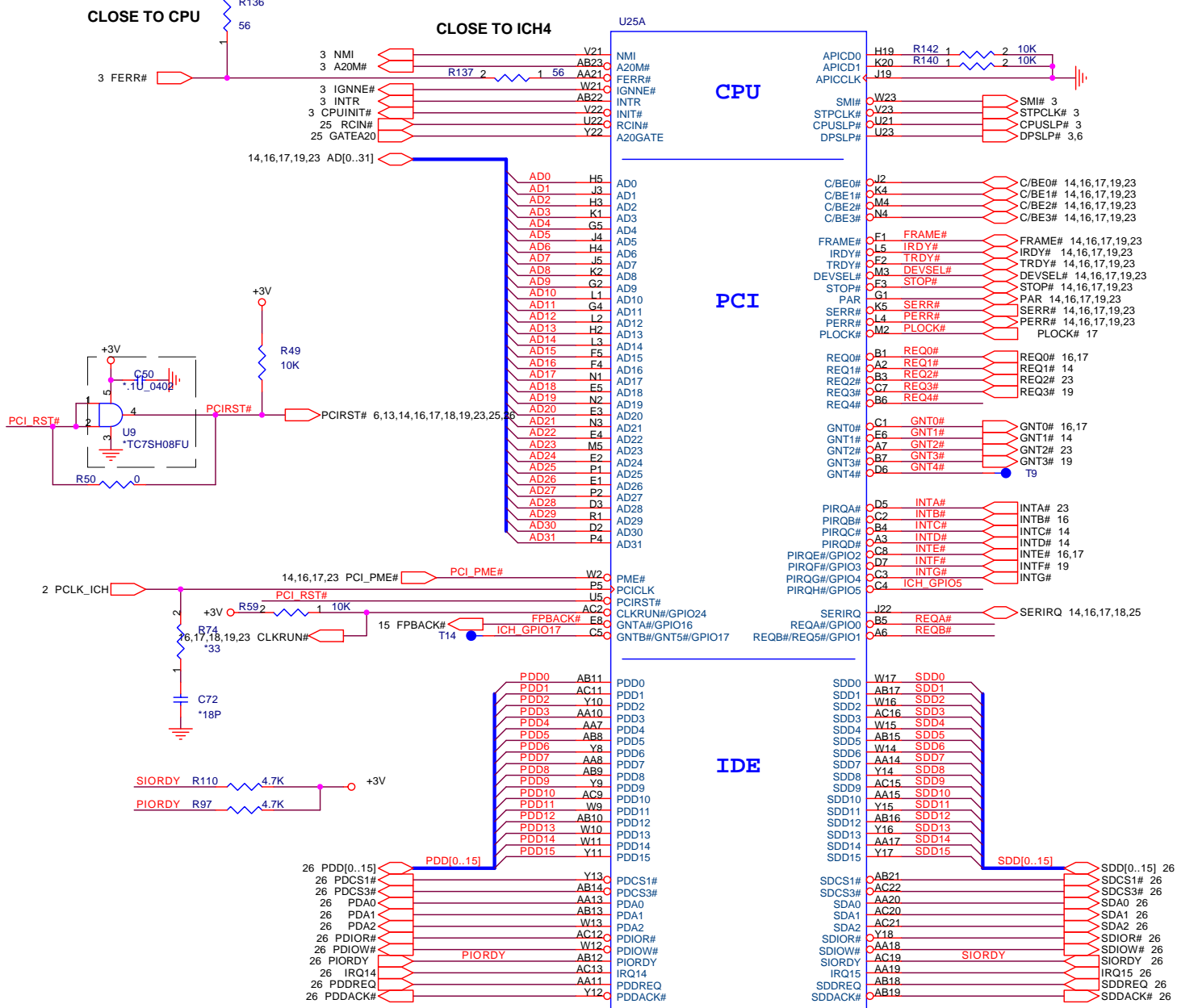
REV:C from 1206 size change to 0805 size




ICH4-M 1/3 (CPU, PCI, IDE)

CT1

PCI Bus pull high resistor





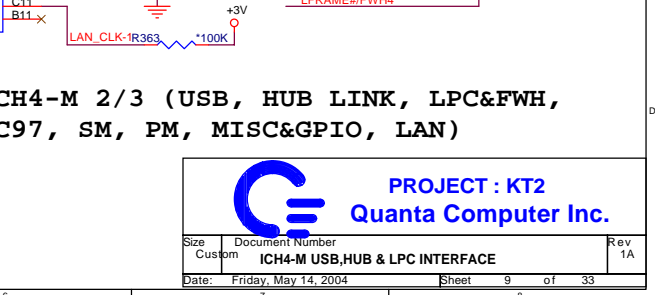
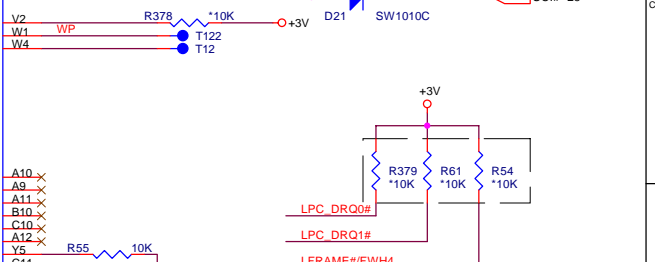
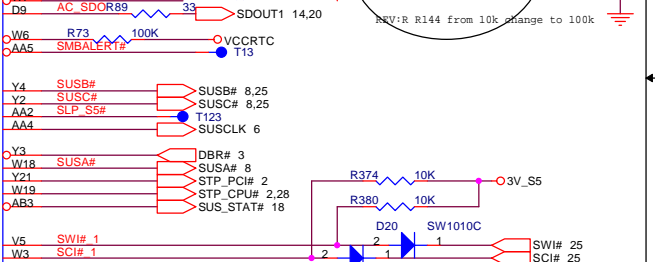
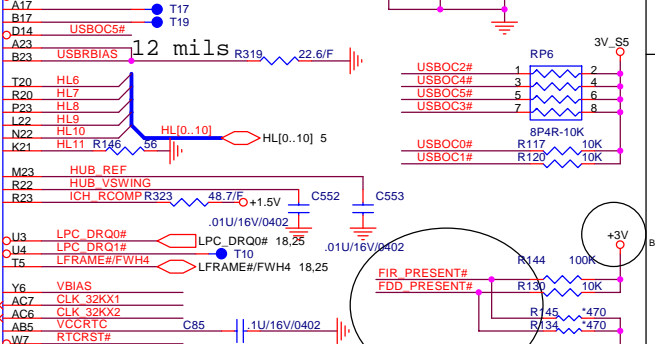
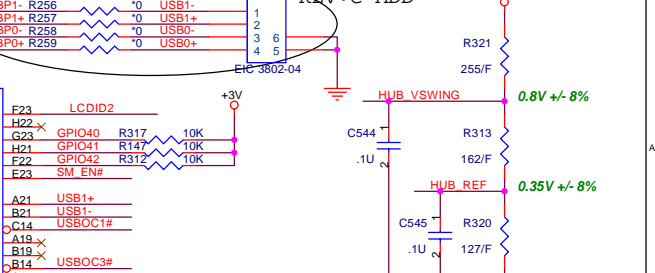
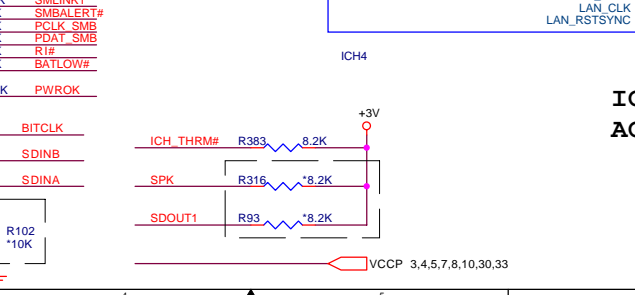
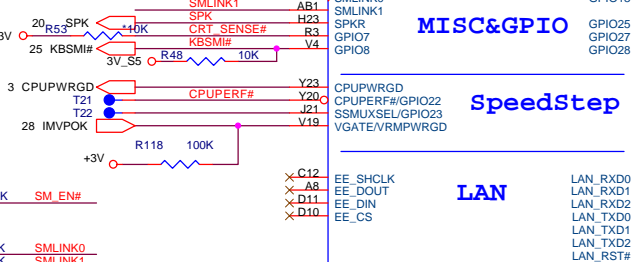
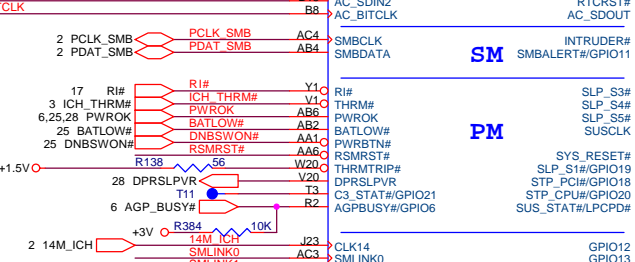
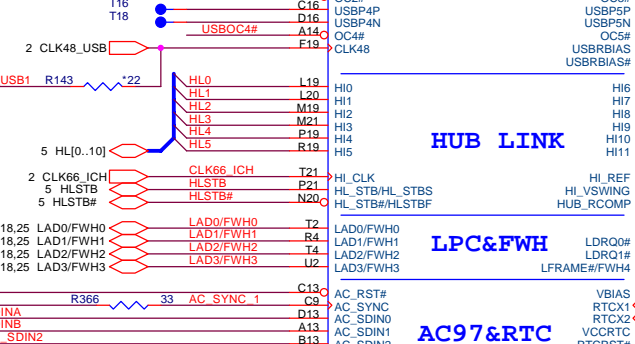
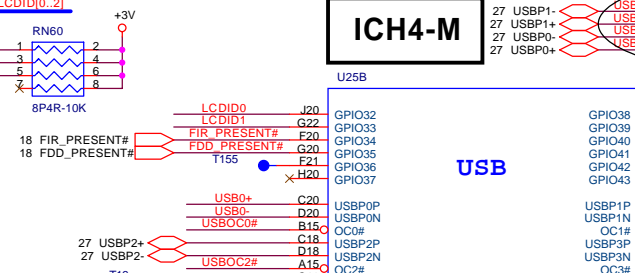
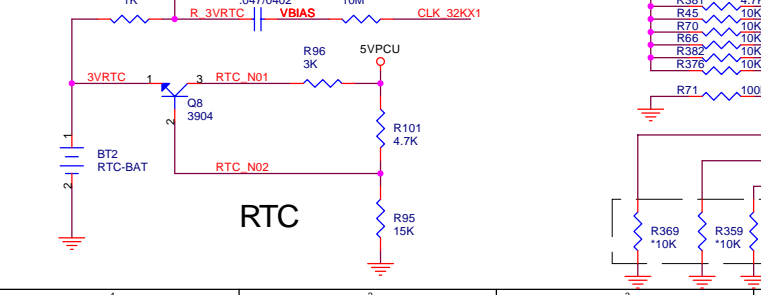
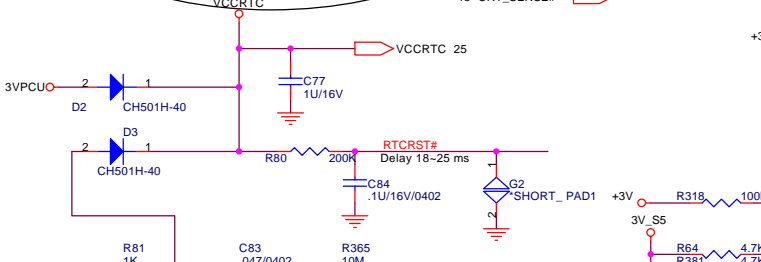
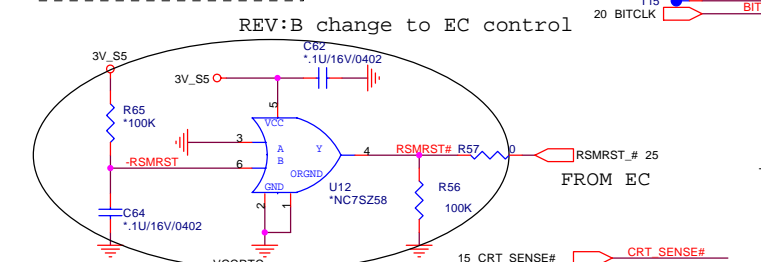
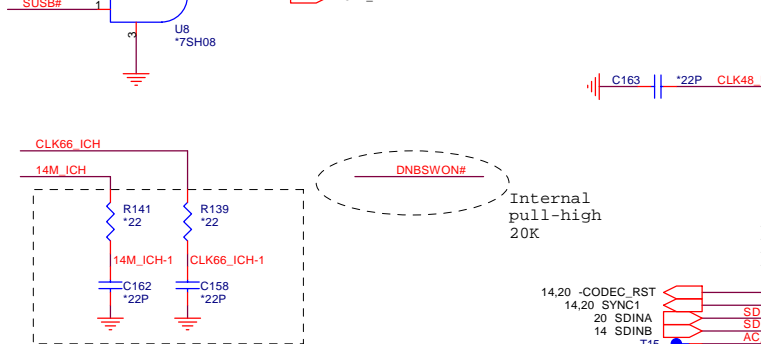
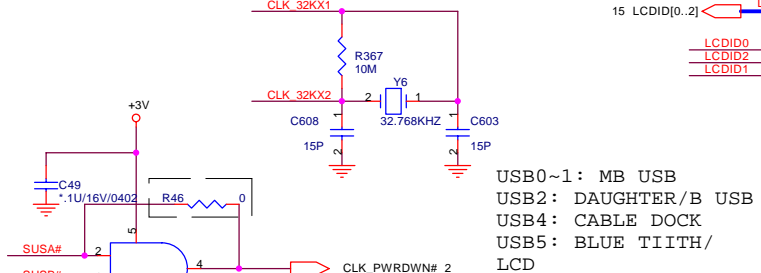
PROJECT : KT2

Quanta Computer Inc.

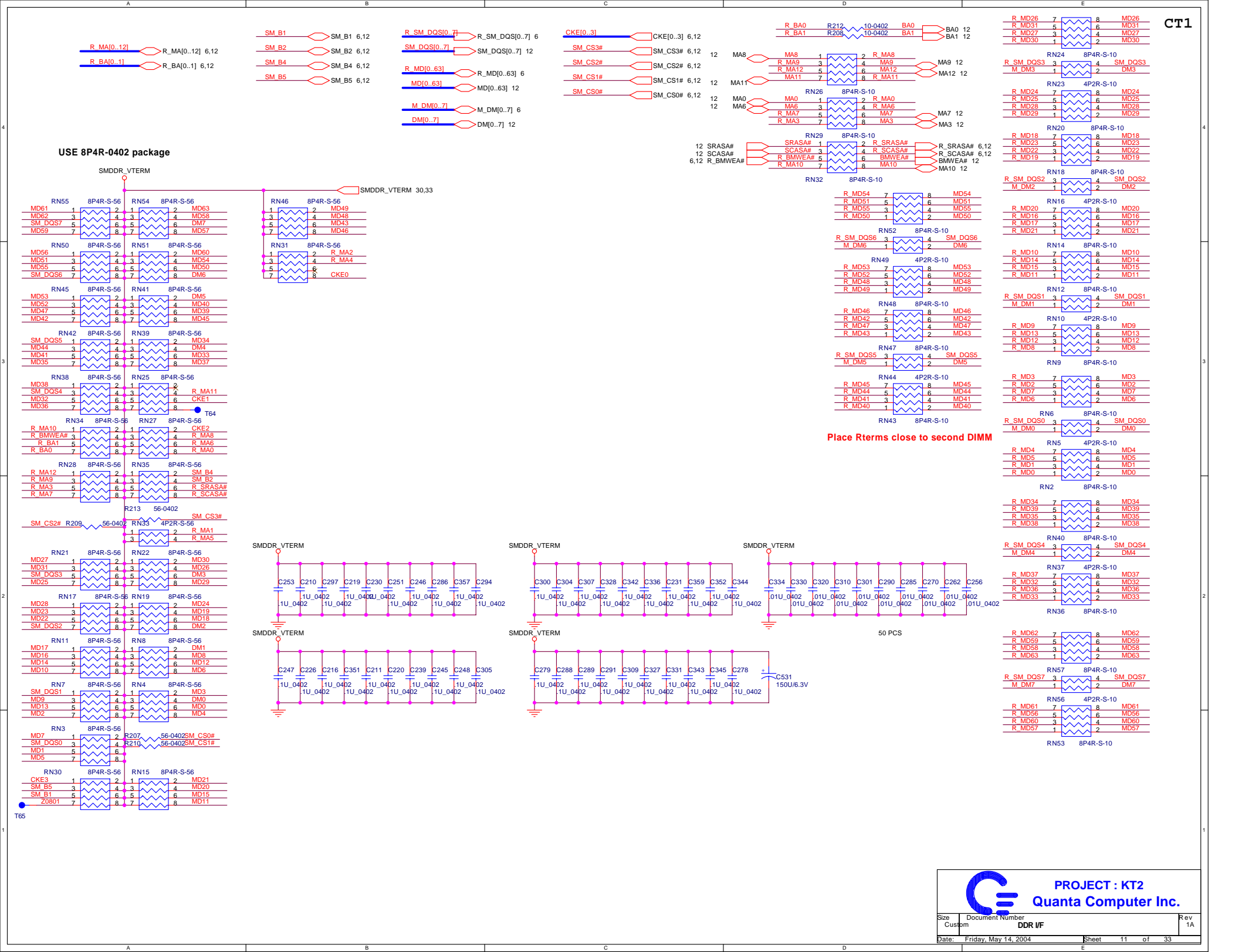
Size	Document Number	Rev
Custom	ICH4-M (CPU,PCI,IDE)	1A
Date:	Friday, May 14, 2004	Sheet 8 of 33

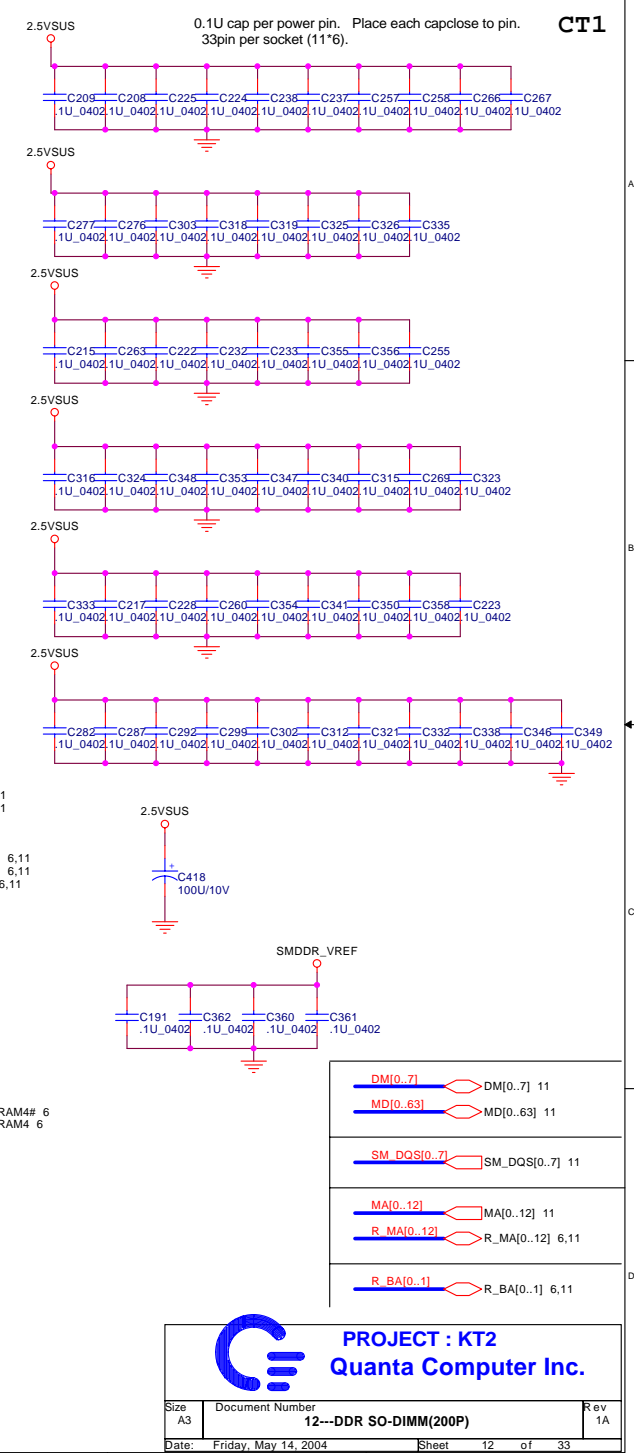
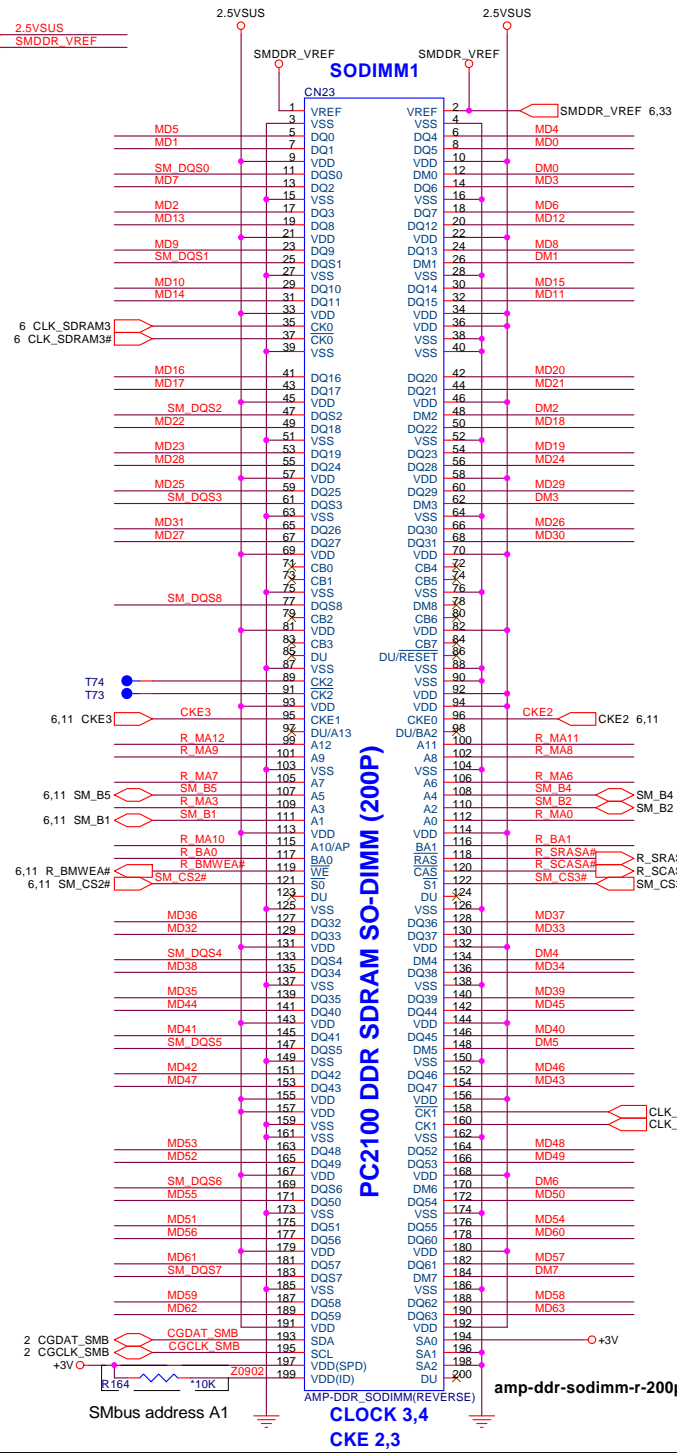
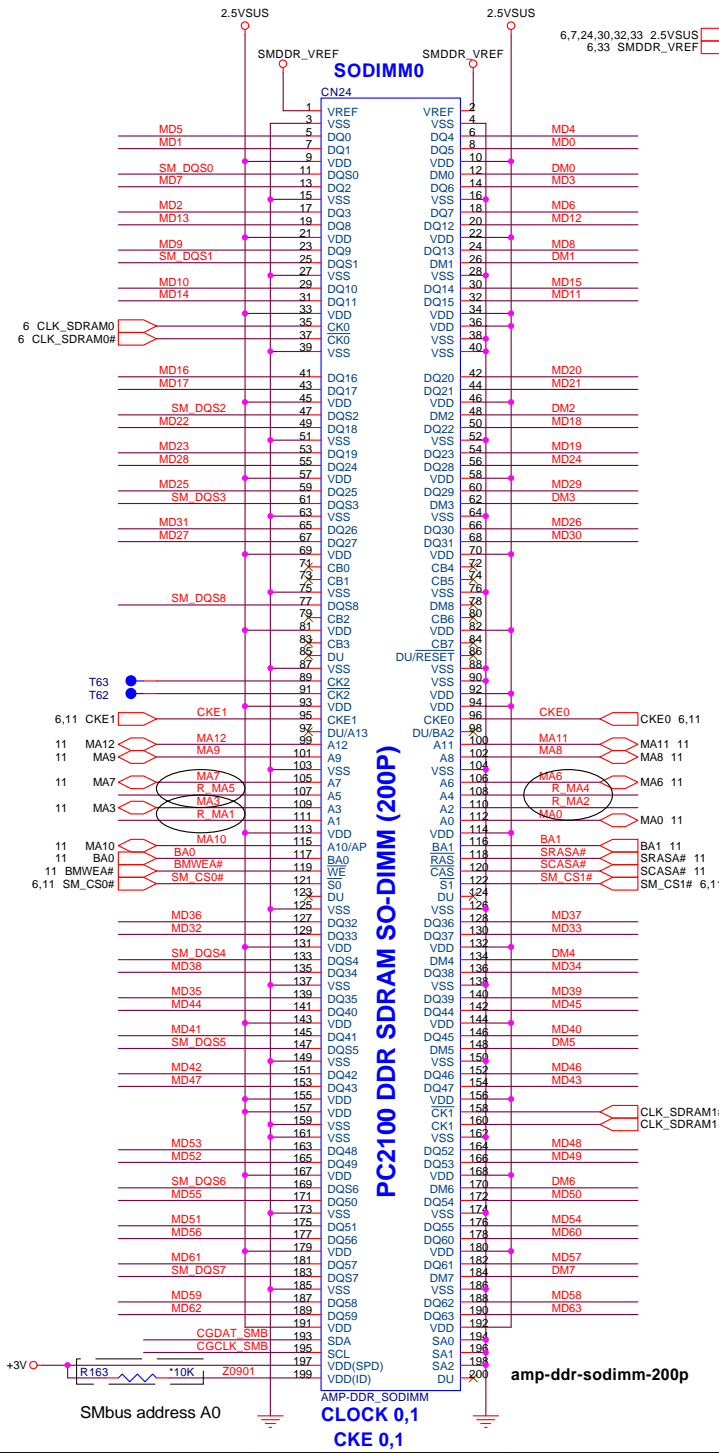


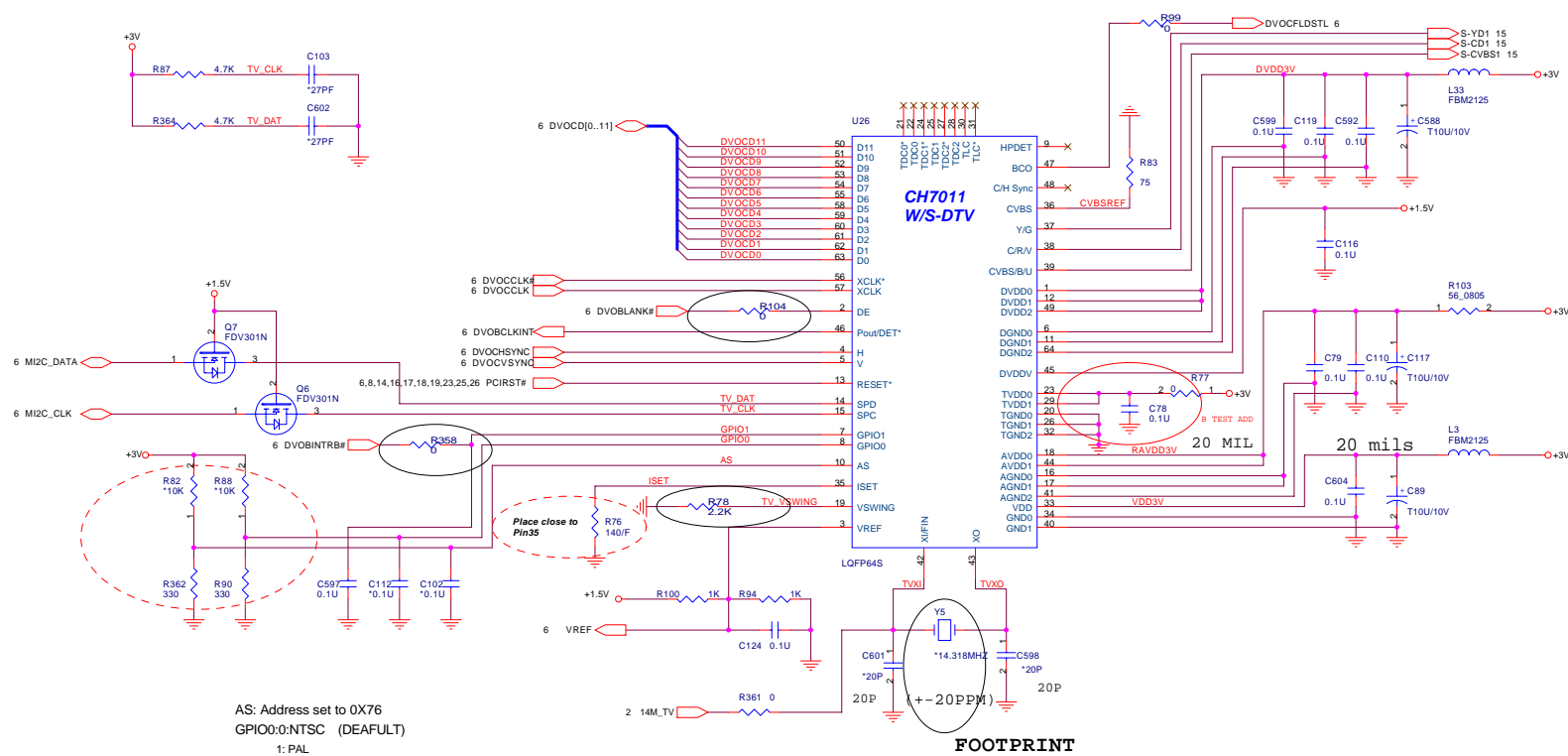
# ICH4-M



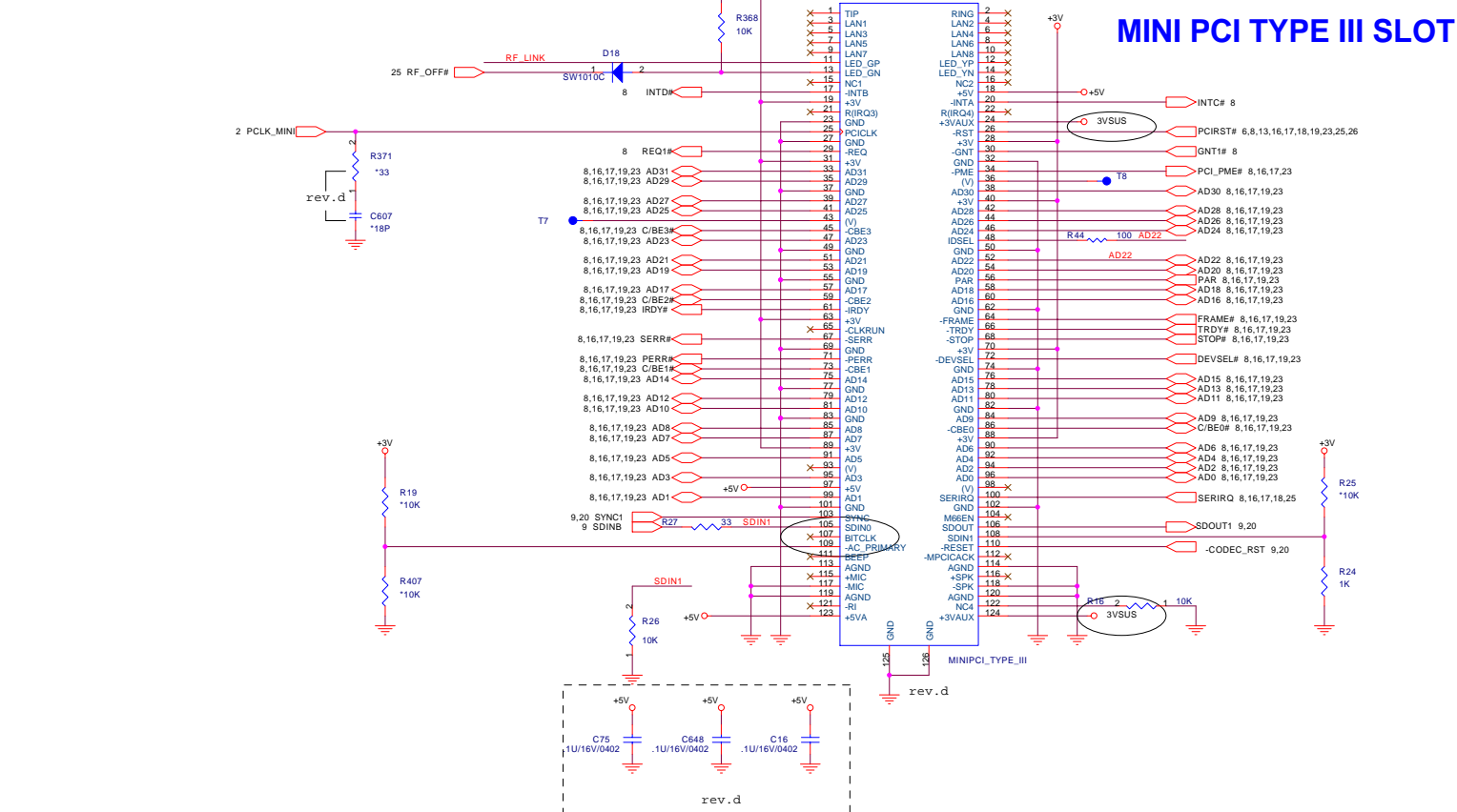






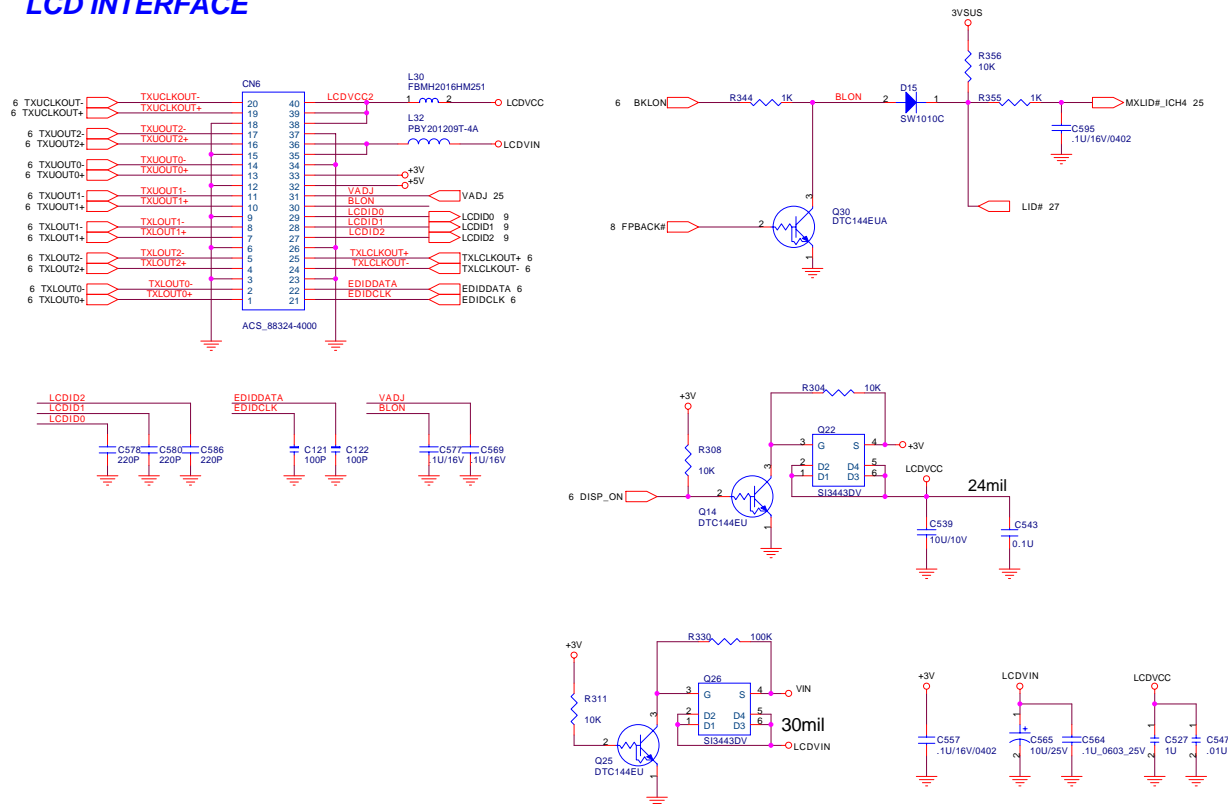


PROJECT : KT2  
Quanta Computer Inc.

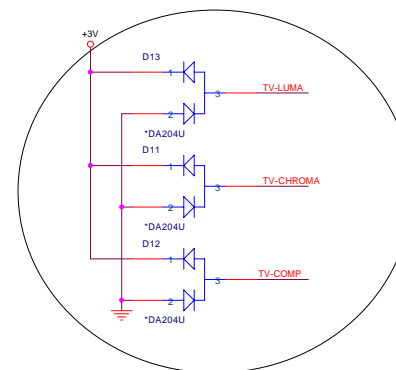
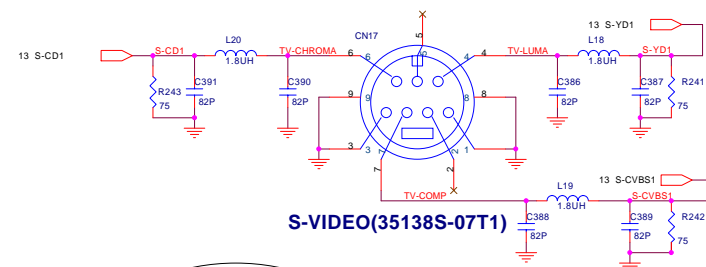




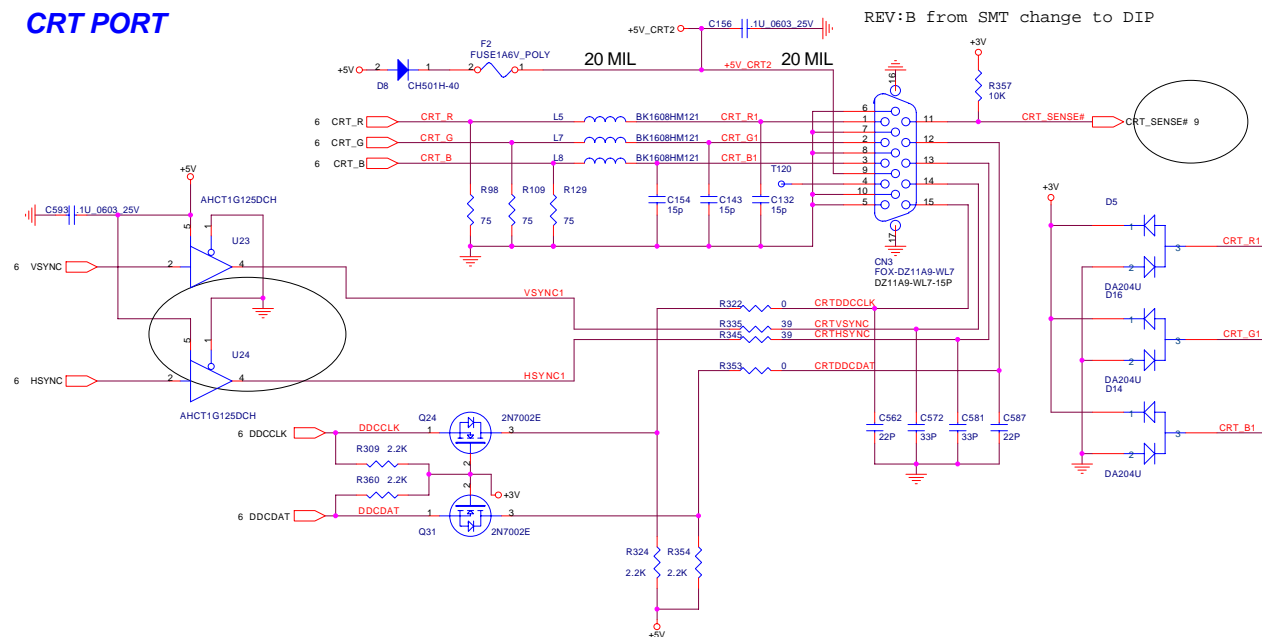
## LCD INTERFACE



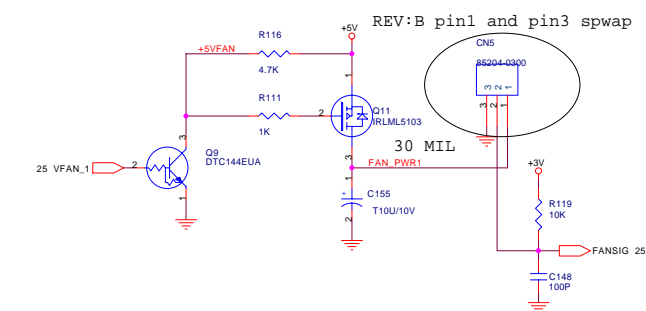
## S-VIDEO CONNECTOR

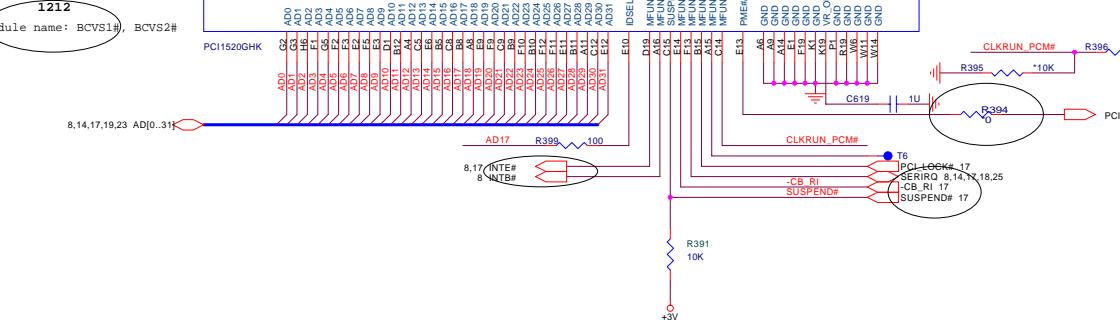
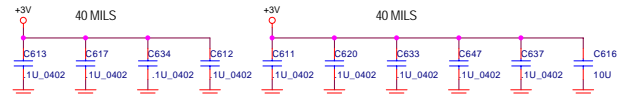
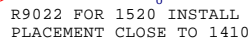


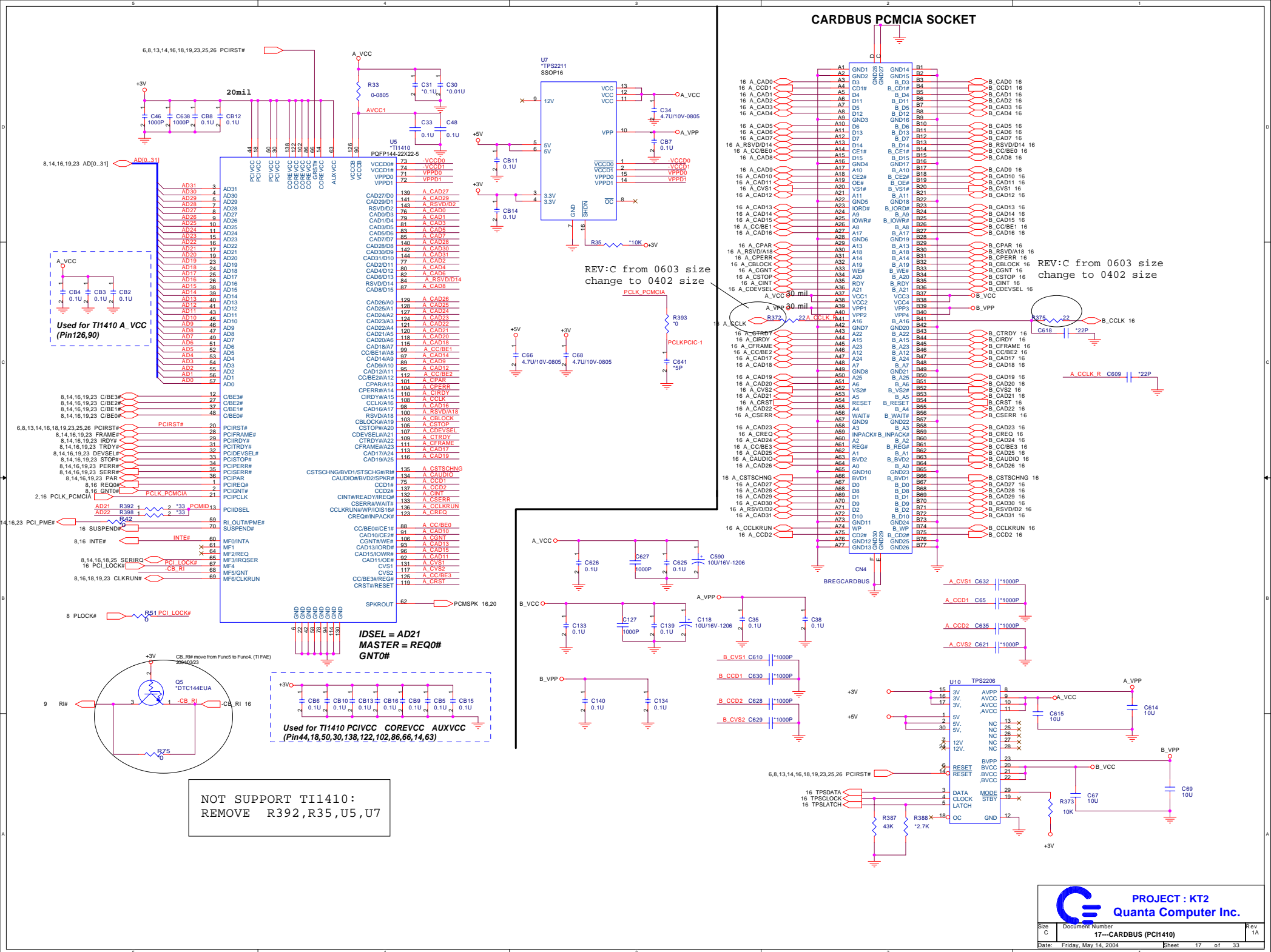
## CRT PORT



## FAN CONTROL

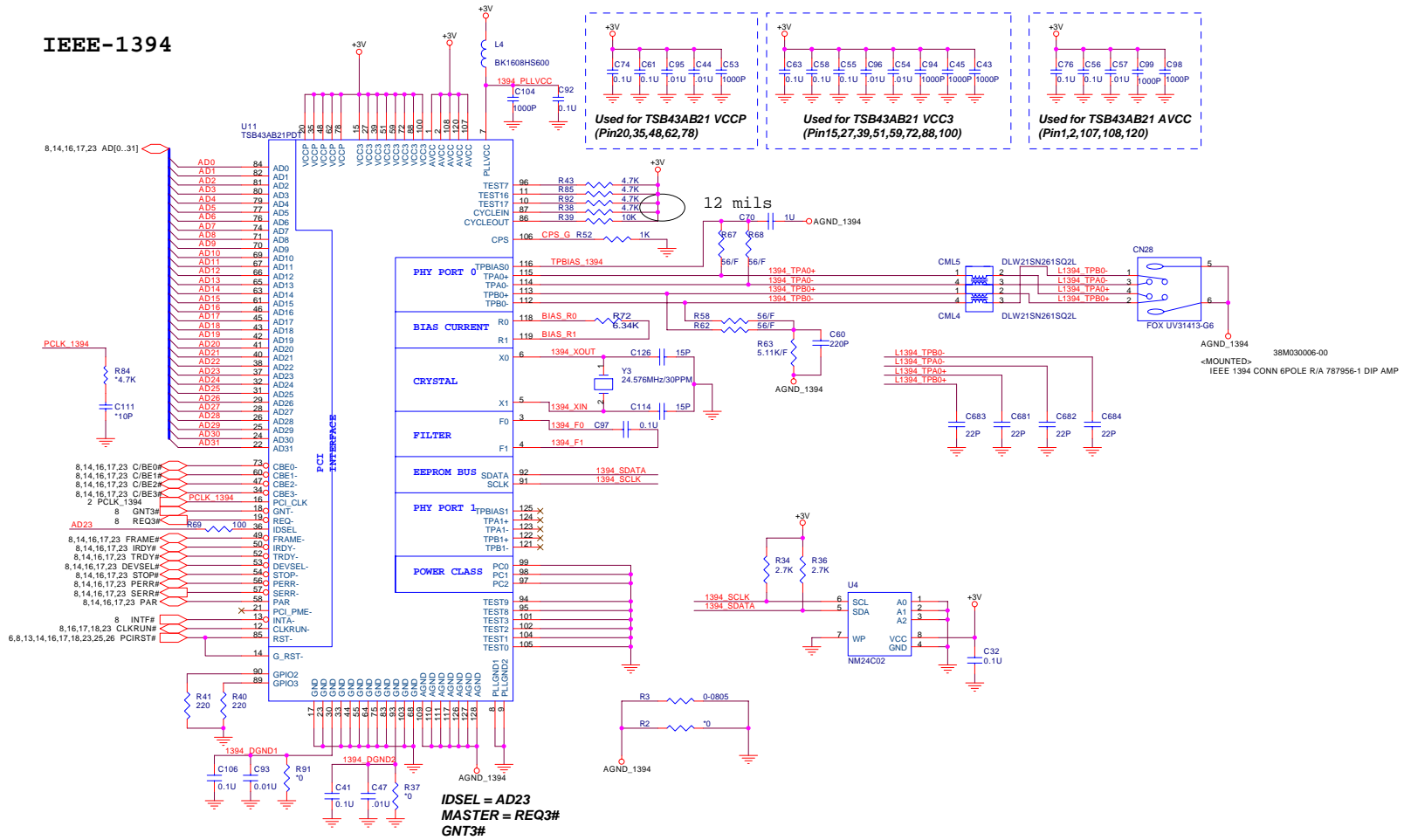








IEEE-1394



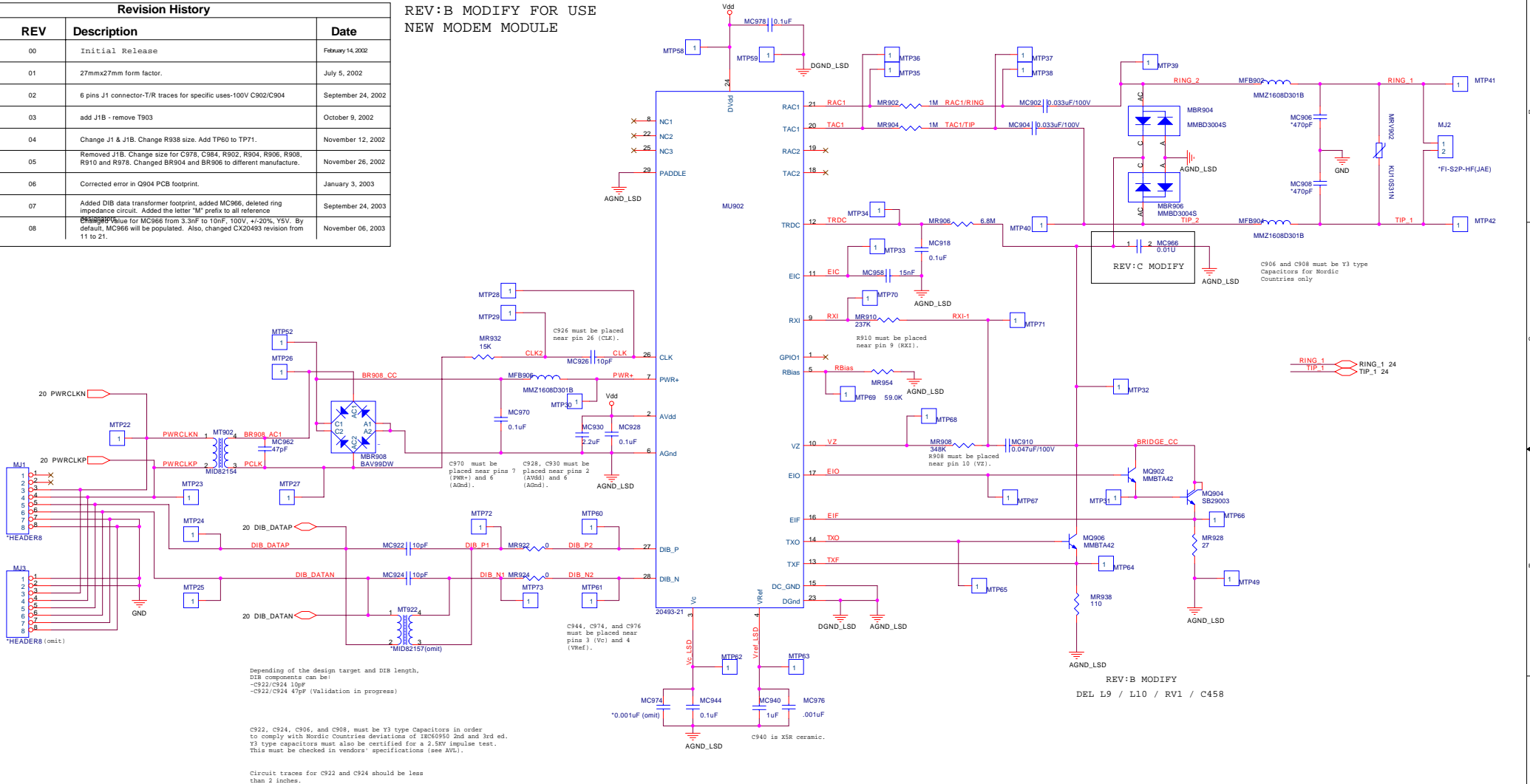







Revision History		
REV	Description	Date
00	Initial Release	February 14, 2002
01	27mmx27mm form factor.	July 5, 2002
02	6 pins J1 connector-T/R traces for specific uses-100V C902/C904	September 24, 2002
03	add J1B - remove T903	October 9, 2002
04	Change J1 & J1B. Change R938 size. Add TP60 to TP71.	November 12, 2002
05	Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture.	November 26, 2002
06	Corrected error in Q904 PCB footprint.	January 3, 2003
07	Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference	September 24, 2003
08	Changed value for MC966 from 3.3nF to 10nF, 100V, +/-20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21.	November 06, 2003

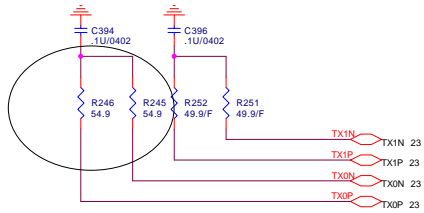
REV:B MODIFY FOR USE  
NEW MODEM MODULE



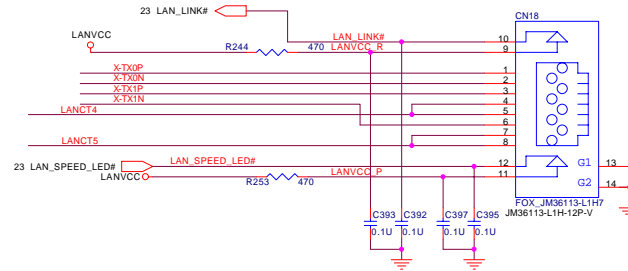
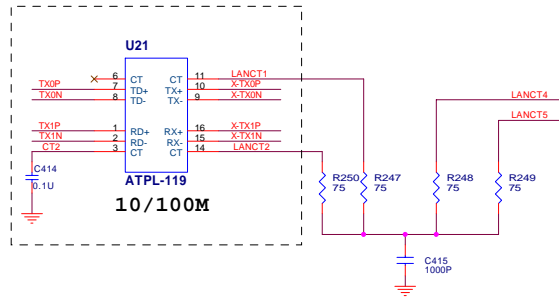
			PROJECT : KT2	
Quanta Computer Inc.				
Size	Document Number			REV
Custom	22--MODEM (DAA)			1A
Date:	Friday, May 14, 2004	Sheet	22 of 33	



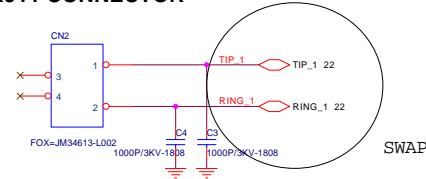
Close to Chip



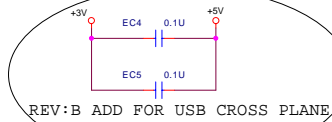
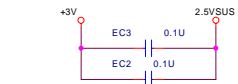
Rev: C from 49.9 change to 54.9 ohm



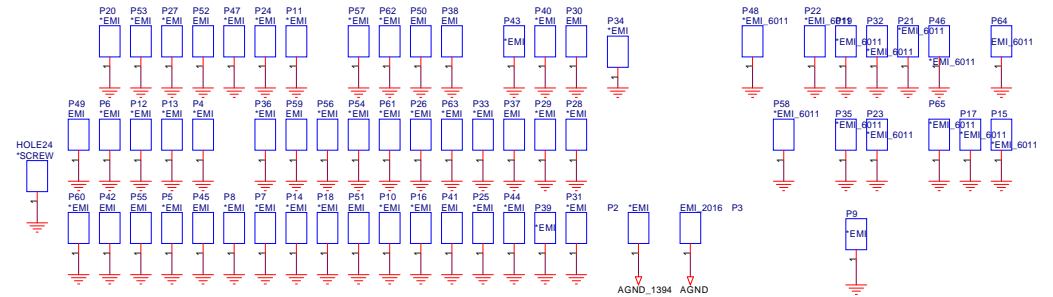
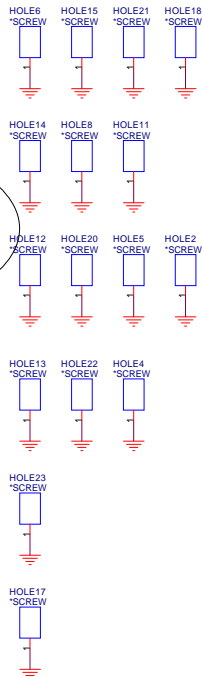
## RJ11 CONNECTOR



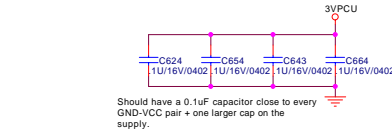
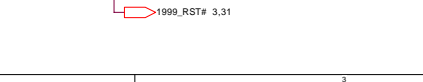
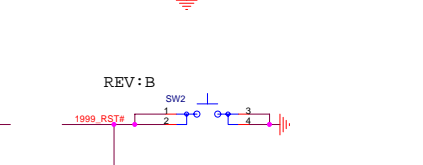
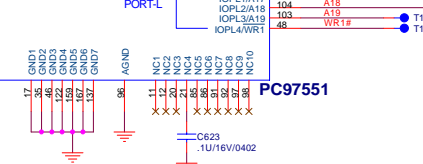
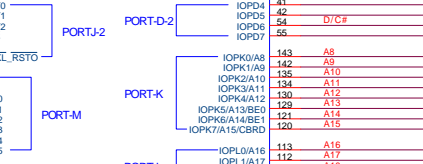
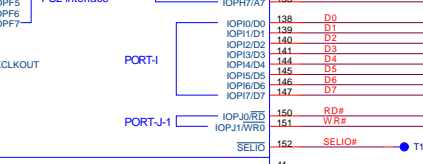
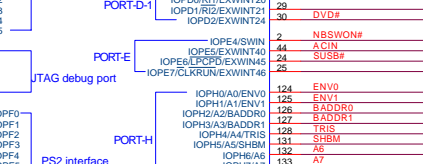
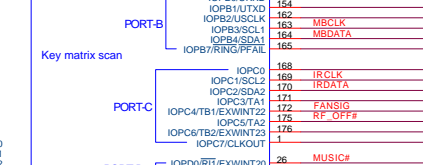
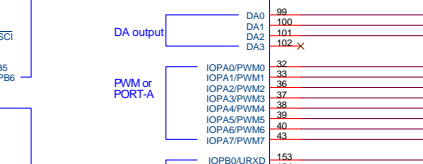
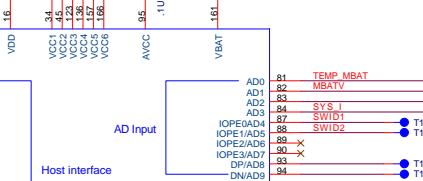
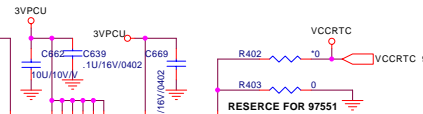
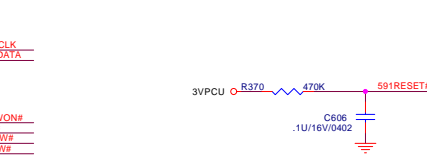
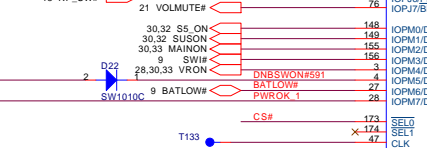
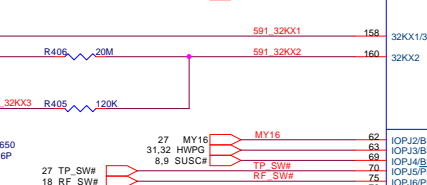
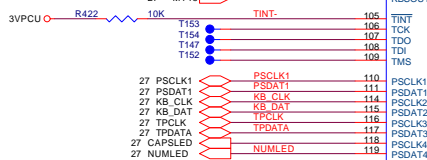
SWAP



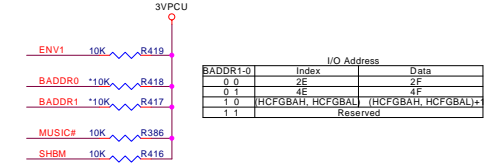
REV:B ADD FOR USB CROSS PLANE



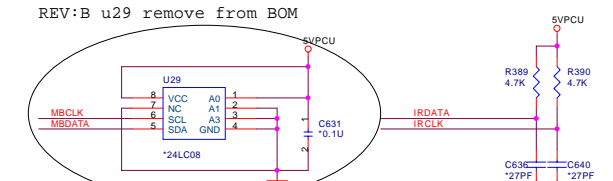
LDRQ#(pin 8) internal is no use



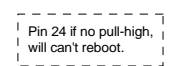
Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.



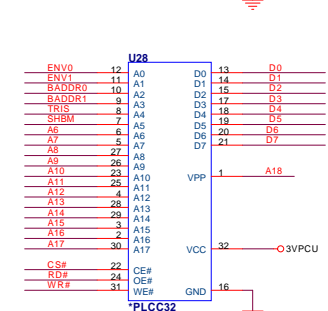
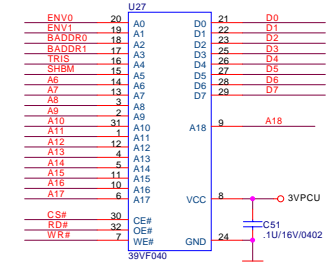
SHBM=1: Enable shared memory with host BIO.



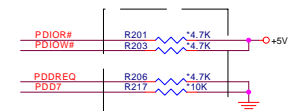
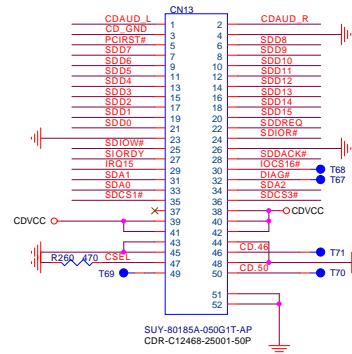
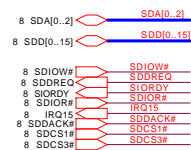
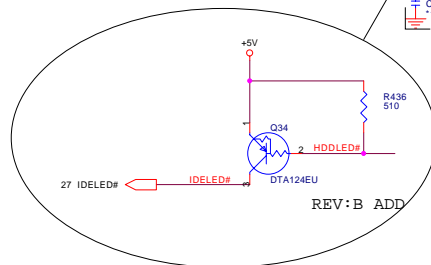
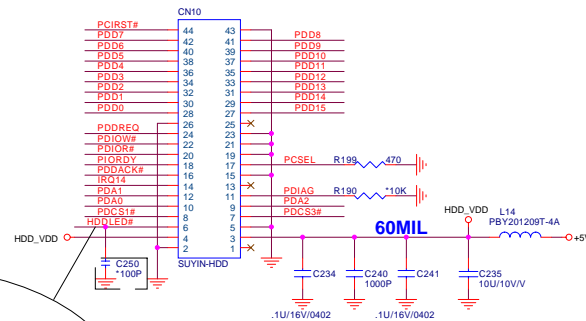
change net name from seroled to padled



## BIOS ROM

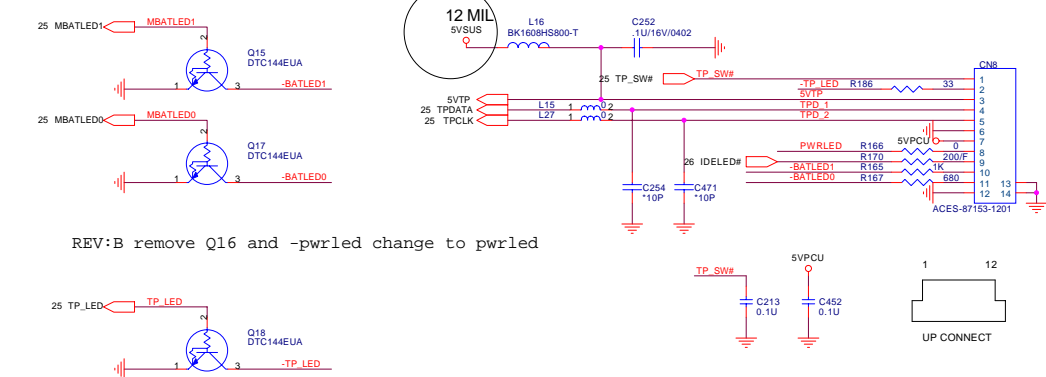


```
Pin 103 internal is
"A19", Can't use to
CPIO
```

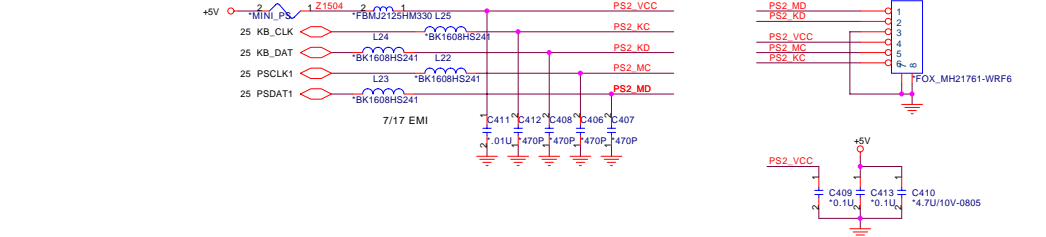
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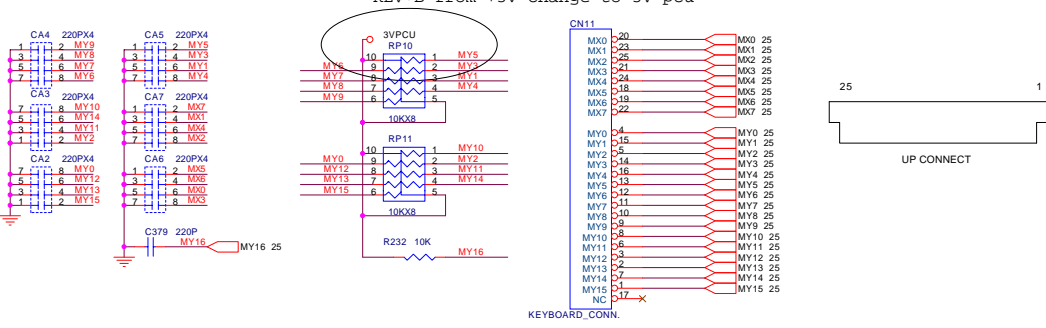
TOUCH PAD CONNECTOR



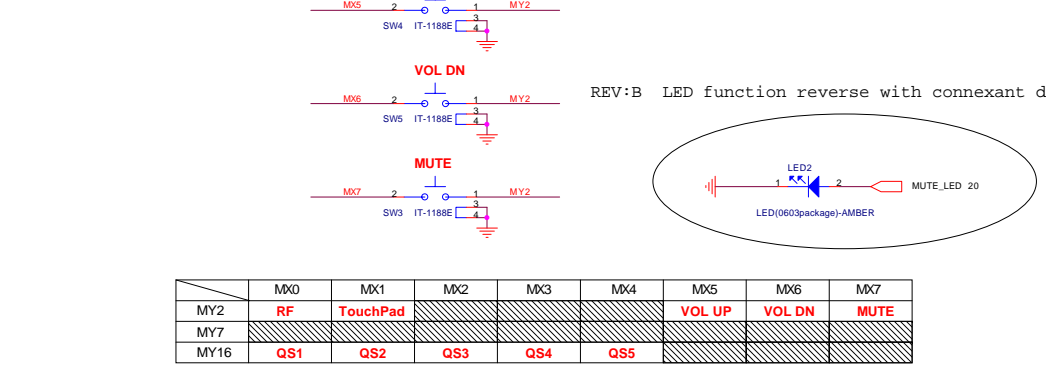
PS/2 PORT



KEYBOARD CONNECTOR

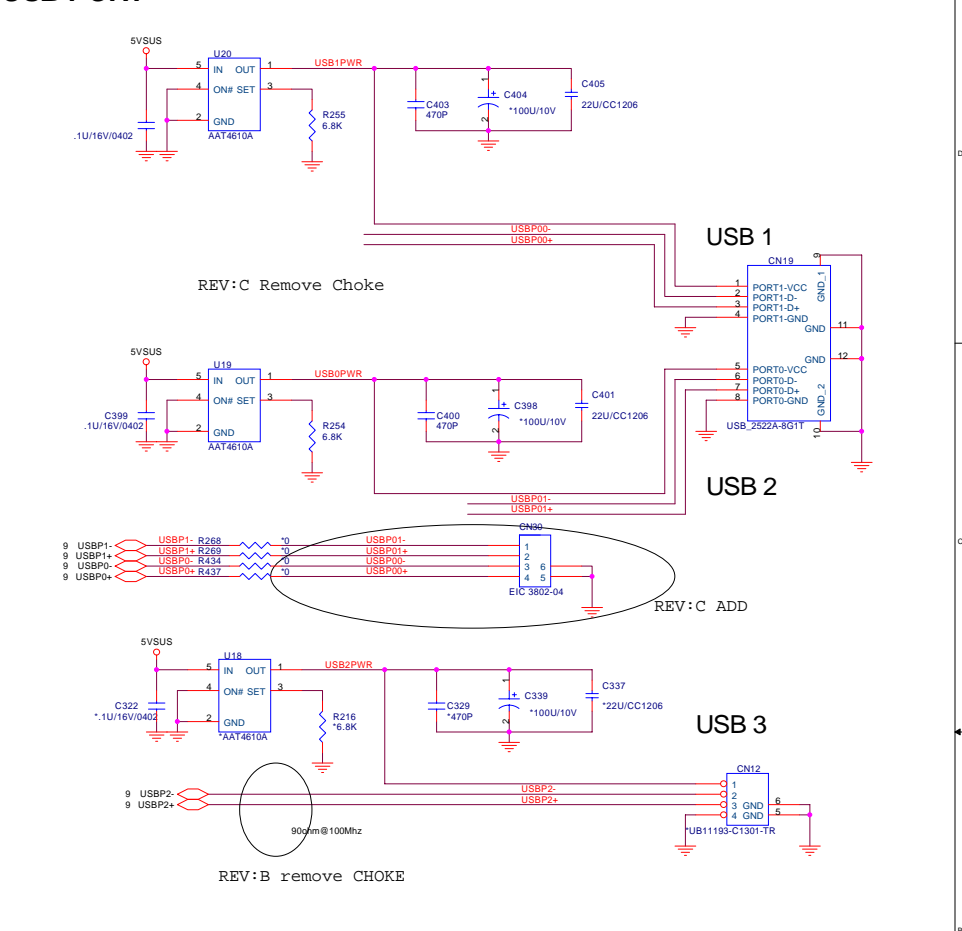


VOLUME CONTROL

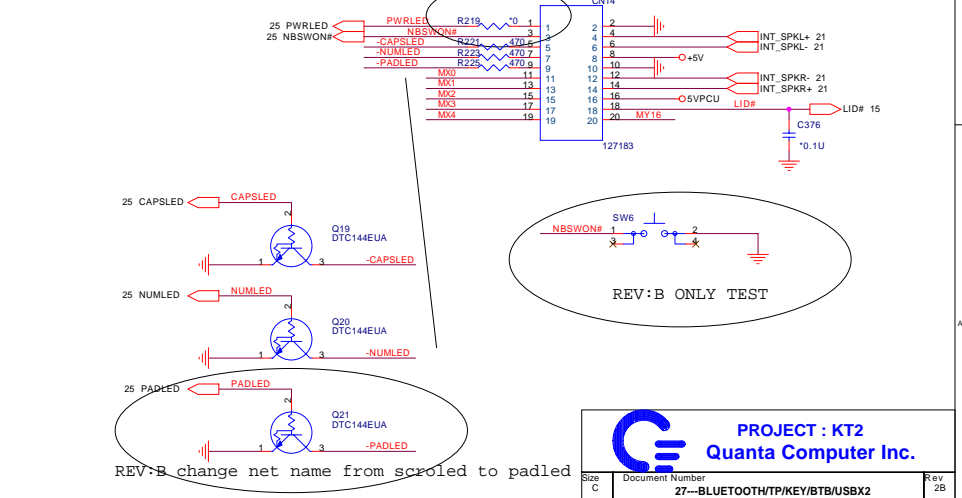


	MX0	MX1	MX2	MX3	MX4	MX5	MX6	MX7
MY2	RF	TouchPad					VOL UP	VOL DN
MY7								MUTE
MY16	QS1	QS2	QS3	QS4	QS5			

USB PORT



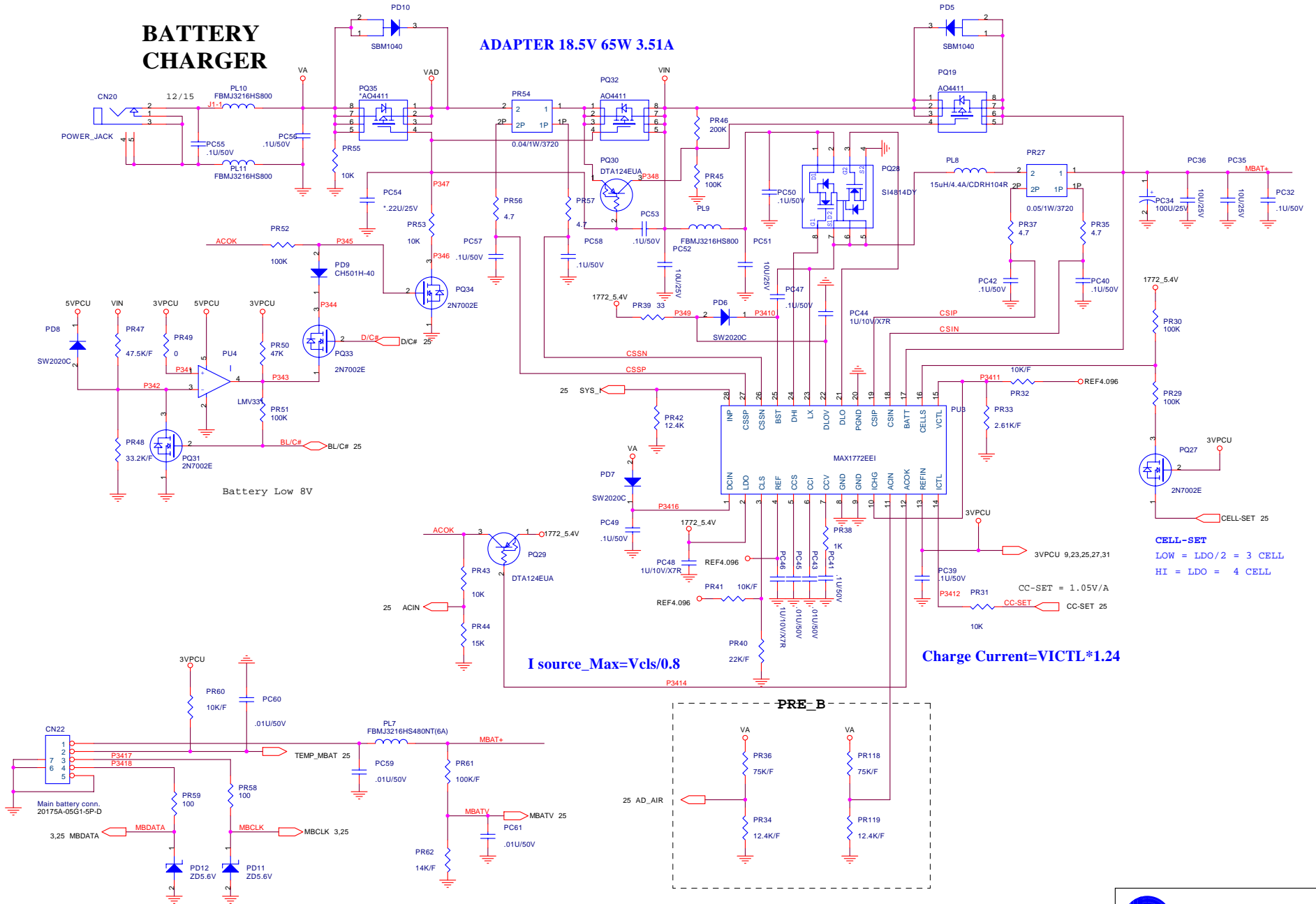
LED BOARD CON

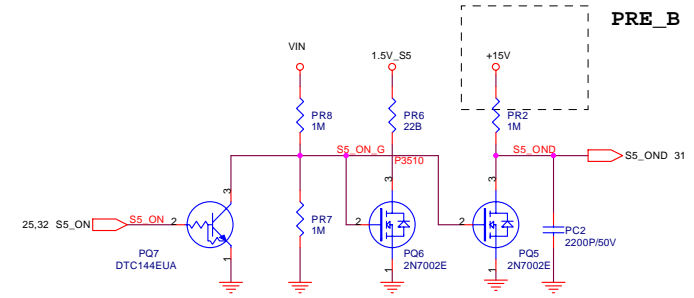
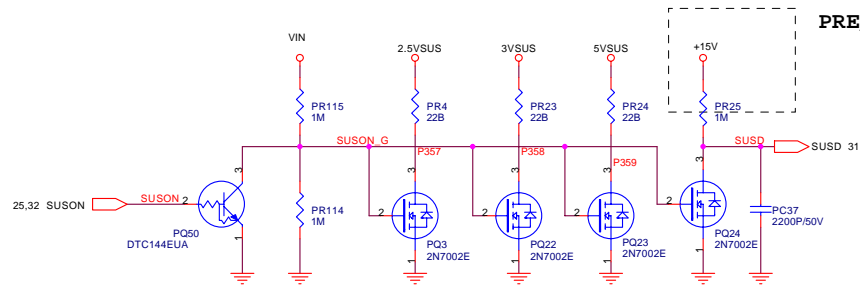
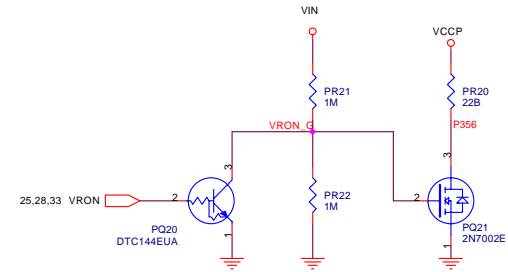
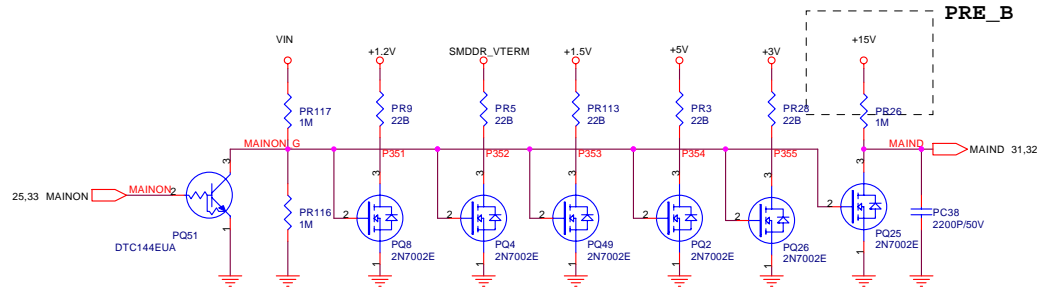


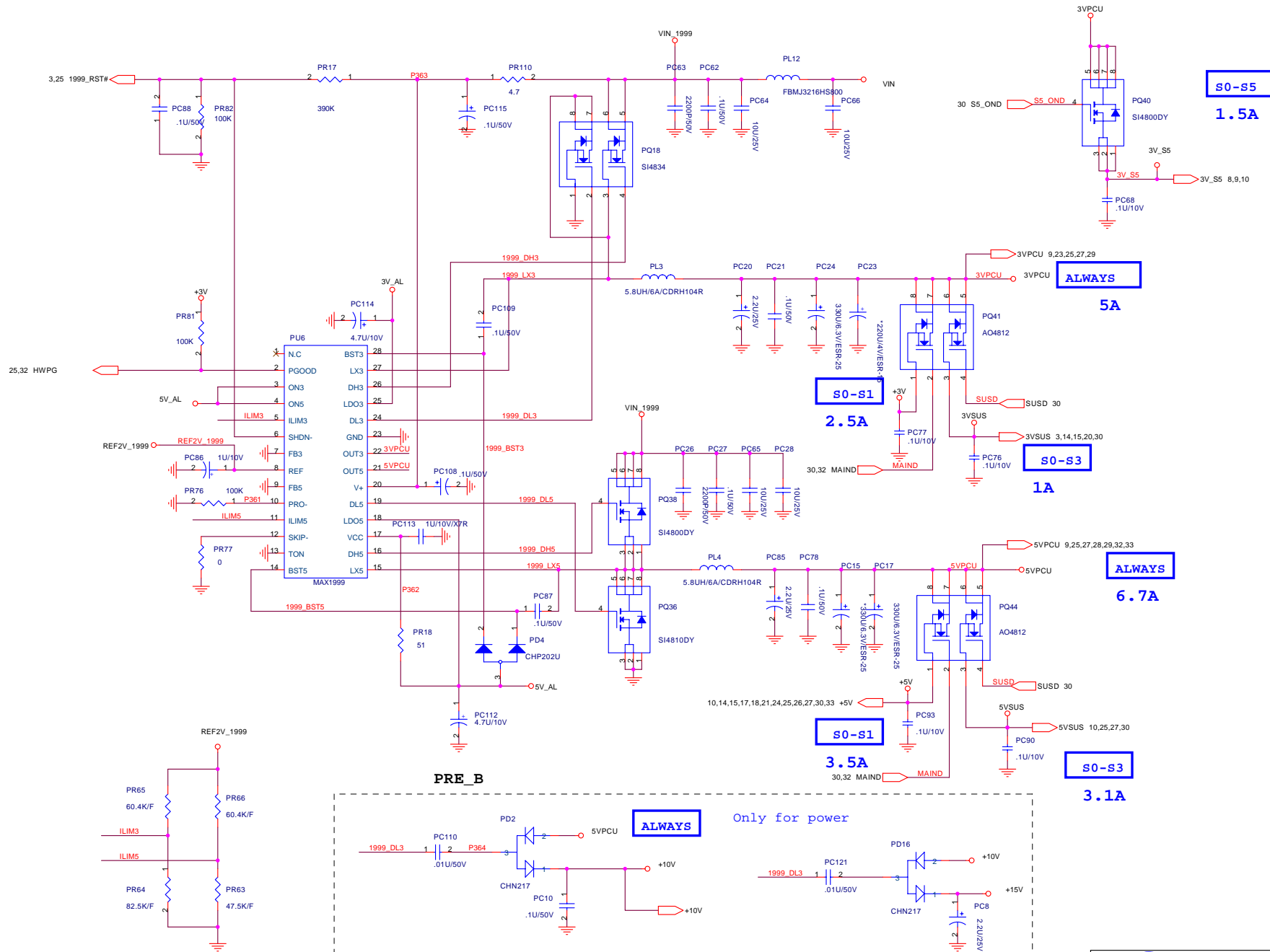


# BATTERY CHARGER

## ADAPTER 18.5V 65W 3.51A







PROJECT : KT2  
Quanta Computer Inc.

Size: Custom Document Number: 31--MAX1999(3V/5V)  
Date: Friday, May 14, 2004 Sheet: 31 of 33





