

Compal Confidential

Model Name :PILP1  
File Name : LA-6921P

# Compal Confidential

## LA-6921 Edge Schematics Document

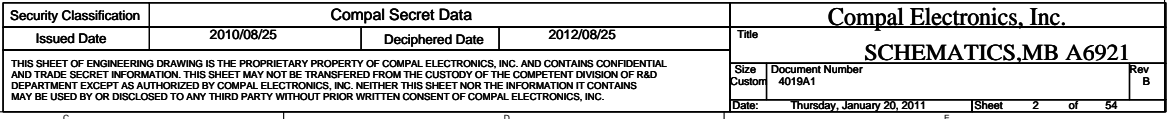
Intel Sandy Bridge Processor with DDRIII + Cougar Point PCH + Whistler 128bit with BACO

2010-11-30

REV:1.0

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## Voltage Rails

State	+B	+5VALW +3VALW	+1.5V	+5VS +3VS +1.8VS +1.5VS +1.0VS +VGF_X_CORE +CPU_CORE +0.75VS +1.5VS_DISVGA +1.0VS +NVVDD +3VS_DISVGA +1.8VS_DISVGA
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	
S3 (Suspend to RAM)	LOW	HIGH	HIGH	ON	ON	OFF	
S4 (Suspend to Disk)	LOW	LOW	HIGH	ON	OFF	OFF	
S5 (Soft OFF)	LOW	LOW	LOW	ON	OFF	OFF	

## BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	
4	
5	
6	
7	

## USB Port Table

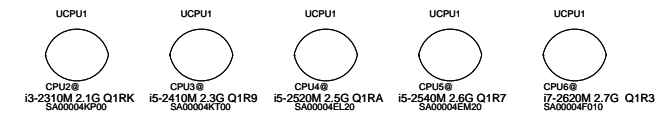
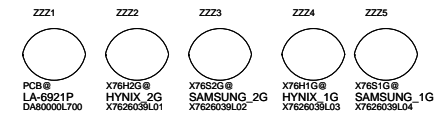
USB 2.0	USB 1.1	Port	3 External USB Port
EHCI1	UHCI0	0	USB_COMBO
		1	USB_PORT
	UHCI1	2	NO USE
		3	NO USE
	UHCI2	4	WWAN
		5	CAMERA
EHCI2	UHCI3	6	HM65 not support
		7	HM65 not support
	UHCI4	8	WLAN
		9	USB_CHARGER
	UHCI5	10	NO USE
		11	NO USE
	UHCI6	12	FINGER-PRINTER
		13	BLUETOOTH

## BOM Structure Table

Structure	Function
CPU1@	CPU type
PX@	PX4.0 SKU
UMA@	UMA SKU
3G@	3G Sim card
X76H1G@	VRAM
PCB@	M/B

Vcc	3.3V +/- 5%				
R613	100K +/- 5%				
Board ID	R682	V <sub>AD_BID</sub> min	V <sub>AD_BID</sub> typ	V <sub>AD_BID</sub> max	
0	0K +/- 5%	0 V	0 V	0 V	SDV1
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V	SDV2
2	18K +/- 5%	0.436 V	0.503 V	0.538 V	FVT
3	33K +/- 5%	0.712 V	0.819 V	0.875 V	SIT
4	56K +/- 5%	1.036 V	1.185 V	1.264 V	SVT

PCH_GPIO69	PCH_GPIO70	PCH_GPIO71	Function
0	0	0	UMA
1	1	0	PX4.0



## EC SM Bus1 address

Device	Address
Smart Battery	0001 011X b

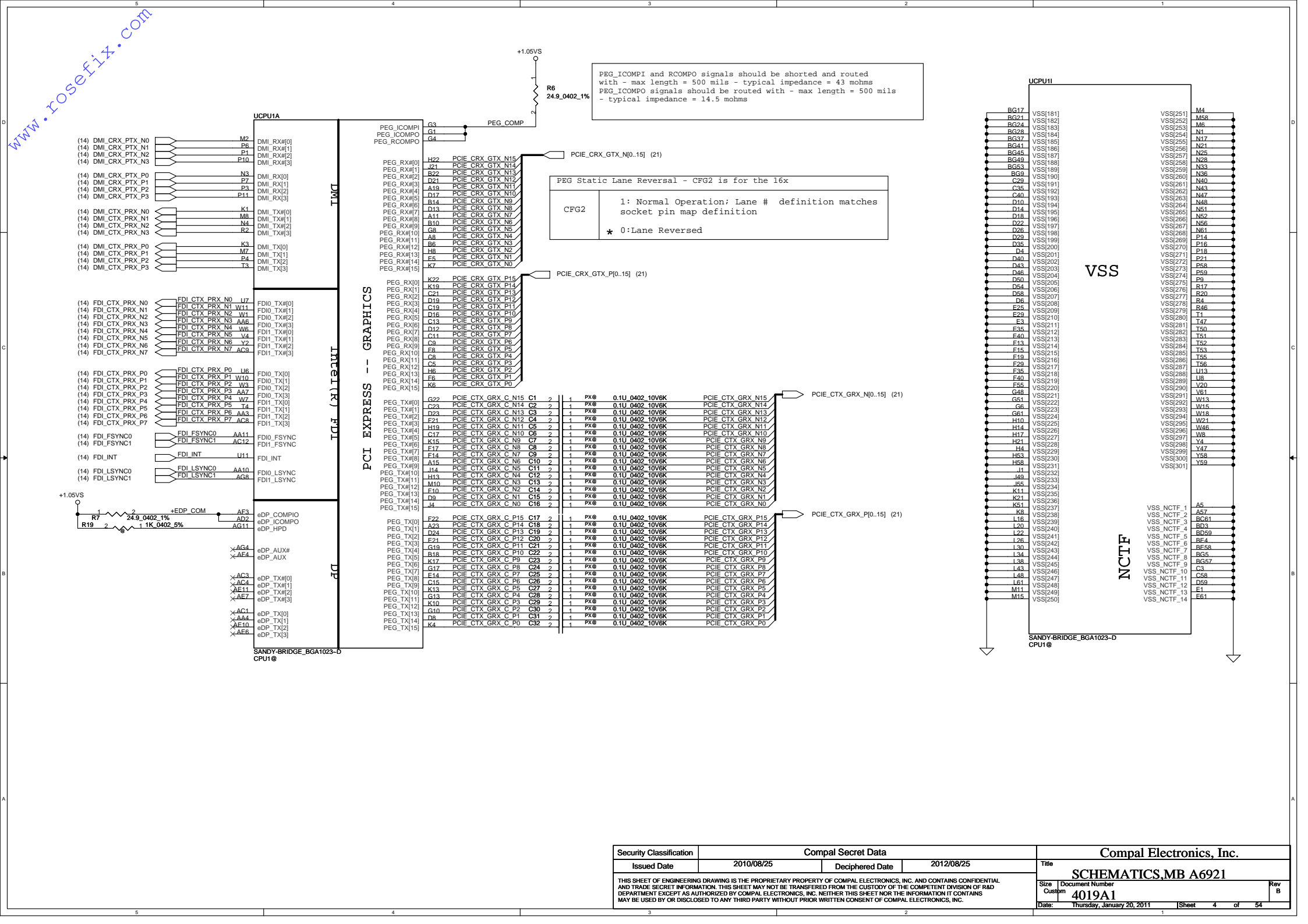
## EC SM Bus2 address

Device	Address
PCH	1001 0110 b
Thermal sensor(U30)	1001 101x b
GPU(U9)	1000_0010 b

## PCH SM Bus address

Device	Address
DDR DIMM0	1010 000xb
DDR DIMM2	1010 010xb
EEPROM(U32)	1010 100xb

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UCPU1A

DMI

FDI

DP

PCI EXPRESS -- GRAPHICS

Intel(R) FDI

UCPU1

VSS

NCTF

SANDY-BRIDGE\_BGA1023-D

CPUI@

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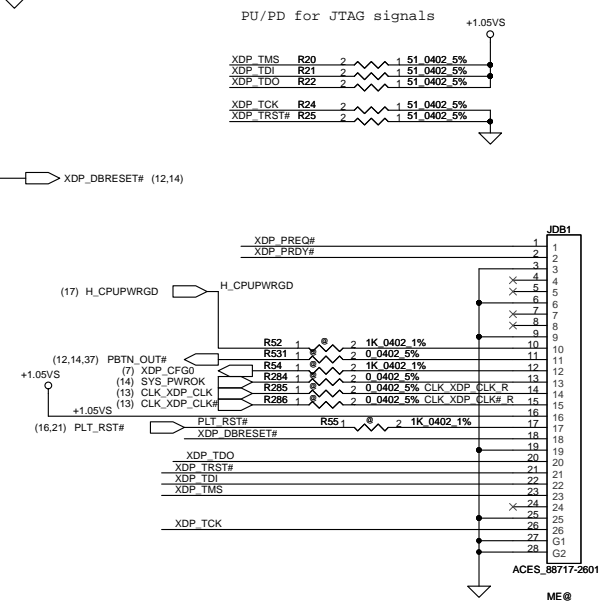
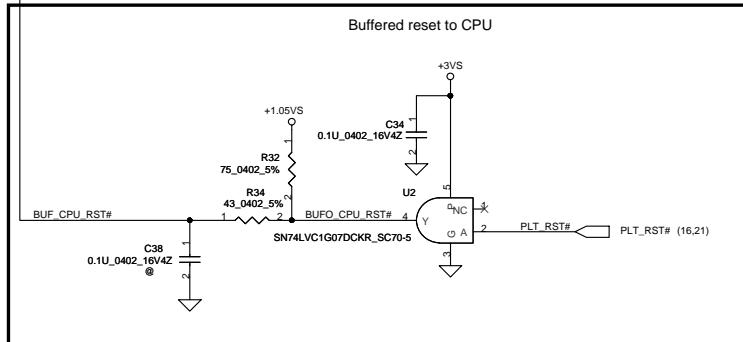
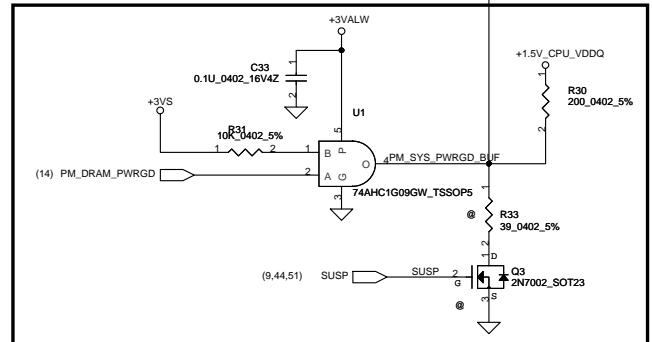
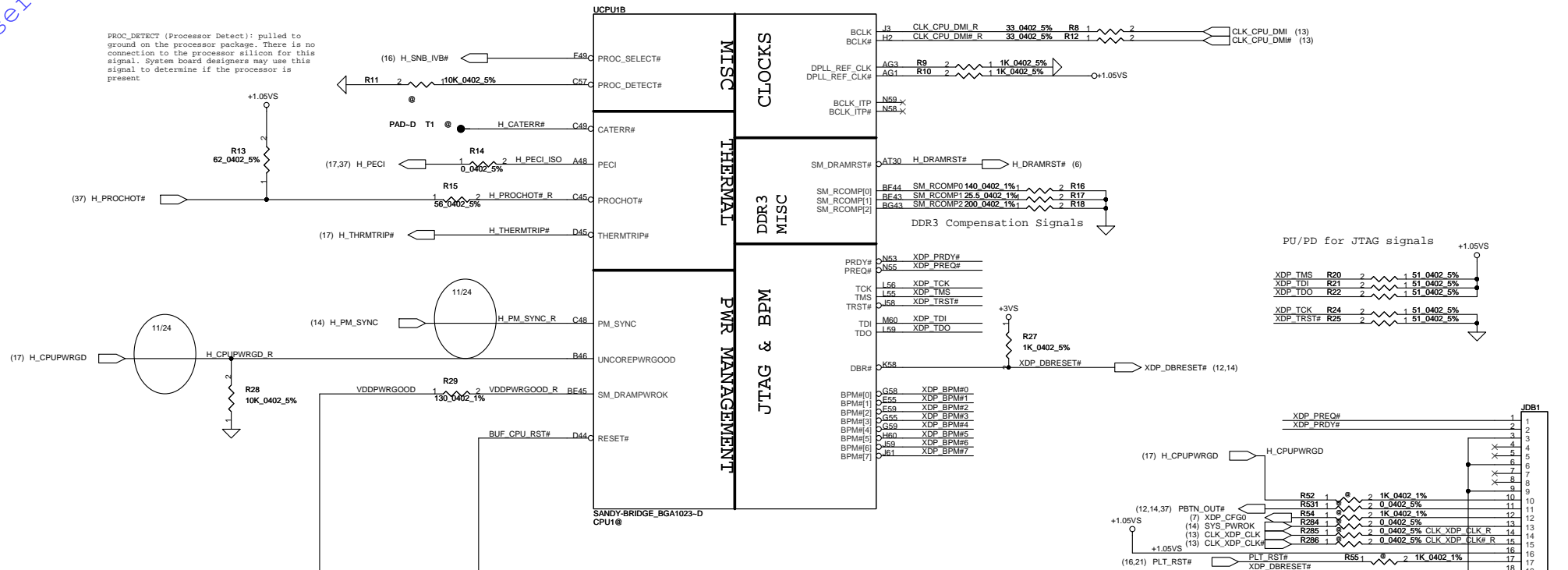
of

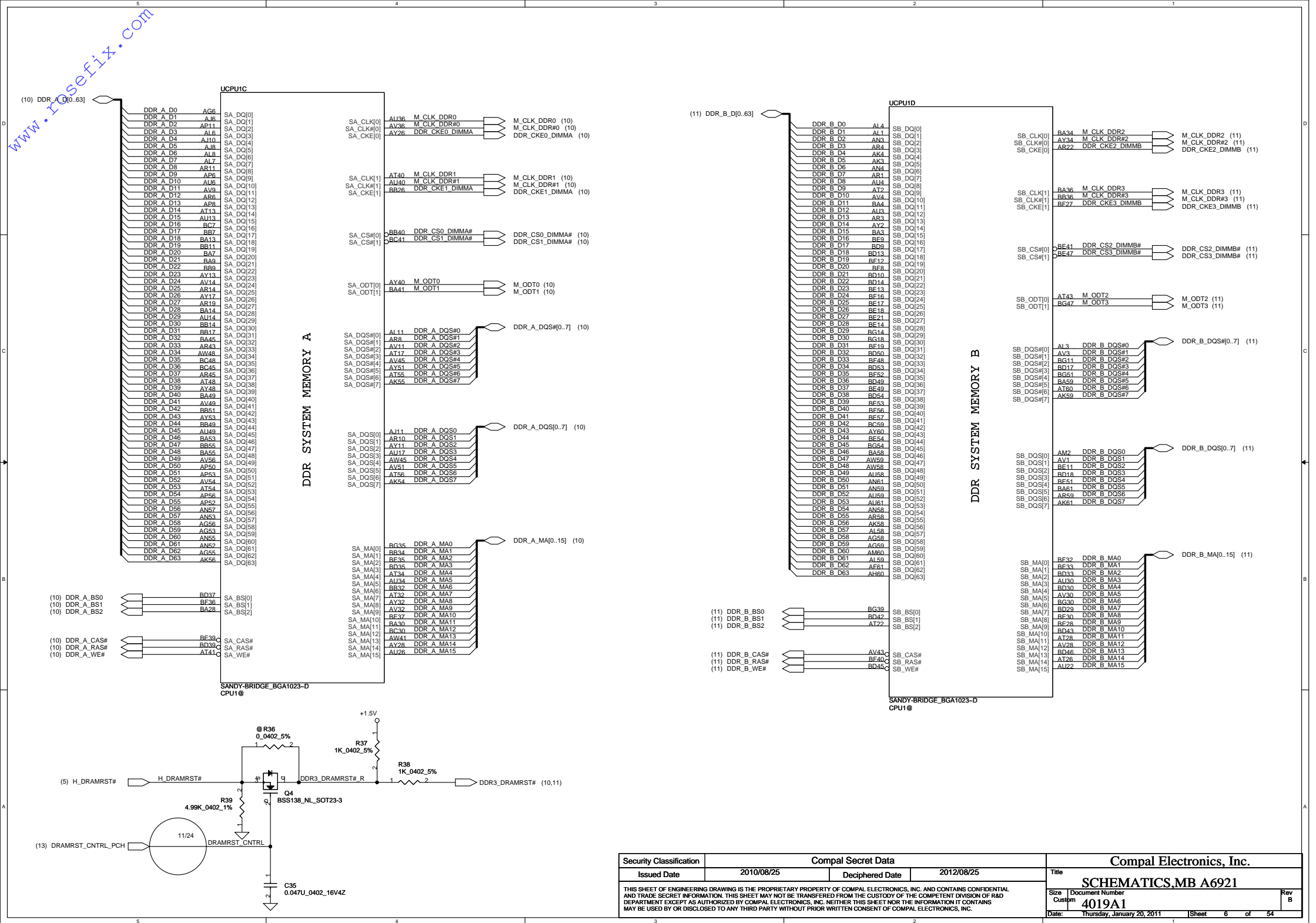
54

Rev

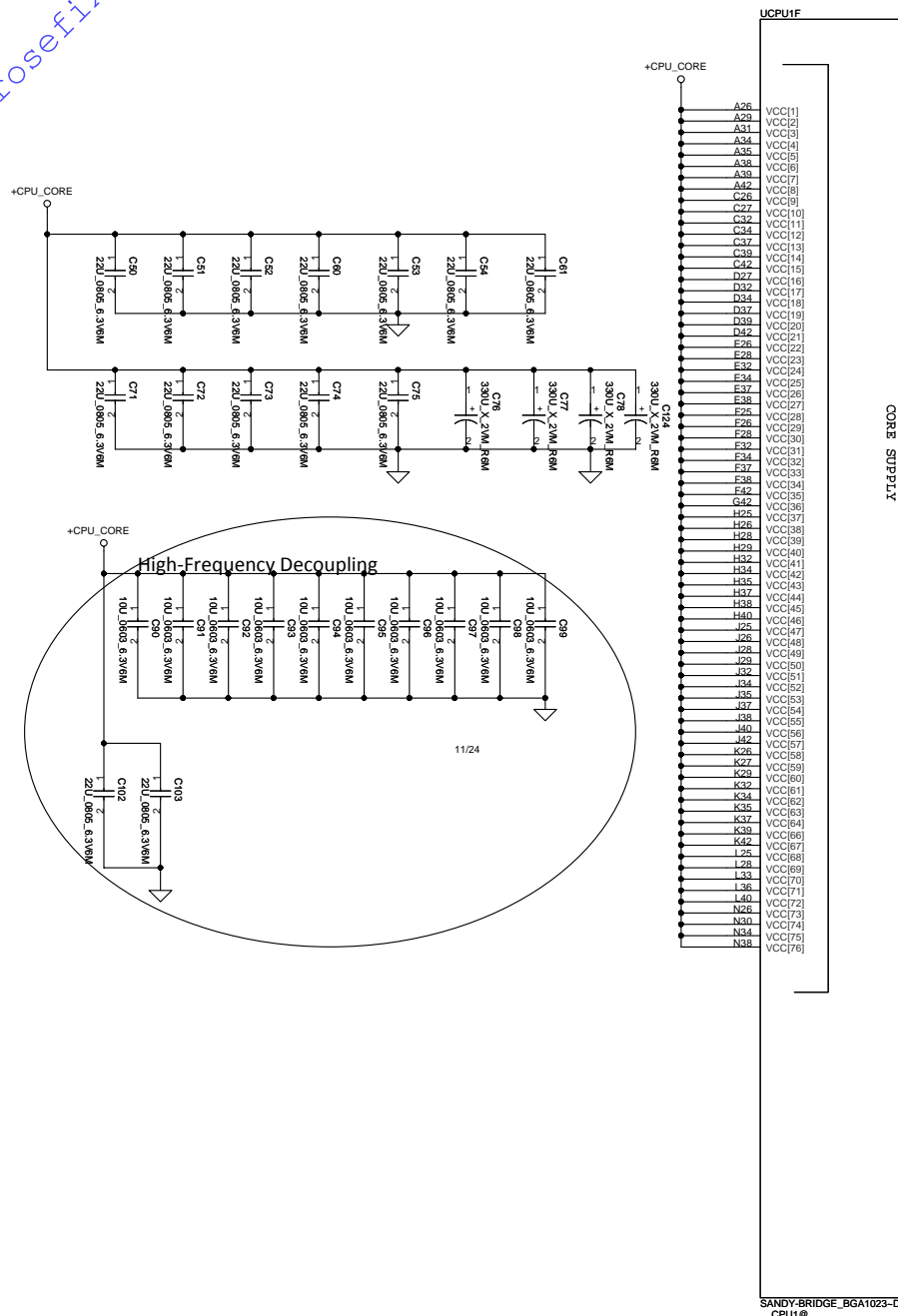
B

PROC\_DETECT (Processor Detect): pulled to ground on the processor package. There is no connection to the processor silicon for this signal. System board designers may use this signal to determine if the processor is present

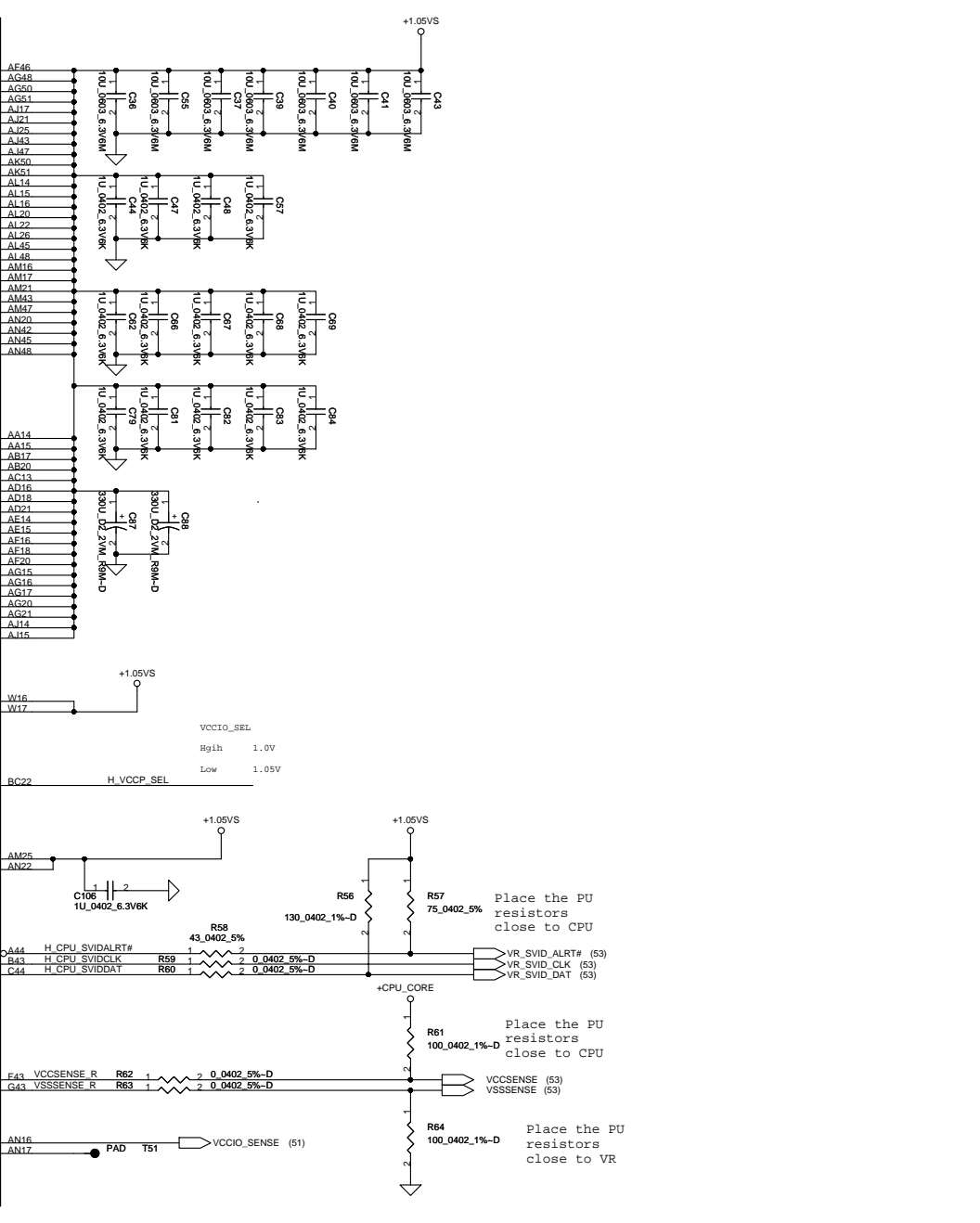
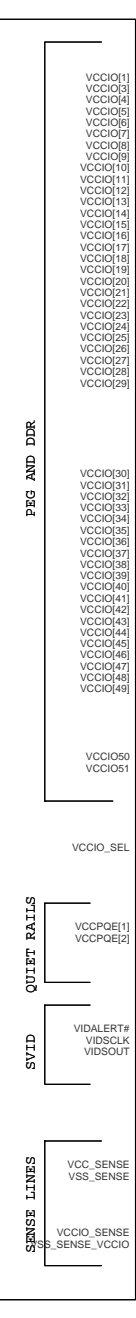




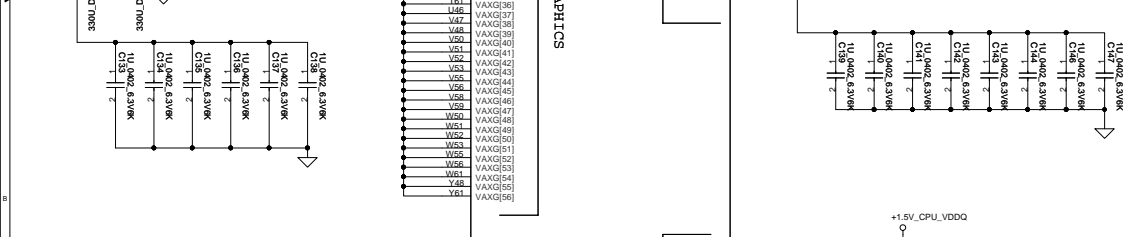
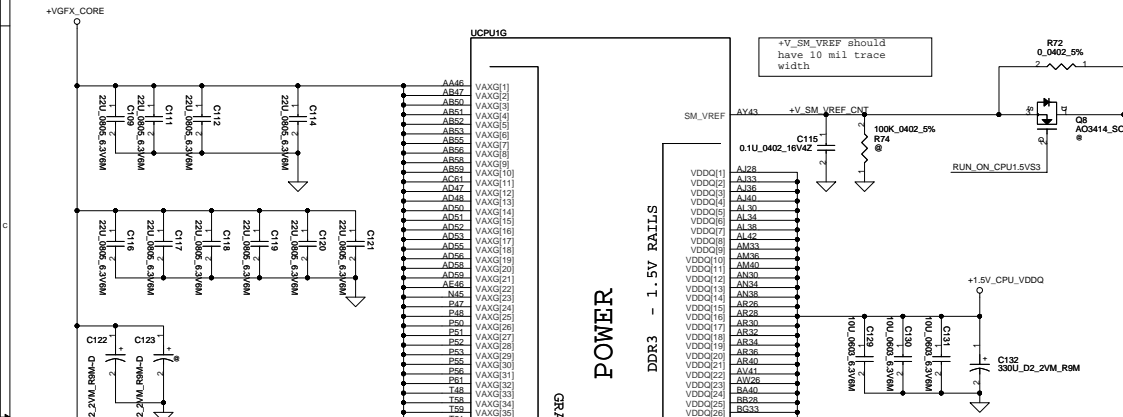
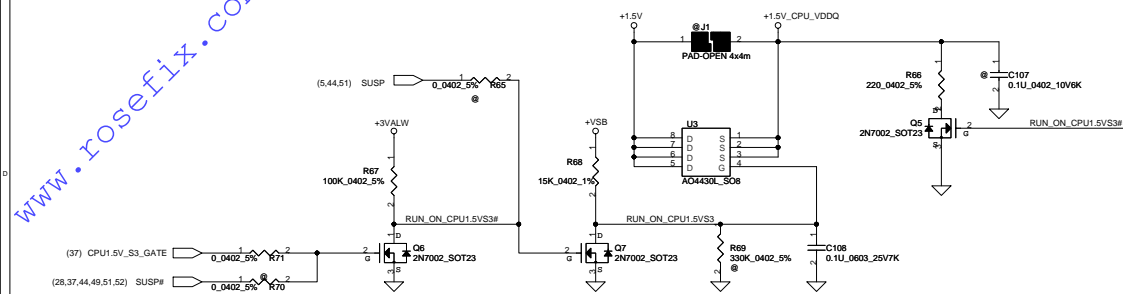


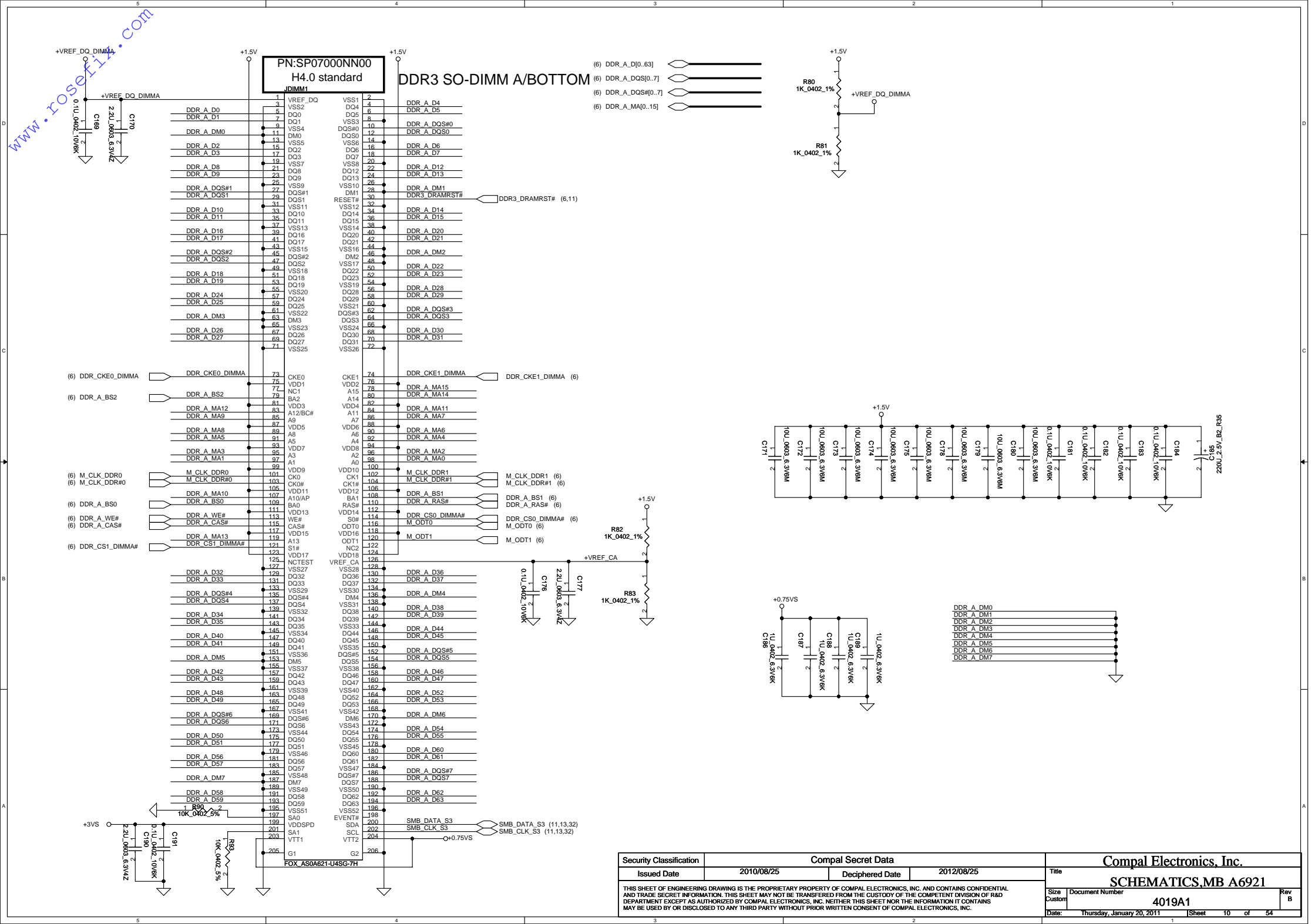


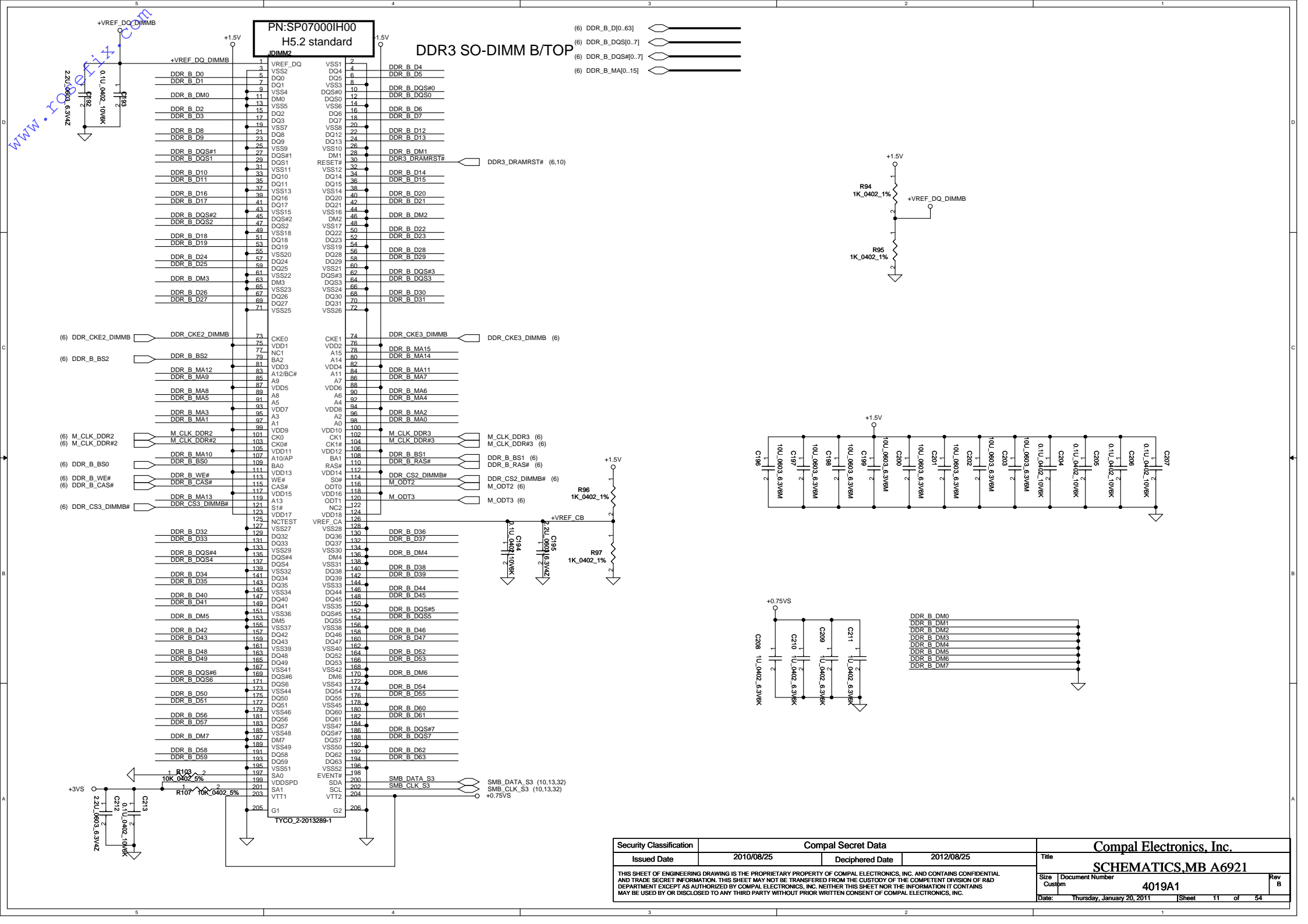
POWER

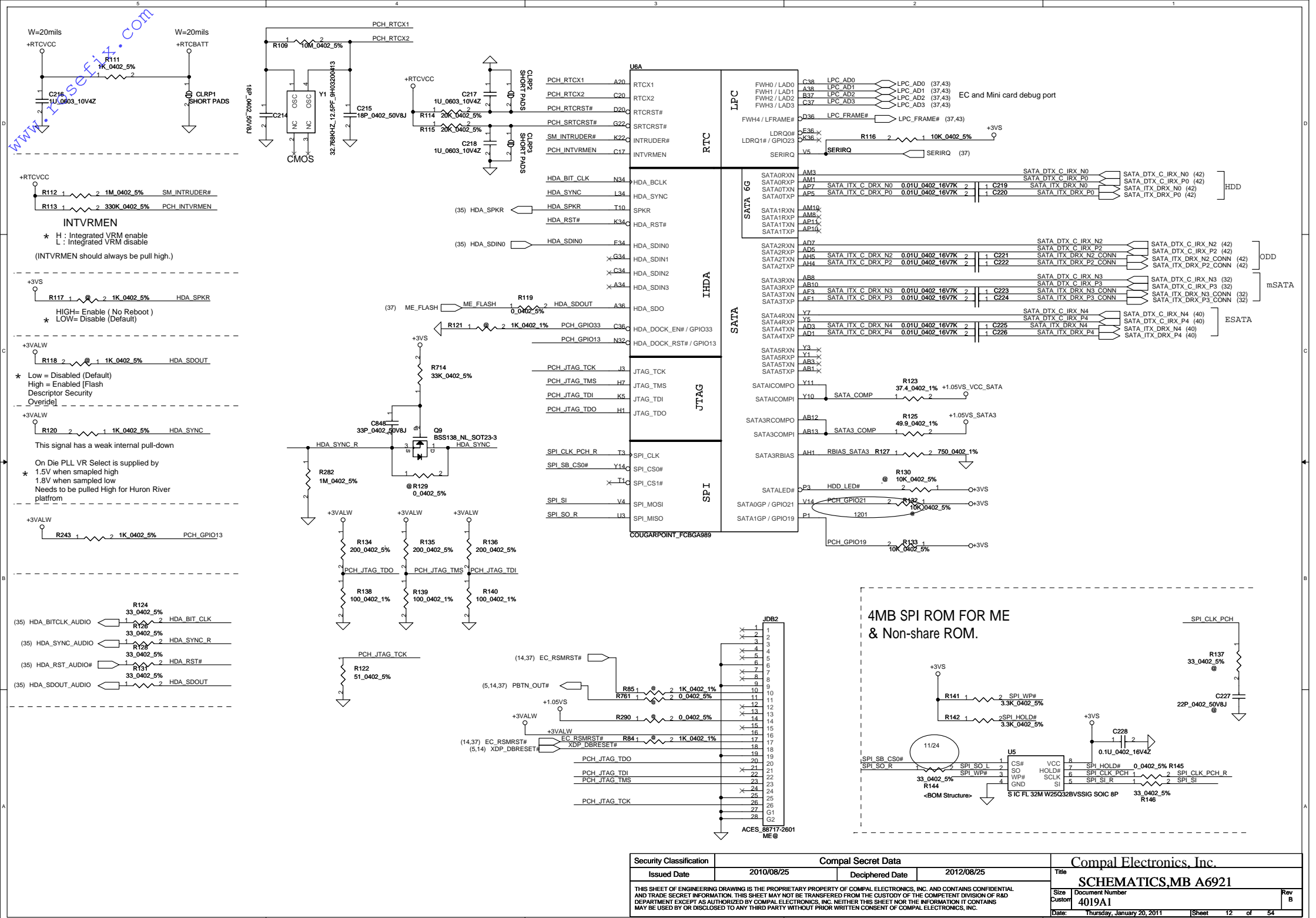


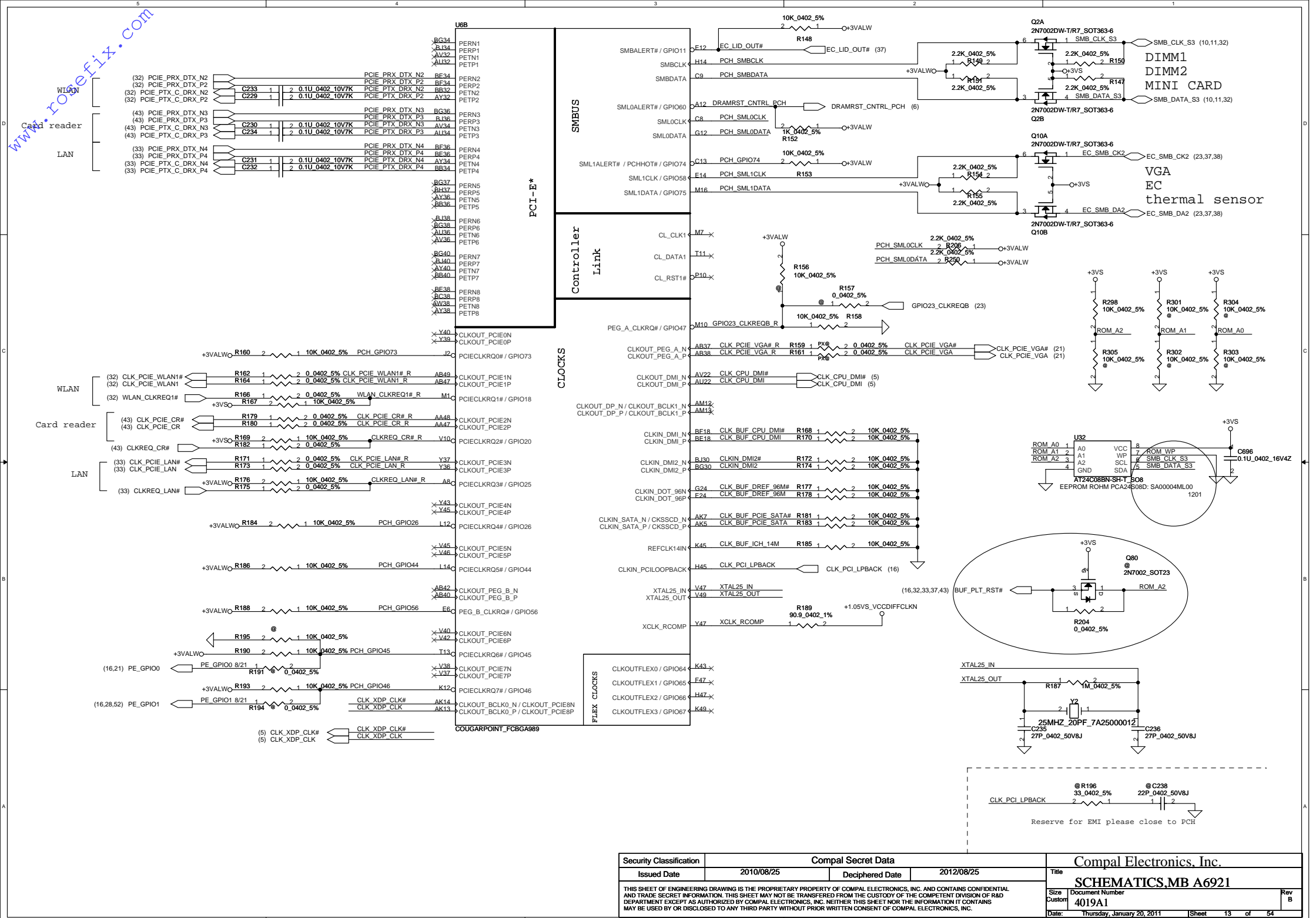


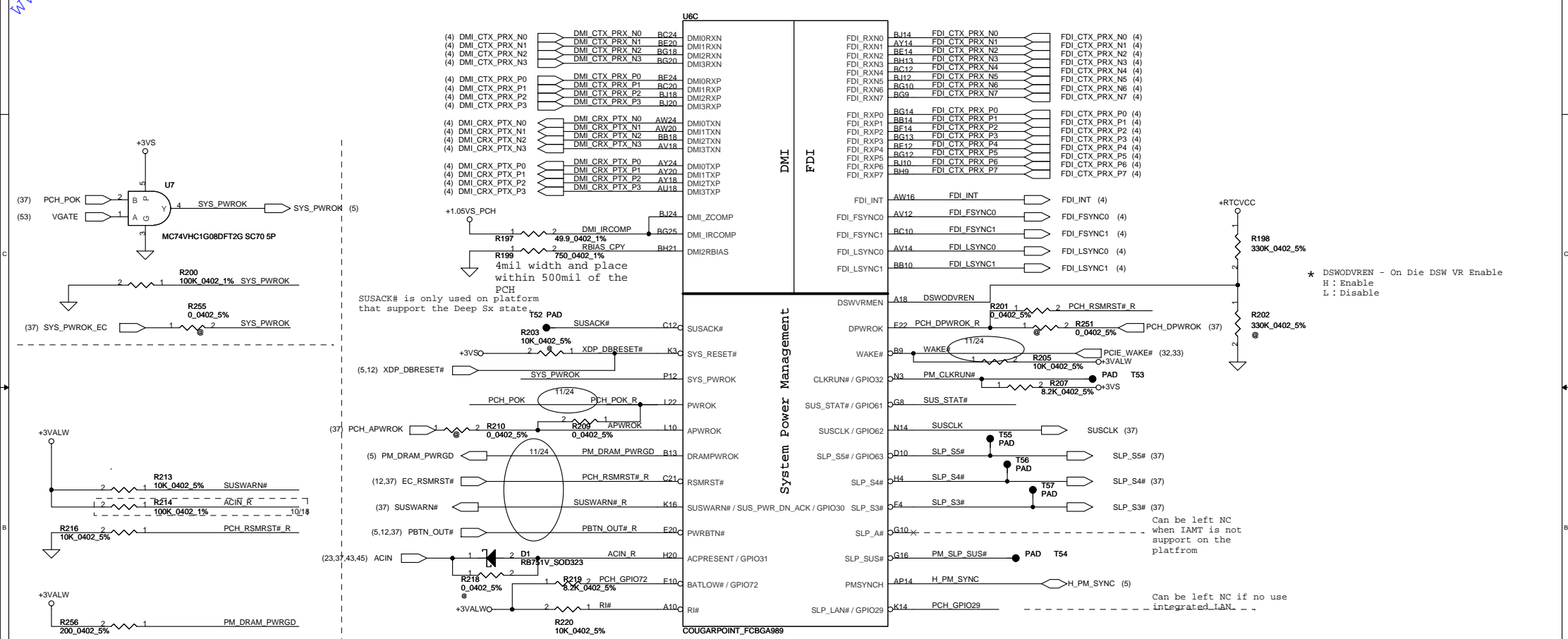




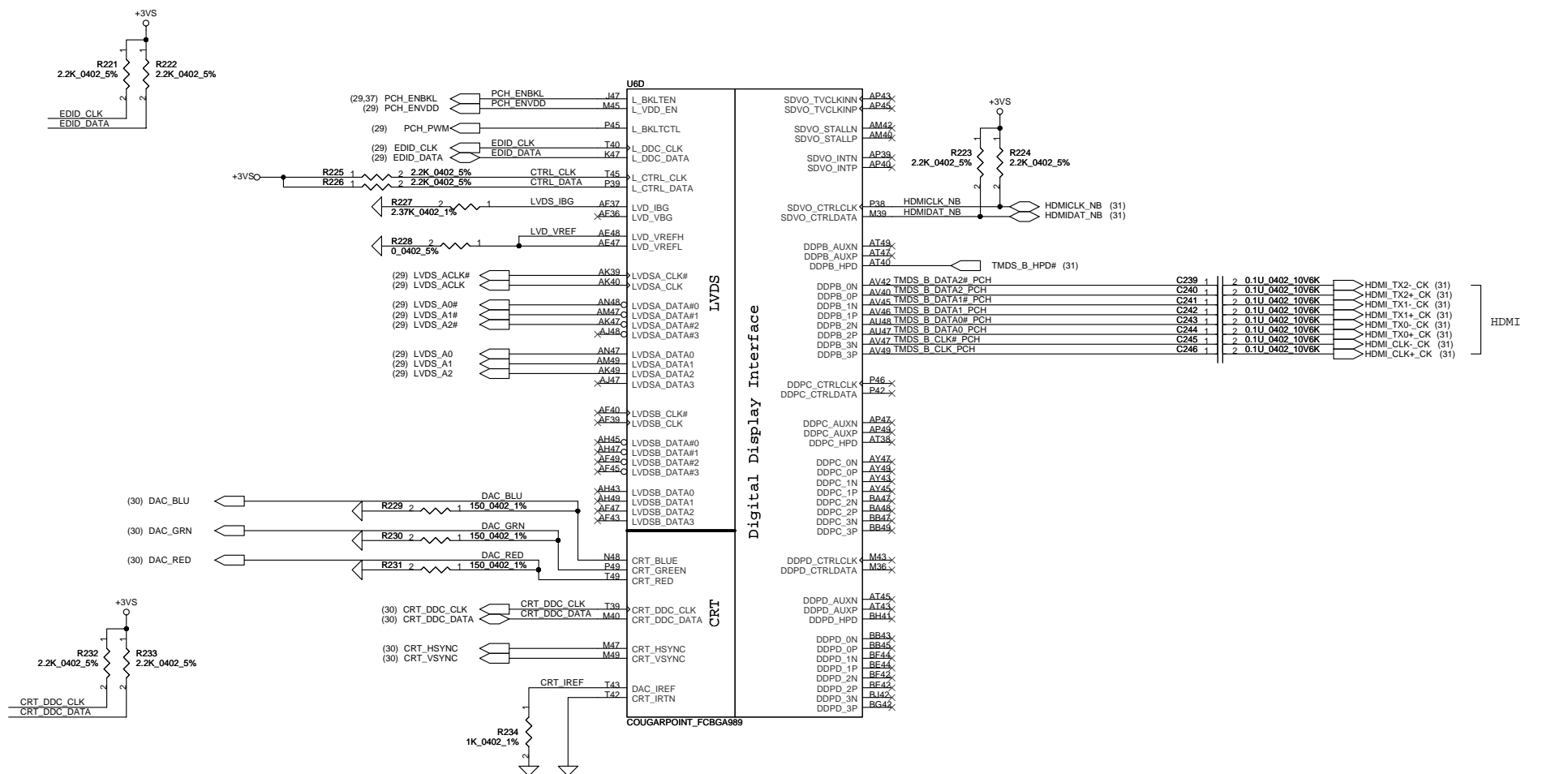






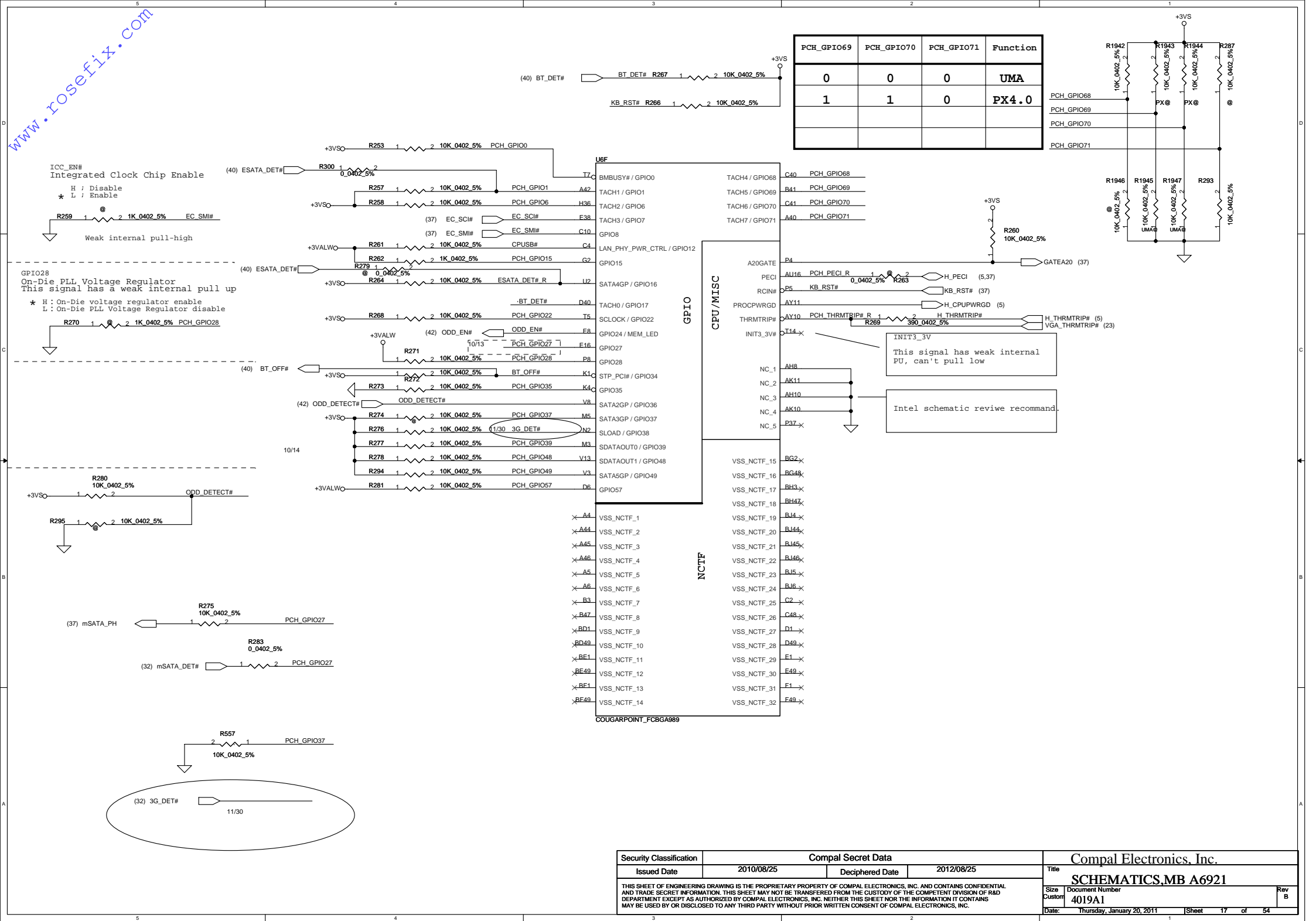




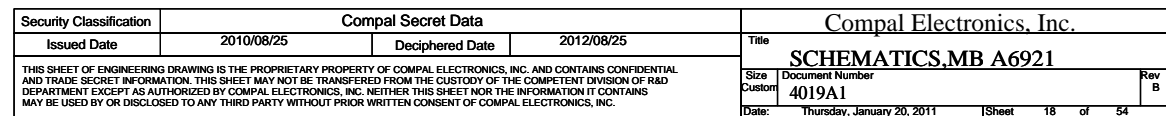


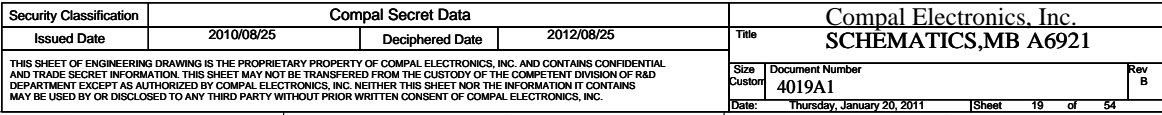


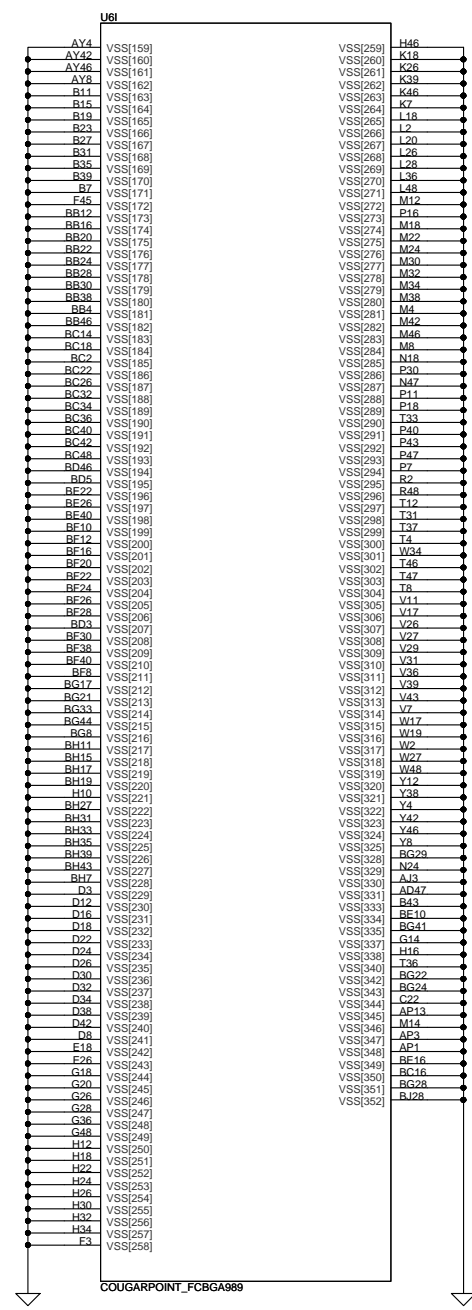
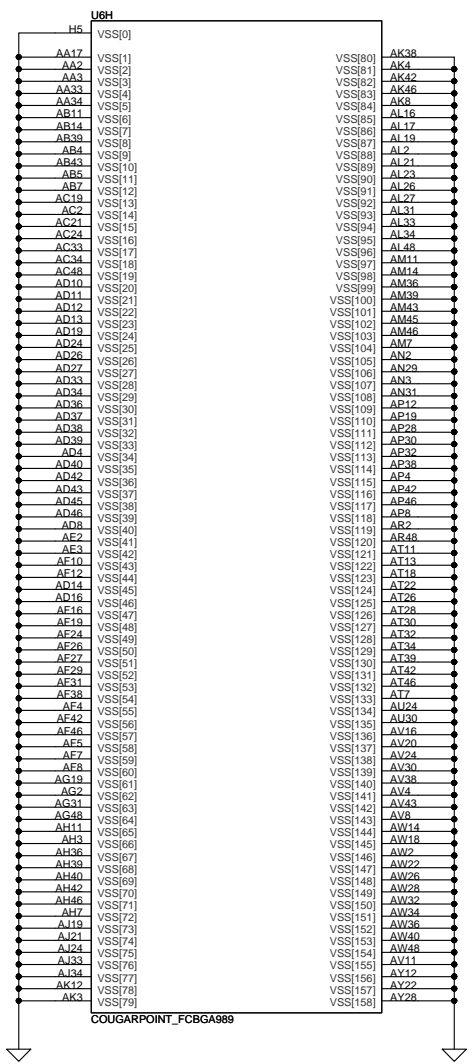




PCH_GPIO69	PCH_GPIO70	PCH_GPIO71	Function
0	0	0	UMA
1	1	0	PX4.0

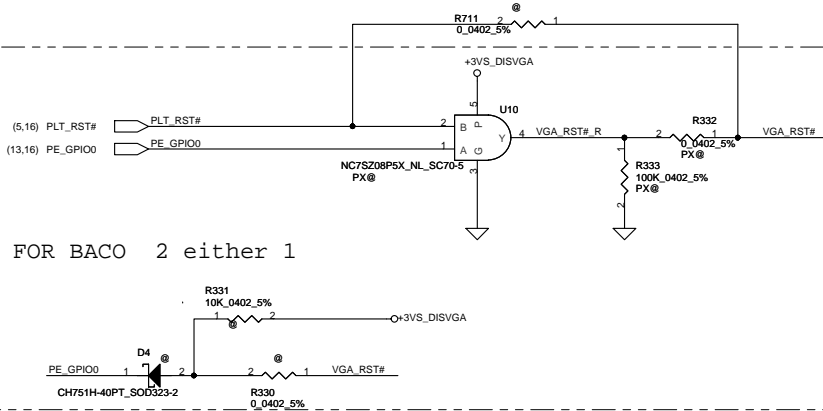
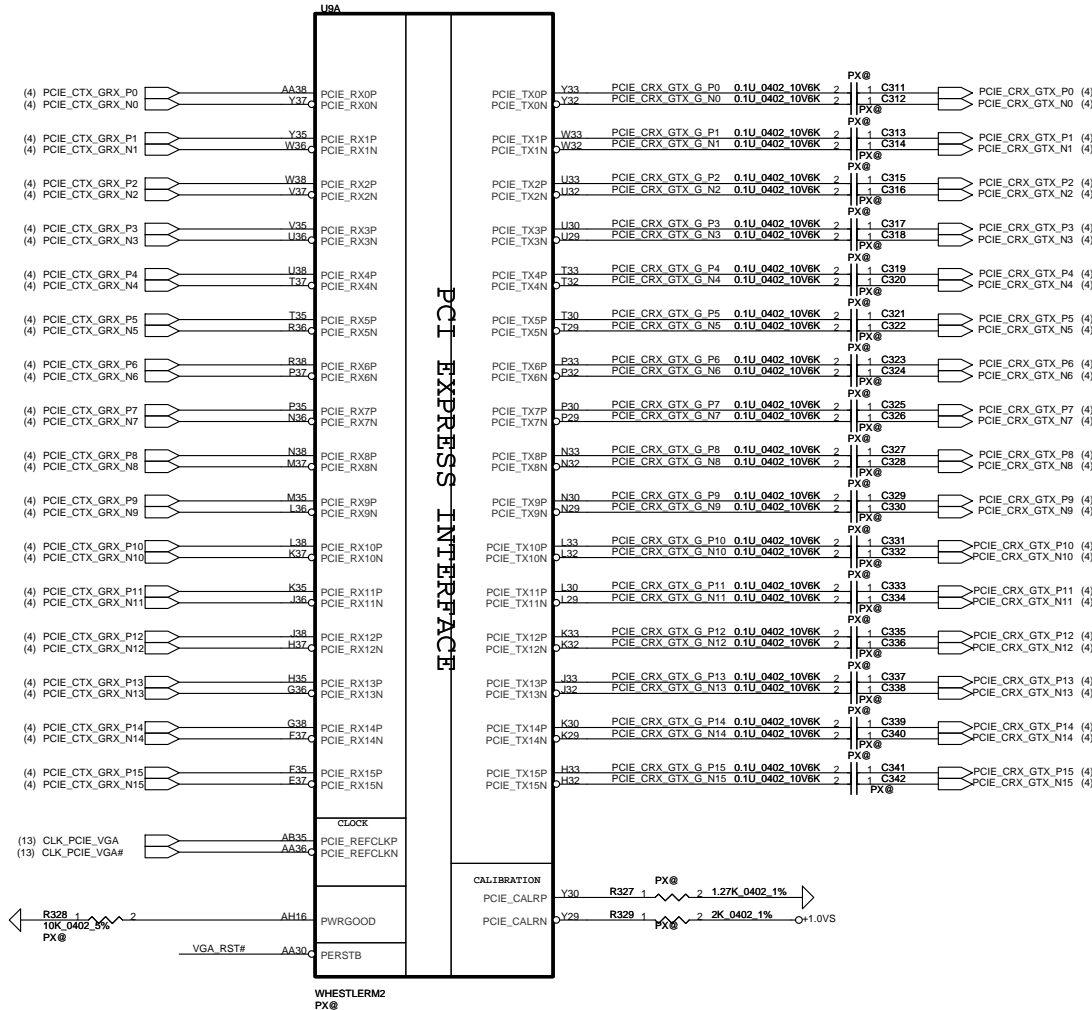
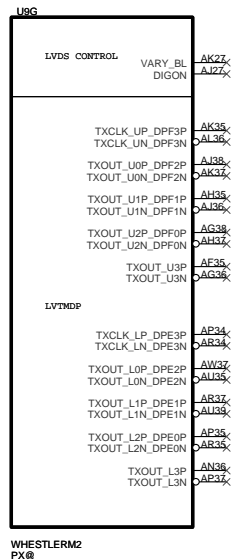


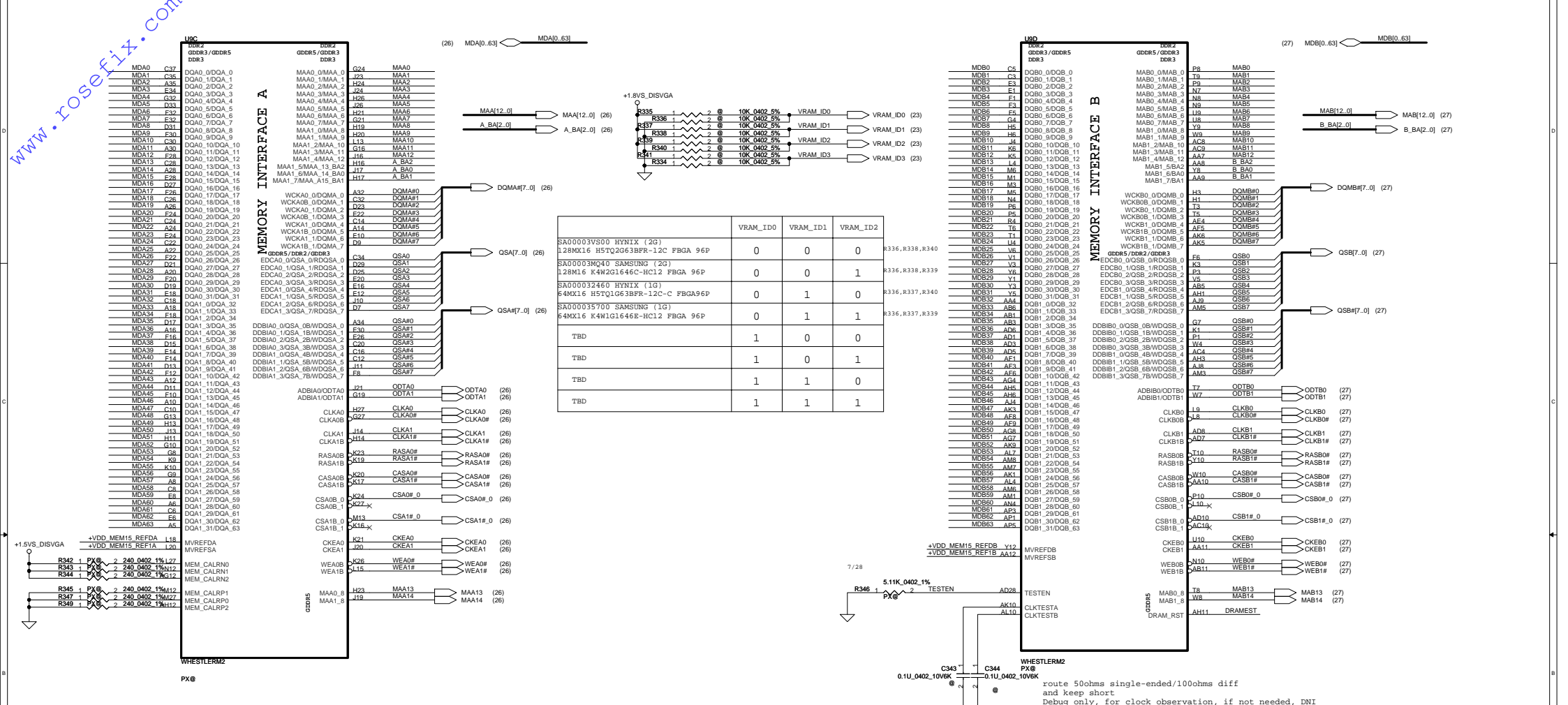




## PEG Interface

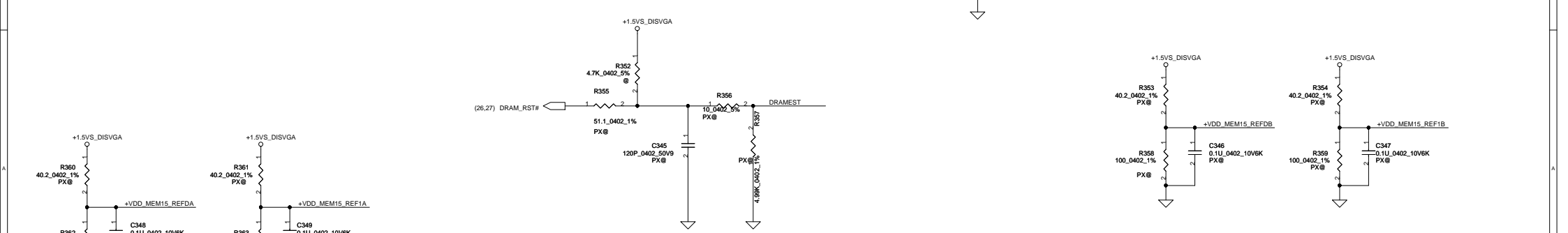
## LVDS Interface

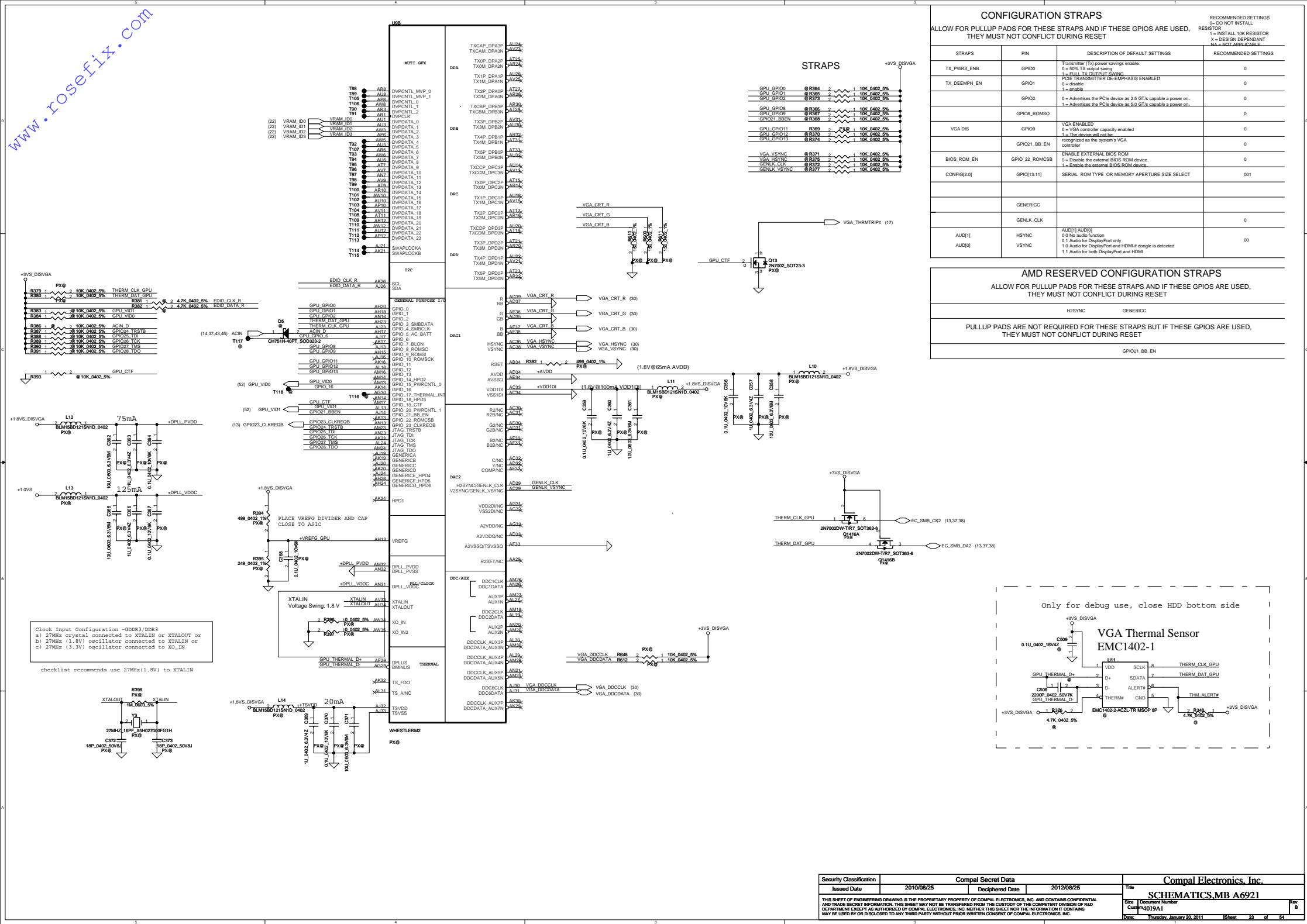




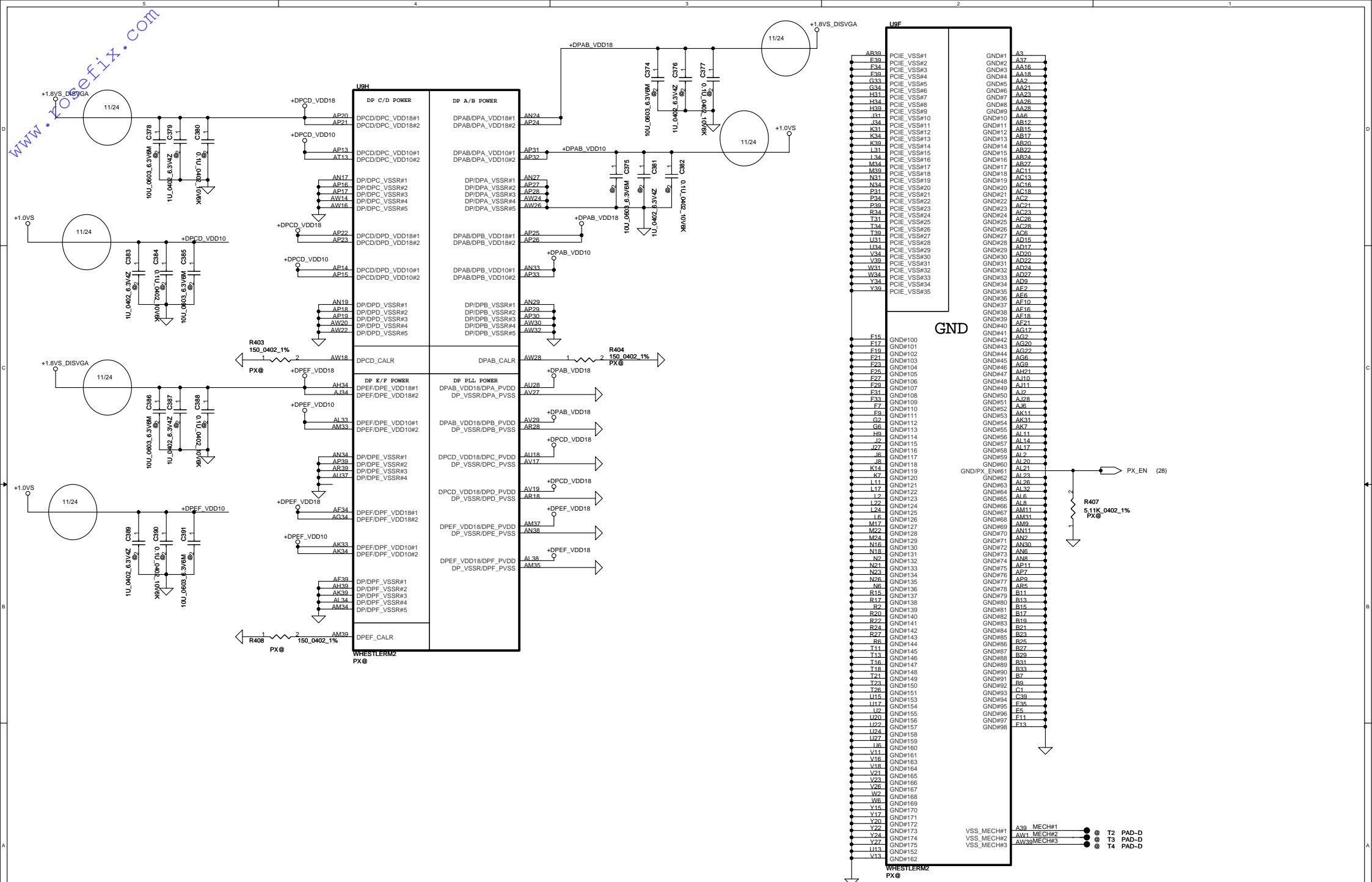
This basic topology should be used for DRAM\_RST for DDR3/GDDR5. These Capacitors and Resistor values are an example only. The Series R and C values will depend on the DRAM load and will have to be calculated for different Memory, DRAM Load and board to pass Reset Signal Spec.

Place all these components very close to GPU (Within 25mm) and keep all component close to each Other (within 5mm) except Rser2





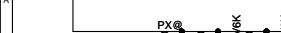




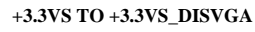
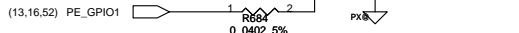
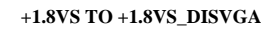
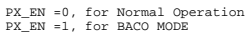






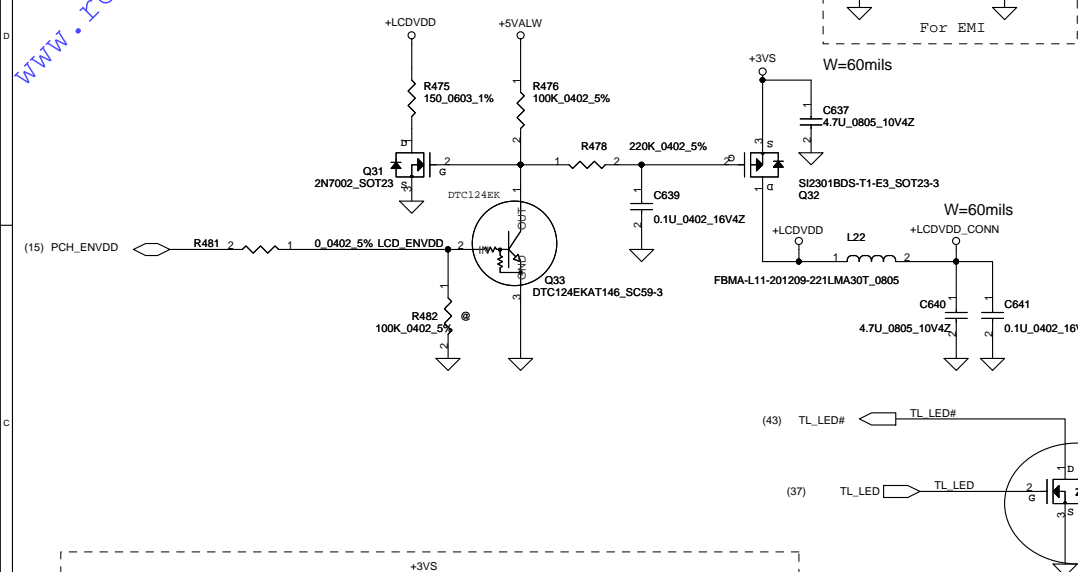


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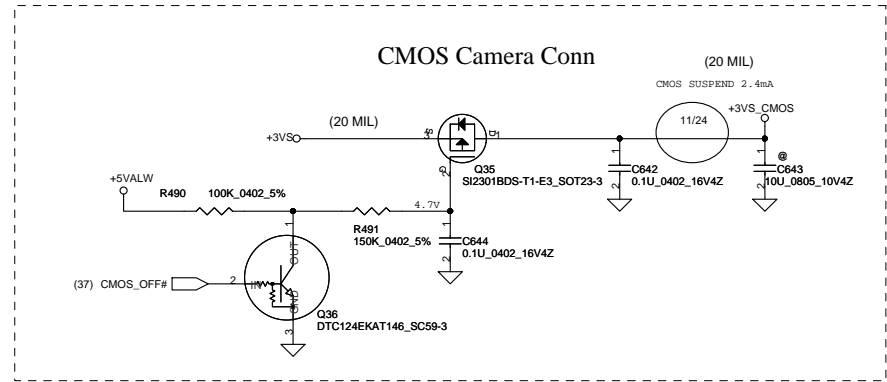
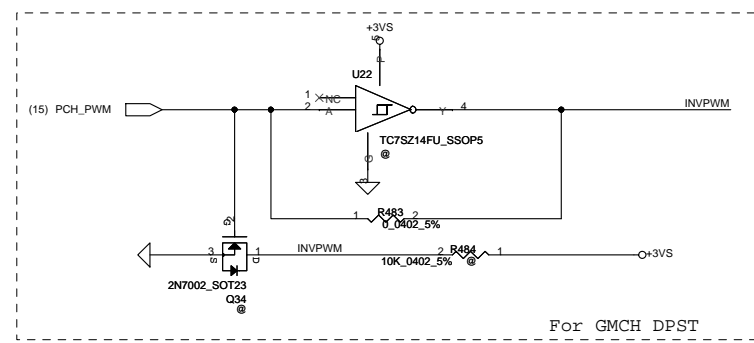
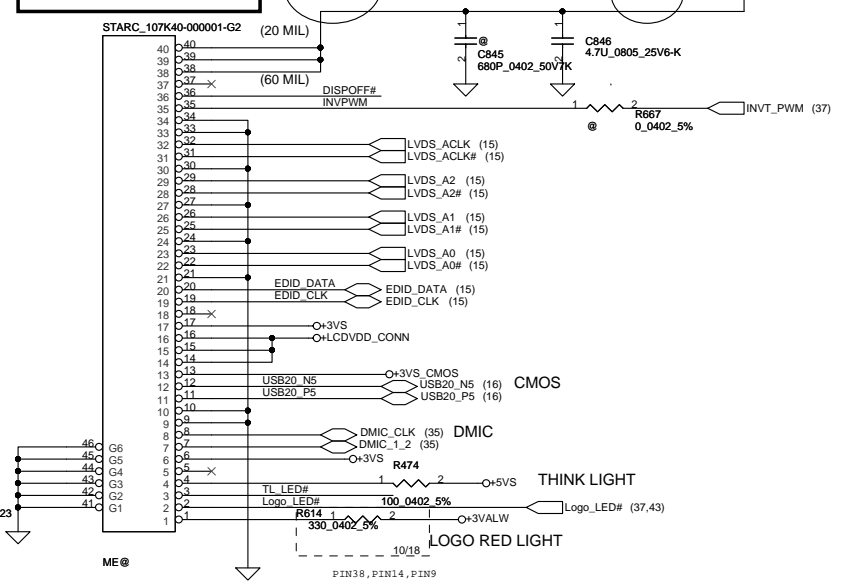


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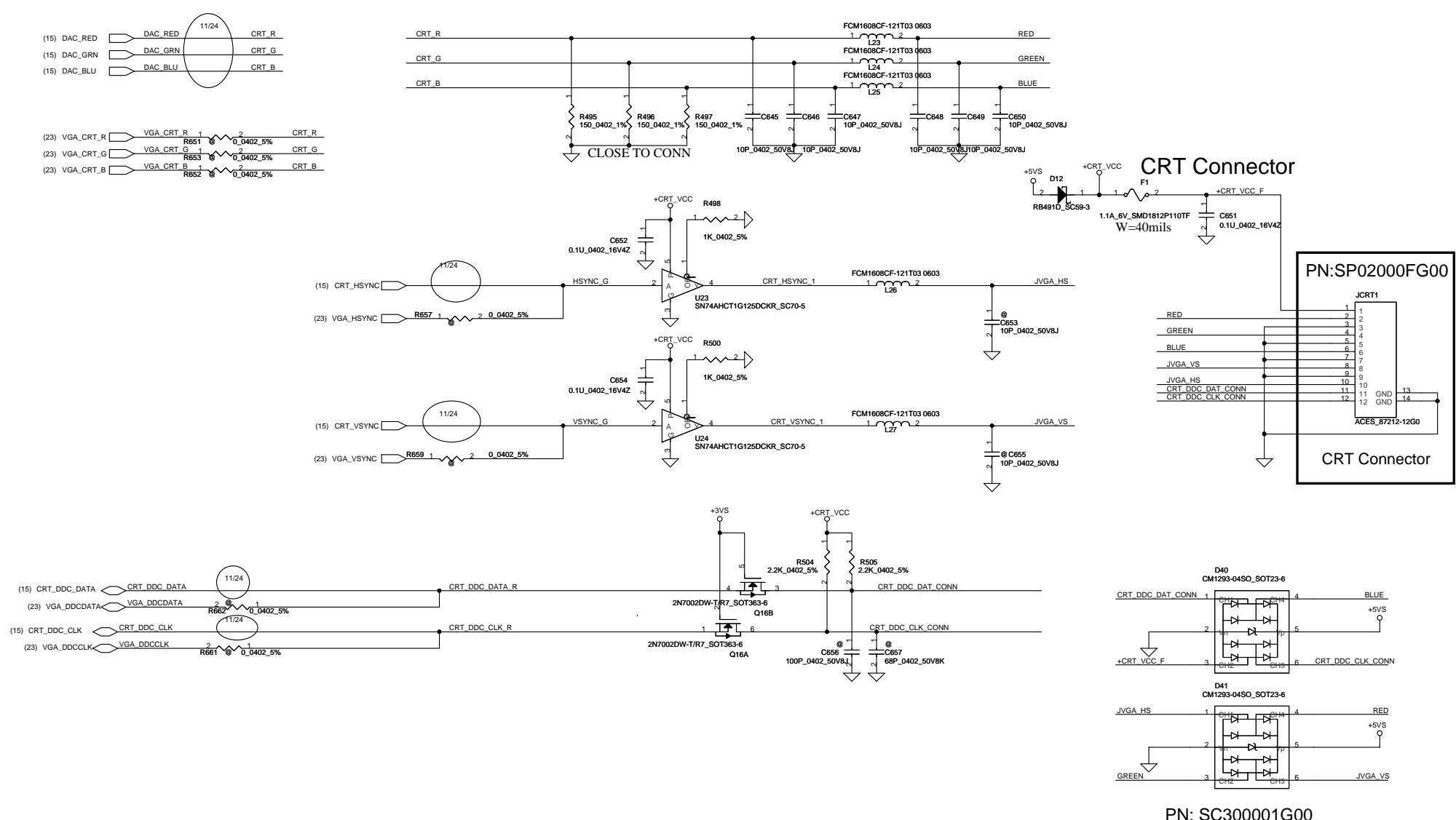
# LCD POWER CIRCUIT

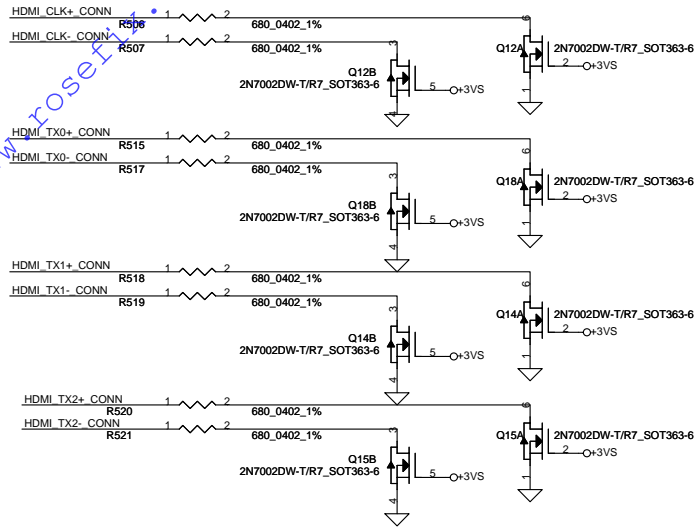


## PN:SP01000XE00



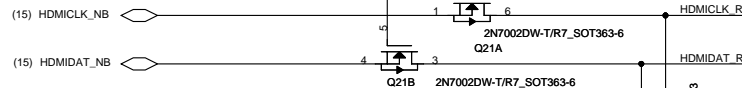
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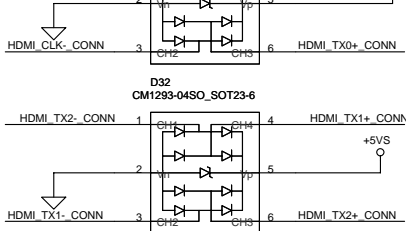
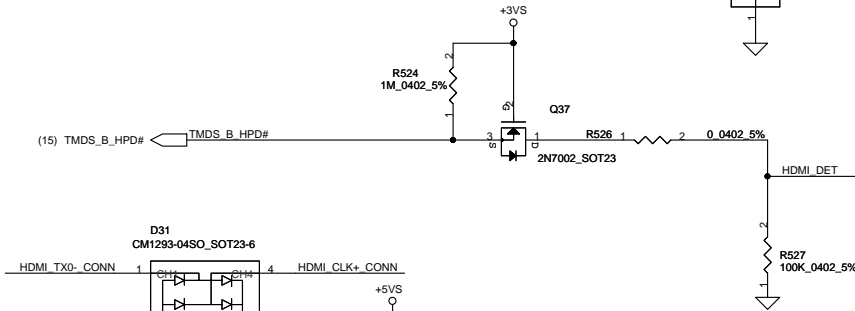


(15) HDMI_CLK+_CK	HDMI_CLK+_CK	R508	1	2	0.0402_5%	HDMI_CLK+_CONN
(15) HDMI_CLK-_CK	HDMI_CLK-_CK	R509	1	2	0.0402_5%	HDMI_CLK-_CONN
(15) HDMI_TX0+_CK	HDMI_TX0+_CK	R510	1	2	0.0402_5%	HDMI_TX0+_CONN
(15) HDMI_TX0-_CK	HDMI_TX0-_CK	R511	1	2	0.0402_5%	HDMI_TX0-_CONN
(15) HDMI_TX1+_CK	HDMI_TX1+_CK	R512	1	2	0.0402_5%	HDMI_TX1+_CONN
(15) HDMI_TX1-_CK	HDMI_TX1-_CK	R513	1	2	0.0402_5%	HDMI_TX1-_CONN
(15) HDMI_TX2+_CK	HDMI_TX2+_CK	R514	1	2	0.0402_5%	HDMI_TX2+_CONN
(15) HDMI_TX2-_CK	HDMI_TX2-_CK	R515	1	2	0.0402_5%	HDMI_TX2-_CONN

Pull up R for PCH SIDE

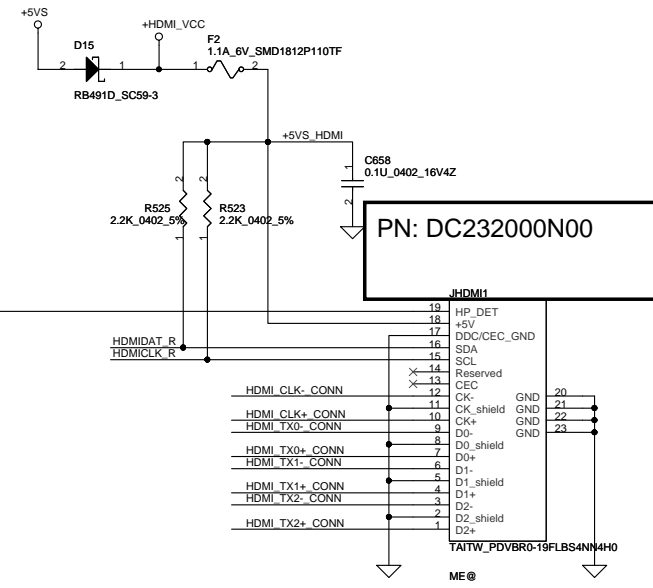
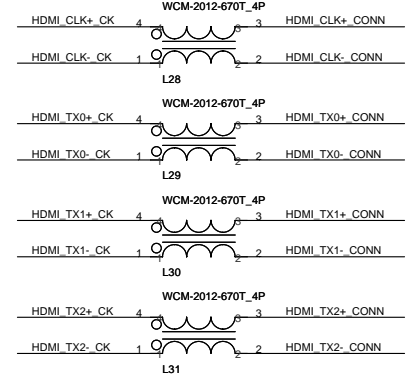


PN: SCA00000U10

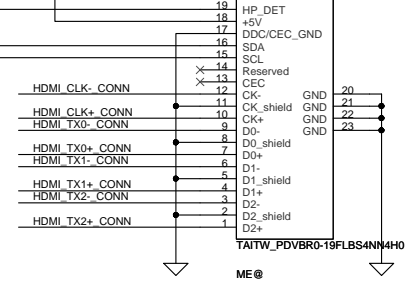


PN: SC300001G00x 2

PN:SM070000I00x 4



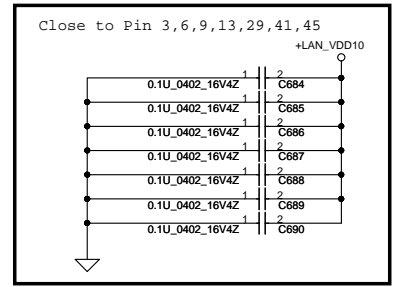
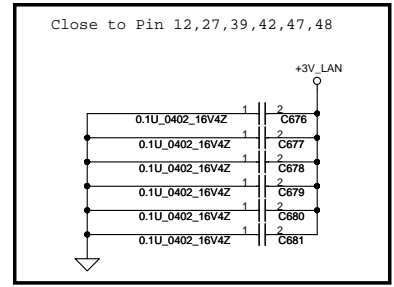
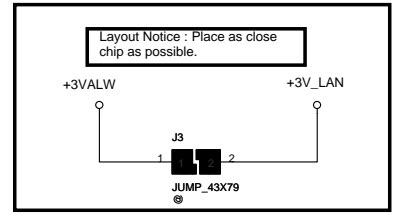
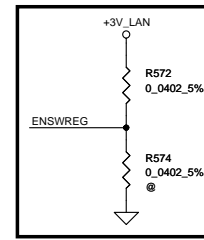
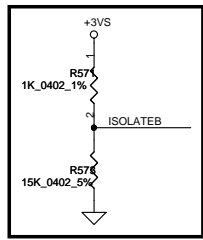
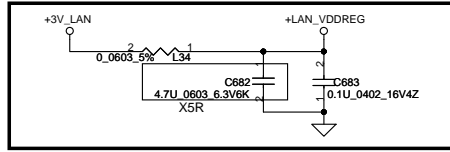
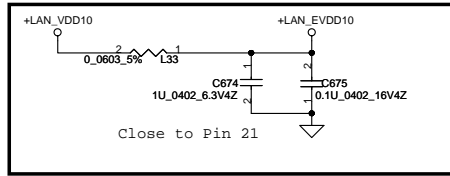
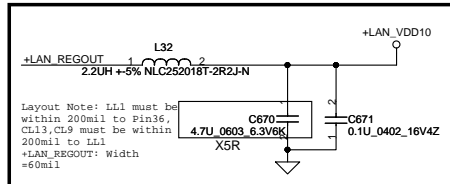
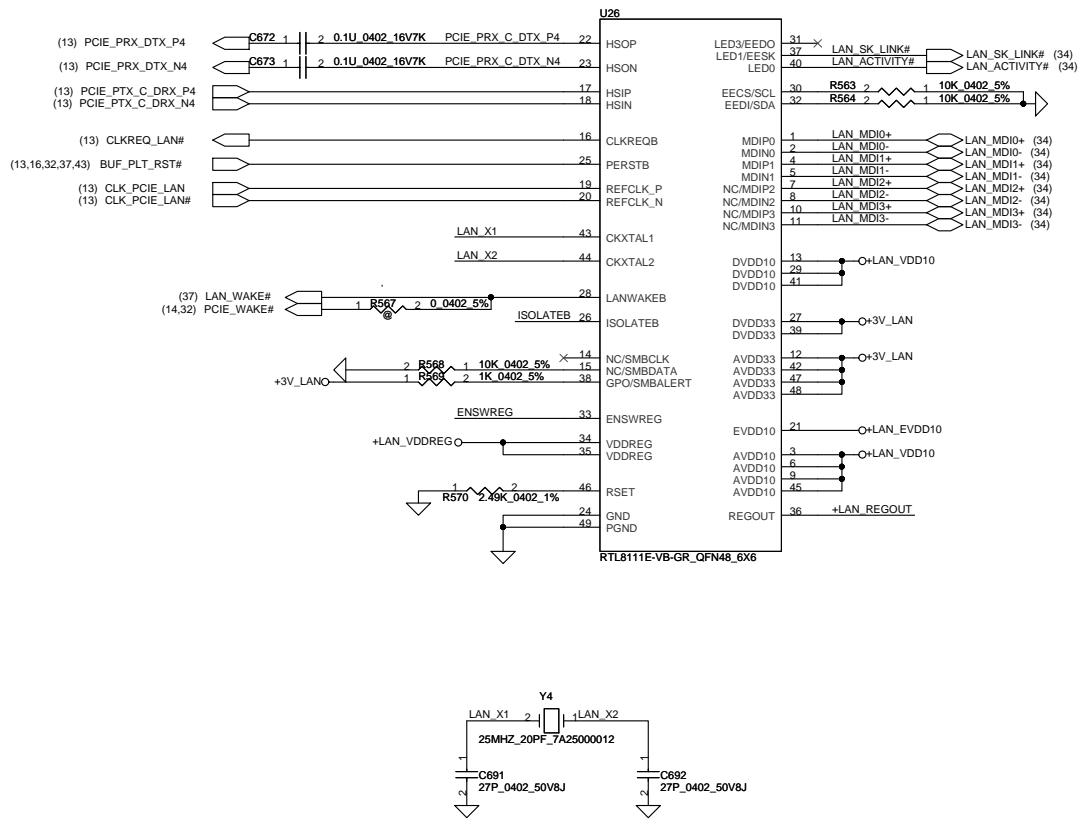
PN: DC232000N00

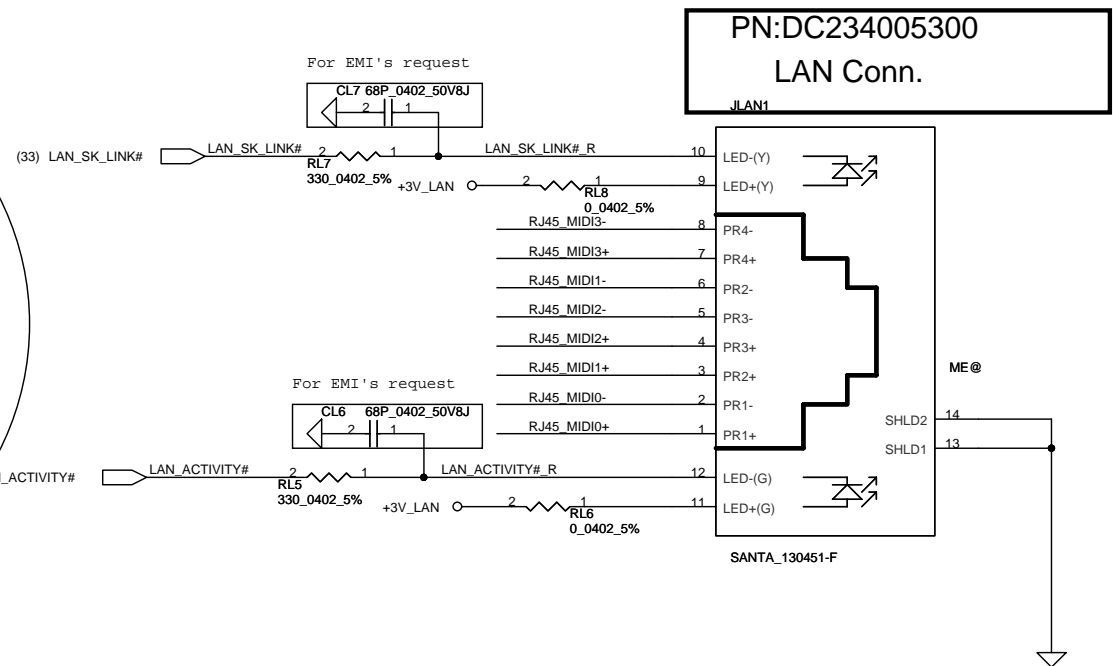
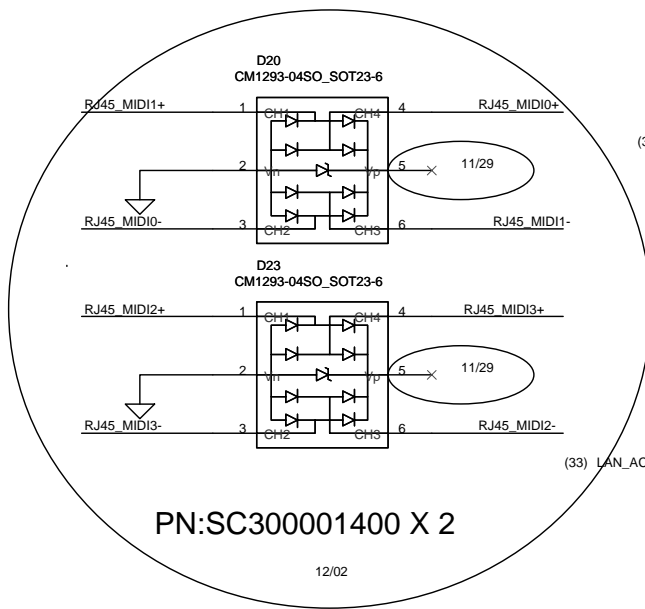
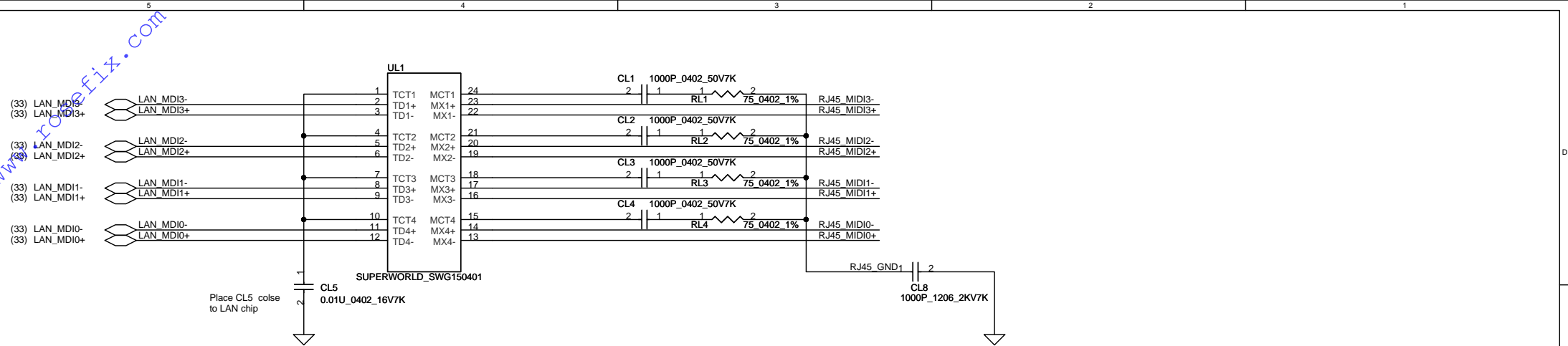


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				Sheet	31	of 54

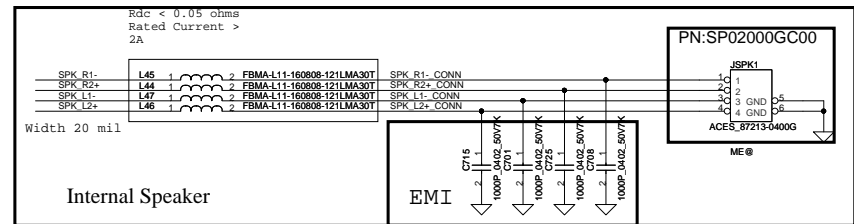
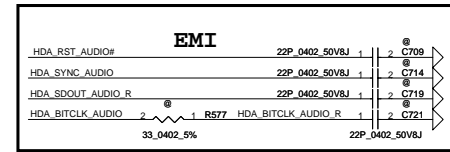
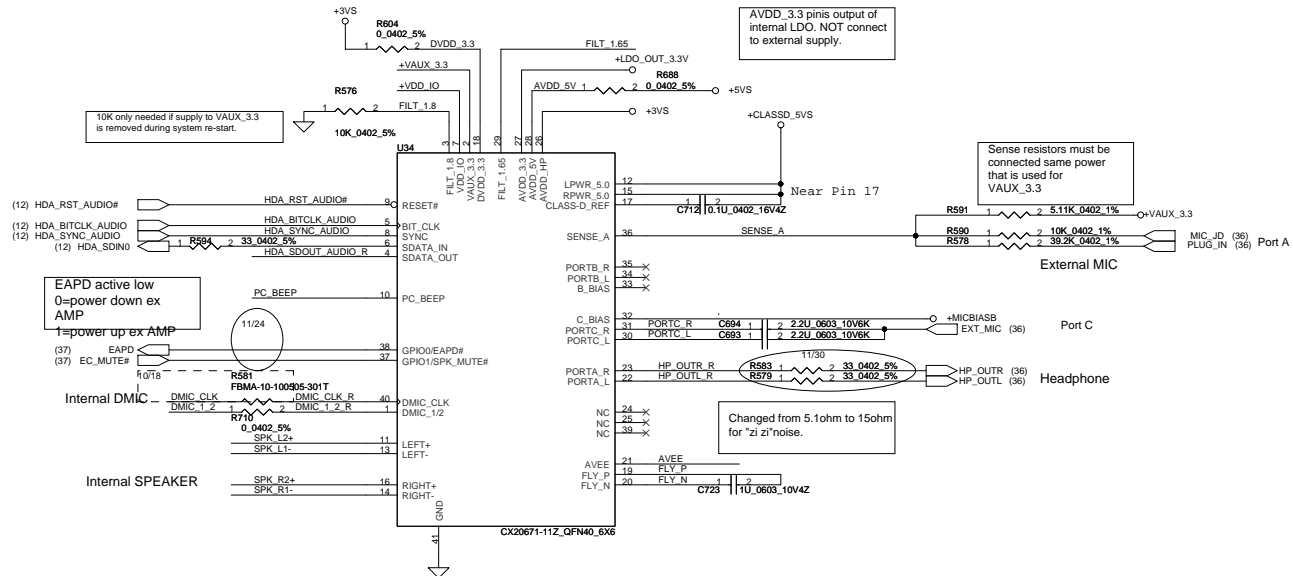
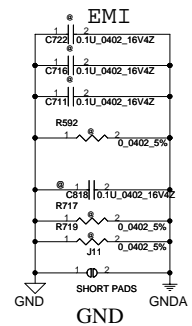
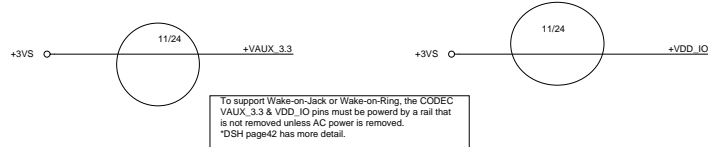
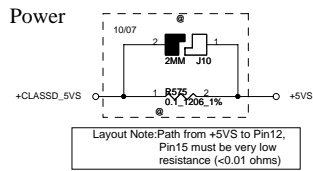
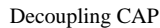
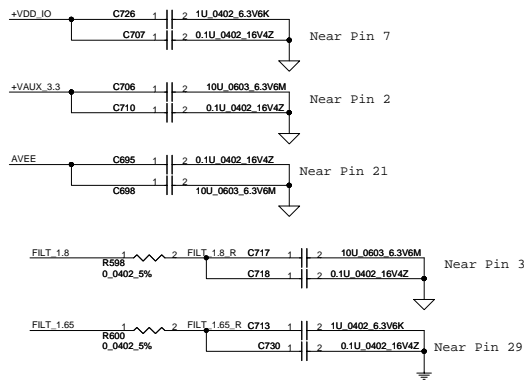
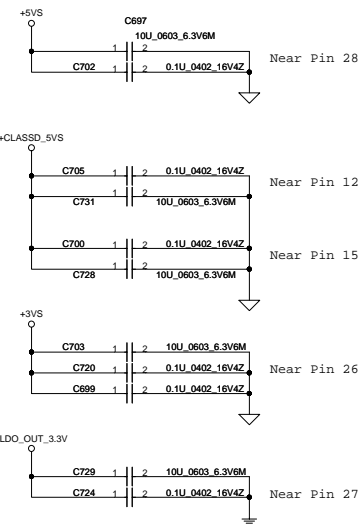
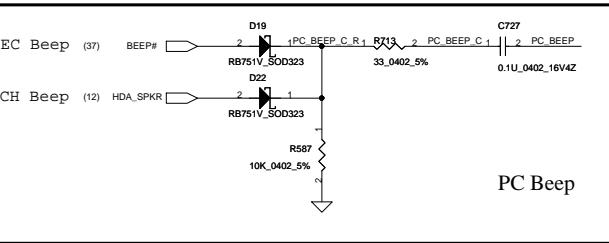
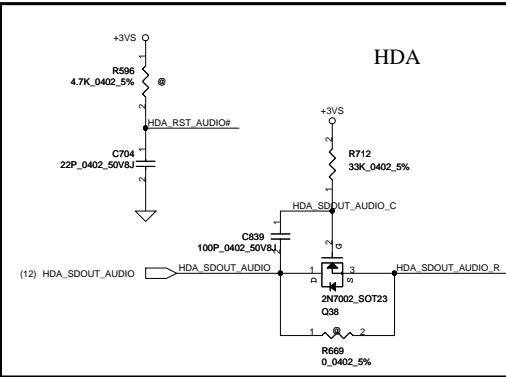




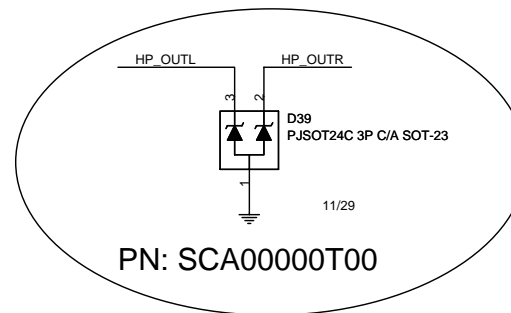
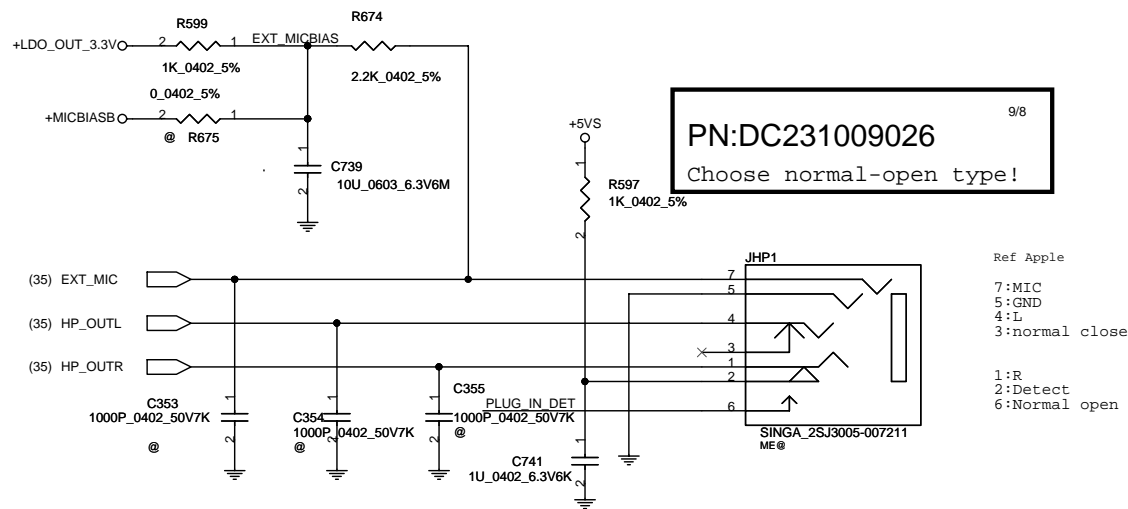
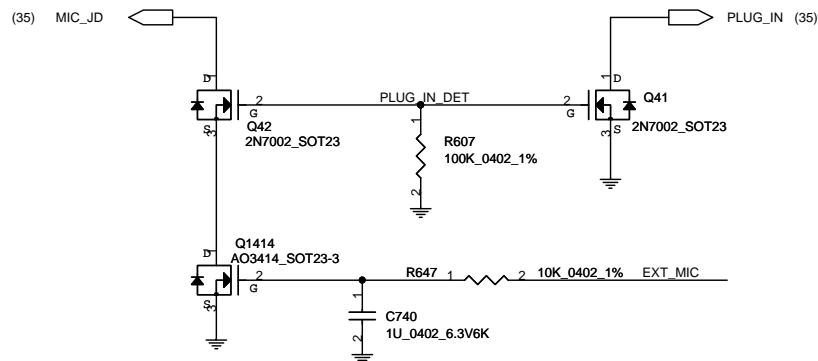




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				Size Custom	Document Number 4019A1 Rev B
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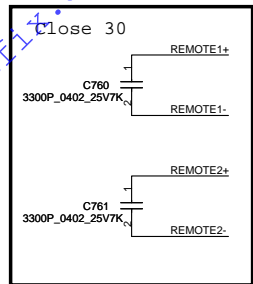
[illegible]

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										Custom		4019A1			
										Date:		Thursday, January 20, 2011			

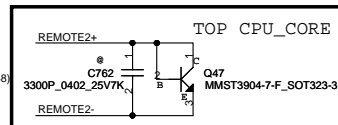
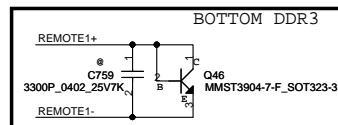
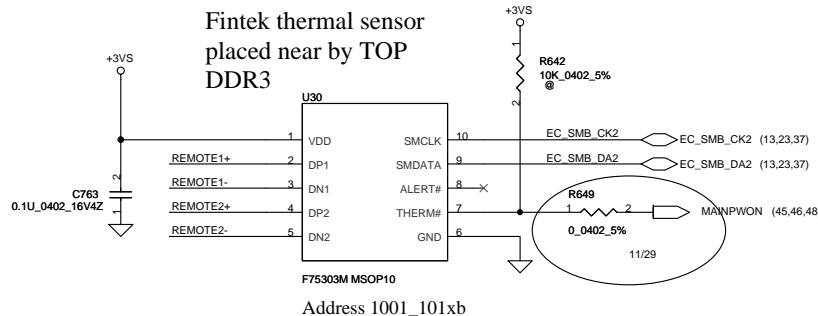


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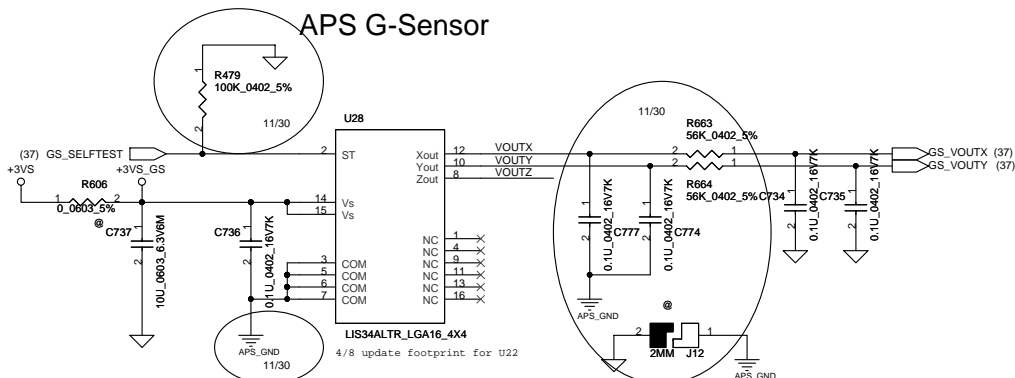
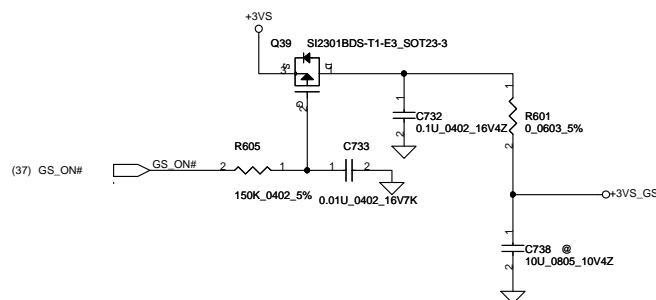
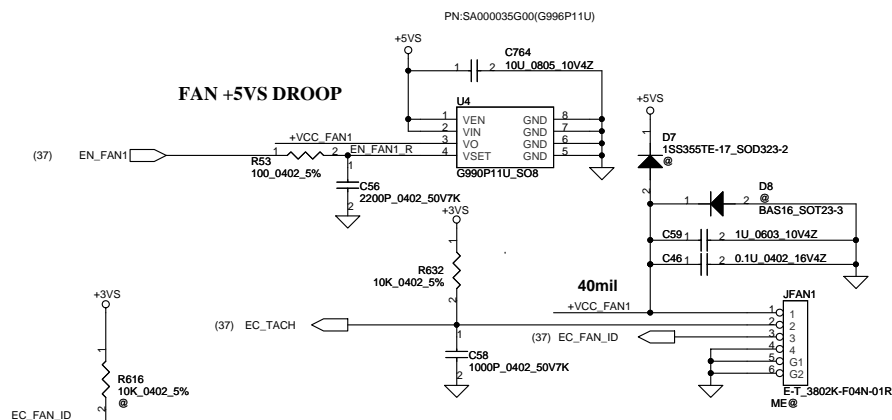




Fintek thermal sensor  
placed near by TOP  
DDR3

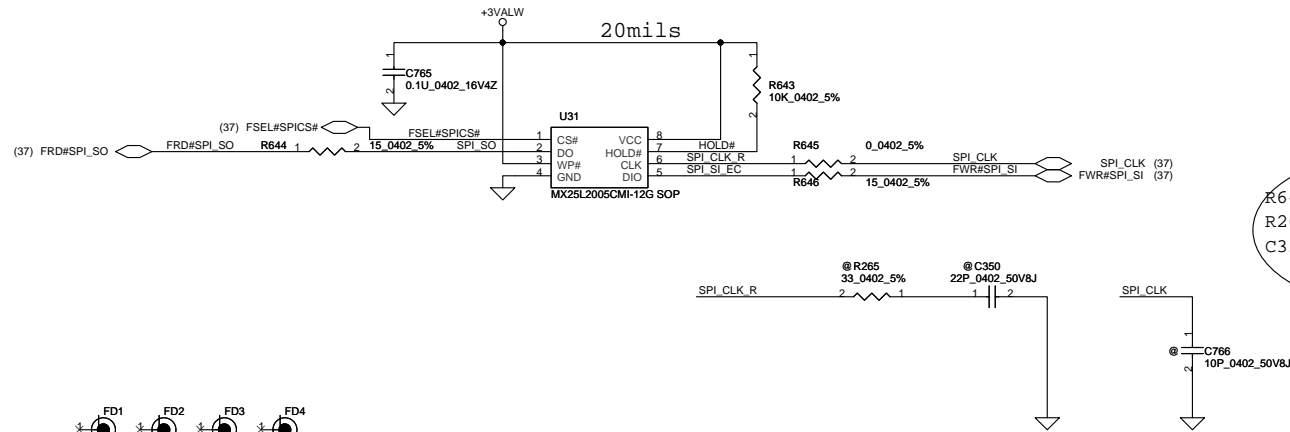


REMOTE1,2+/-:  
Trace width/space:10/10 mil  
Trace length:<8"

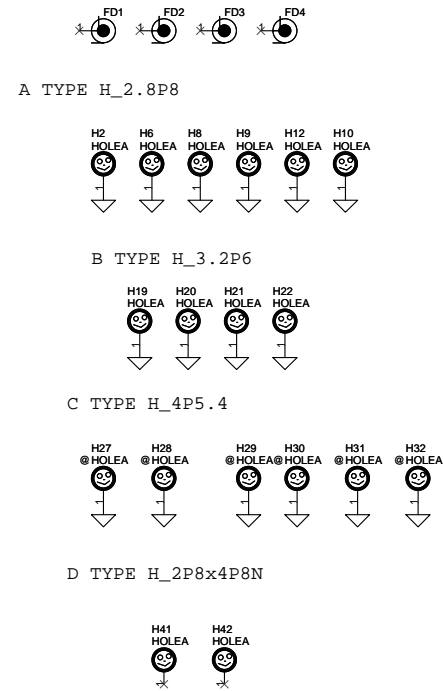


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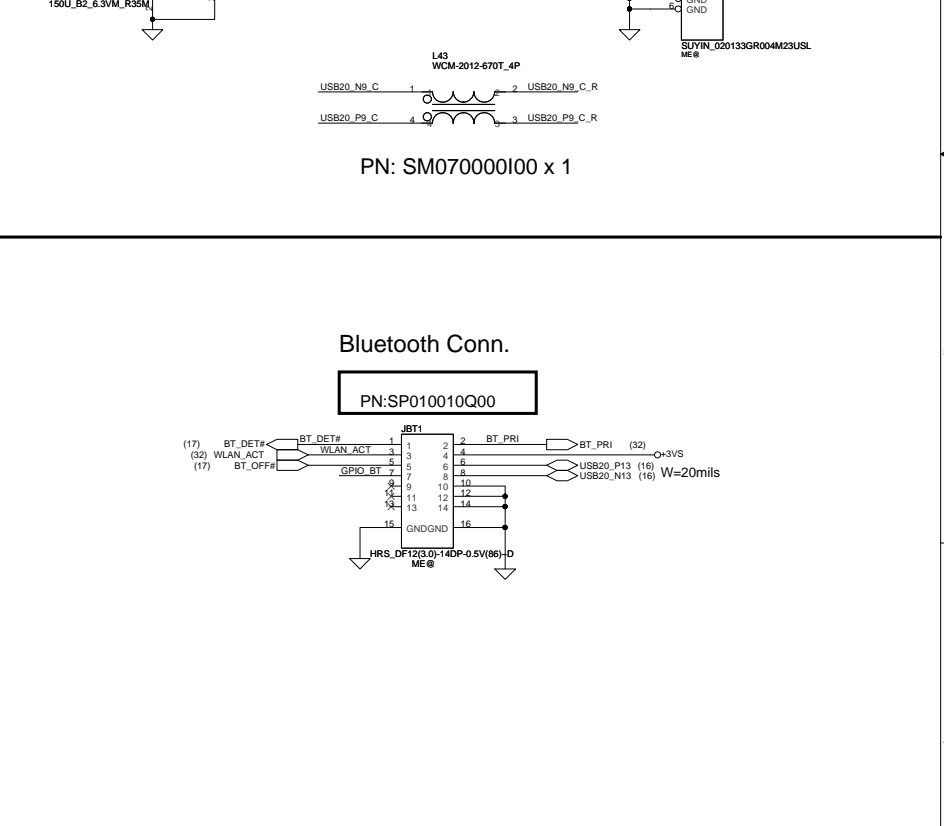
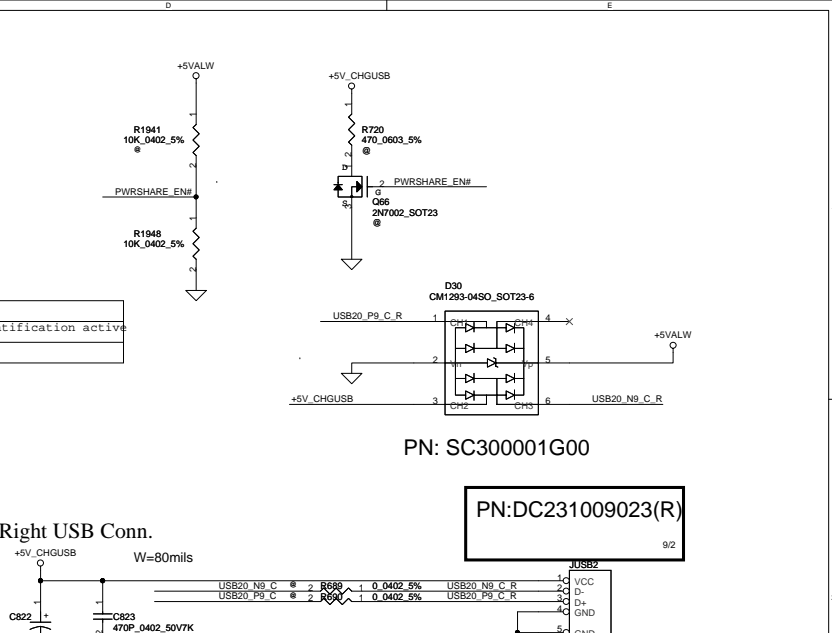
FOR EC 128KB SPI ROM  
(150mil PACKAGE)  
PN:SA00002C100



R645: BEAD 100 OHM, SM01000DI00 (SIT)  
R265: 33 OHM (SIT)  
C350: 22pf (SIT)

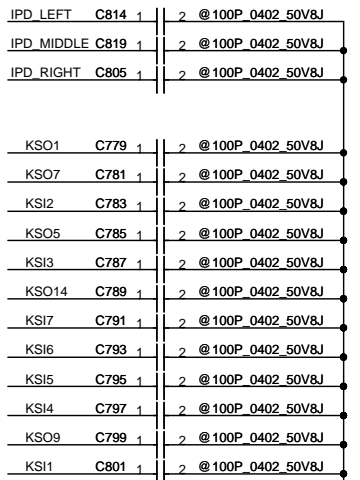
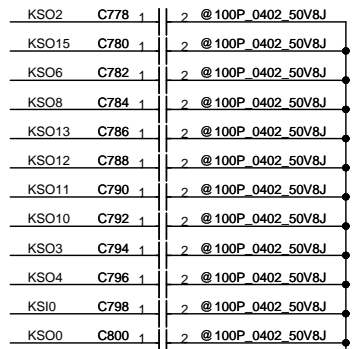


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# INT\_KBD Conn.

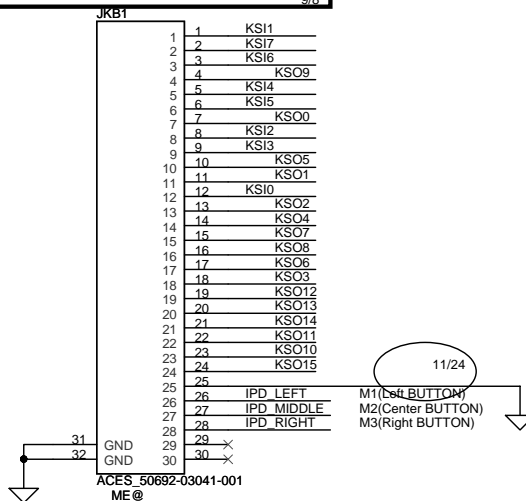


CONN PIN define need double check

Reserve for ESD.

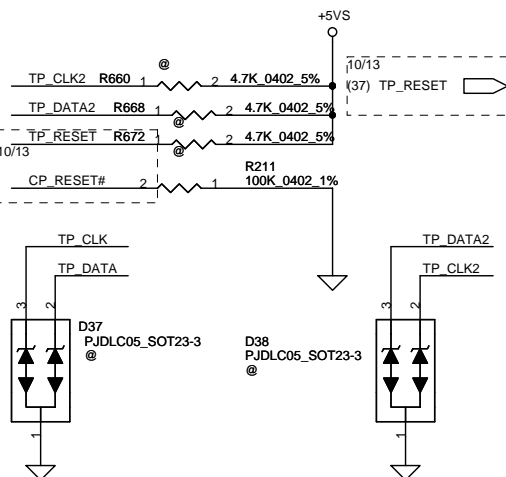
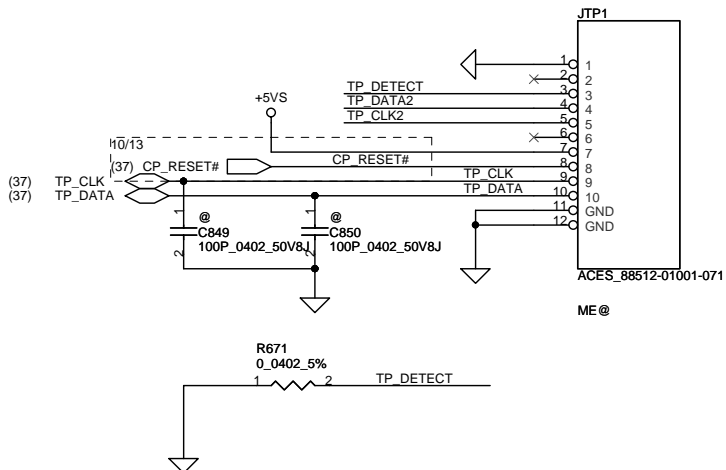
PN:SP011009012

9/8



PN:LTCX002RM00

Click pad 10PIN

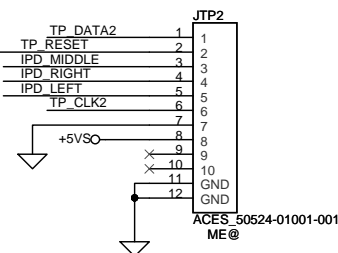


PN: SCA00000U10 X 2

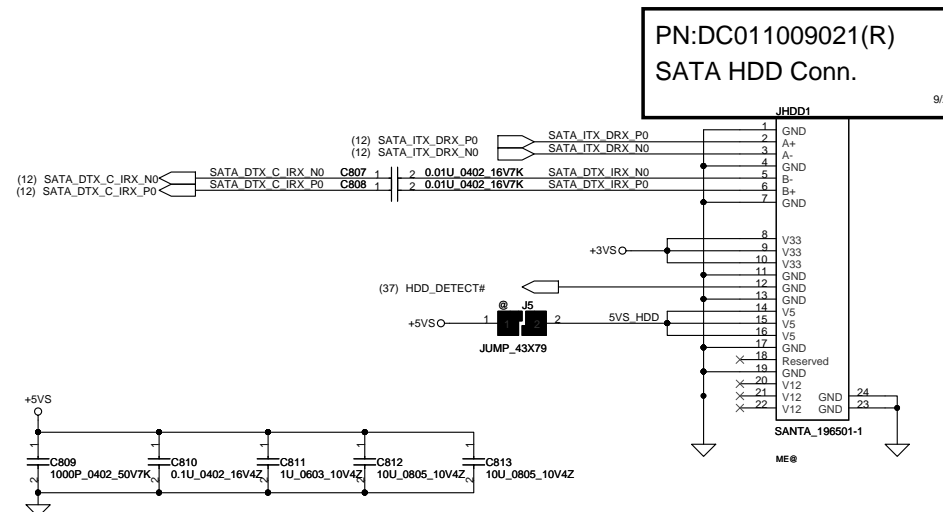
PN:SP011009011

Track point

9/8



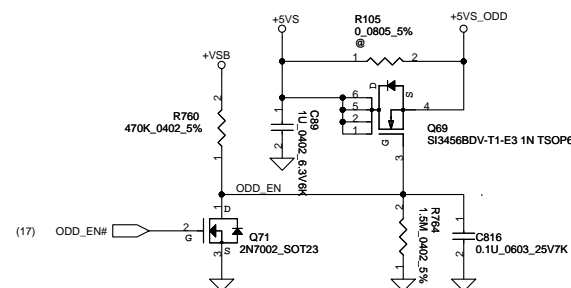
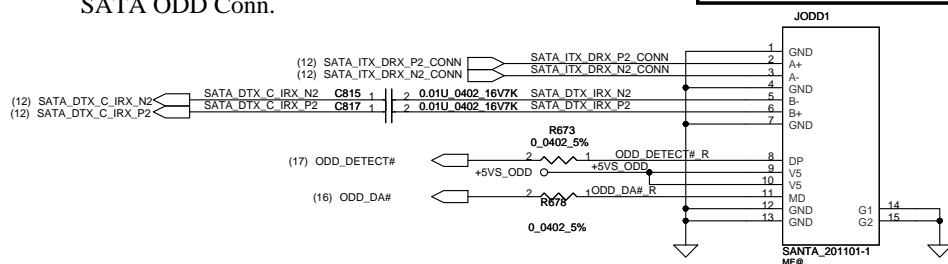
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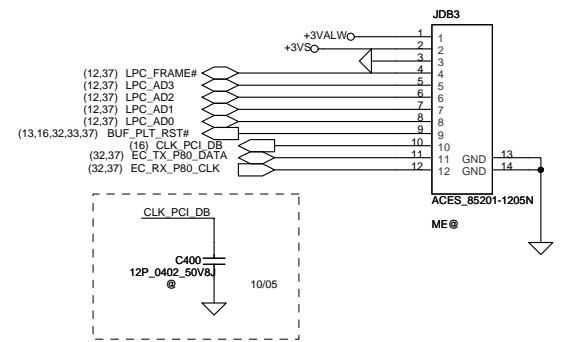
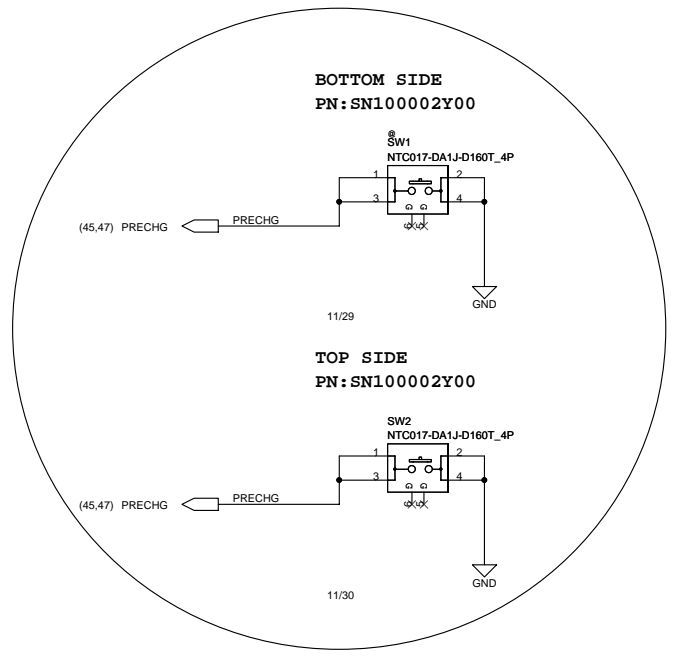
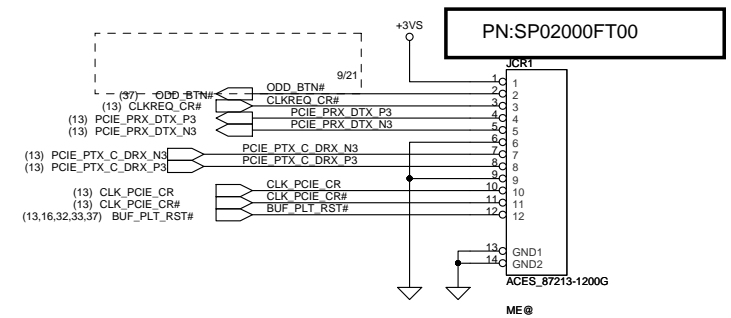
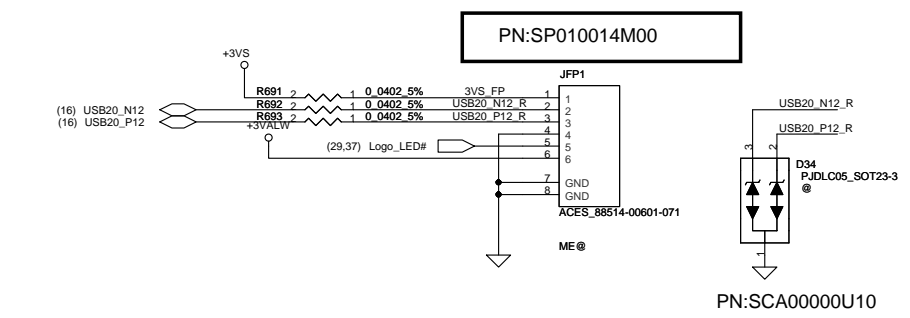
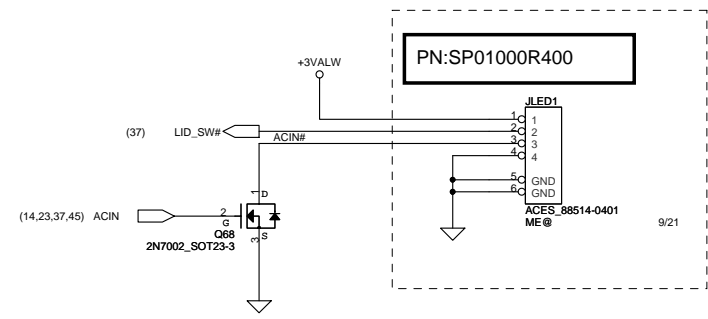
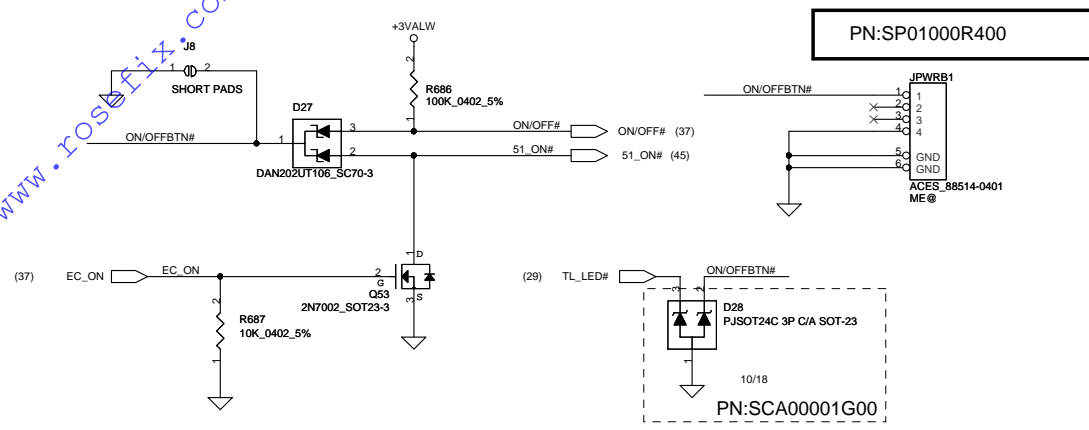
SATA ODD Conn.

PN:LTCX002XN00

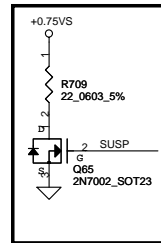
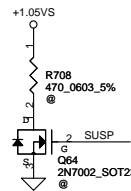
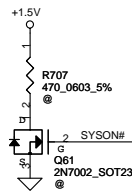
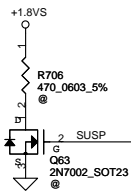
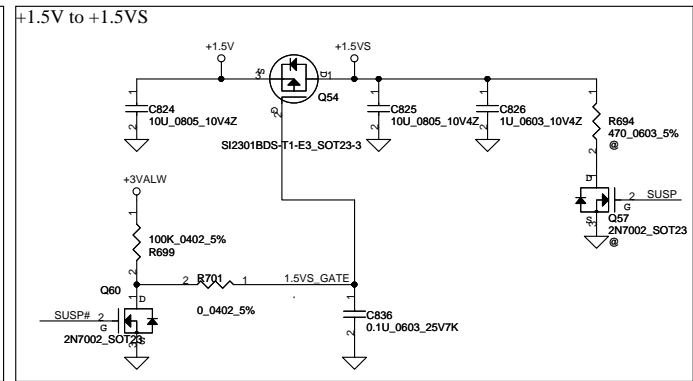
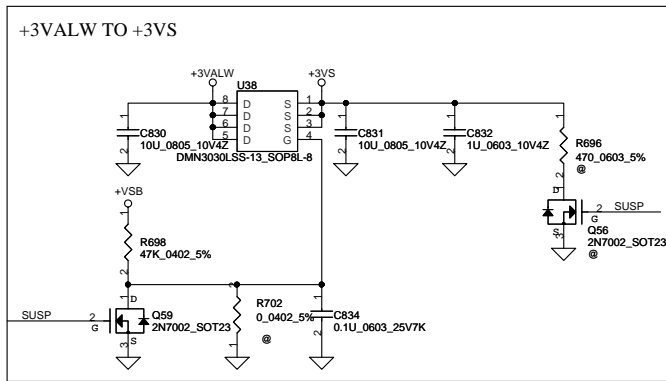
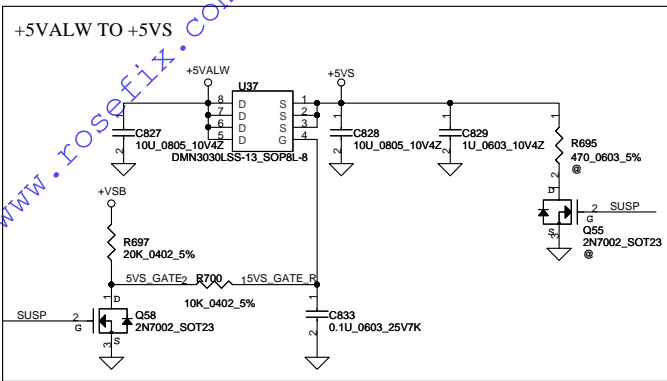
9/2



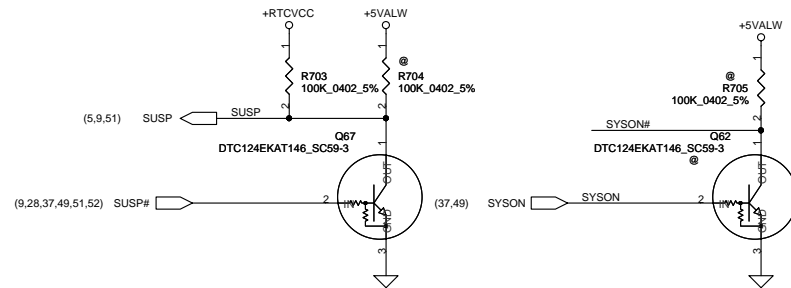
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For Intel S3 Power Reduction.

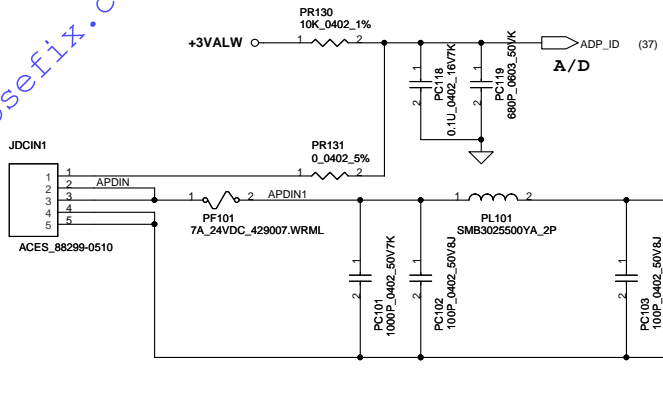


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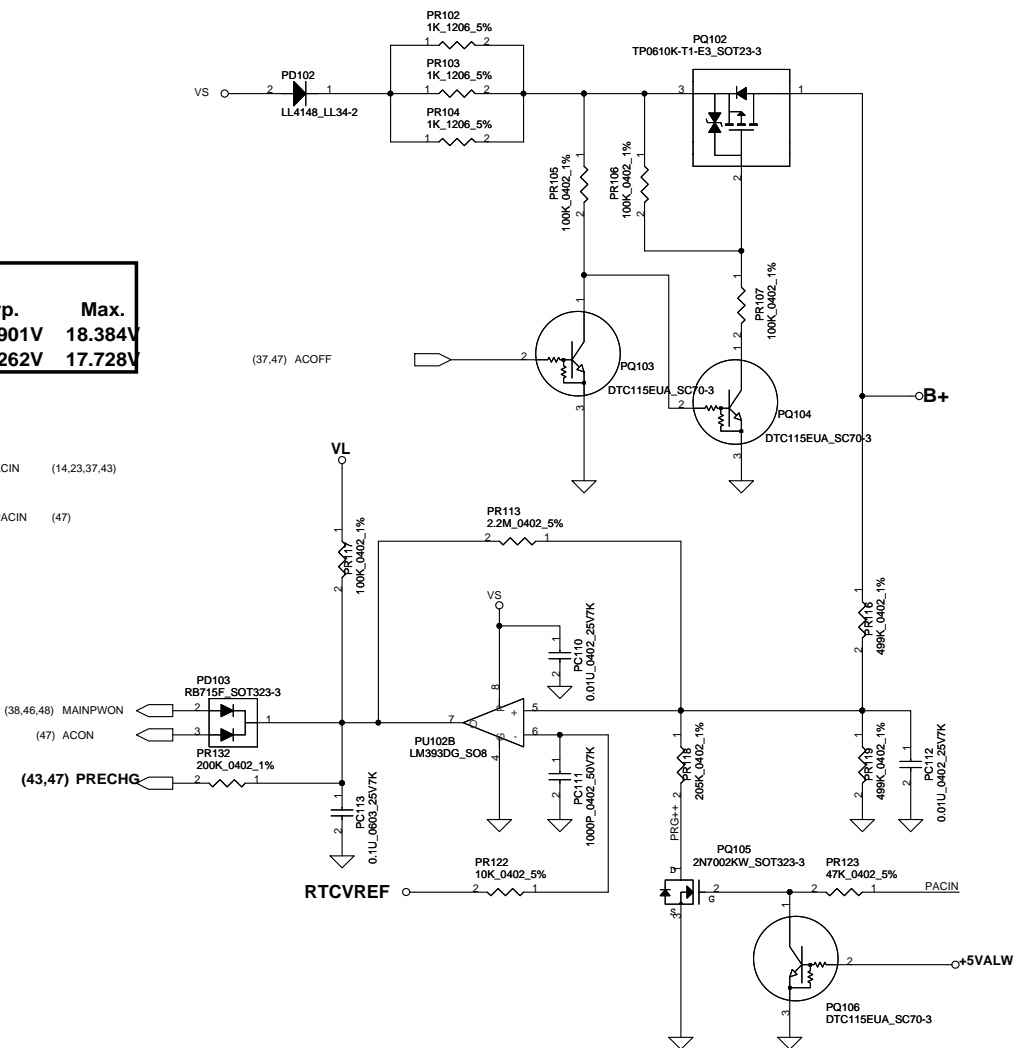
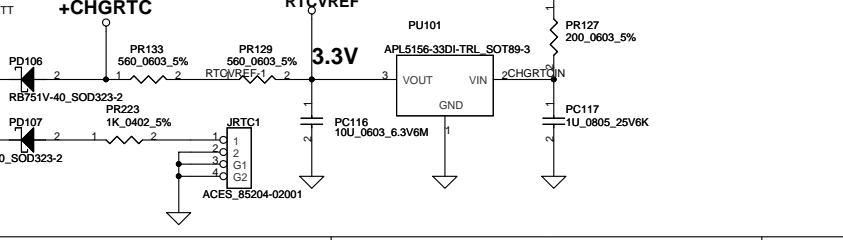
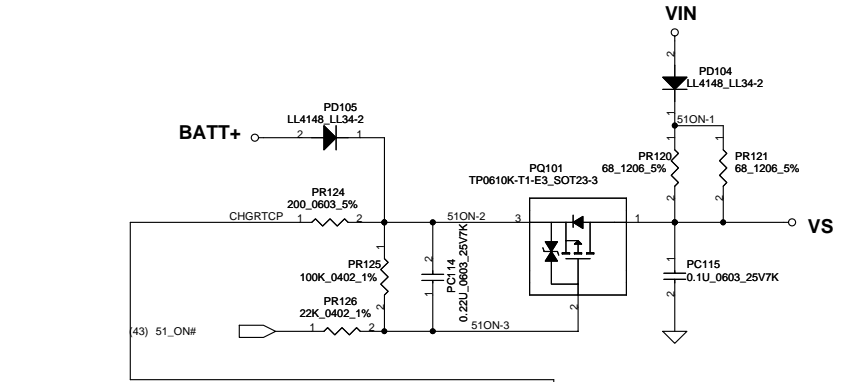
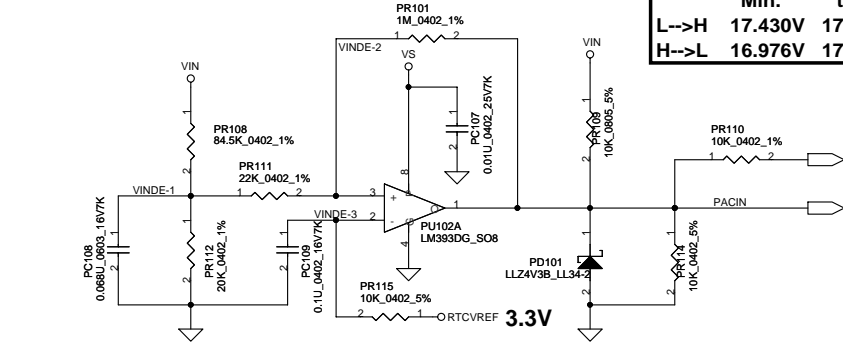
ADP_ID			
	AC Adapter	90W	65W
R(K ohm)	open	10	
ADP_ID(V)	3.3	1.65	
Detection voltage	>2.64	1.32~1.98	

ACIN				
	Precharge detector	Min.	typ.	Max
L-->H		14.991V	15.381V	15.782V
H-->L		13.860V	14.247V	14.621V

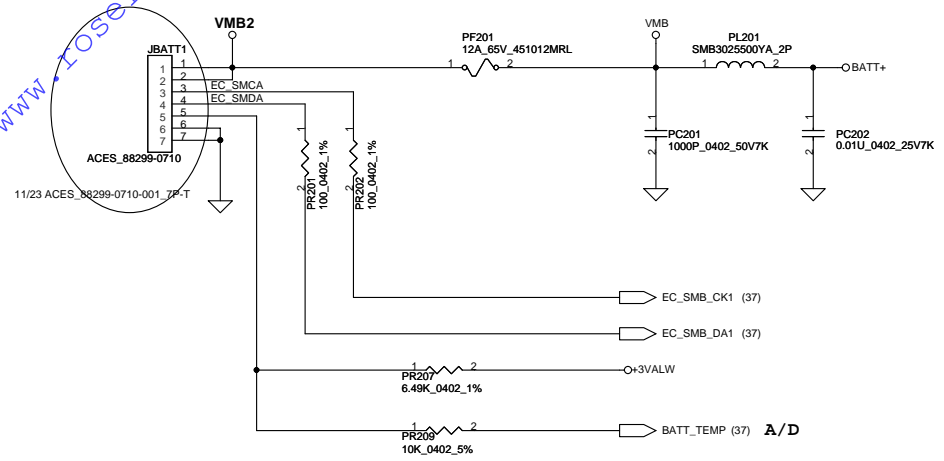
BATT ONLY				
	Precharge detector	Min.	typ.	Max
L-->H		7.196V	7.349V	7.505V
H-->L		6.138V	6.214V	6.056V



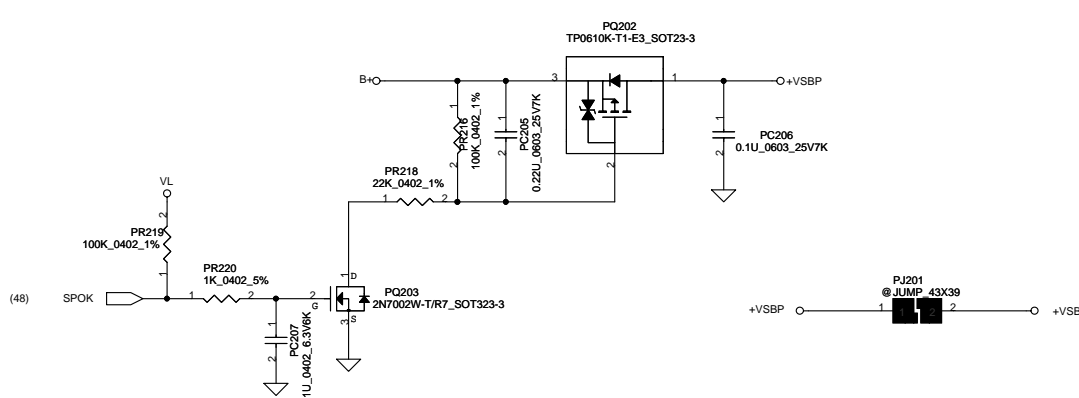
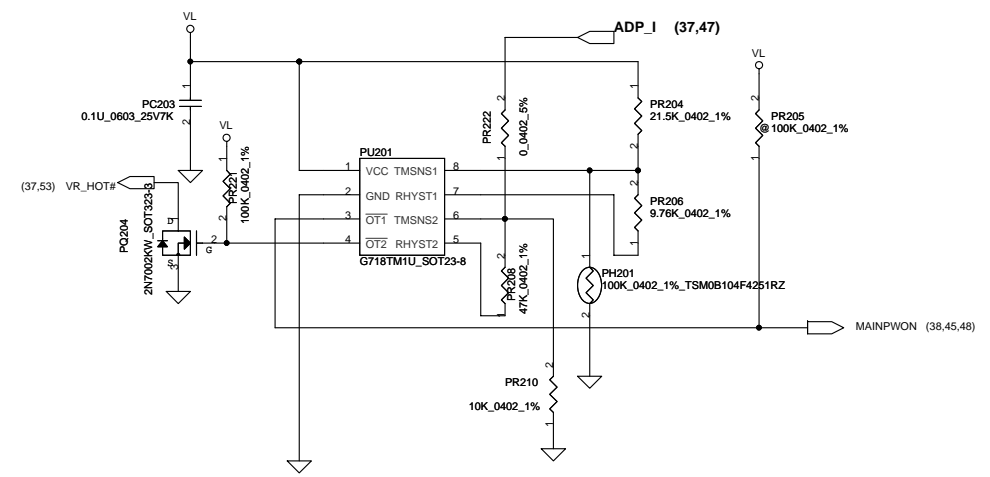
Vin Detector			
	Min.	typ.	Max.
L-->H	17.430V	17.901V	18.384V
H-->L	16.976V	17.262V	17.728V



www.iosifix.com

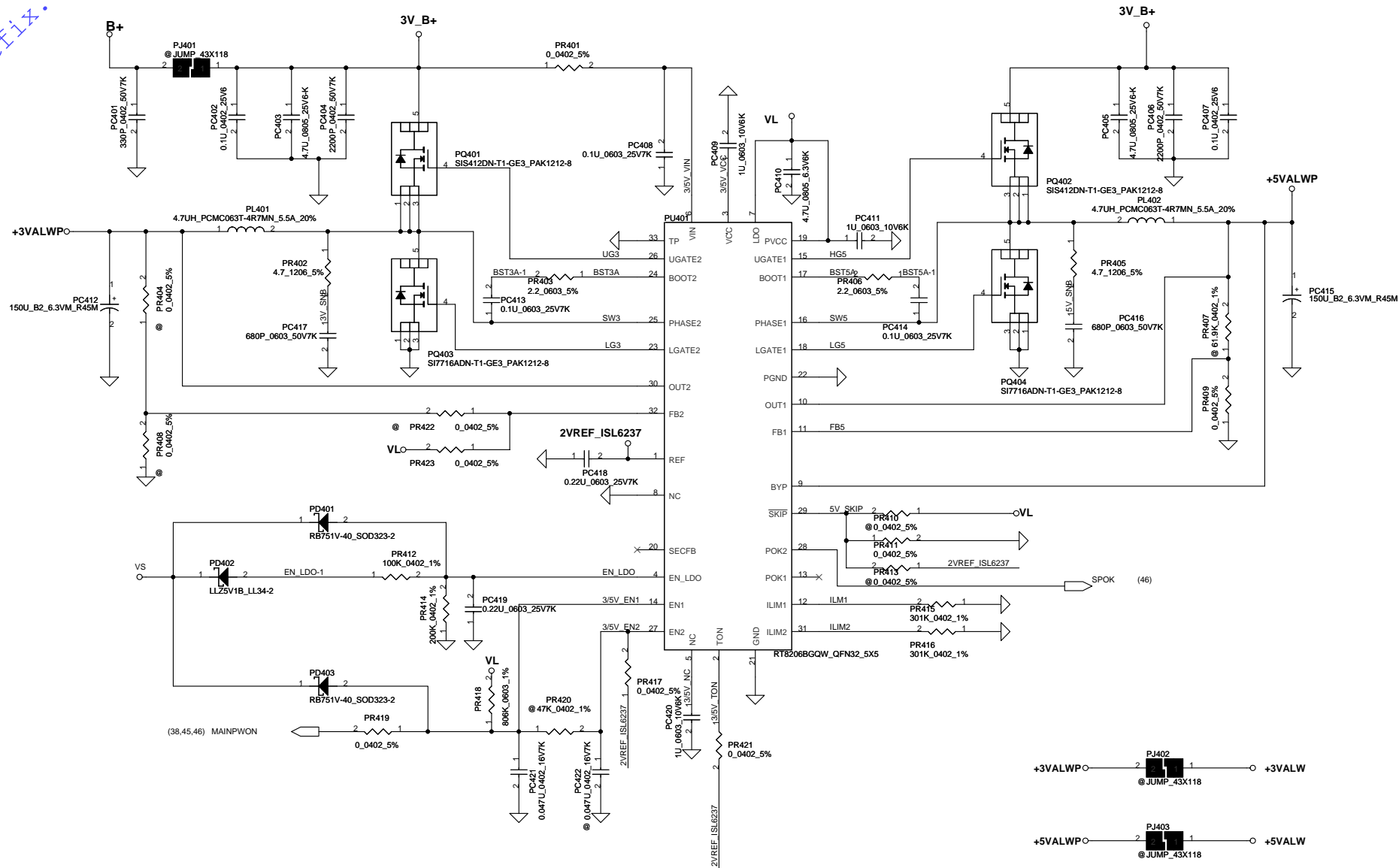


PH201 under CPU botten side :  
CPU thermal protection at 92 degree C  
Recovery at 56 degree C

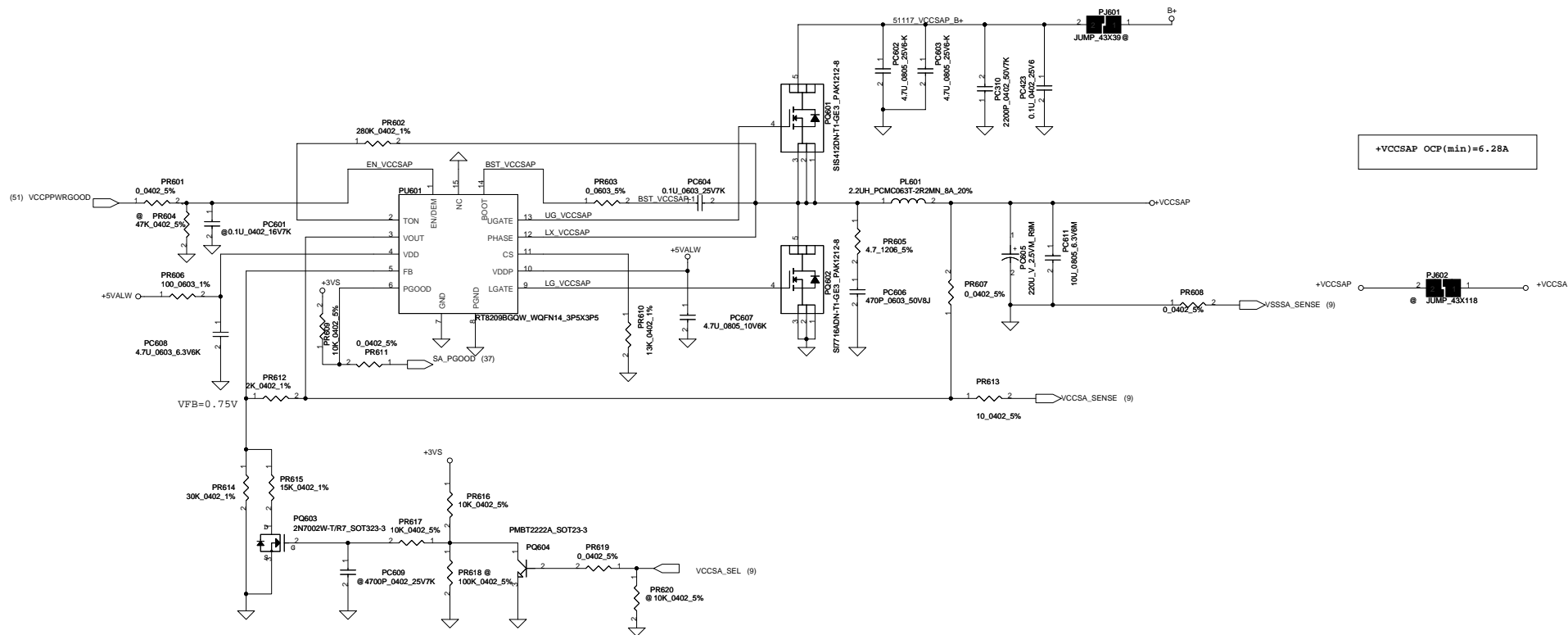








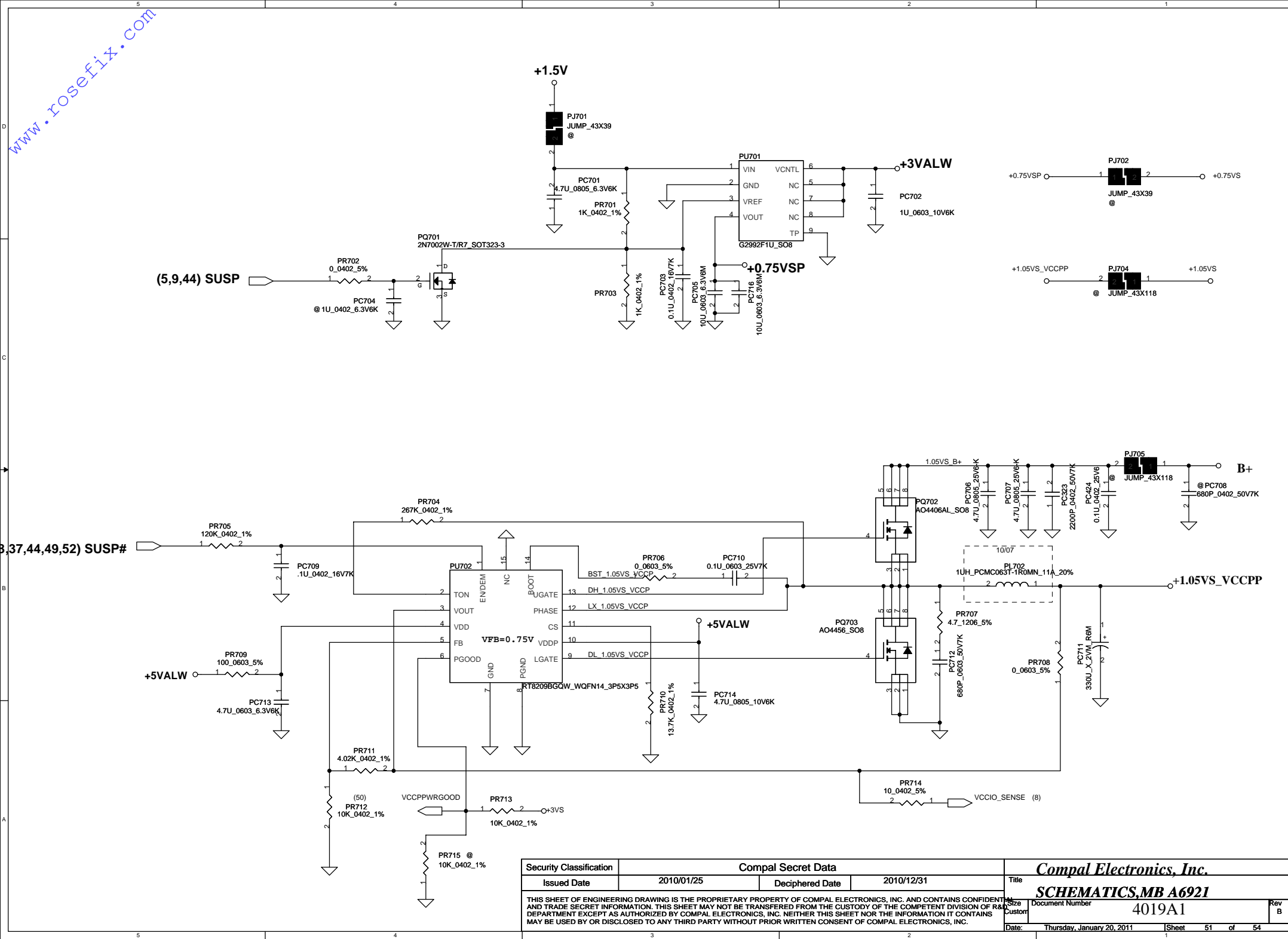




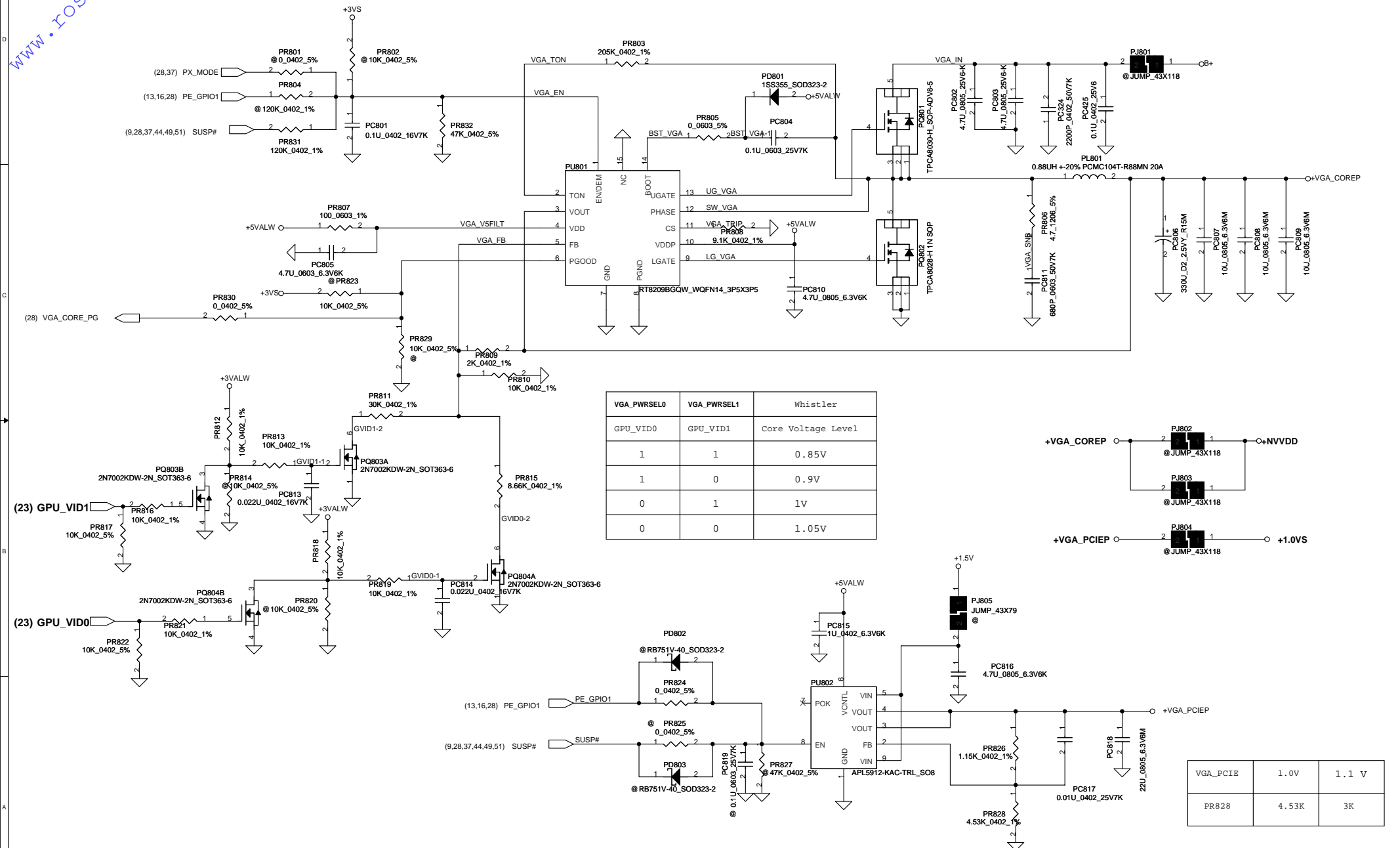
+VCCSAP OCP(min)=6.28A

VID[0]	VID[1]	VCCSA Vout	Require on 2011/ 2012 Required
0	0	0.9 V	Yes/Yes
0	1	0.8 V	Yes/Yes
1	1	0.75V	No/Yes
1	1	0.65V	No/Yes

Note: Use VCCSA\_SEL to switch High & Low Level for VID[1] (ie. VCCSA\_SEL) due to the VID[0] is don't care for this setting.



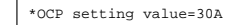
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VGA_PWRSEL0	VGA_PWRSEL1	Whistler
GPU_VID0	GPU_VID1	Core Voltage Level
1	1	0.85V
1	0	0.9V
0	1	1V
0	0	1.05V

VGA_PCIE	1.0V	1.1 V
PR828	4.53K	3K

so the PU resistor in



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