

MALIBU-3 EXT SDV

- 1.TITLE PAGE
- 2.EC HISTORY
- 3.CPU(1/3)
- 4.CPU(2/3)
- 5.CPU(3/3)
- 6.ITP CONNECTOR
- 7.CANTIGA(1/8):HOST I/F
- 8.CANTIGA(2/8):DMI/CLK/CONTROL
- 9.CANTIGA(3/8):PEG/GRAPHICS
- 10.CANTIGA(4/8):DDR3 CHANNEL-A
- 11.CANTIGA(5/8):DDR3 CHANNEL-B
- 12.CANTIGA(6/8):POWER
- 13.CANTIGA(7/8):POWER/FILTER
- 14.CANTIGA(8/8):GND
- 15.DDR3 SO DIMM CHANNEL-A
- 16.DDR3 SO DIMM CHANNEL-B
- 17.DDR3 DECOUPLING
- 18.CLOCK GEN
- 19.M82XT-S(1/6):PEG I/F
- 20.M82XT-S(2/6):DIGITAL OUT I/F
- 21.M82XT-S(3/6):VRAM I/F
- 22.M82XT-S(4/6):GPIO
- 23.M82XT-S(5/6):POWER
- 24.M82XT-S(6/6):GND
- 25.BLANK
- 26.VRAM CHANNEL-A
- 27.BACK BIAS POWER
- 28.LVDS SWITCH
- 29.LCD CONNECTOR
- 30.RGB SWITCH
- 31.EXT CRT INTERFACE
- 32.BLANK
- 33.ICH9-M(1/6):SATA/HDA/GLAN/LPC
- 34.ICH9-M(2/6):DMI/PCIE/USB/SPI
- 35.ICH9-M(3/6):PCI/INTERRUPT
- 36.ICH9-M(4/6):GPIO/CLK/PM
- 37.ICH9-M(5/6):POWER
- 38.ICH9-M(6/6):GND
- 39.RTC BATTERY
- 40.SATA HDD CONN
- 41.SATA BAY I/F CONN
- 42.USB POWER/CONN
- 43.AUDIO CX20561
- 44.AUDIO MISC
- 45.AUDIO INT MIC
- 46.AUDIO HP&EXT MIC CONN
- 47.AUDIO EXT MIC AMP
- 48.AUDIO SPEAKER
- 49.AUDIO HDP AMP
- 50.AUDIO BEEP
- 51.CDC CONNECTOR

- 52.GBE BOAZMAN
- 53.GBE LAN SWITCH
- 54.GBE MAGNETICS
- 55.RJ11/RJ45 CONN CTOR
- 56.PCIE MINI CARD SLOT
- 57.PCIE HALF MINI CARD SLOT
- 58.CARD BUS CONTROLLER(1/2)
- 59.CARD BUS CONTROLLER(2/2)
- 60.CARD BUS SLOT
- 61.CARD BUS POWER CONTROL
- 62.BLANK
- 63.SPI FLASH
- 64.DOCKING CONNECTOR
- 65.H8S/2116B(1/2)
- 66.H8S/2116B(2/2)
- 67.KEYBOARD CONNECTOR
- 68.TOUCH PAD CONNECTOR
- 69.WIRELESS DISABLE SW
- 70.FAN CONNECTOR
- 71.G-SENSOR
- 72.MISC G/A(PMH7)
- 73.SUPER I/O
- 74.THERMAL SENSOR
- 75.EEPROM/SMBUS SW/TPM
- 76.DC-IN
- 77.BATTERY INPUT
- 78.BATTERY CHARGER
- 79.CHARGE SELECTOR
- 80.BATTERY MONITOR
- 81.POWER SEQUENCE
- 82.DC/DC VCC5M/VCC3M
- 83.DC/DC VCCCPUCORE
- 84.VCCCPUCORE DECOUPLING
- 85.DC/DC VCCGFXCORE
- 86.BLANK
- 87.BLANK
- 88.DC/DC VCC1R5A/VCC1R05AMT
- 89.DC/DC VCC0R75AMT
- 90.DC/DC VCC1R8VIDEO
- 91.CLPWRG
- 92.DC/DC RINKAN
- 93.LOAD SW AMT&LAN
- 94.LOAD SW VIDEO
- 95.LOAD SW AUX
- 96.LOAD SW B
- 97.DC/DC VCC1R8B
- 98.LOAD SW UBAY&WWAN
- 99.CPU POWER MONITOR
- 100.EMC DECOUPLING
- 101.PTH FOR SCREW HOLES

DATE		EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
			1 / 101	

MLB3D-1

VER.1.42

SEP/28/'07

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	TITLE PAGE	
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EC HISTORY

MALIBU-3 EXT PRE-DV
BASE LOGIC MALIBU-1 EXT VER.3.11
CORONADO-5 PRE-DV EXT VER.0.31

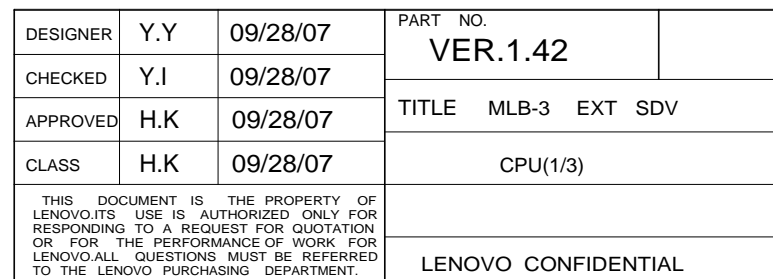
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VER.0.04 APPLY MLB3_PDV_EC027-030
VER.0.05 APPLY MLB3_PDV_EC031-036
VER.0.06 APPLY MLB3_PDV_EC037-046
VER.0.07 APPLY MLB3_PDV_EC047-054
VER.0.08 APPLY MLB3_PDV_EC055-063
VER.0.09 APPLY MLB3_PDV_EC064-065
VER.0.10 APPLY MLB3_PDV_EC066-069
VER.0.11 APPLY MLB3_PDV_EC070-078
VER.0.12 APPLY MLB3_PDV_EC079-086
VER.0.13 APPLY MLB3_PDV_EC087-091
VER.0.14 APPLY MLB3_PDV_EC092-096
VER.0.15 APPLY MLB3_PDV_EC097-103
VER.0.16 APPLY MLB3_PDV_EC104-111
VER.0.17 APPLY MLB3_PDV_EC112-122
VER.0.18 APPLY MLB3_PDV_EC123-133
VER.0.19 APPLY MLB3_PDV_EC134-143
VER.0.20 APPLY MLB3_PDV_EC144-161
VER.0.21 APPLY MLB3_PDV_EC162-174
VER.0.22 APPLY MLB3_PDV_EC175-180
VER.0.23 APPLY MLB3_PDV_EC181-182
VER.0.24 APPLY MLB3_PDV_EC183
VER.0.25 APPLY MLB3_PDV_EC184-185
VER.0.26 APPLY MLB3_PDV_EC186-190
VER.0.27 APPLY MLB3_PDV_EC191-194
VER.0.28 APPLY MLB3_PDV_EC 197
VER.0.29 APPLY MLB3_PDV_EC195,198,199

VER.1.00 APPLY MLB3_PDV_EC200,201
VER.1.01 APPLY MLB3_PDV_EC202-205
VER.1.02 APPLY MLB3_PDV_EC206-211
VER.1.03 APPLY MLB3_PDV_EC212-228
VER.1.04 APPLY MLB3_PDV_EC229-234
VER.1.05 APPLY MLB3_PDV_EC235-244
VER.1.06 APPLY MLB3_PDV_EC245-262
VER.1.07 APPLY MLB3_PDV_EC263-267
VER.1.08 APPLY MLB3_PDV_EC268-275

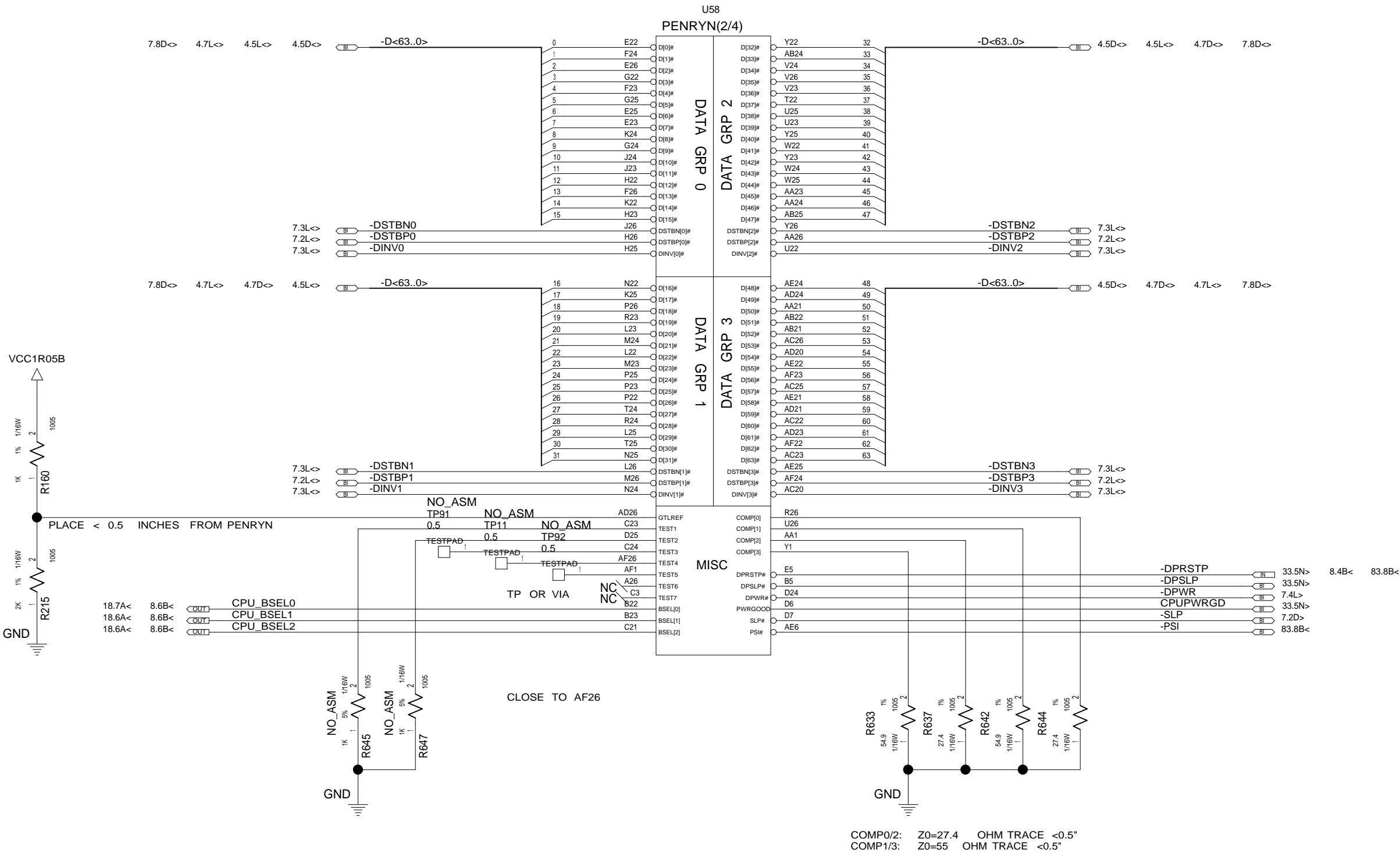
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VER.1.13 APPLY MLB3_PDV_EC298-300
VER.1.14 APPLY MLB3_PDV_EC301-304
VER.1.15 APPLY MLB3_PDV_EC305,306
VER.1.16 APPLY MLB3_PDV_EC308-313
VER.1.17 APPLY MLB3_PDV_EC314-317
VER.1.18 APPLY MLB3_PDV_EC318-320
VER.1.19 APPLY MLB3_PDV_EC321-325
VER.1.20 APPLY MLB3_PDV_EC326,327
VER.1.21 APPLY MLB3_PDV_EC328,329
VER.1.22 APPLY MLB3_PDV_EC330
VER.1.23 APPLY MLB3_PDV_EC331-333
VER.1.24 APPLY MLB3_PDV_EC334
VER.1.25 APPLY MLB3_PDV_EC335-340
VER.1.26 APPLY MLB3_PDV_EC341-343
VER.1.27 APPLY MLB3_PDV_EC345,346
VER.1.28 APPLY MLB3_PDV_EC347
VER.1.29 APPLY MLB3_PDV_EC348-350
VER.1.30 APPLY MLB3_PDV_EC351-354
VER.1.31 APPLY MLB3_PDV_EC355,356
VER.1.32 APPLY MLB3_PDV_EC357,358
VER.1.33 APPLY MLB3_PDV_EC359,360
VER.1.34 APPLY MLB3_PDV_EC361-366
VER.1.35 APPLY MLB3_PDV_EC367
VER.1.36 APPLY MLB3_PDV_EC368
VER.1.37 APPLY MLB3_PDV_EC369,370
VER.1.38 APPLY MLB3_PDV_EC371
VER.1.39 APPLY MLB3_PDV_EC372
VER.1.40 APPLY MLB3_PDV_EC374
VER.1.41 APPLY MLB3_PDV_EC375
VER.1.42 APPLY MLB3_PDV_EC376

DATE		EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				2 / 101

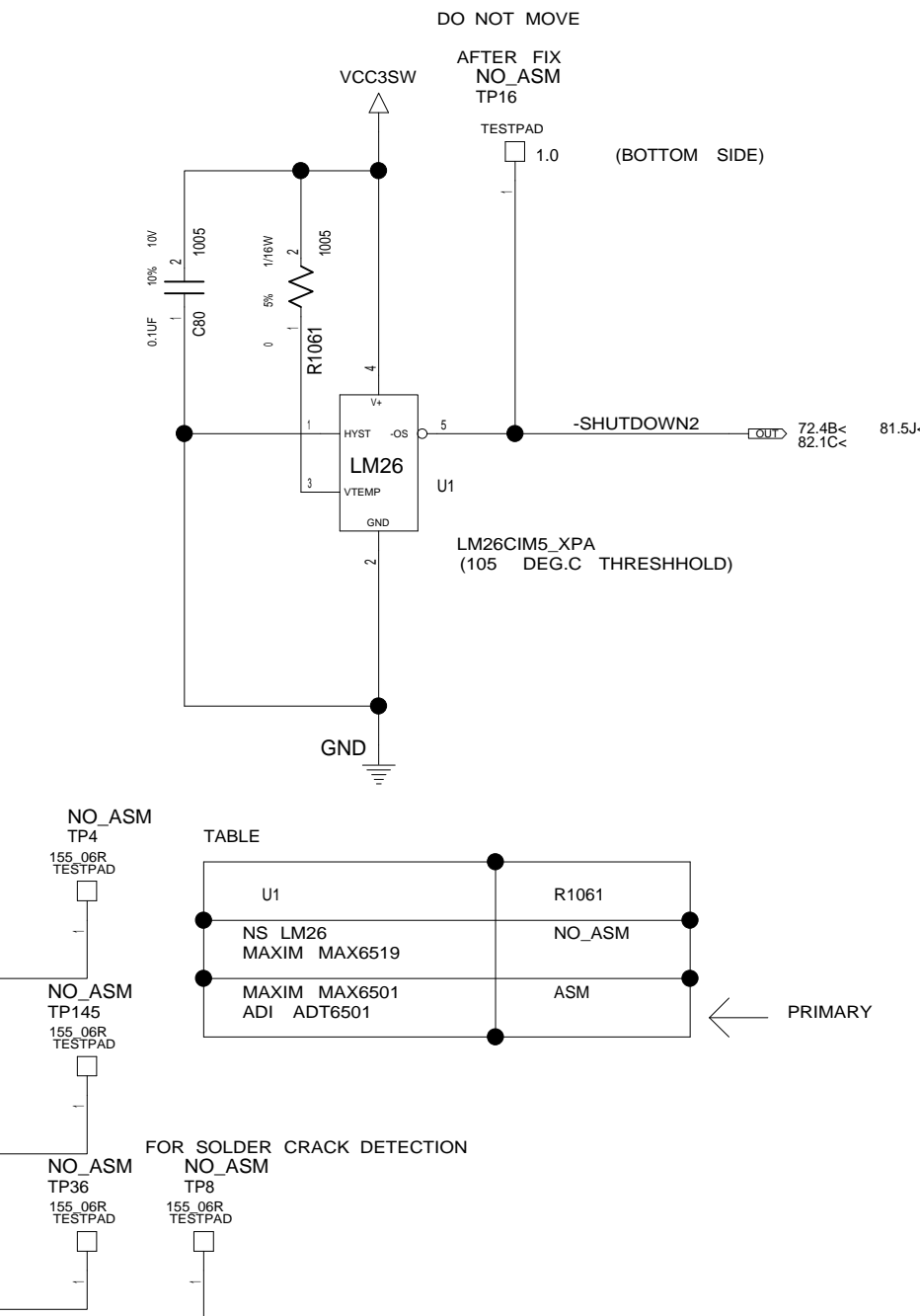
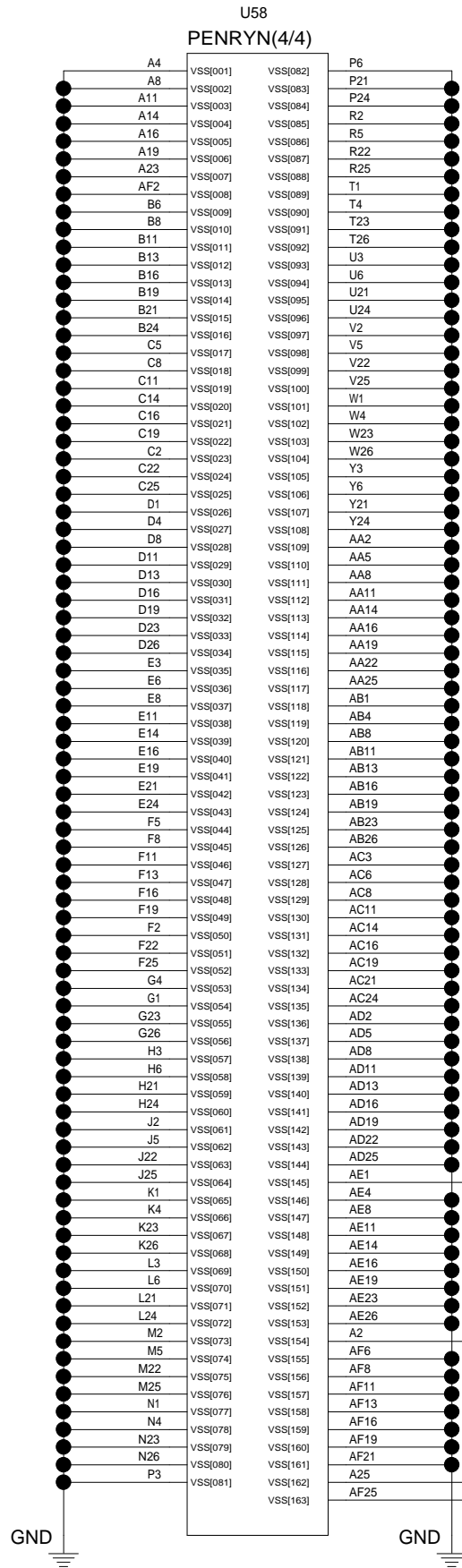
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APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	EC HISTORY	
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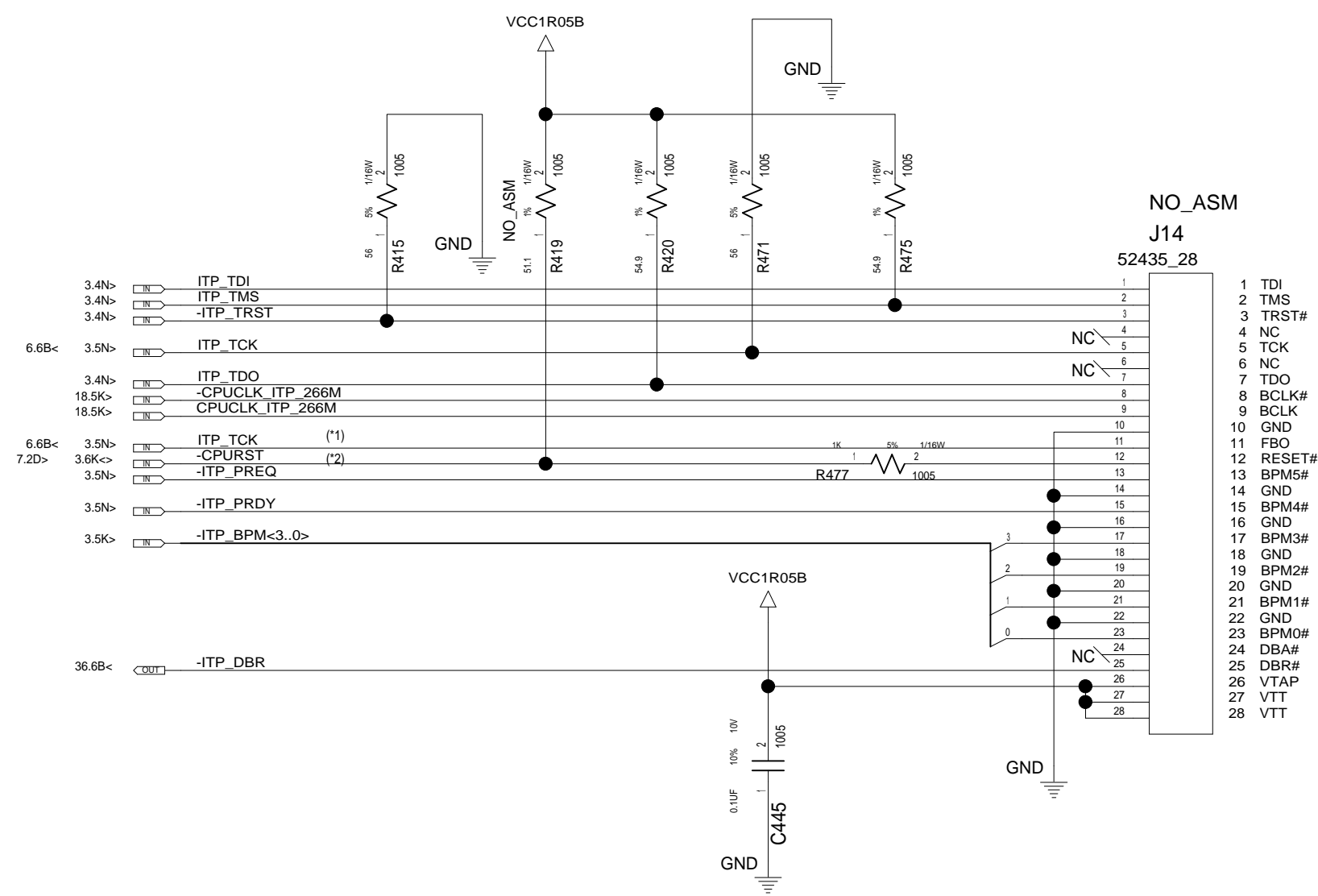
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REL		DATE	EC NO.	PART NO. VER.1.42			
				DEVELOPMENT NO.		Q/M	
						4 / 101	



DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CPU(2/3)	
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	DATE	EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				6 / 101



- J14
52435_28
- 1 TDI
 - 2 TMS
 - 3 TRST#
 - 4 NC
 - 5 TCK
 - 6 NC
 - 7 TDO
 - 8 BCLK#
 - 9 BCLK
 - 10 GND
 - 11 FBO
 - 12 RESET#
 - 13 BPM5#
 - 14 GND
 - 15 BPM4#
 - 16 GND
 - 17 BPM3#
 - 18 GND
 - 19 BPM2#
 - 20 GND
 - 21 BPM1#
 - 22 GND
 - 23 BPM0#
 - 24 DBA#
 - 25 DBR#
 - 26 VTAP
 - 27 VTT
 - 28 VTT

TABLE

REF	DES	FOR ITP700FLEX	FOR ITP-XDP
J14	NO_ASM ---> ASM		NO_ASM ---> ASM
C445	ASM(NO CHANGE)		ASM(NO CHANGE)
R717	ASM ---> 150 5% ASM		ASM(NO CHANGE)
R477	ASM ---> 22.6 1% ASM		ASM(NO CHANGE)
R415	ASM(NO CHANGE)		ASM(NO CHANGE)
R419	NO_ASM ---> ASM		NO_ASM(NO CHANGE)
R420	ASM(NO CHANGE)		ASM(NO CHANGE)
R471	ASM ---> 27.4 1% ASM		ASM(NO CHANGE)
R475	ASM ---> 39.2 1% ASM		ASM(NO CHANGE)

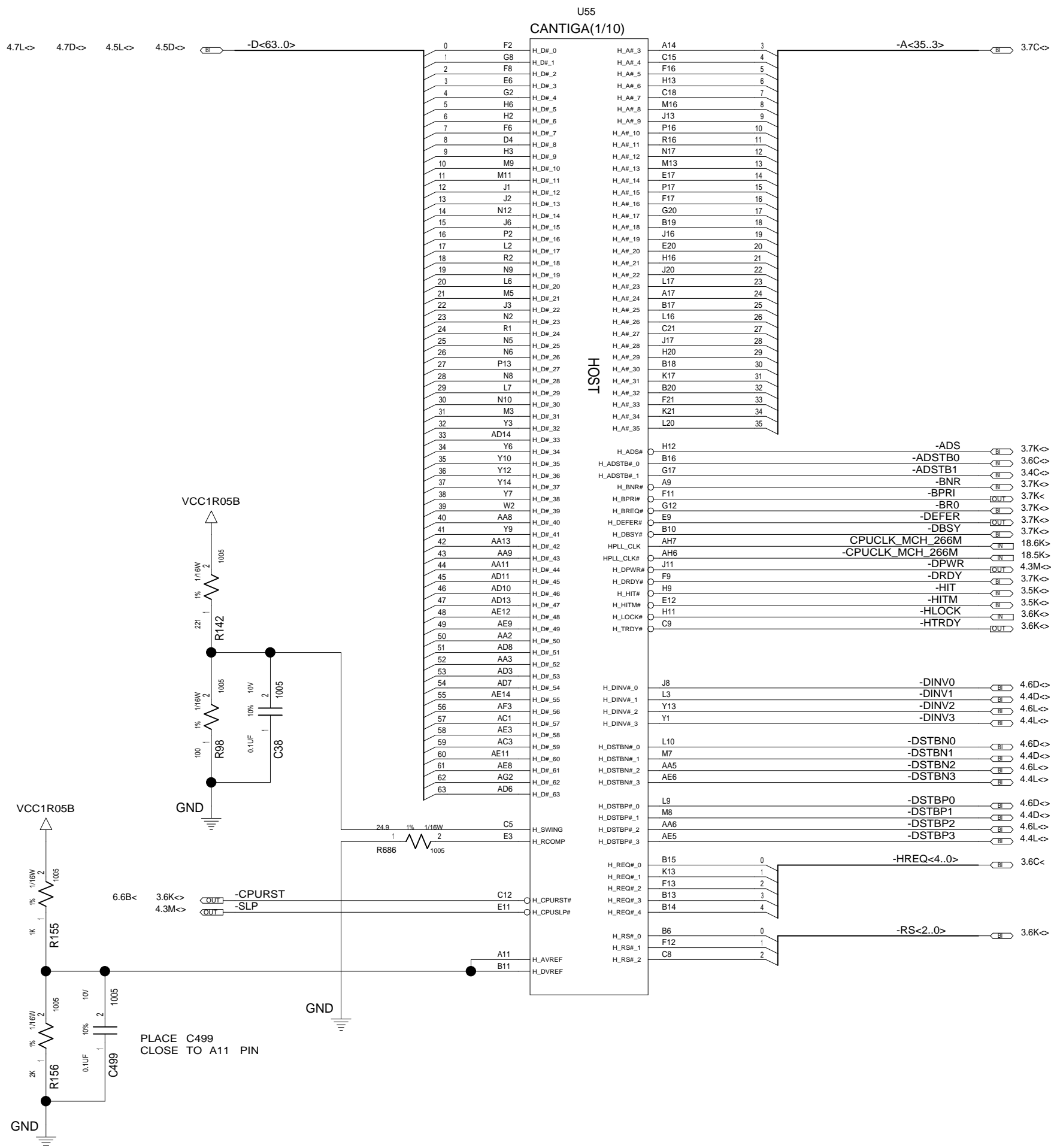
NOTES : C445 AND R477 "ASM" BEFORE SIT. "NO_ASM" FROM SIT.

(*1) TCK SIGNAL IS BRANCHED AT PENRYN PIN

(*2) -CPURST SIGNAL IS BRANCHED AT CANTIGA PIN

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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	ITP CONNECTOR	
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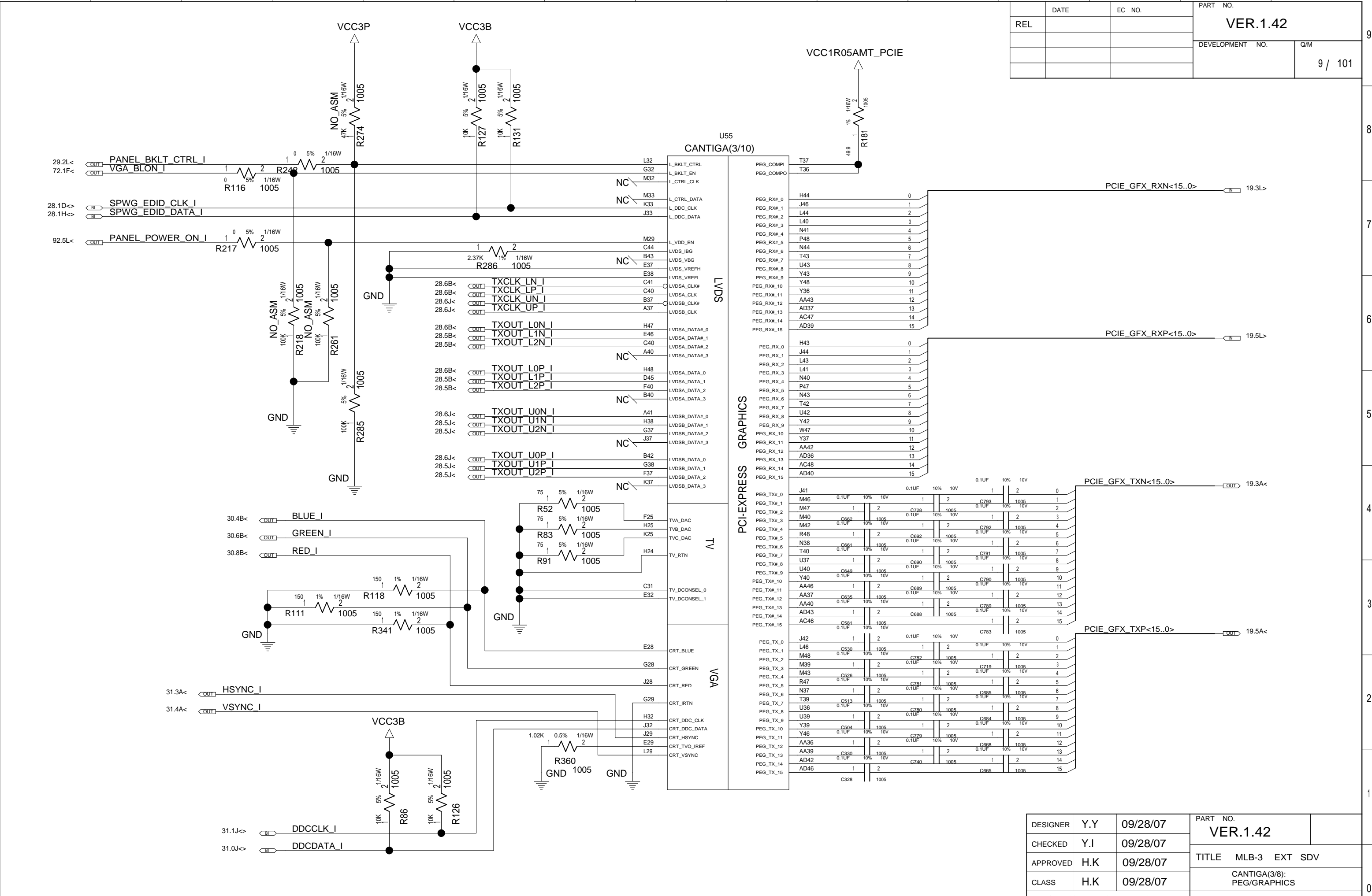
REL	DATE	EC NO.	PART NO.	
			VER.1.42	
			DEVELOPMENT NO.	Q/M
				7 / 101



VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CANTIGA(1/8):HOST I/F	
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	Q/M
					9 / 101



VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CANTIGA(3/8): PEG/GRAPHICS	
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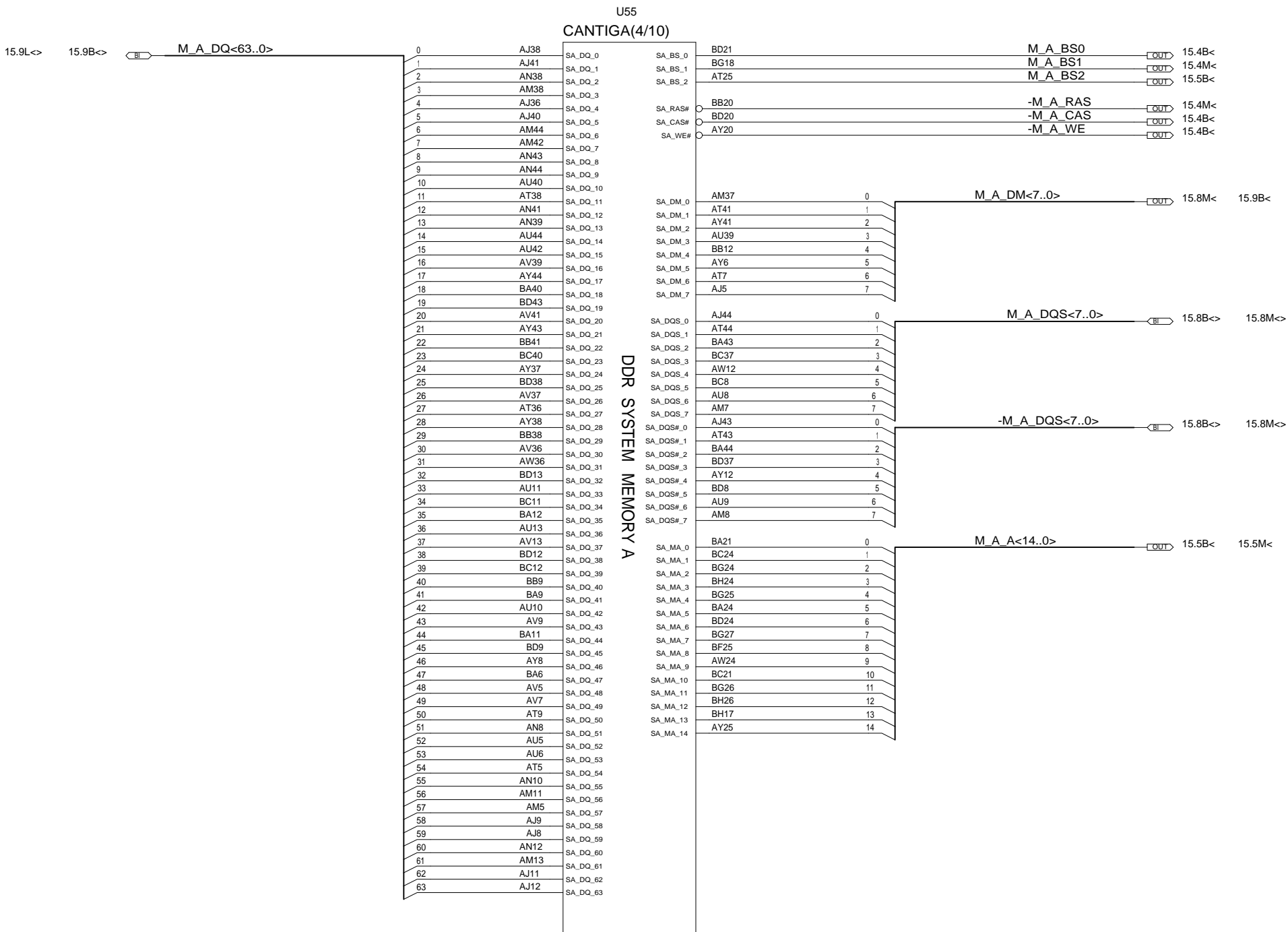
PART NO.

VER.1.42

DEVELOPMENT NO.

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10 / 101



VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE	MLB-3 EXT SDV
CLASS	H.K	09/28/07	CANTIGA(4/8): DDR3 CHANNEL-A	
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PART NO.

VER.1.42

DEVELOPMENT NO.

Q/M

11 / 101

U55
CANTIGA(5/10)

16.9L<> 16.9B<> M_B_DQ<63..0>

0	AK47	SB_DQ_0
1	AH46	SB_DQ_1
2	AP47	SB_DQ_2
3	AP46	SB_DQ_3
4	AJ46	SB_DQ_4
5	AJ48	SB_DQ_5
6	AM48	SB_DQ_6
7	AP48	SB_DQ_7
8	AU47	SB_DQ_8
9	AU46	SB_DQ_9
10	BA48	SB_DQ_10
11	AY48	SB_DQ_11
12	AT47	SB_DQ_12
13	AR47	SB_DQ_13
14	BA47	SB_DQ_14
15	BC47	SB_DQ_15
16	BC46	SB_DQ_16
17	BC44	SB_DQ_17
18	BG43	SB_DQ_18
19	BF43	SB_DQ_19
20	BE45	SB_DQ_20
21	BC41	SB_DQ_21
22	BF40	SB_DQ_22
23	BF41	SB_DQ_23
24	BG38	SB_DQ_24
25	BF38	SB_DQ_25
26	BH35	SB_DQ_26
27	BG35	SB_DQ_27
28	BH40	SB_DQ_28
29	BG39	SB_DQ_29
30	BG34	SB_DQ_30
31	BH34	SB_DQ_31
32	BH14	SB_DQ_32
33	BG12	SB_DQ_33
34	BH11	SB_DQ_34
35	BG8	SB_DQ_35
36	BH12	SB_DQ_36
37	BF11	SB_DQ_37
38	BF8	SB_DQ_38
39	BG7	SB_DQ_39
40	BC5	SB_DQ_40
41	BC6	SB_DQ_41
42	AY3	SB_DQ_42
43	AY1	SB_DQ_43
44	BF6	SB_DQ_44
45	BF5	SB_DQ_45
46	BA1	SB_DQ_46
47	BD3	SB_DQ_47
48	AV2	SB_DQ_48
49	AU3	SB_DQ_49
50	AR3	SB_DQ_50
51	AN2	SB_DQ_51
52	AY2	SB_DQ_52
53	AV1	SB_DQ_53
54	AP3	SB_DQ_54
55	AR1	SB_DQ_55
56	AL1	SB_DQ_56
57	AL2	SB_DQ_57
58	AJ1	SB_DQ_58
59	AH1	SB_DQ_59
60	AM2	SB_DQ_60
61	AM3	SB_DQ_61
62	AH3	SB_DQ_62
63	AJ3	SB_DQ_63

DDR SYSTEM MEMORY B

BC16	M_B_BS0	OUT	16.4B<	
BB17	M_B_BS1	OUT	16.4M<	
BB33	M_B_BS2	OUT	16.5B<	
AU17	-M_B_RAS	OUT	16.4M<	
BG16	-M_B_CAS	OUT	16.4B<	
BF14	-M_B_WE	OUT	16.4B<	
AM47	0	M_B_DM<7..0>	OUT	16.8M< 16.9B<
AY47	1			
BD40	2			
BF35	3			
BG11	4			
BA3	5			
AP1	6			
AK2	7			
AL47	0	M_B_DQS<7..0>	B	16.8B<> 16.8M<>
AV48	1			
BG41	2			
BG37	3			
BH9	4			
BB2	5			
AU1	6			
AN6	7			
AL46	0	-M_B_DQS<7..0>	B	16.8B<> 16.8M<>
AV47	1			
BH41	2			
BH37	3			
BG9	4			
BC2	5			
AT2	6			
AN5	7			
AV17	0	M_B_A<14..0>	OUT	16.5B< 16.5M<
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BC25	2			
AU25	3			
AW25	4			
BB28	5			
AU28	6			
AW28	7			
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BD33	9			
BB16	10			
AW33	11			
AY33	12			
BH15	13			
AU33	14			

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	VER.1.42
CHECKED	Y.I	09/28/07	TITLE	MLB-3 EXT SDV
APPROVED	H.K	09/28/07	CANTIGA(5/8):	DDR3 CHANNEL-B
CLASS	H.K	09/28/07	THIS DOCUMENT IS THE PROPERTY OF LENOVO.ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO A REQUEST FOR QUOTATION OR FOR THE PERFORMANCE OF WORK FOR LENOVO.ALL QUESTIONS MUST BE REFERRED TO THE LENOVO PURCHASING DEPARTMENT.	LENOVO CONFIDENTIAL

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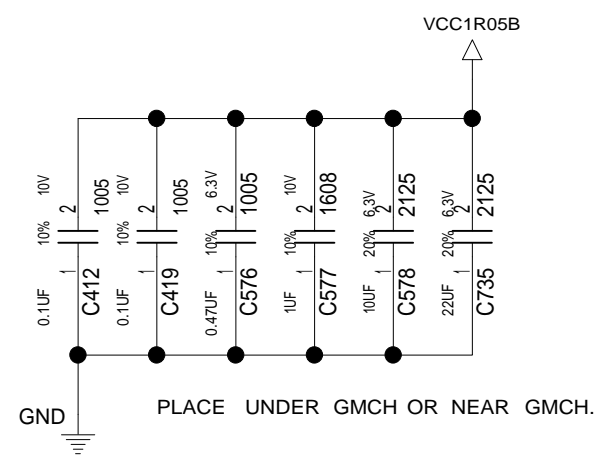
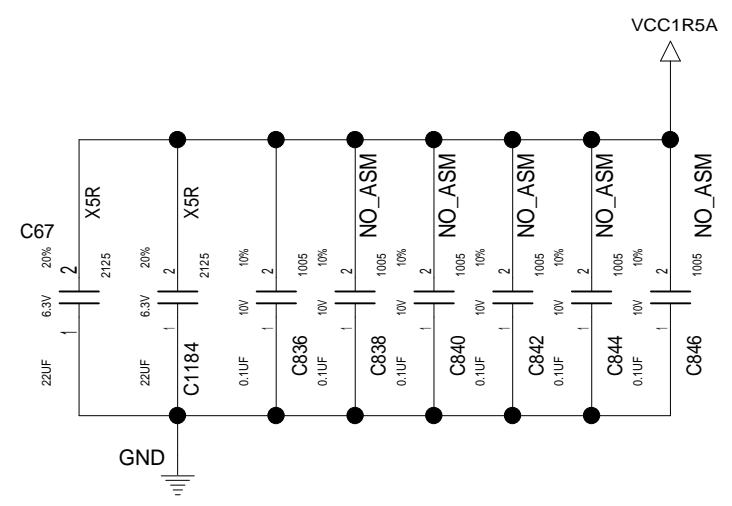
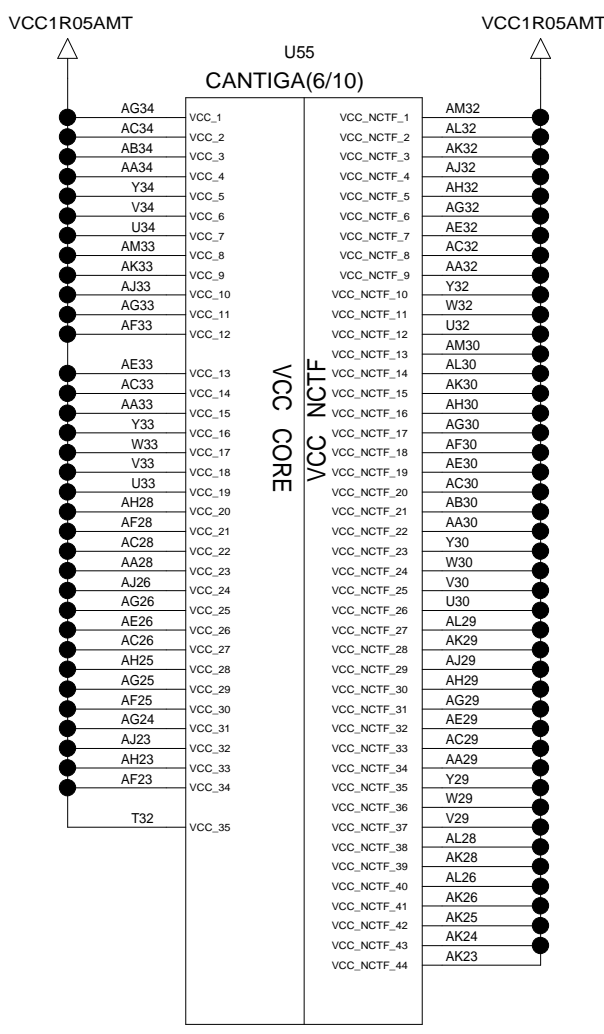
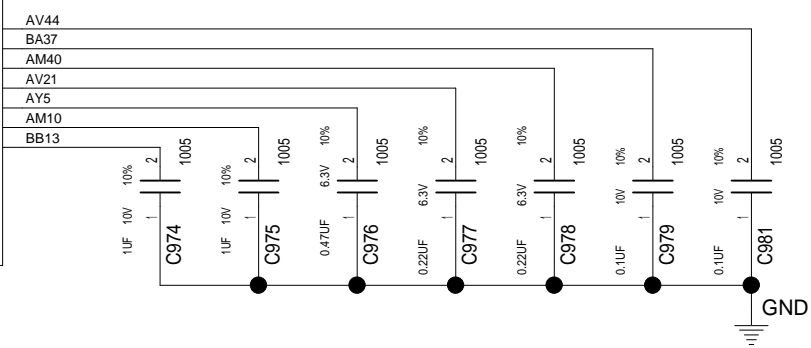
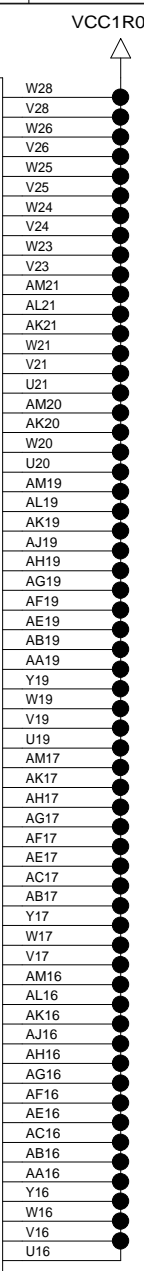
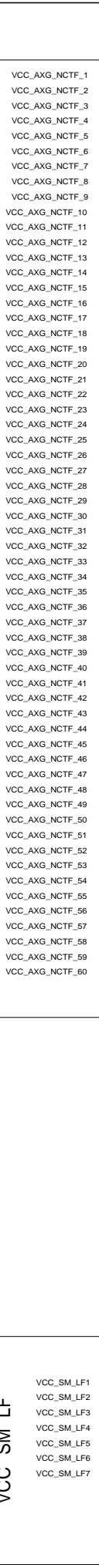
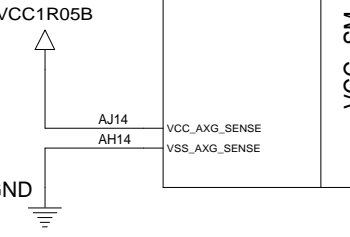
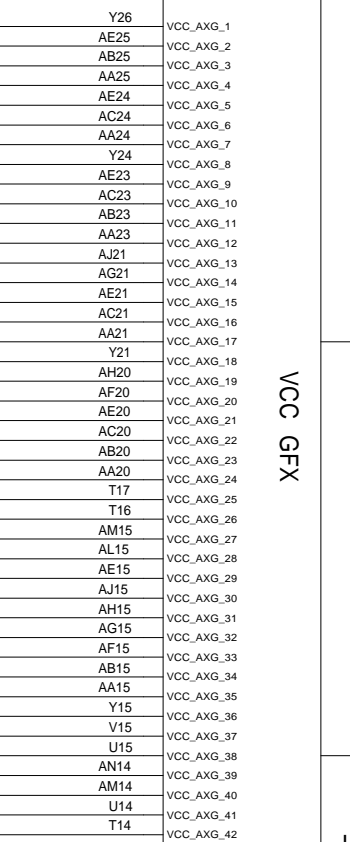
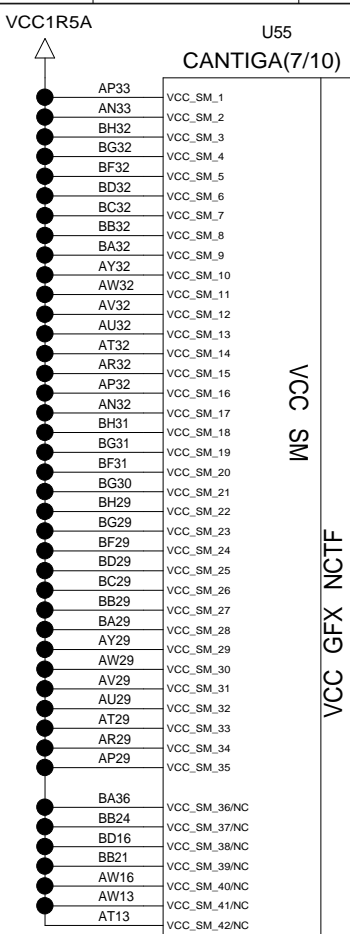
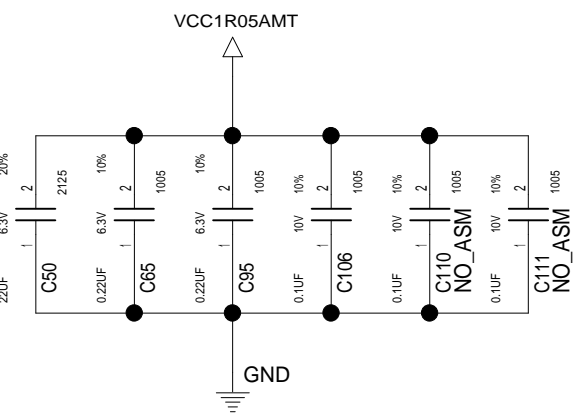
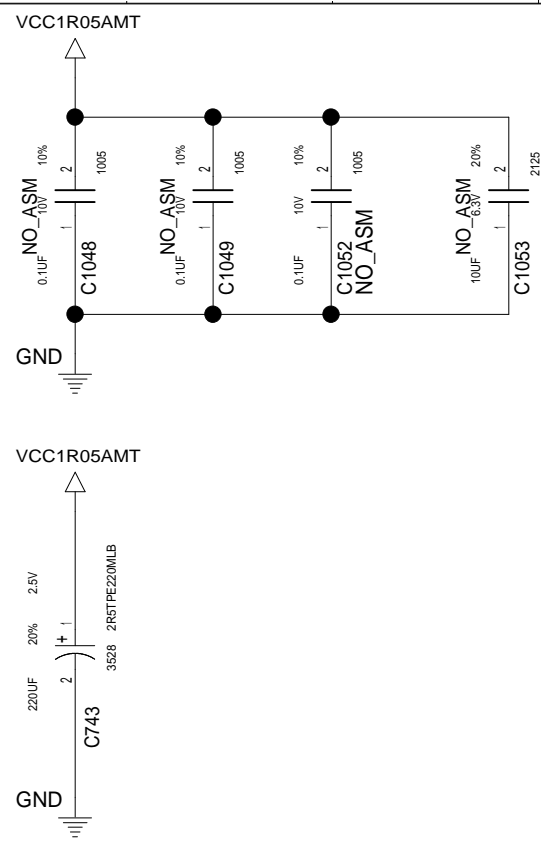
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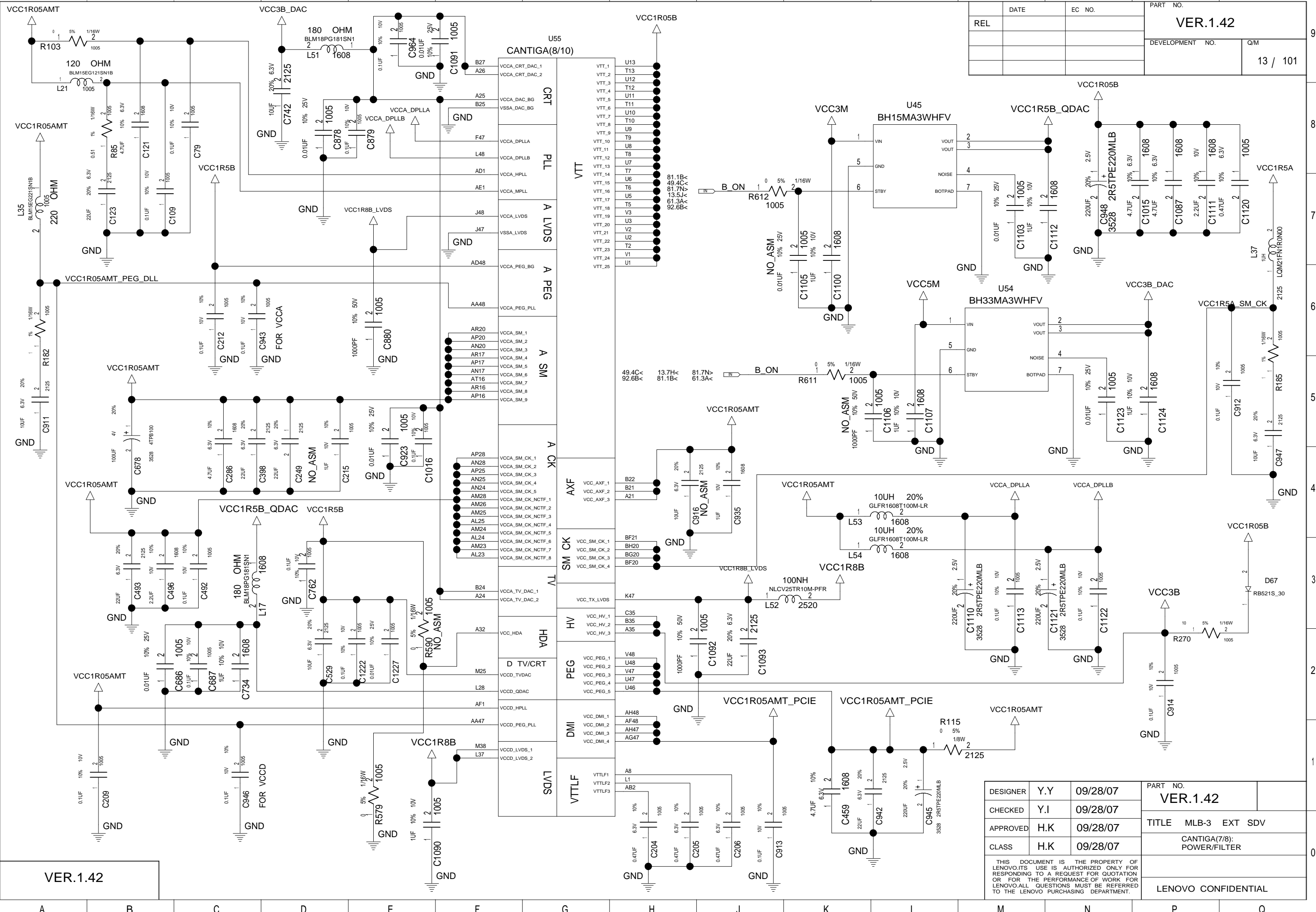
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VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	VER.1.42
CHECKED	Y.I	09/28/07	TITLE	MLB-3 EXT SDV
APPROVED	H.K	09/28/07	CANTIGA(6/8):POWER	
CLASS	H.K	09/28/07	LENOVO CONFIDENTIAL	

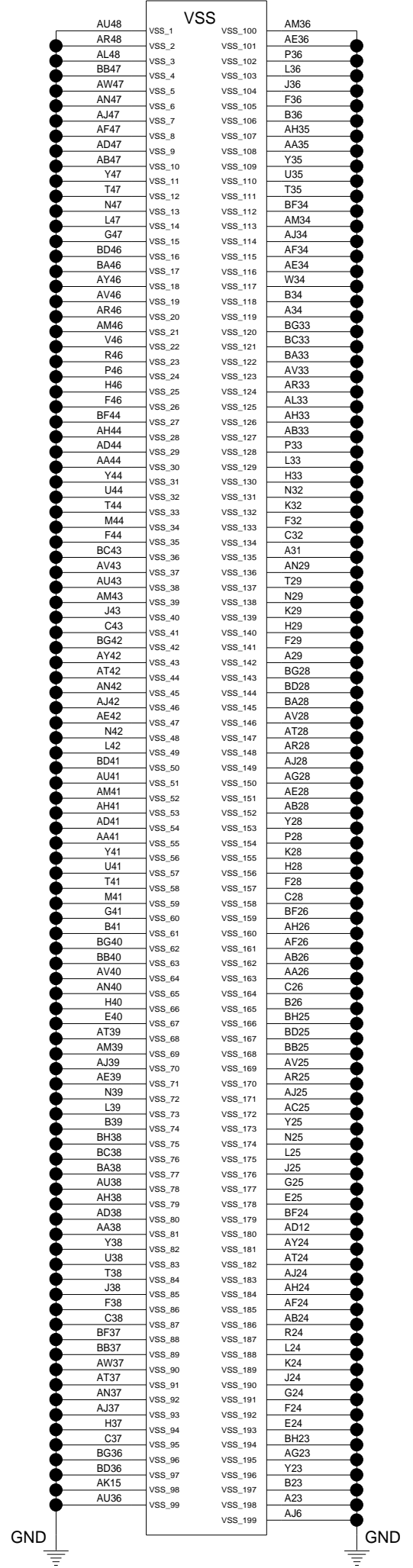
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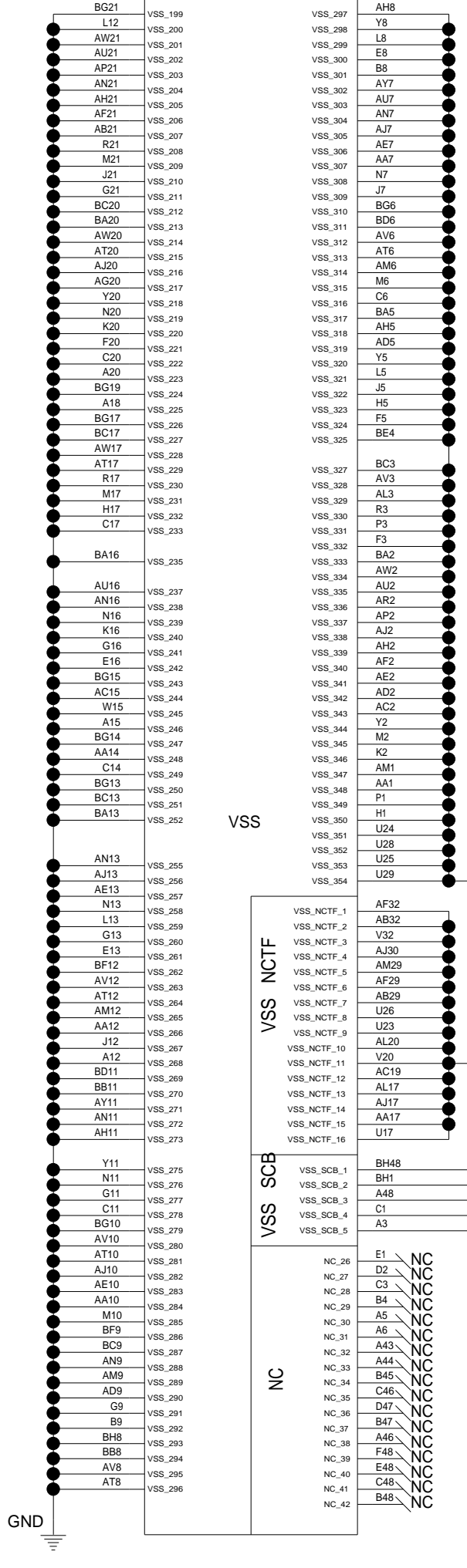
REL	DATE	EC NO.	PART NO.
			VER.1.42
			DEVELOPMENT NO.
			Q/M
			13 / 101

DESIGNER	Y.Y	09/28/07	PART NO.
CHECKED	Y.I	09/28/07	VER.1.42
APPROVED	H.K	09/28/07	TITLE
CLASS	H.K	09/28/07	MLB-3 EXT SDV
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			LENOVO CONFIDENTIAL

U55 CANTIGA(9/10)

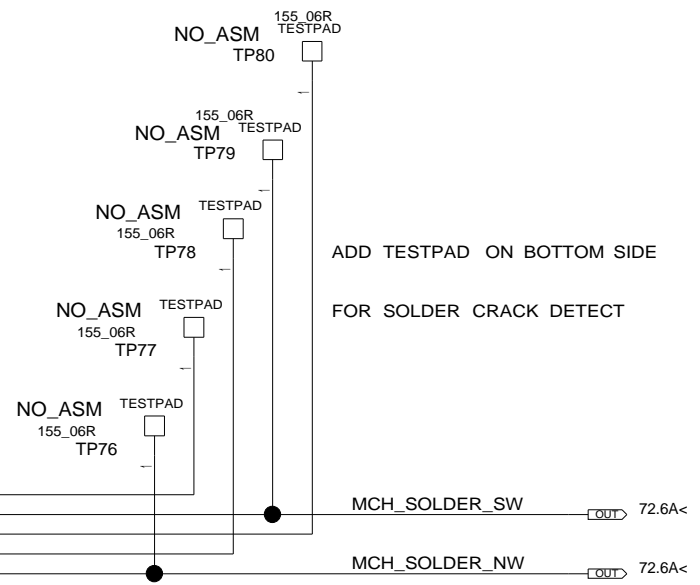
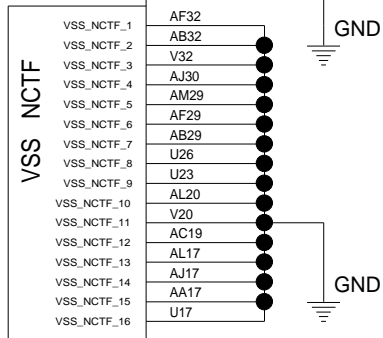


U55 CANTIGA(10/10)



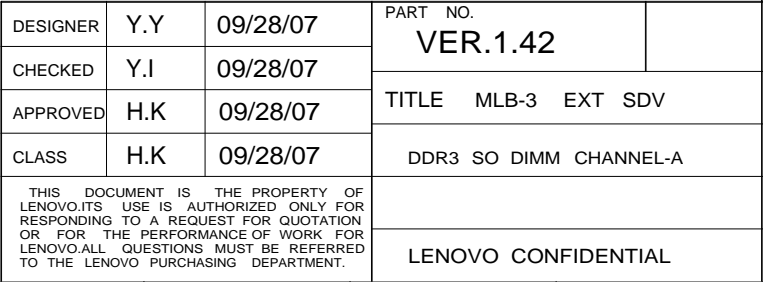
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REL			VER.1.42	
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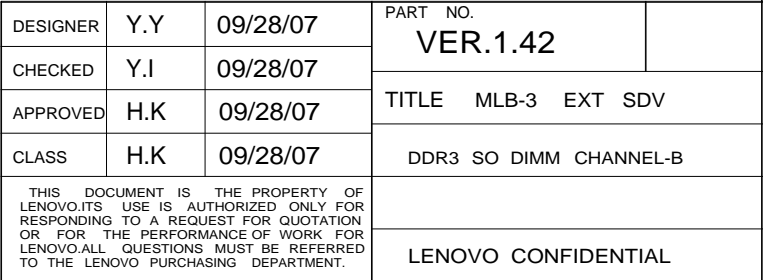
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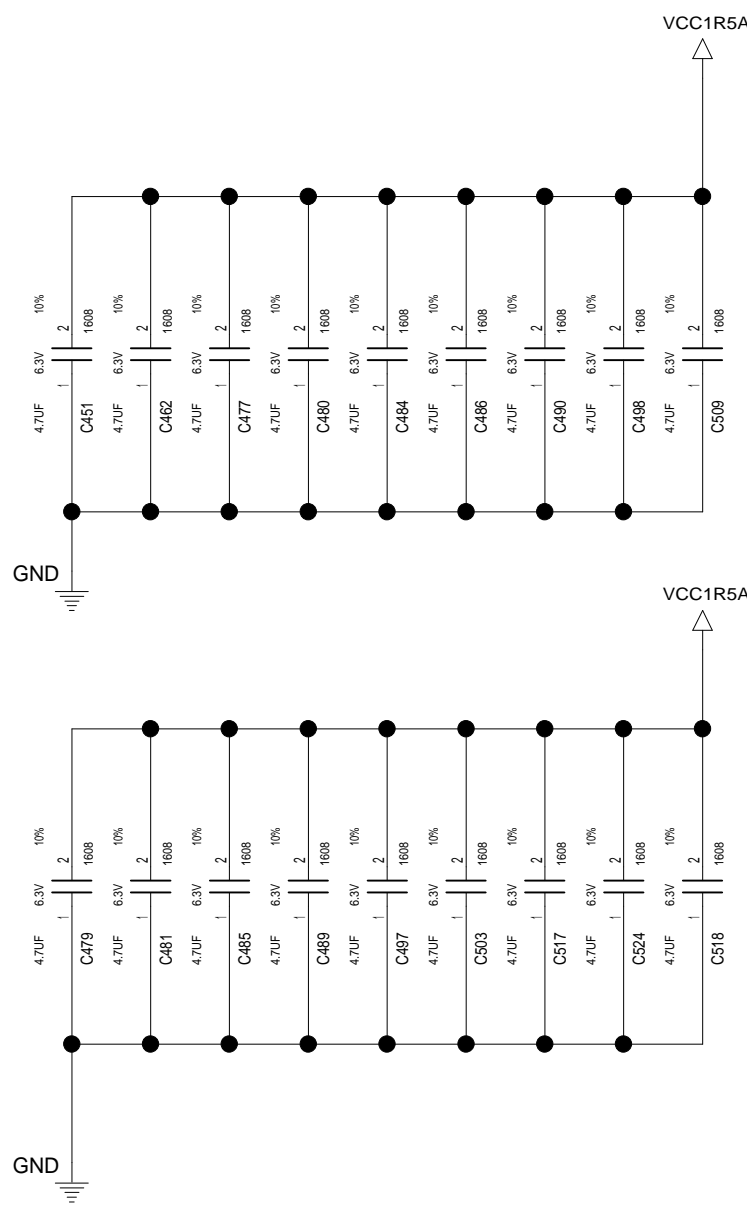
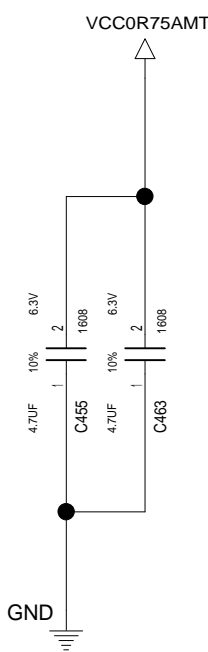
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CANTIGA(8/8):GND	
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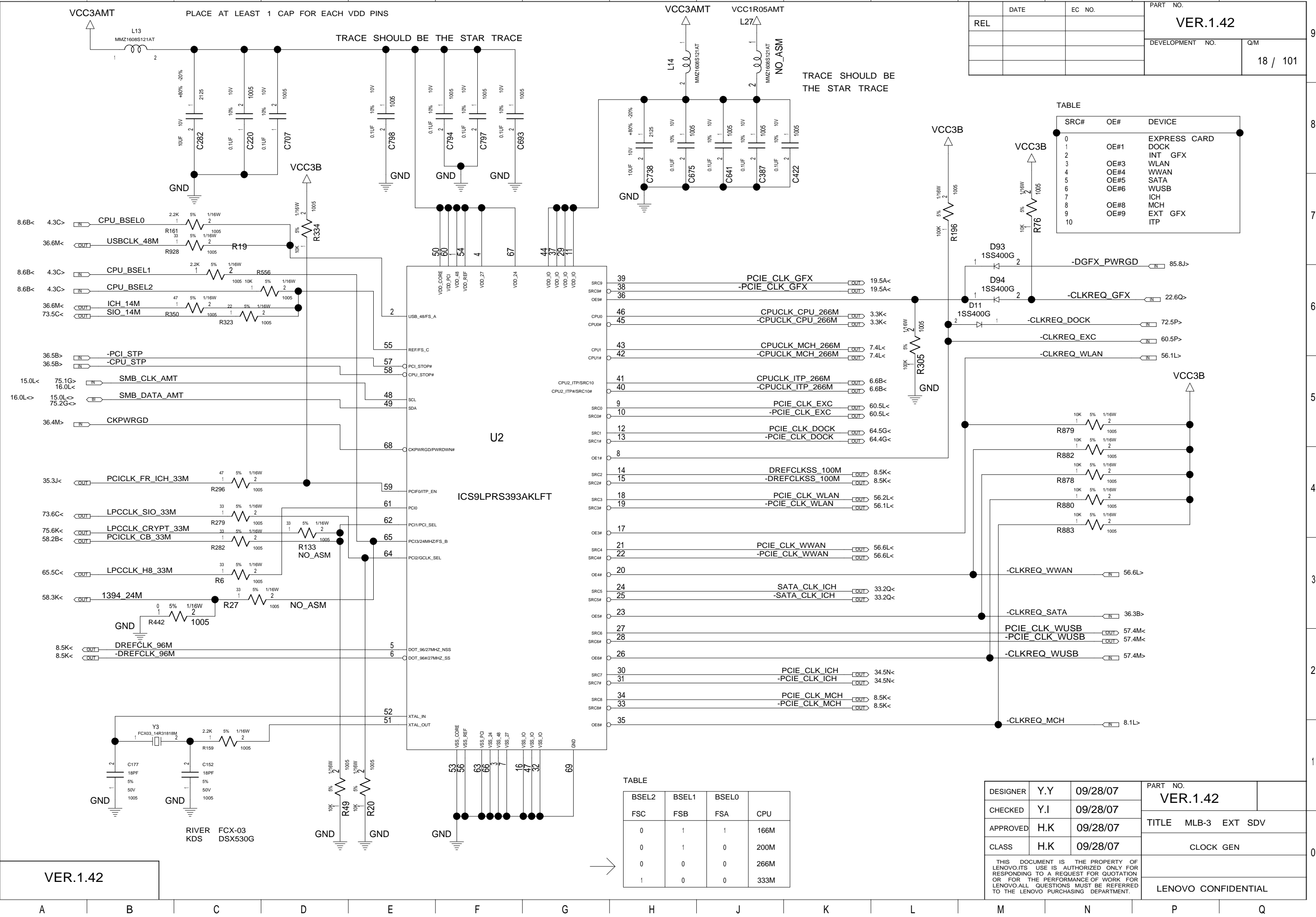


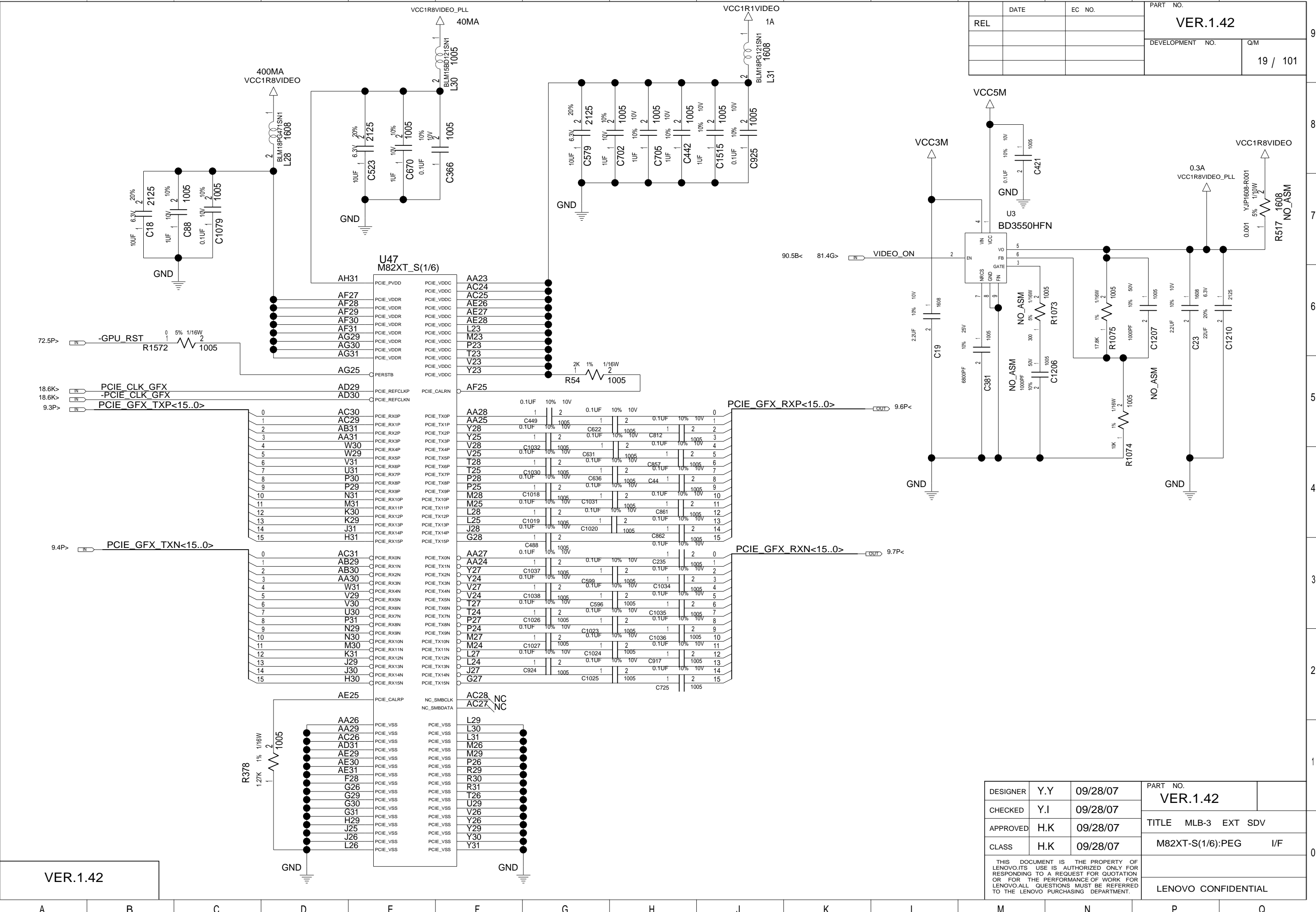


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REL			VER.1.42	
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				17 / 101

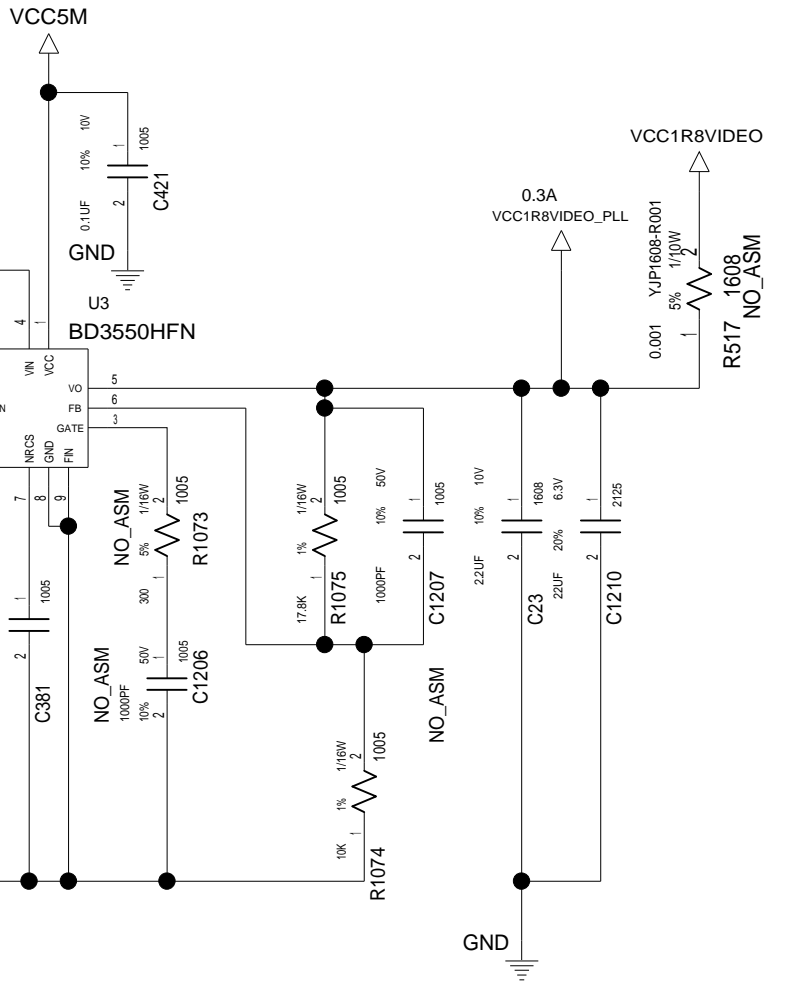


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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	DDR3 DECOUPLING	
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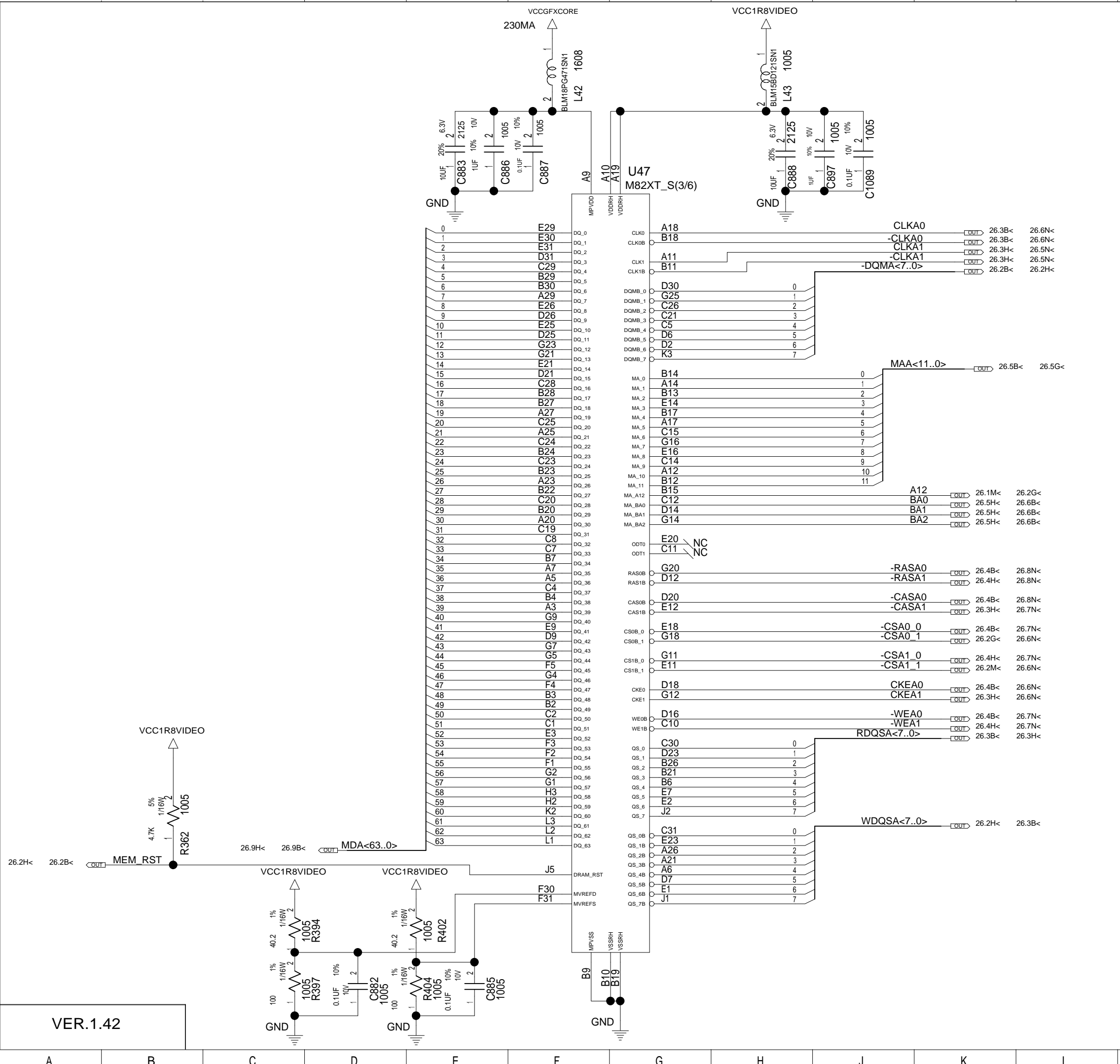
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DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	M82XT-S(1/6):PEG I/F	
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DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	M82XT-S(3/6):VRAM I/F	
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LOGIC

M82XT-S(4/6):GPIO

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DESIGNER	Y.Y	09/28/07	PART NO. VER.1.42	
CHECKED	Y.I	09/28/07		
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	M82XT-S(5/6):POWER	

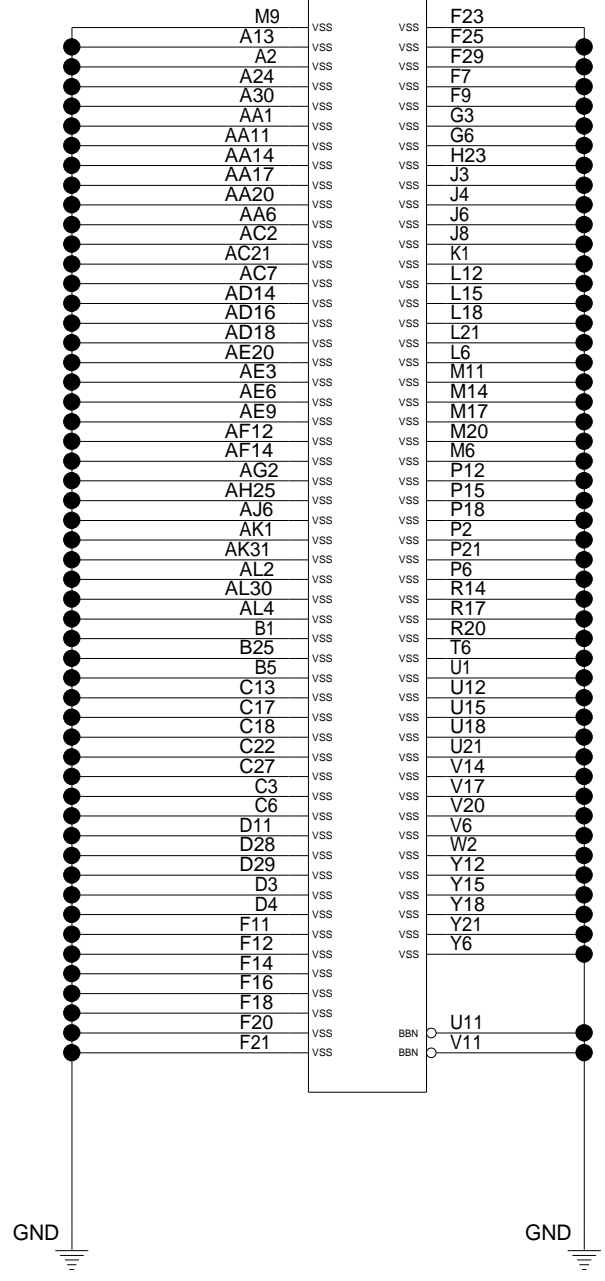


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	DATE	EC NO.	PART NO.	
REL			VER.1.42	
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				24 / 101

U47

M82XT_S(6/6)



DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	M82XT-S(6/6):GND	
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	DATE	EC NO.	PART NO.	
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DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
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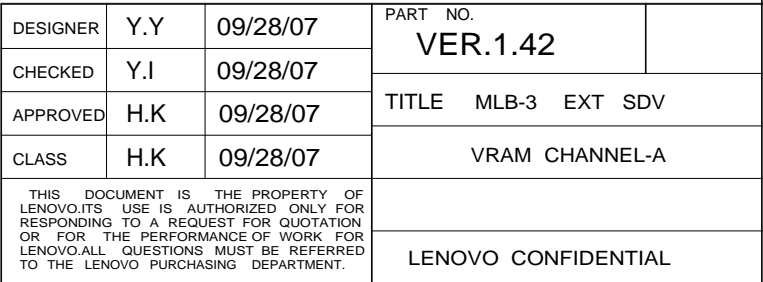
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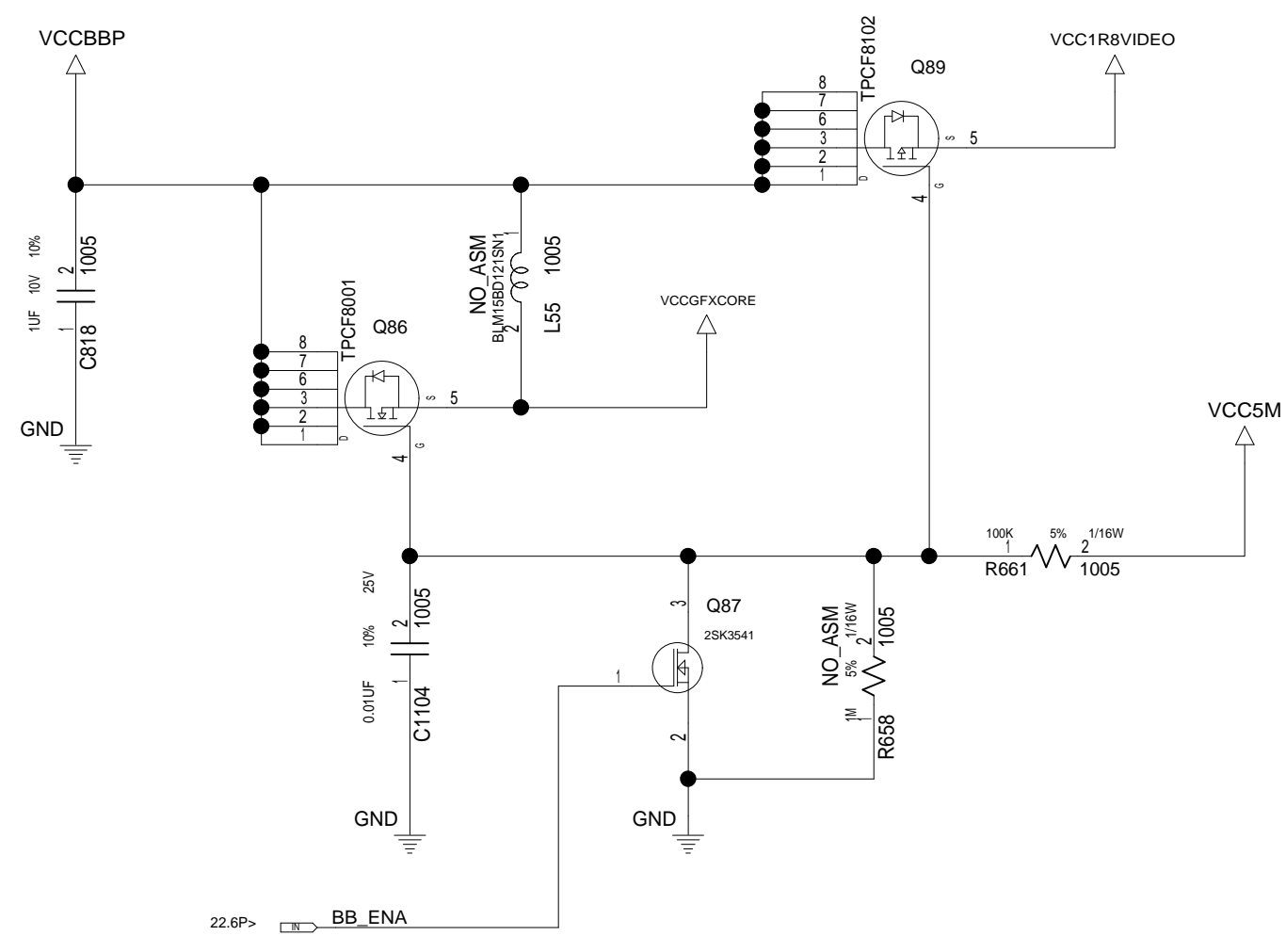
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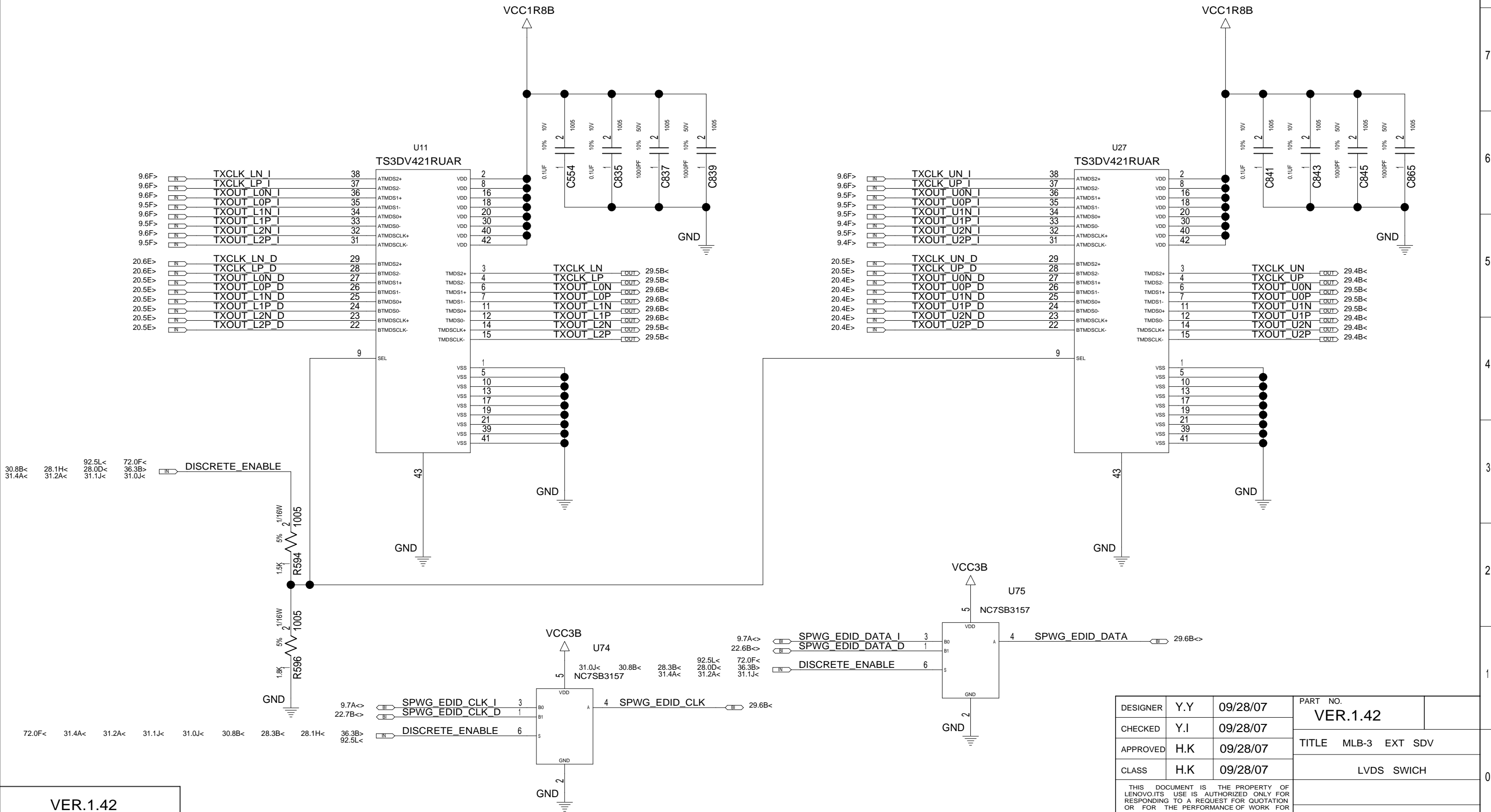


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REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				27 / 101



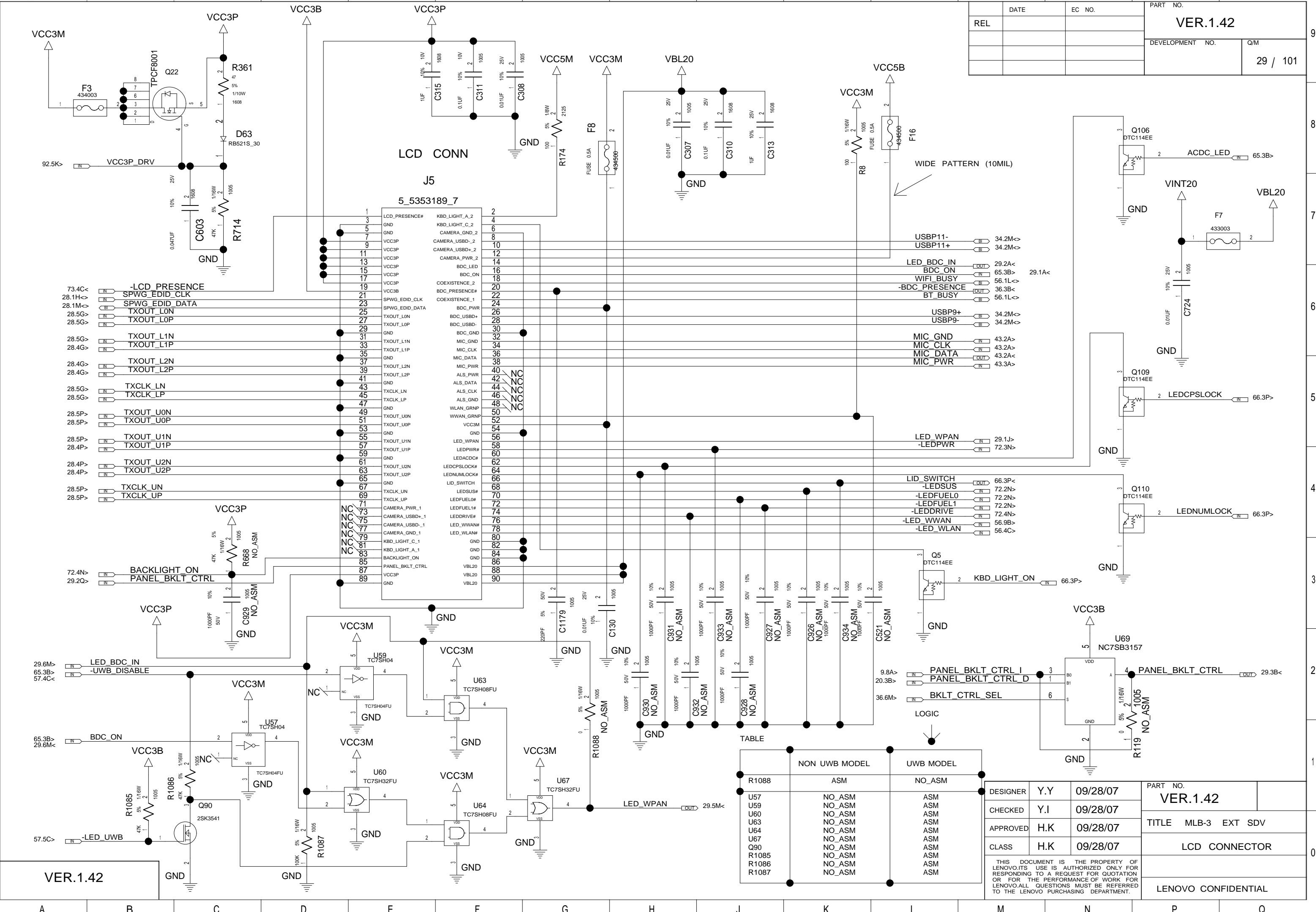
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CLASS	H.K	09/28/07	BACK BIAS POWER	
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REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				28 / 101



VER.1.42

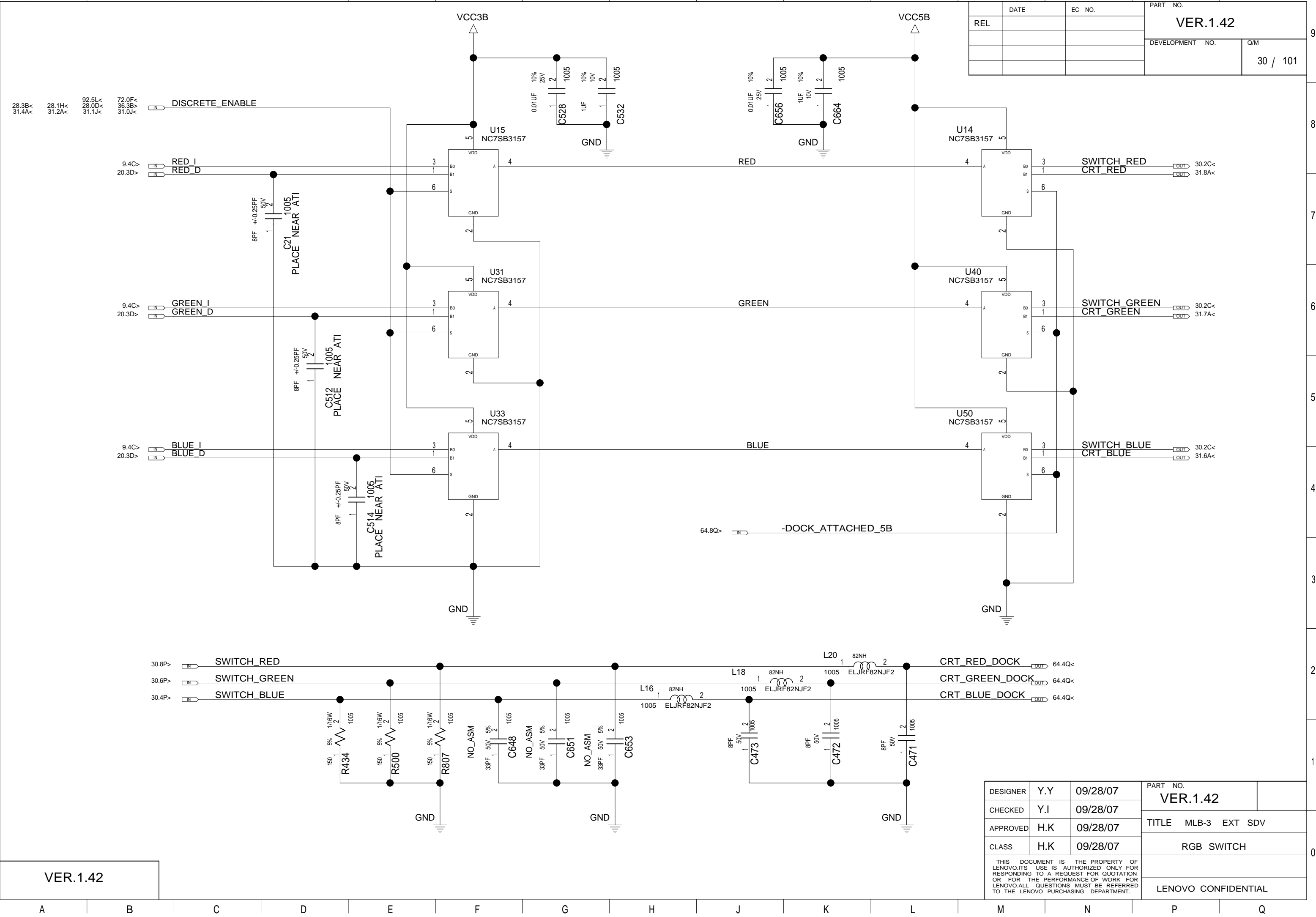
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CLASS	H.K	09/28/07	LVDS SWICH	
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			VER.1.42
			DEVELOPMENT NO.
			Q/M
			29 / 101

DESIGNER	Y.Y	09/28/07	PART NO.
CHECKED	Y.I	09/28/07	VER.1.42
APPROVED	H.K	09/28/07	TITLE
CLASS	H.K	09/28/07	MLB-3 EXT SDV
			LCD CONNECTOR
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REL				VER.1.42	
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				Q/M	
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VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	RGB SWITCH	
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GND GUARDING
EACH SIGNAL WIDTH=4MIL,
SPACING=8MIL

30.7P>

CRT_RED

30.6P>

CRT_GREEN

30.4P>

CRT_BLUE

VCCCRT

NEAR CRT CONN

CRT CONN

J31

DZ11A91_WH41

MF-NSMF150-2
NANOSMDC150F

F1

VCC5B

VCCCRT

DATE

EC NO.

PART NO.

VER.1.42

DEVELOPMENT NO.

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9.2C>

VSYNC_I

28.1H<

36.3B<

28.0D<

28.3B<

30.8B<

31.0J<

31.1J<

31.2A<

72.0F<

92.5L<

DISCRETE_ENABLE

9.2C>

HSYNC_I

72.0F<

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30.8B<

28.1H<

36.3B<

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VSYNC_D

DISCRETE_ENABLE

20.2D>

HSYNC_D

VER.1.42

VCC5B

VCCCRT

9.1C<
31.4A<
28.1H<
31.0J<
92.5L<
31.2A<
28.0D<
30.8B<
72.0F<
9.0C<
22.1F<
36.3B<
28.3B<
28.1H<
30.8B<
28.3B<
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28.3B<
28.1H<
31.1J<
92.5L<

CRT_VSYNC_DOCK
CART_HSYNC_DOCK

DDCLK_I
DDCLK_D
DISCRETE_ENABLE
DDCDATA_I
DDCDATA_D
DISCRETE_ENABLE

DESIGNER Y.Y 09/28/07
CHECKED Y.I 09/28/07
APPROVED H.K 09/28/07
CLASS H.K 09/28/07

PART NO.
VER.1.42
TITLE MLB-3 EXT SDV
EXT CRT INTERFACE

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	DATE	EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	
			Q/M	
			32 / 101	

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VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
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CLASS	H.K	09/28/07	BLANK	
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			LENOVO CONFIDENTIAL	

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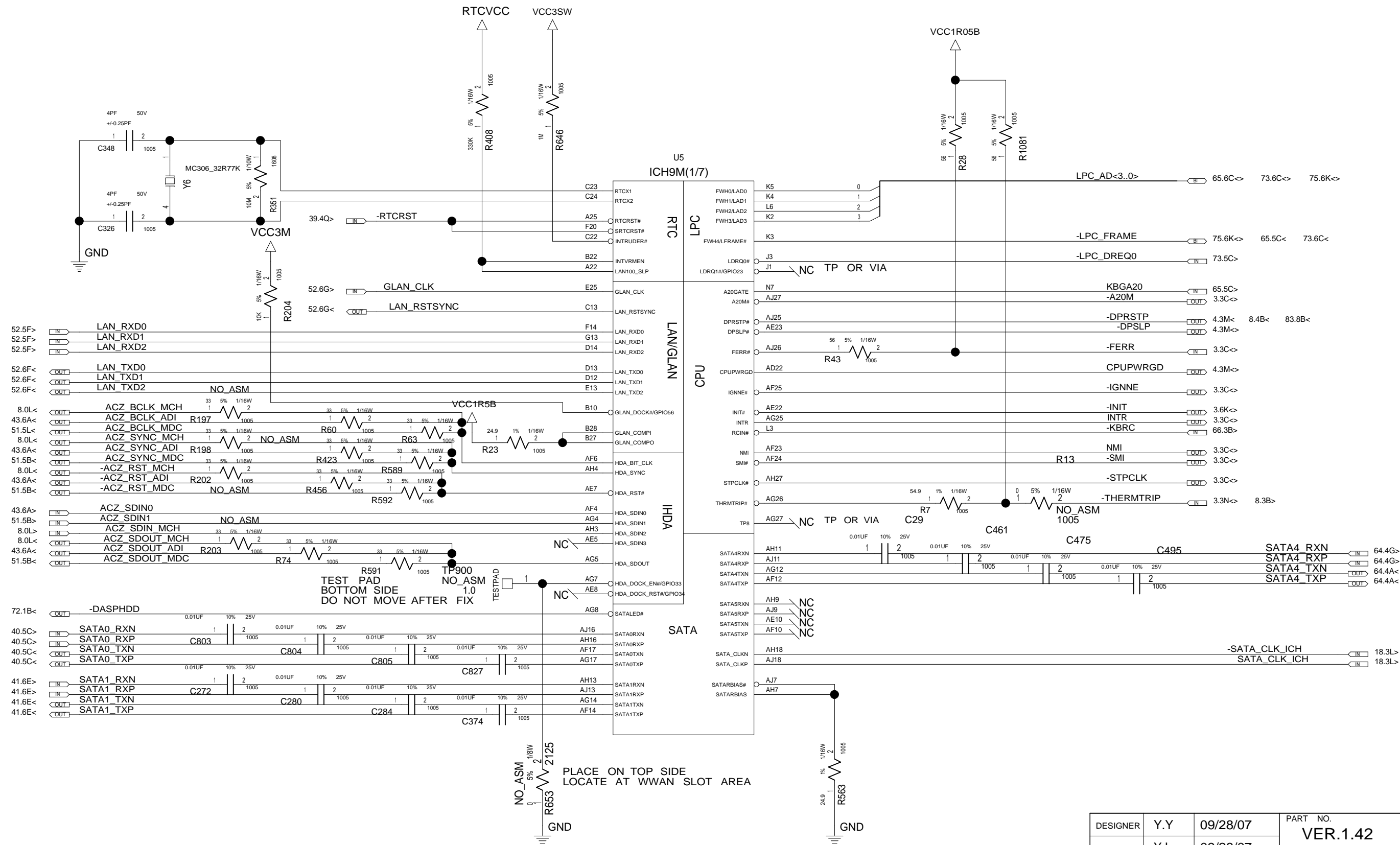
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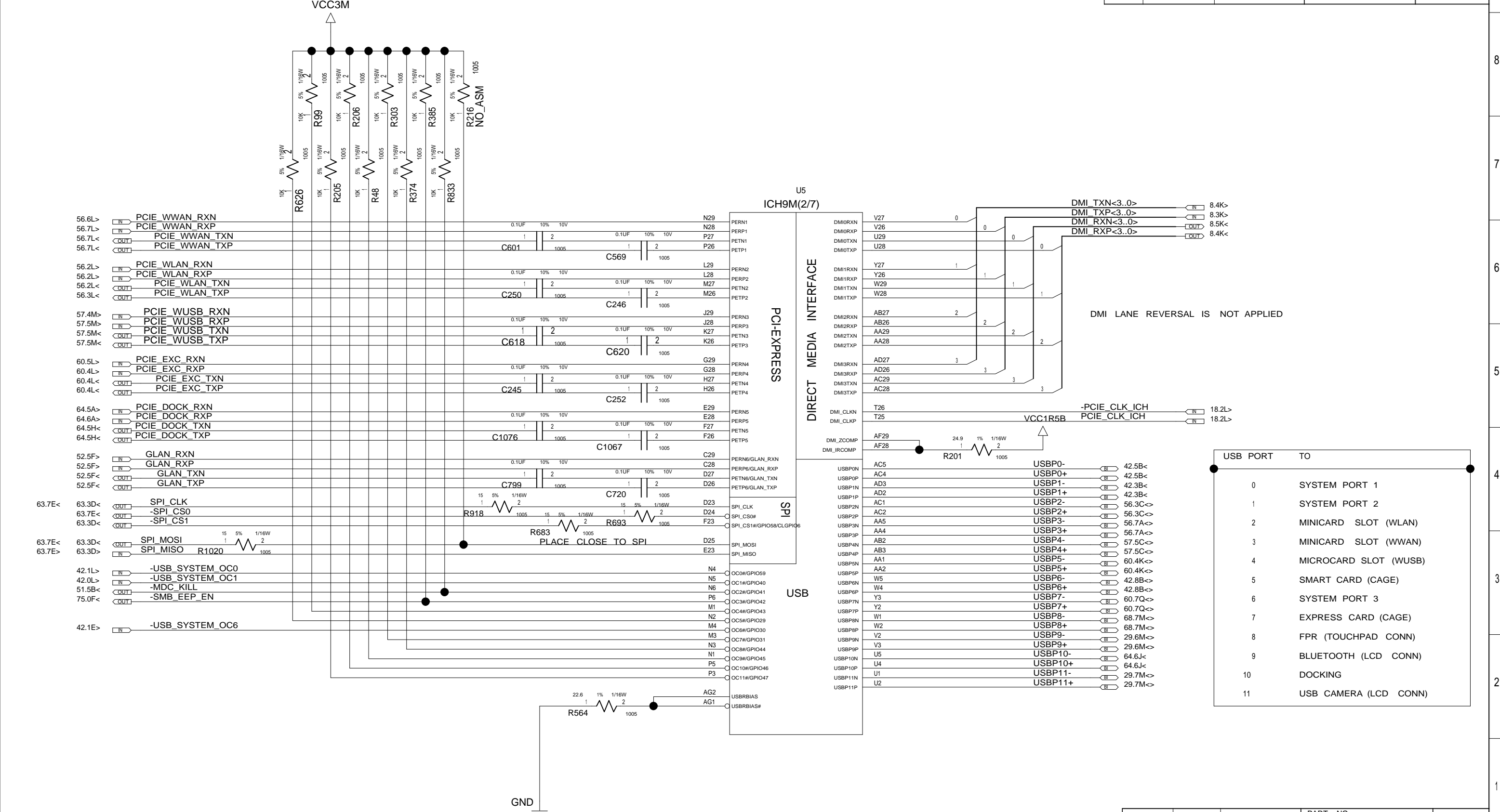
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REL	DATE	EC NO.	PART NO.	
			VER.1.42	
			DEVELOPMENT NO.	Q/M
				33 / 101



DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	ICH9-M(1/6): SATA/HDA/GLAN/LPC	
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DATE		EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				34 / 101

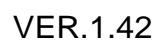


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DMI TXP<3..0>
DMI RXN<3..0>
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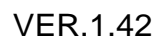
DMI LANE REVERSAL IS NOT APPLIED

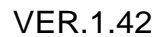
USB PORT	TO
0	SYSTEM PORT 1
1	SYSTEM PORT 2
2	MINICARD SLOT (WLAN)
3	MINICARD SLOT (WWAN)
4	MICROCARD SLOT (WUSB)
5	SMART CARD (CAGE)
6	SYSTEM PORT 3
7	EXPRESS CARD (CAGE)
8	FPR (TOUCHPAD CONN)
9	BLUETOOTH (LCD CONN)
10	DOCKING
11	USB CAMERA (LCD CONN)

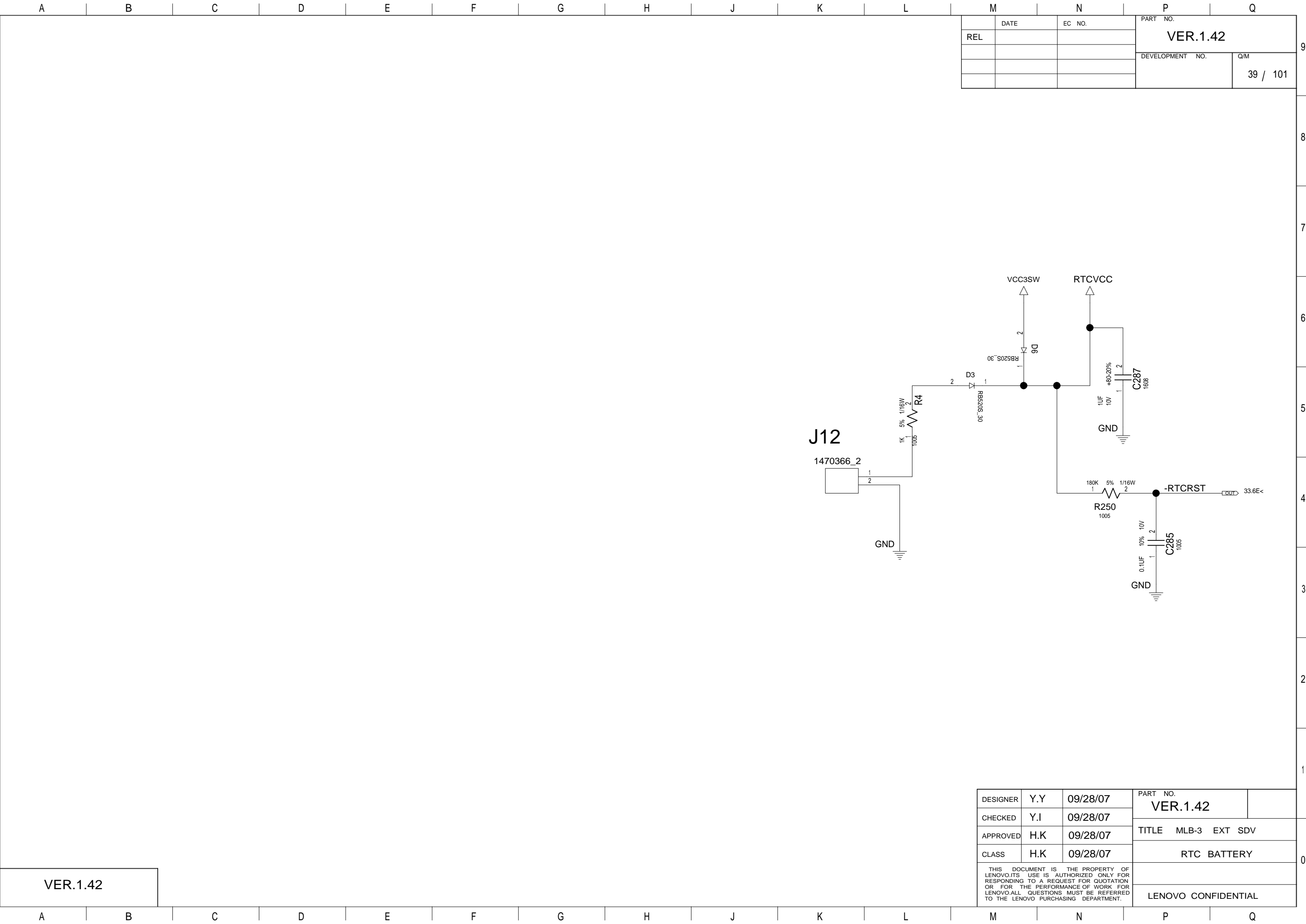
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	ICH9-M(2/6): DMI/PCI-EXPRESS/USB/SPI	
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DESIGNER	Y.Y	09/28/07	PART NO. VER.1.42
CHECKED	Y.I	09/28/07	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV
CLASS	H.K	09/28/07	ICH9-M(3/6): PCI/INTERRUPT
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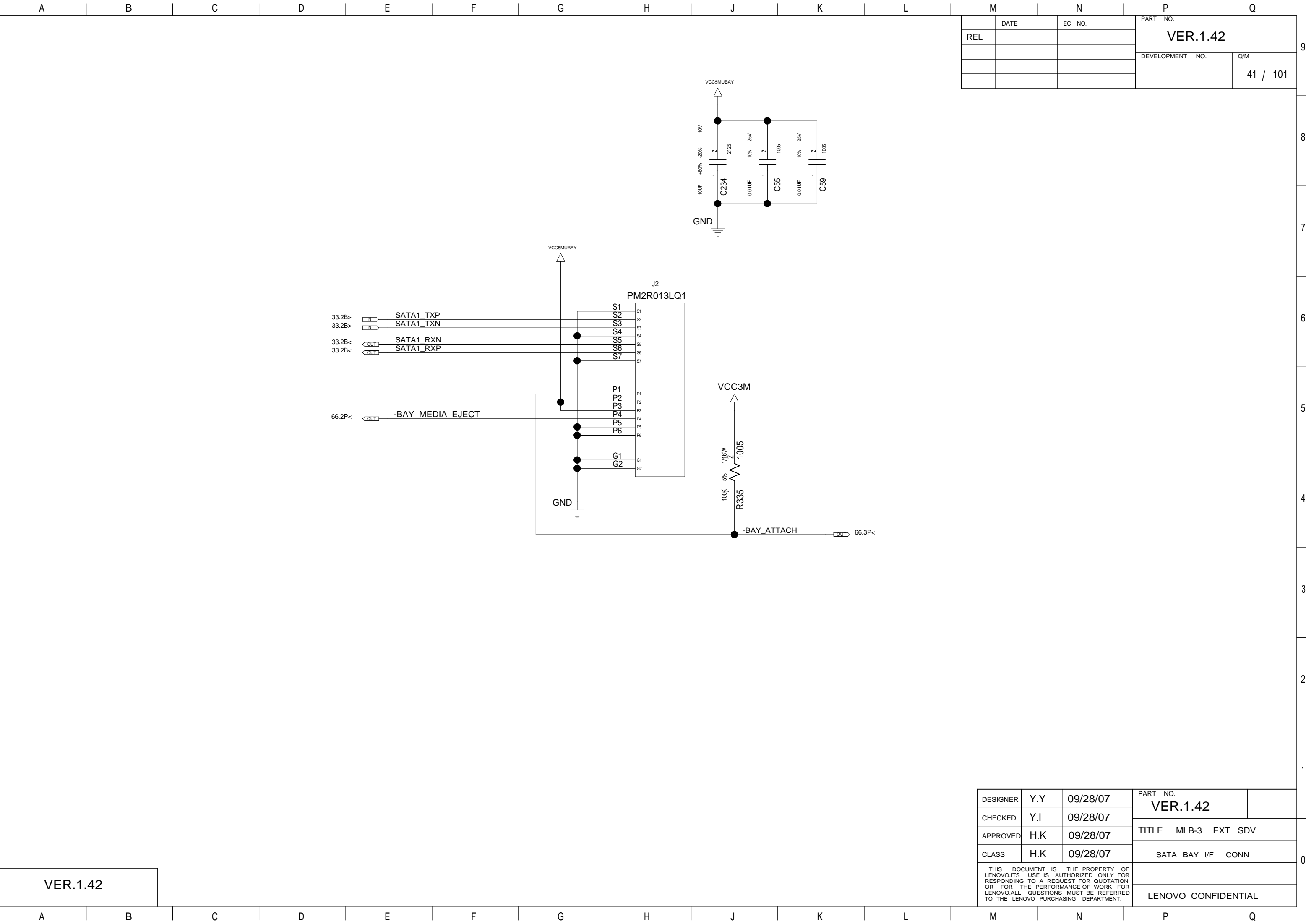




DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	RTC BATTERY	
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REL						VER.1.42	
						DEVELOPMENT NO.	
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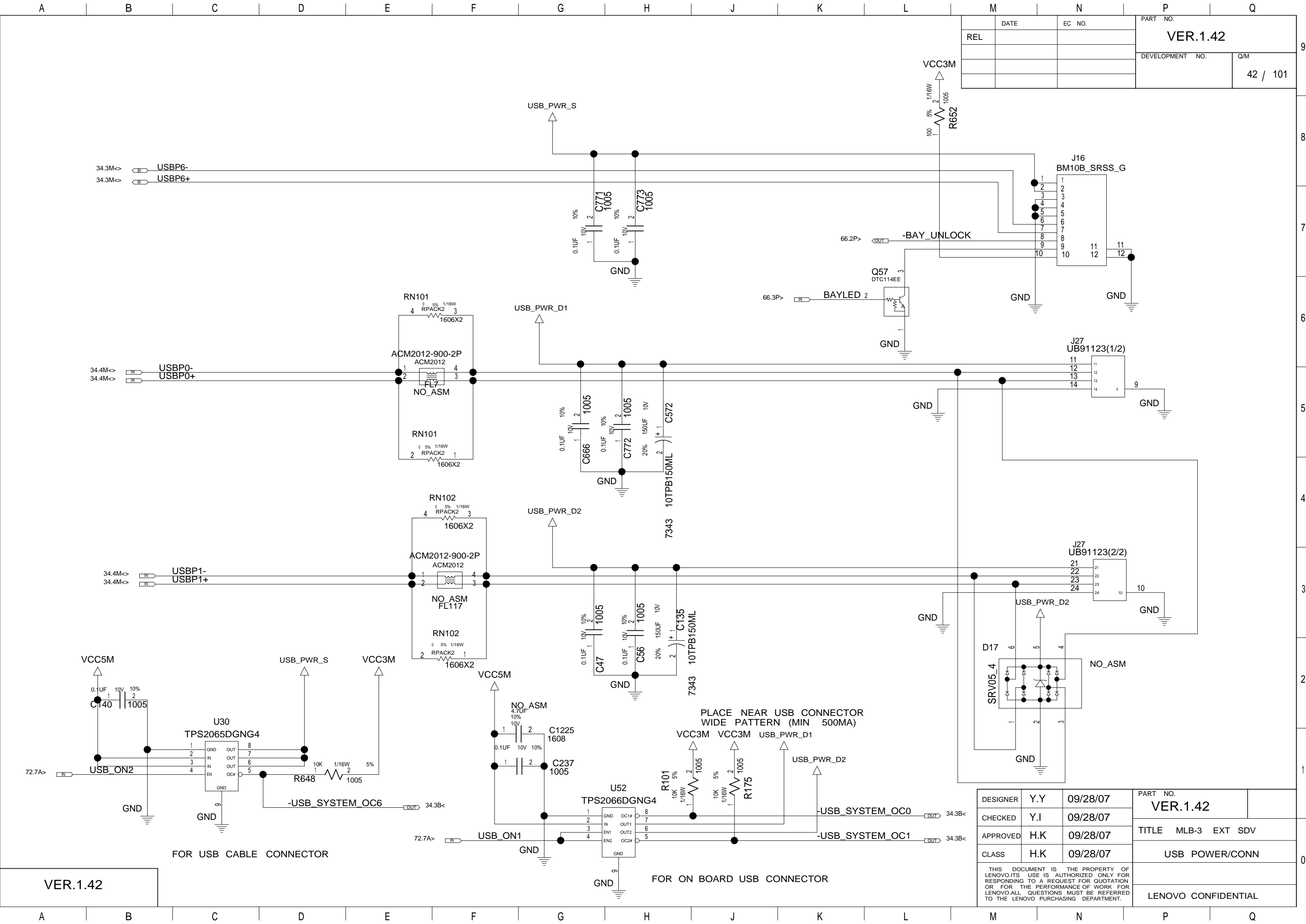
VER.1.42



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	DATE	EC NO.		PART NO.			
REL				VER.1.42			
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VER.1.42

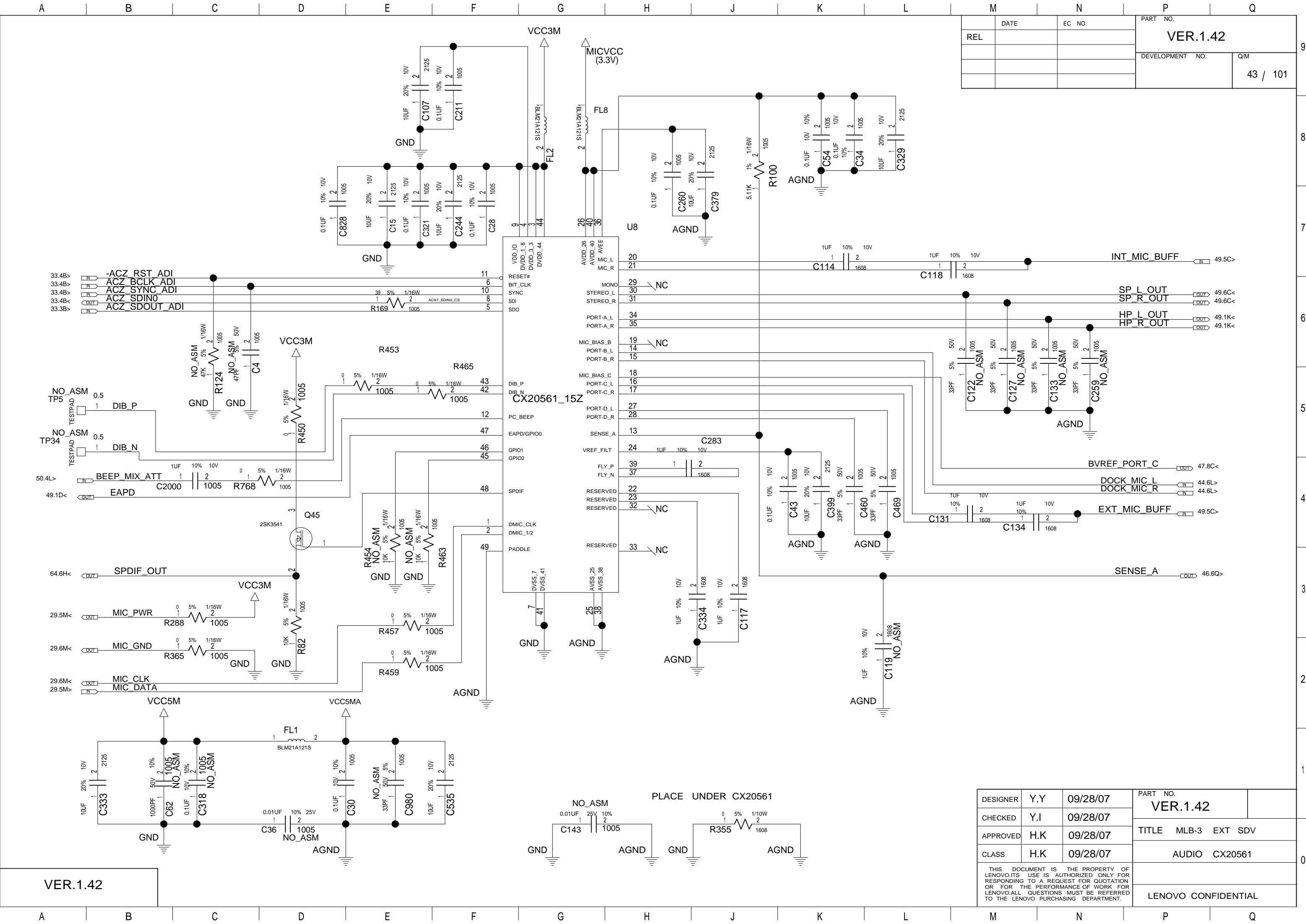
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APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV			
CLASS	H.K	09/28/07	SATA BAY I/F CONN			
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	DATE	EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				42 / 101

VER.1.42

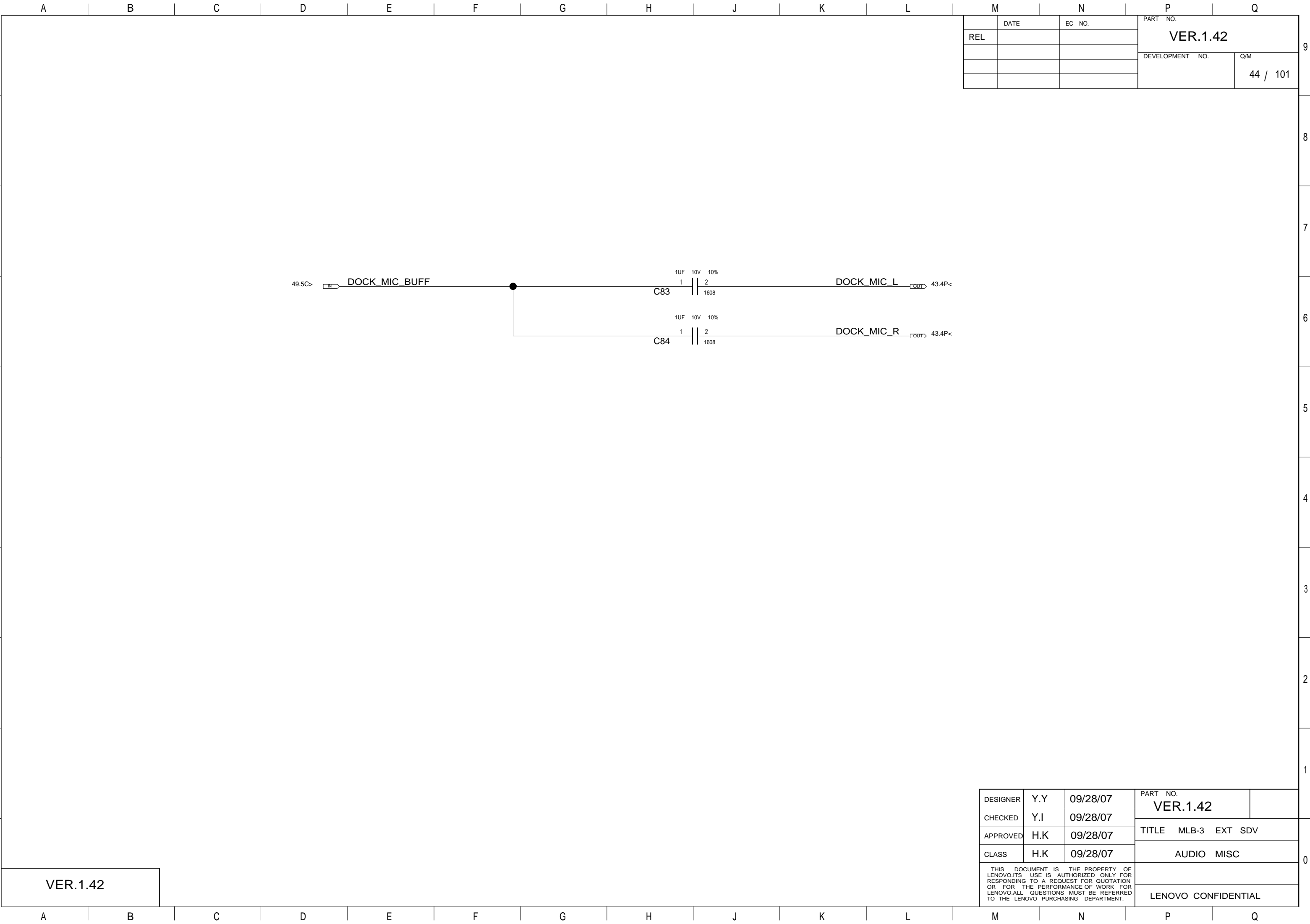
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	USB POWER/CONN	
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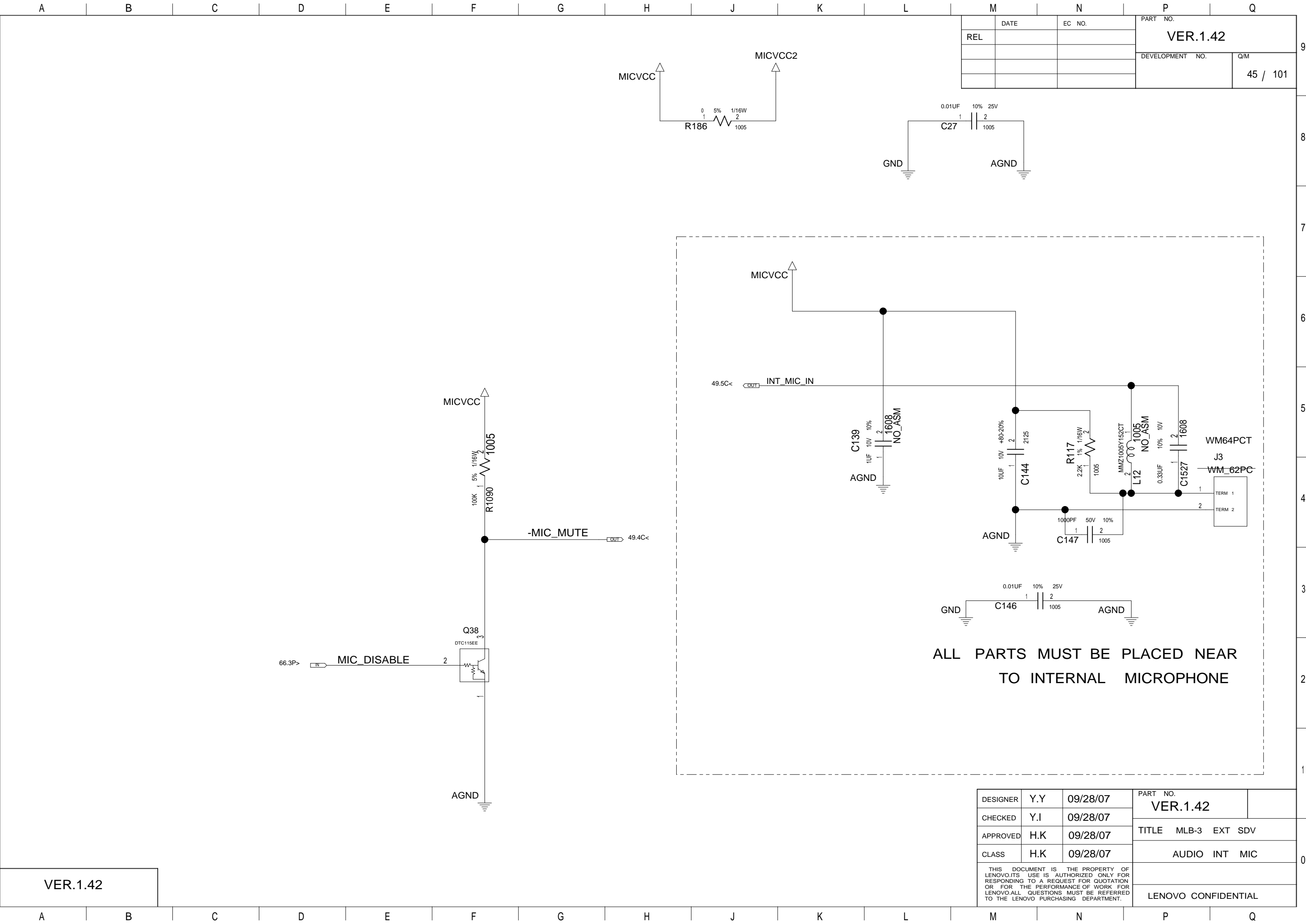


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REL				VER.1.42	
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VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	AUDIO CX20561	
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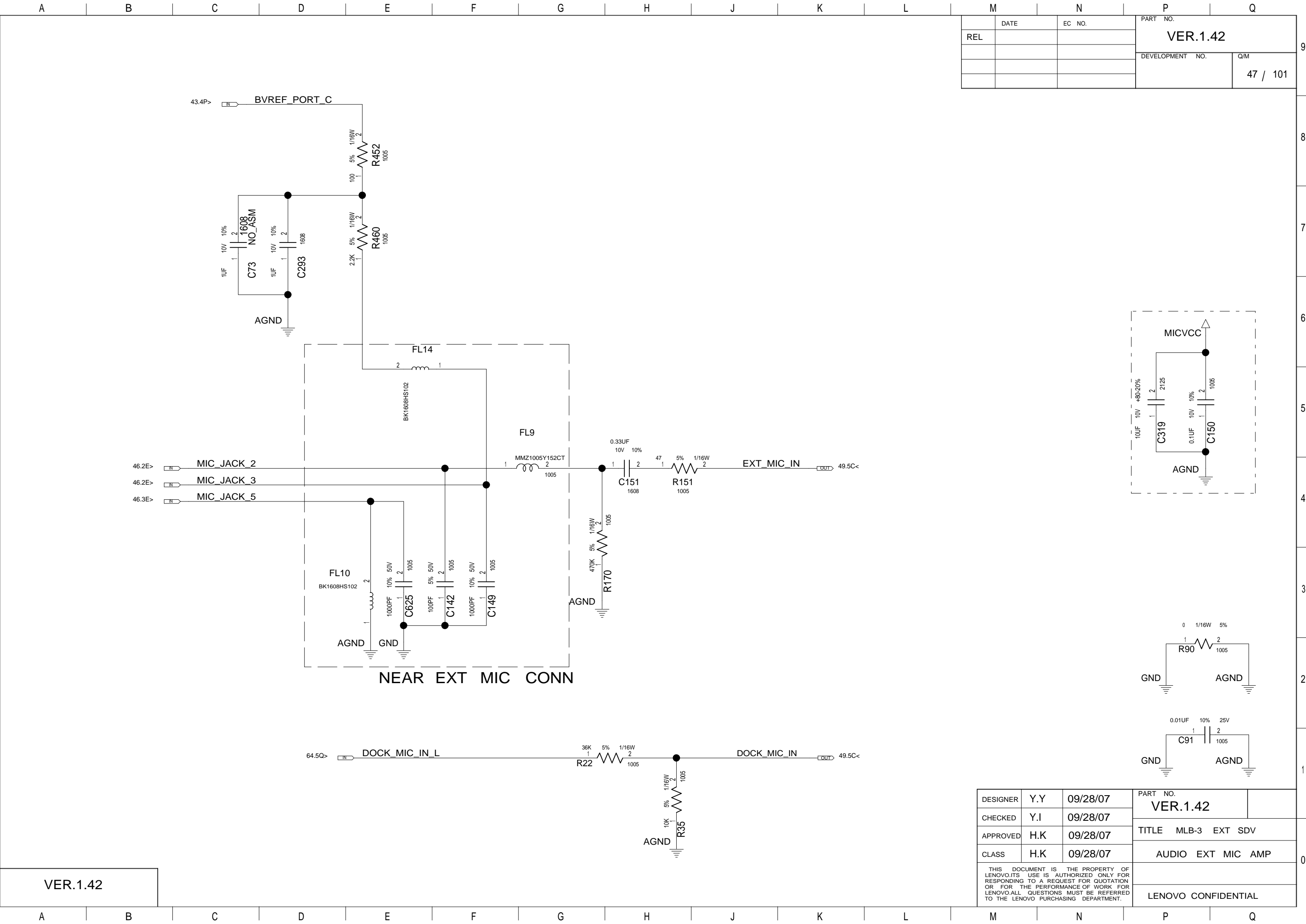




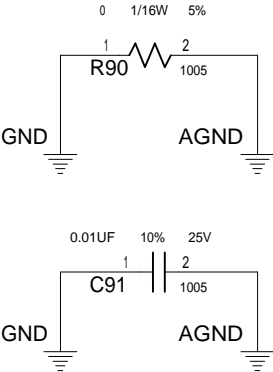
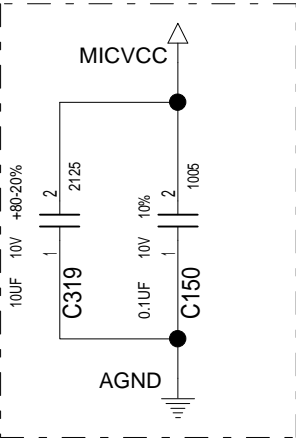
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APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	AUDIO INT MIC	
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REL		DATE	EC NO.	PART NO.	
				VER.1.42	
				DEVELOPMENT NO.	Q/M
					45 / 101

VER.1.42

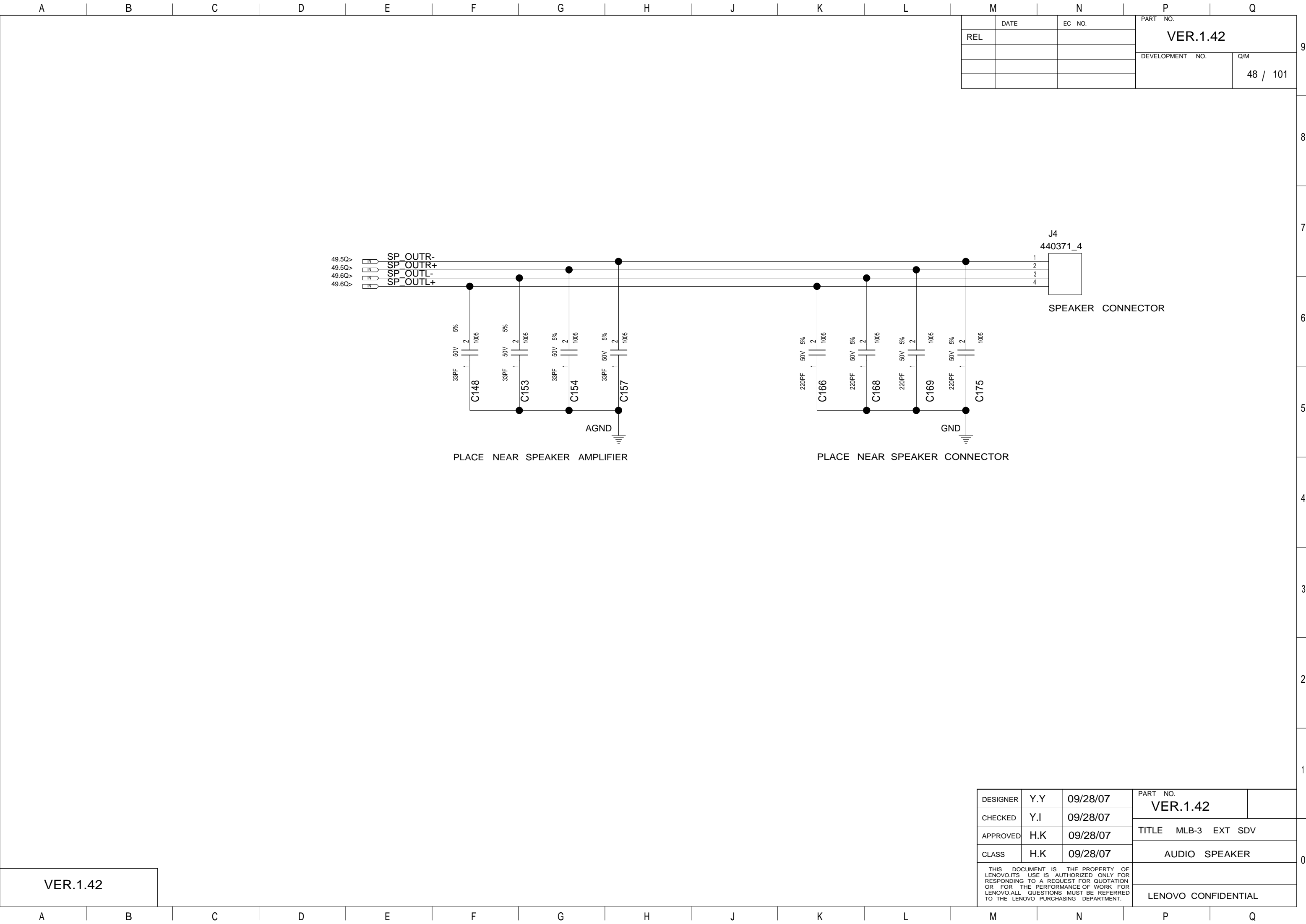


	DATE	EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	
			Q/M	
			47 / 101	



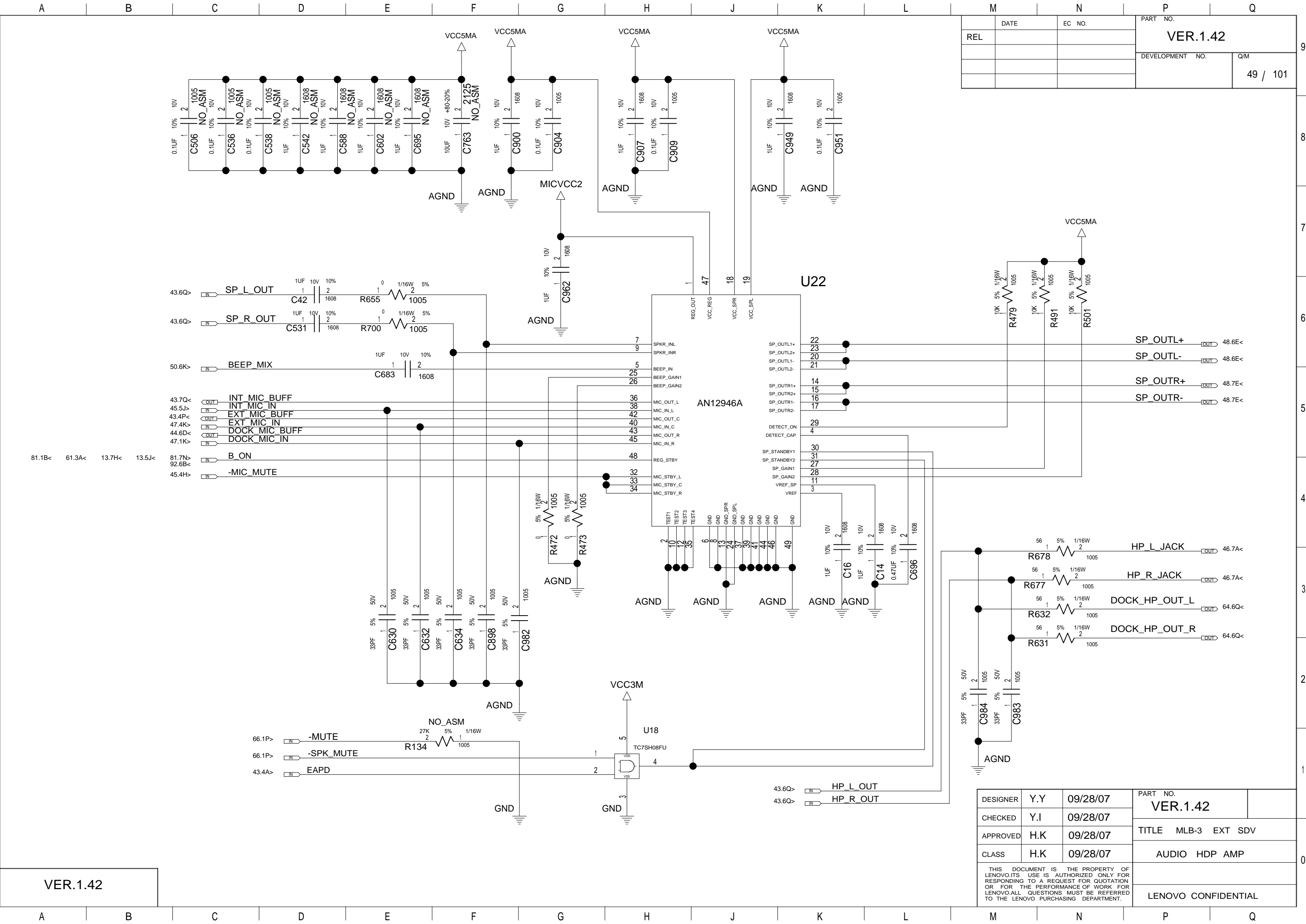
VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
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CLASS	H.K	09/28/07	AUDIO EXT MIC AMP	
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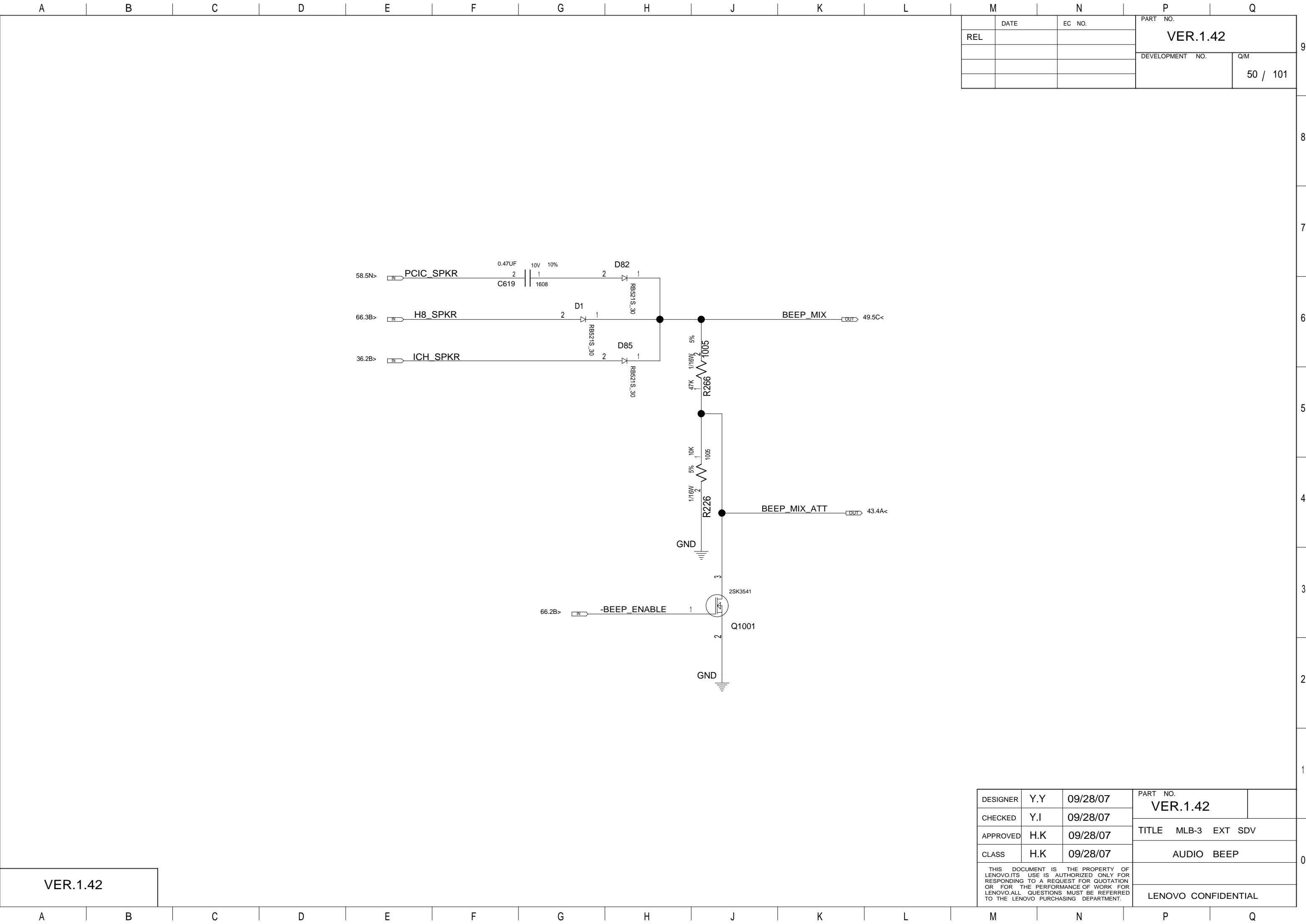
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	DATE	EC NO.		PART NO.			
REL				VER.1.42			
				DEVELOPMENT NO.			
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CHECKED	Y.I	09/28/07				
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV			
CLASS	H.K	09/28/07	AUDIO SPEAKER			
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				49 / 101	

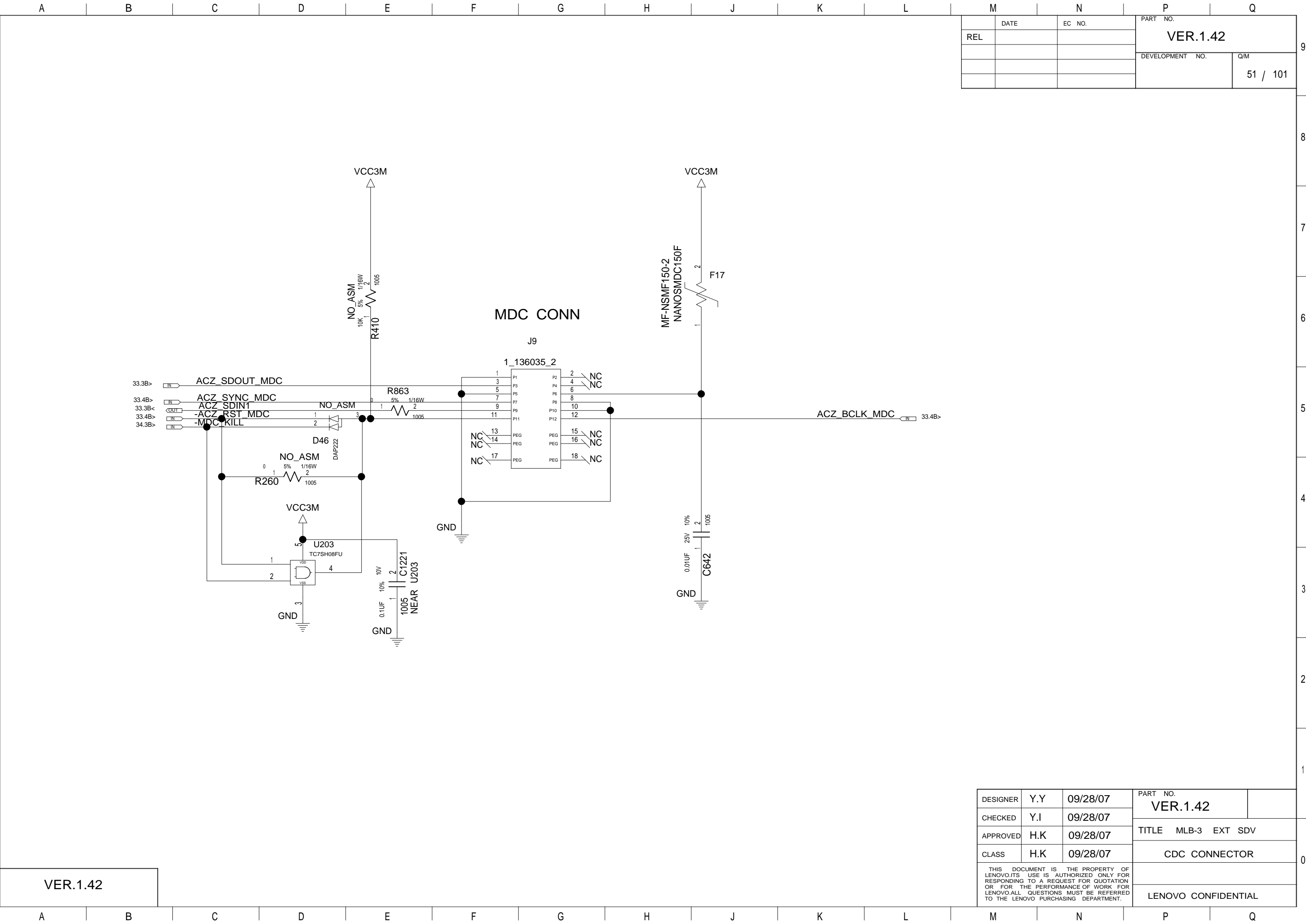
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CLASS	H.K	09/28/07	AUDIO HDP AMP	
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M		N		P		Q	
		DATE		EC NO.		PART NO.	
REL						VER.1.42	
						DEVELOPMENT NO.	
						Q/M	
						50 / 101	

VER.1.42

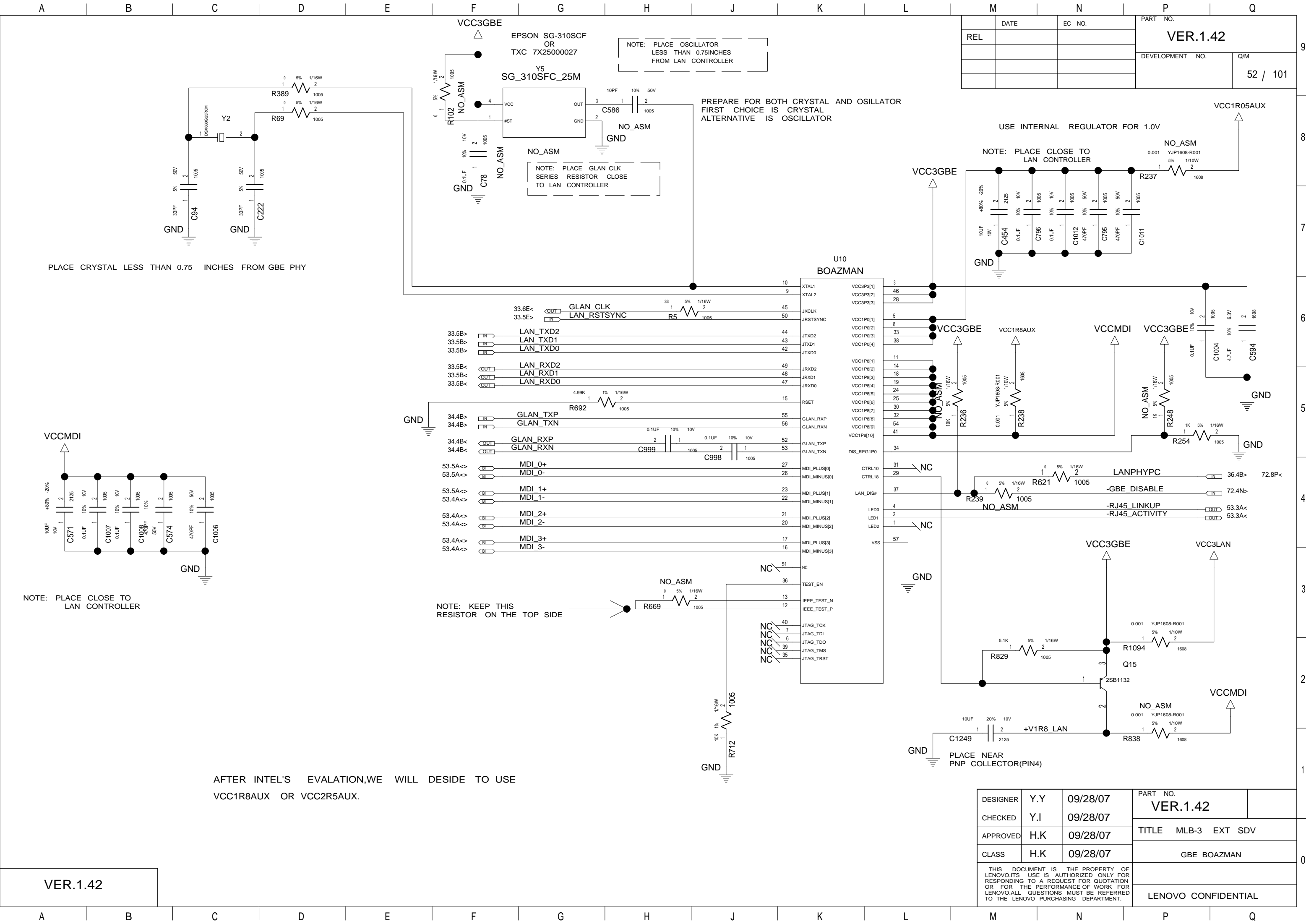
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CLASS	H.K	09/28/07	AUDIO BEEP			
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				51 / 101	

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CDC CONNECTOR	
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	DATE	EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				52 / 101

USE INTERNAL REGULATOR FOR 1.0V

NOTE: PLACE CLOSE TO LAN CONTROLLER

NO_ASM

YJP1608-R001

5% 1/10W

1608

VCC1R05AUX

NOTE: PLACE CLOSE TO LAN CONTROLLER

NO_ASM

YJP1608-R001

5% 1/10W

1608

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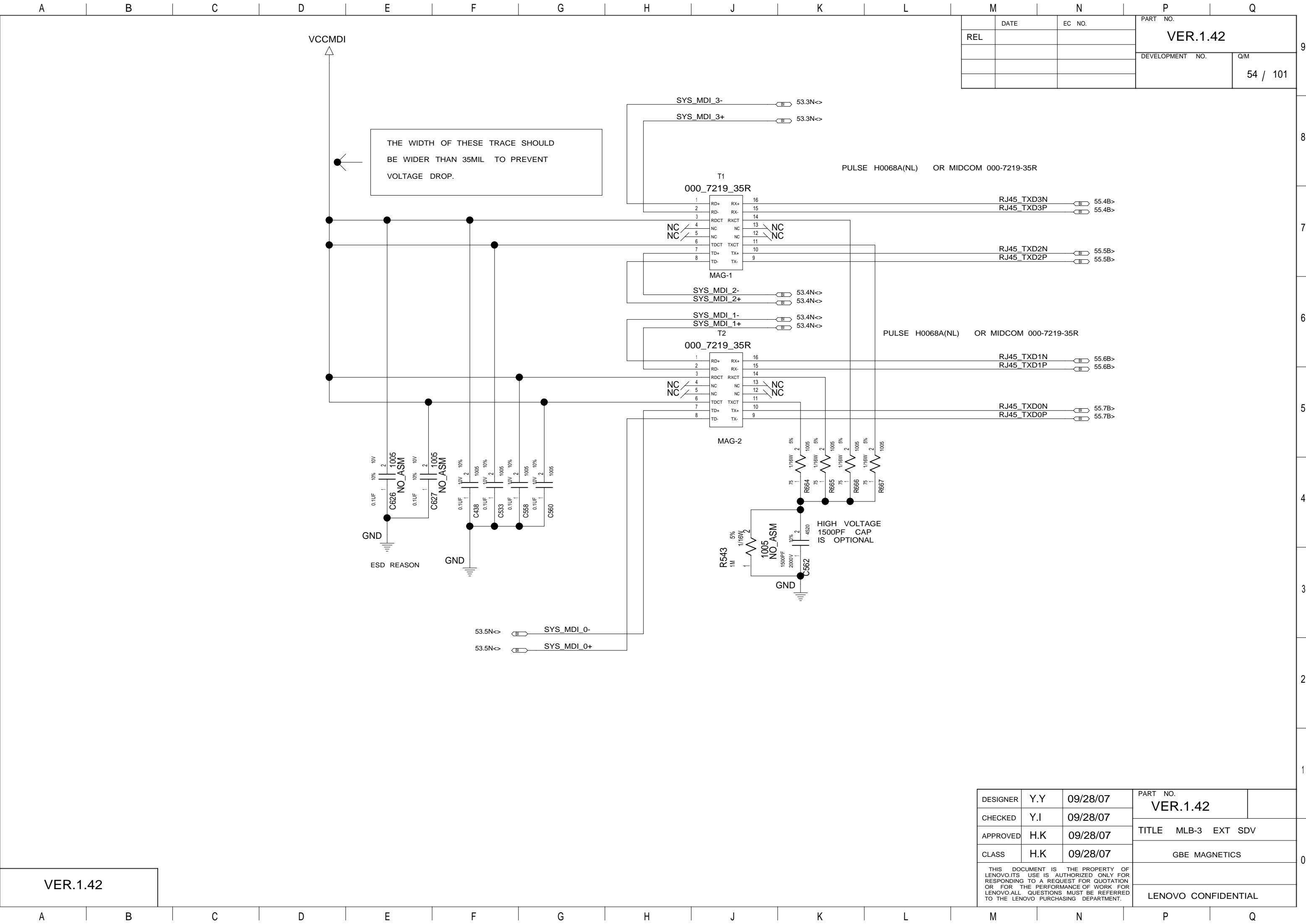
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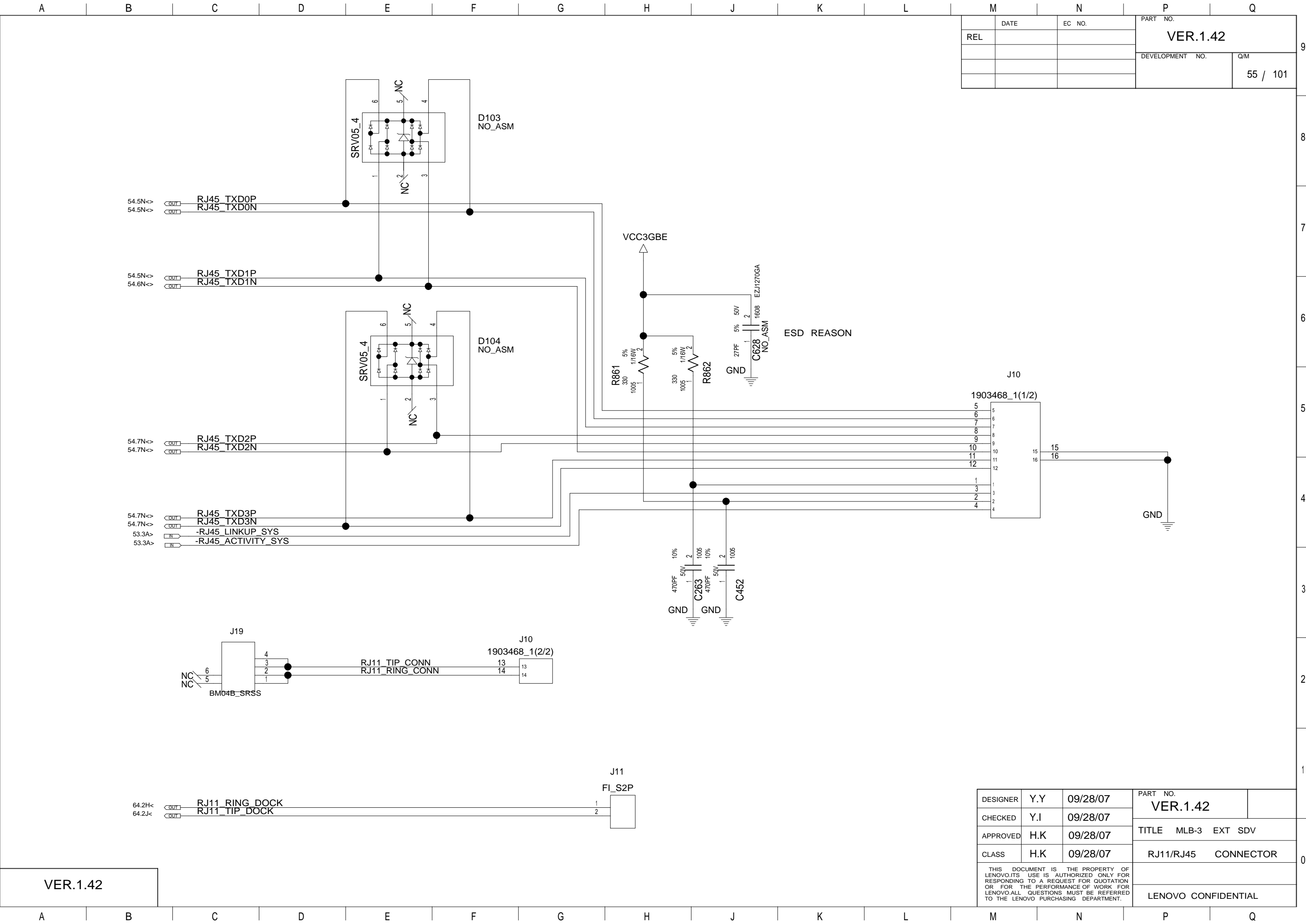
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	GBE BOAZMAN	
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M		N		P		Q	
REL		EC NO.		PART NO.		VER.1.42	
				DEVELOPMENT NO.		Q/M	
						54 / 101	

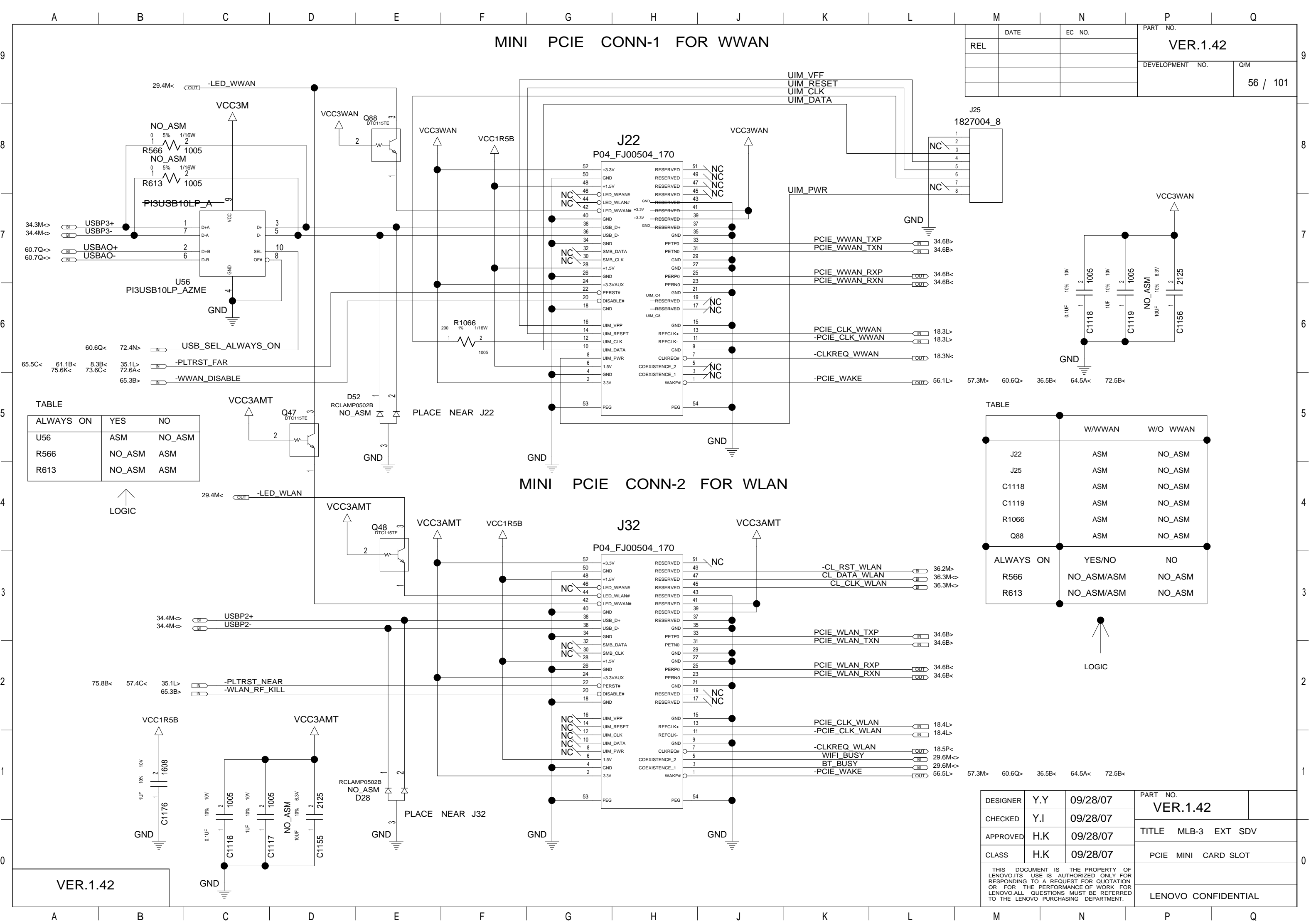
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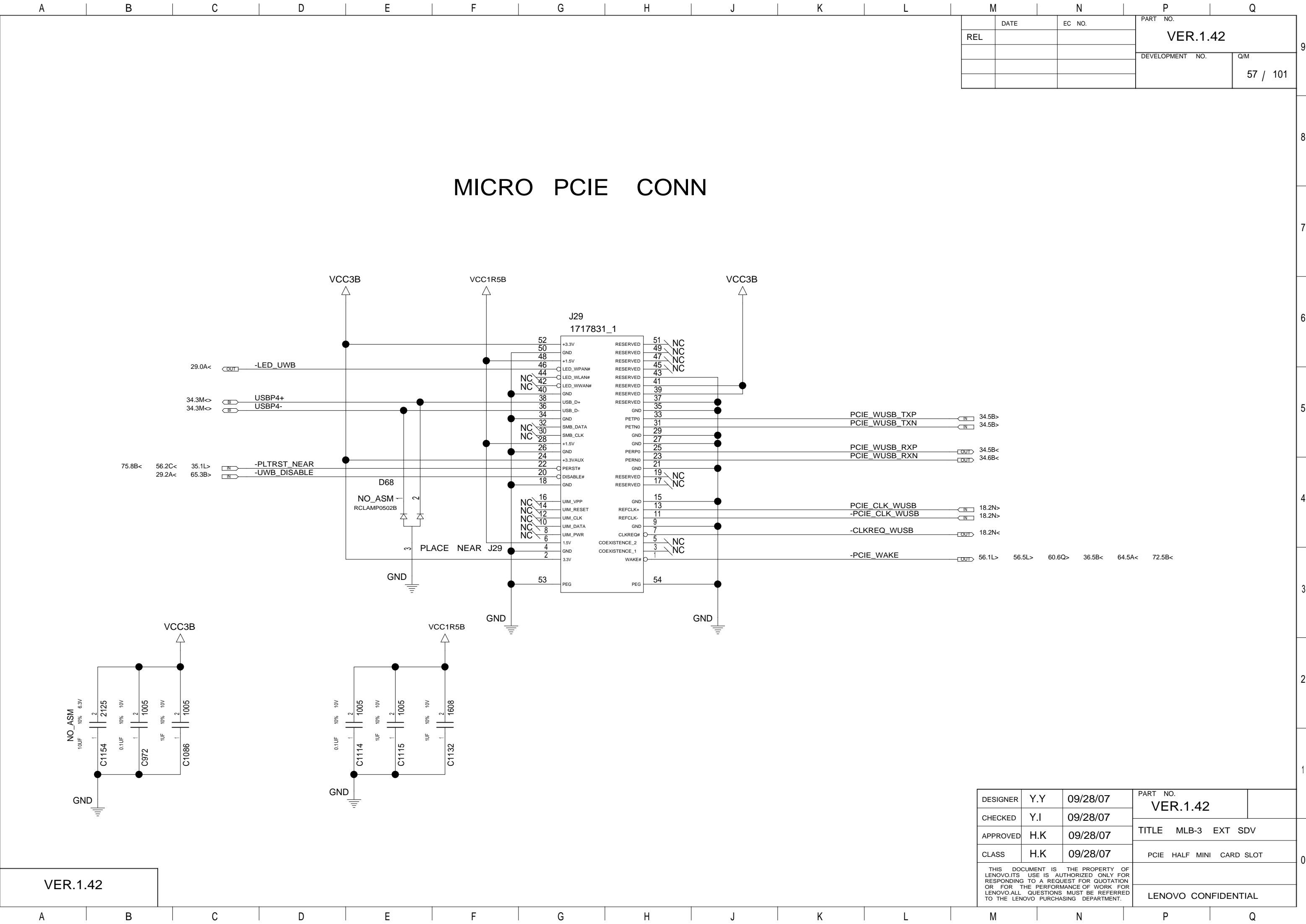
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CLASS	H.K	09/28/07	GBE MAGNETICS			
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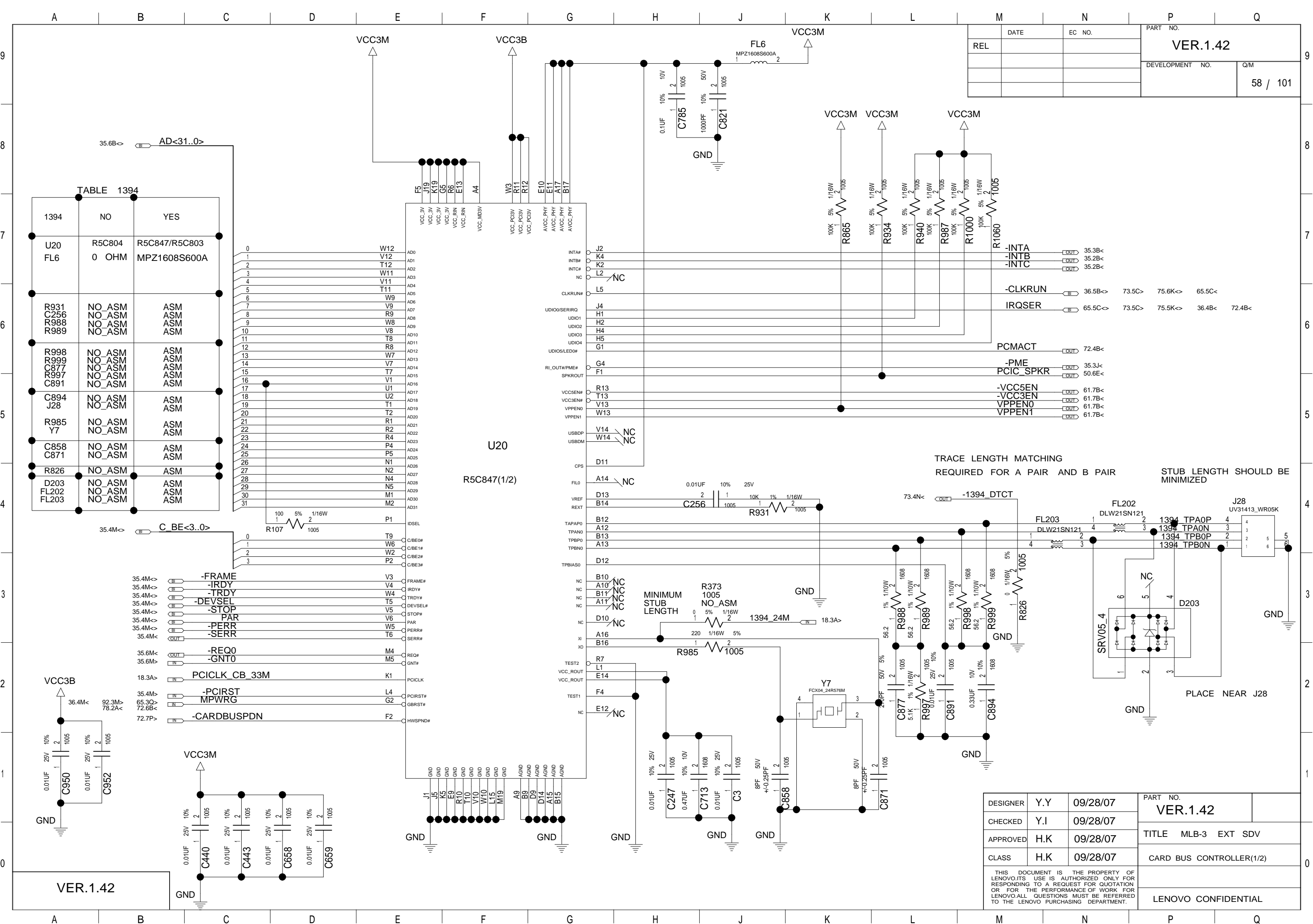


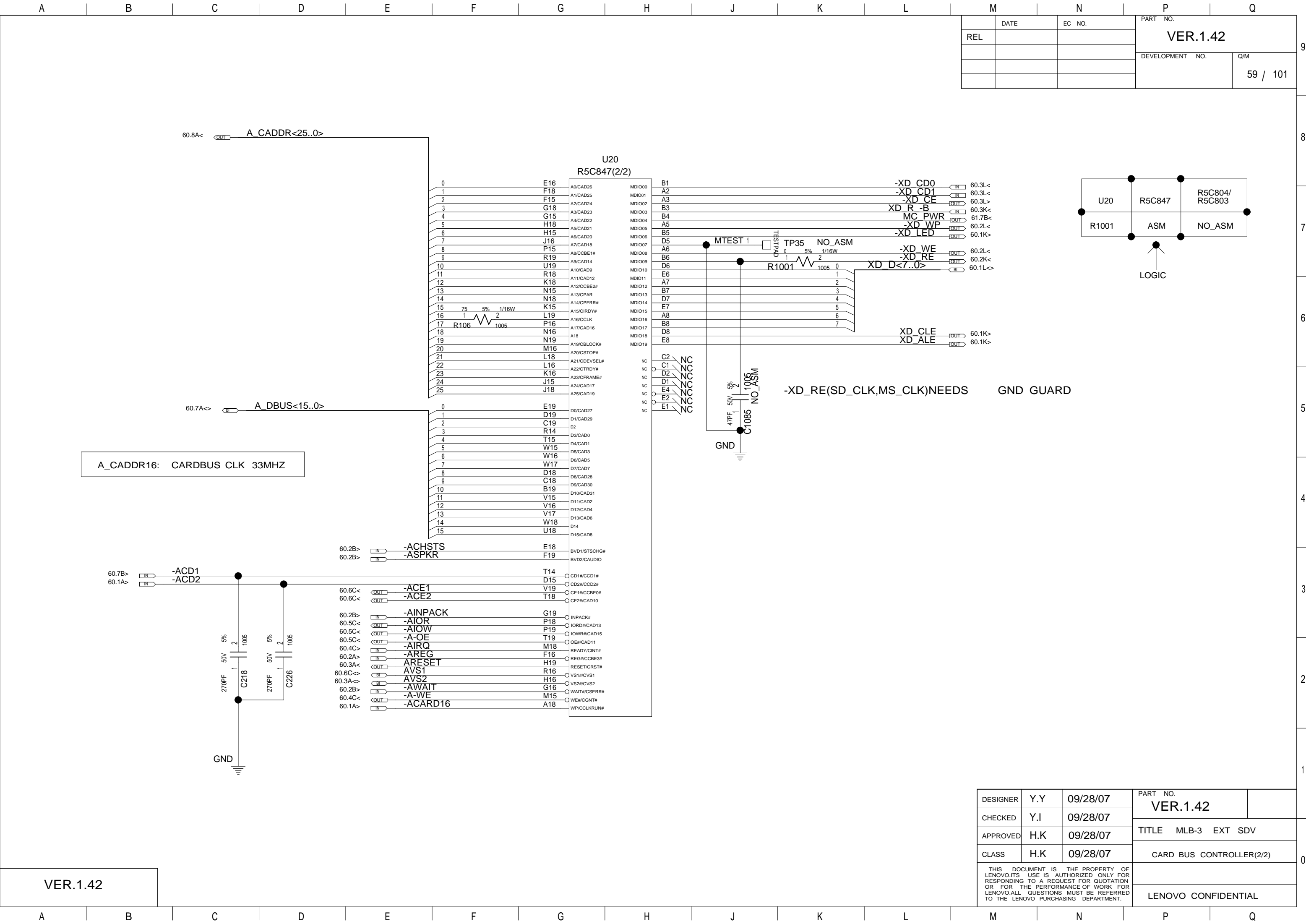
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REL			VER.1.42	
			DEVELOPMENT NO.	
			Q/M	
			55 / 101	

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	RJ11/RJ45 CONNECTOR	
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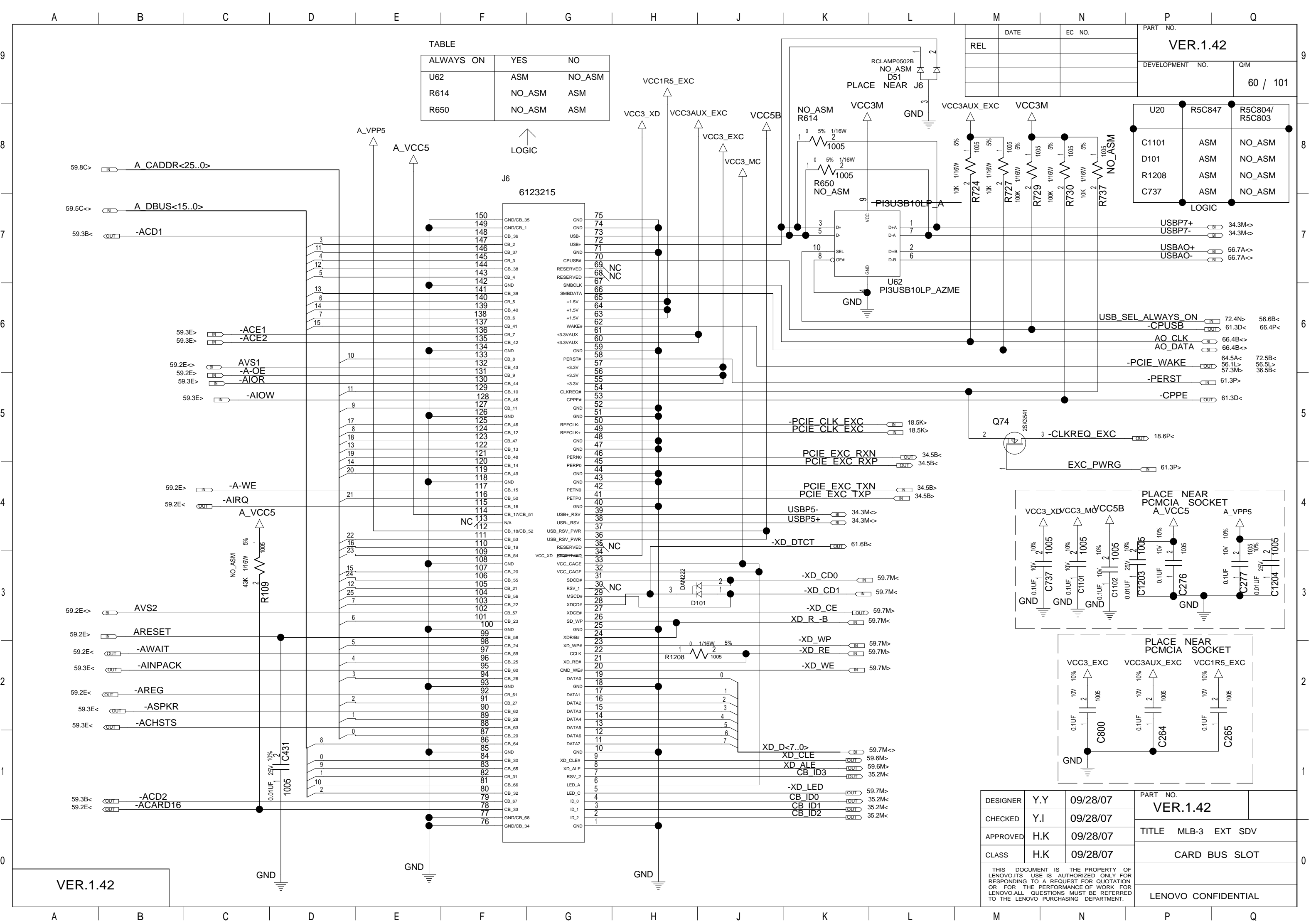




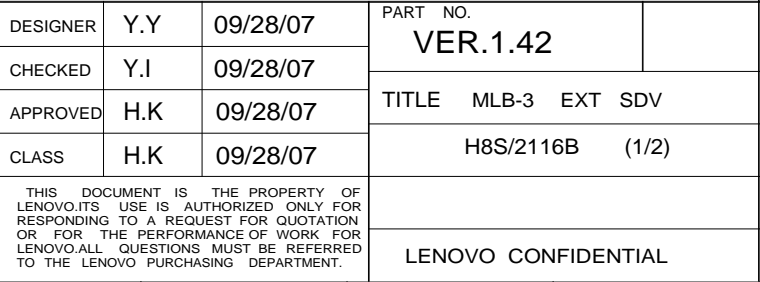


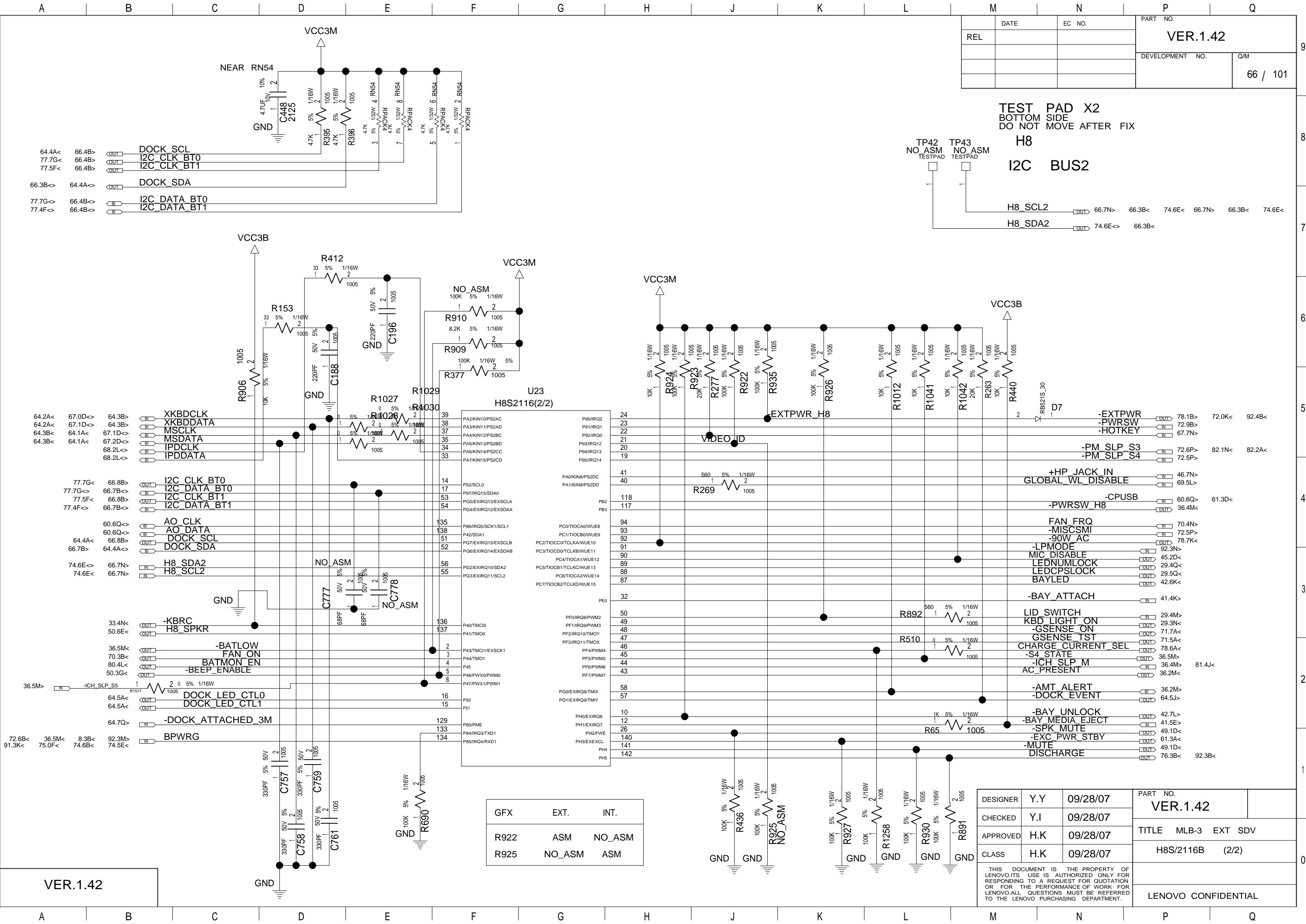


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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	CARD BUS CONTROLLER(2/2)	
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[illegible]



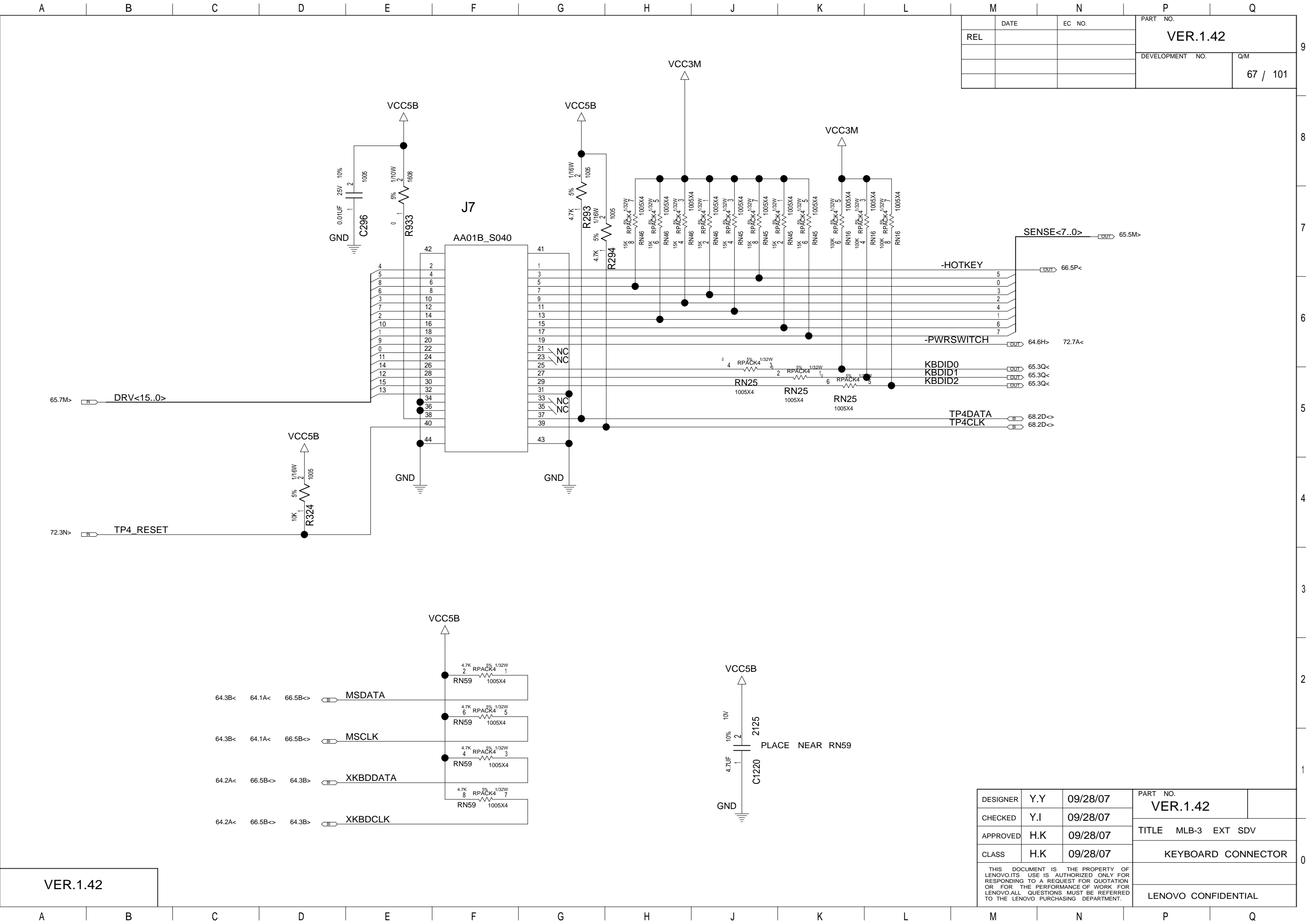


DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				66 / 101	

TEST PAD X2
BOTTOM SIDE
DO NOT MOVE AFTER FIX
H8
I2C BUS2

H8_SCL2
H8_SDA2

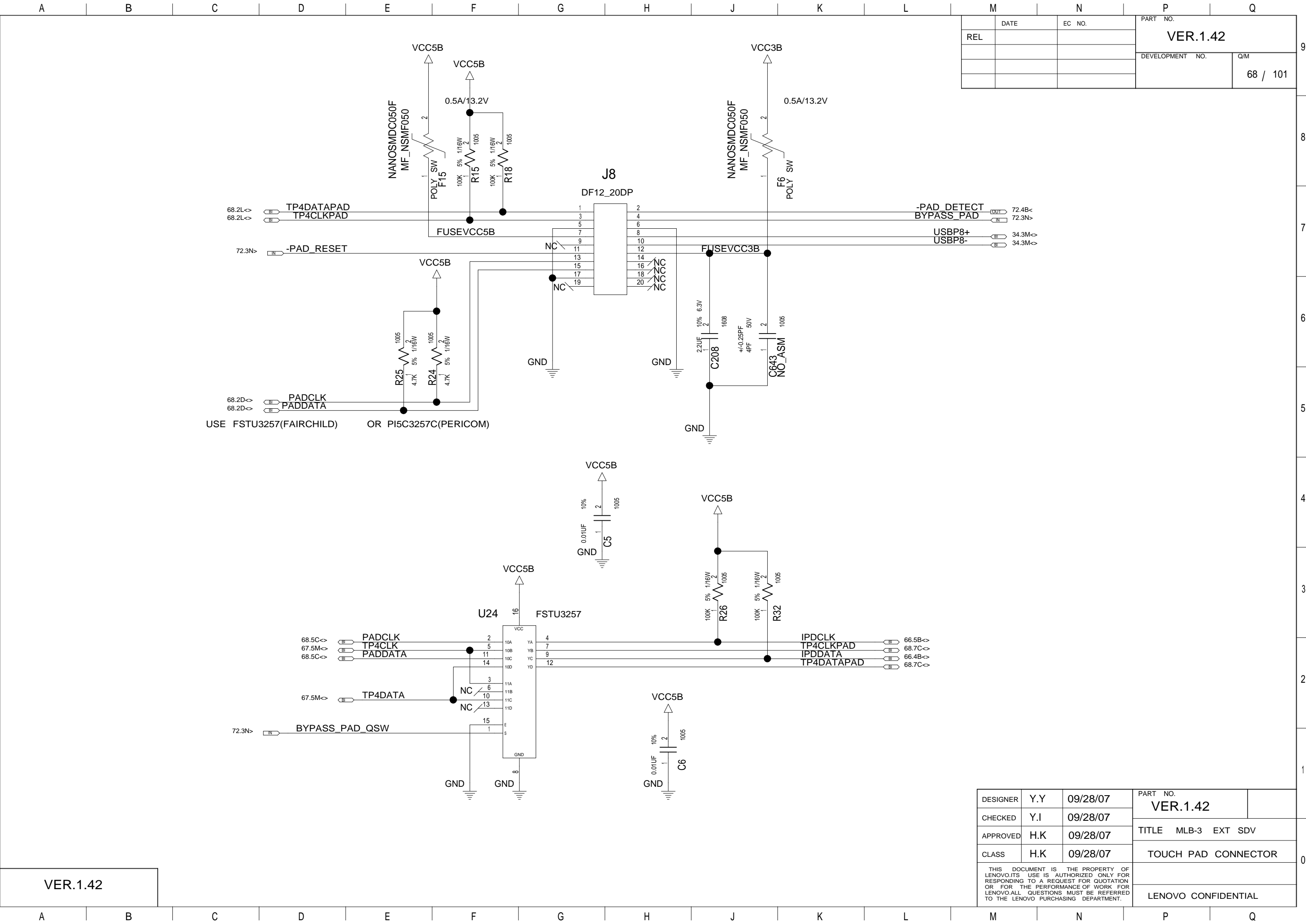
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE	
CLASS	H.K	09/28/07	H8S/2116B (2/2)	
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M		N		P		Q	
	DATE	EC NO.		PART NO.			
REL				VER.1.42			
				DEVELOPMENT NO.		Q/M	
						67 / 101	

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.		
CHECKED	Y.I	09/28/07	VER.1.42		
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV		
CLASS	H.K	09/28/07	KEYBOARD CONNECTOR		
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				68 / 101	

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	TOUCH PAD CONNECTOR	
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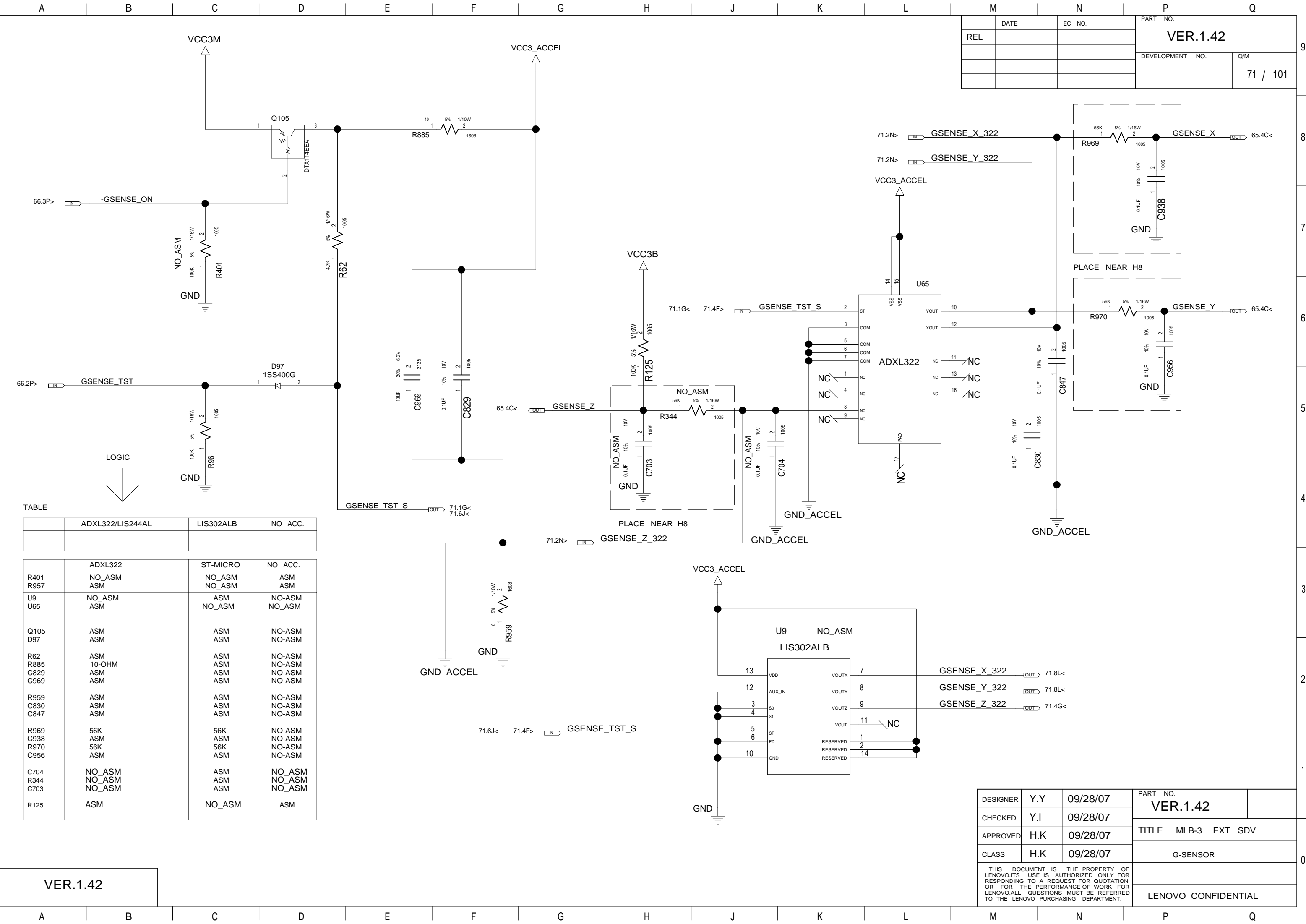
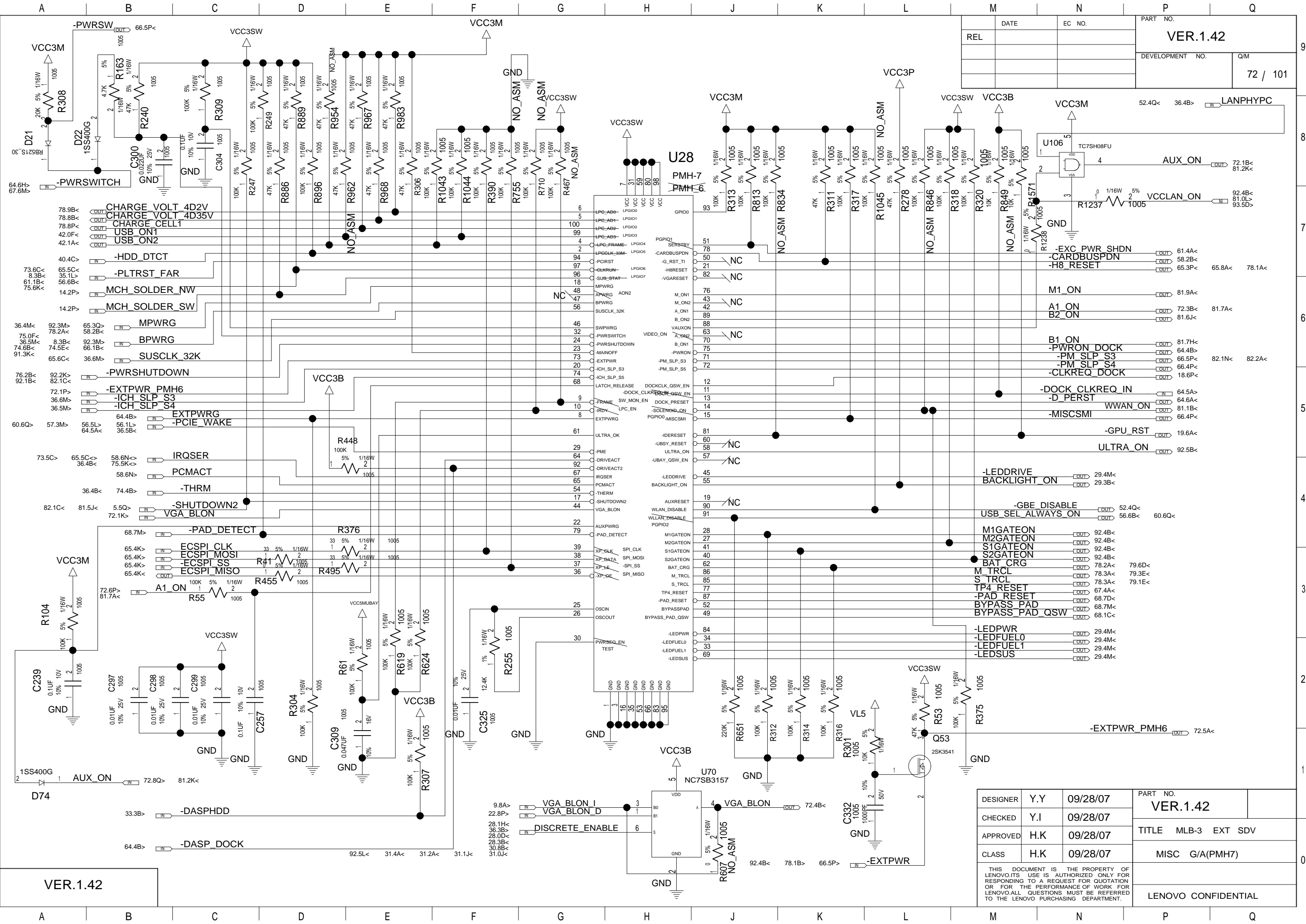


TABLE			
	ADXL322/LIS244AL	LIS302ALB	NO ACC.
	ADXL322	ST-MICRO	NO ACC.
R401	NO_ASM	NO_ASM	ASM
R957	ASM	NO_ASM	ASM
U9	NO_ASM	ASM	NO-ASM
U65	ASM	NO_ASM	NO-ASM
Q105	ASM	ASM	NO-ASM
D97	ASM	ASM	NO-ASM
R62	ASM	ASM	NO-ASM
R885	10-OHM	ASM	NO-ASM
C829	ASM	ASM	NO-ASM
C969	ASM	ASM	NO-ASM
R959	ASM	ASM	NO-ASM
C830	ASM	ASM	NO-ASM
C847	ASM	ASM	NO-ASM
R969	56K	56K	NO-ASM
C938	ASM	ASM	NO-ASM
R970	56K	56K	NO-ASM
C956	ASM	ASM	NO-ASM
C704	NO_ASM	ASM	NO_ASM
R344	NO_ASM	ASM	NO_ASM
C703	NO_ASM	ASM	NO_ASM
R125	ASM	NO_ASM	ASM

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	G-SENSOR	
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			VER.1.42	
			DEVELOPMENT NO.	Q/M
				72 / 101

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	MISC G/A(PMH7)	
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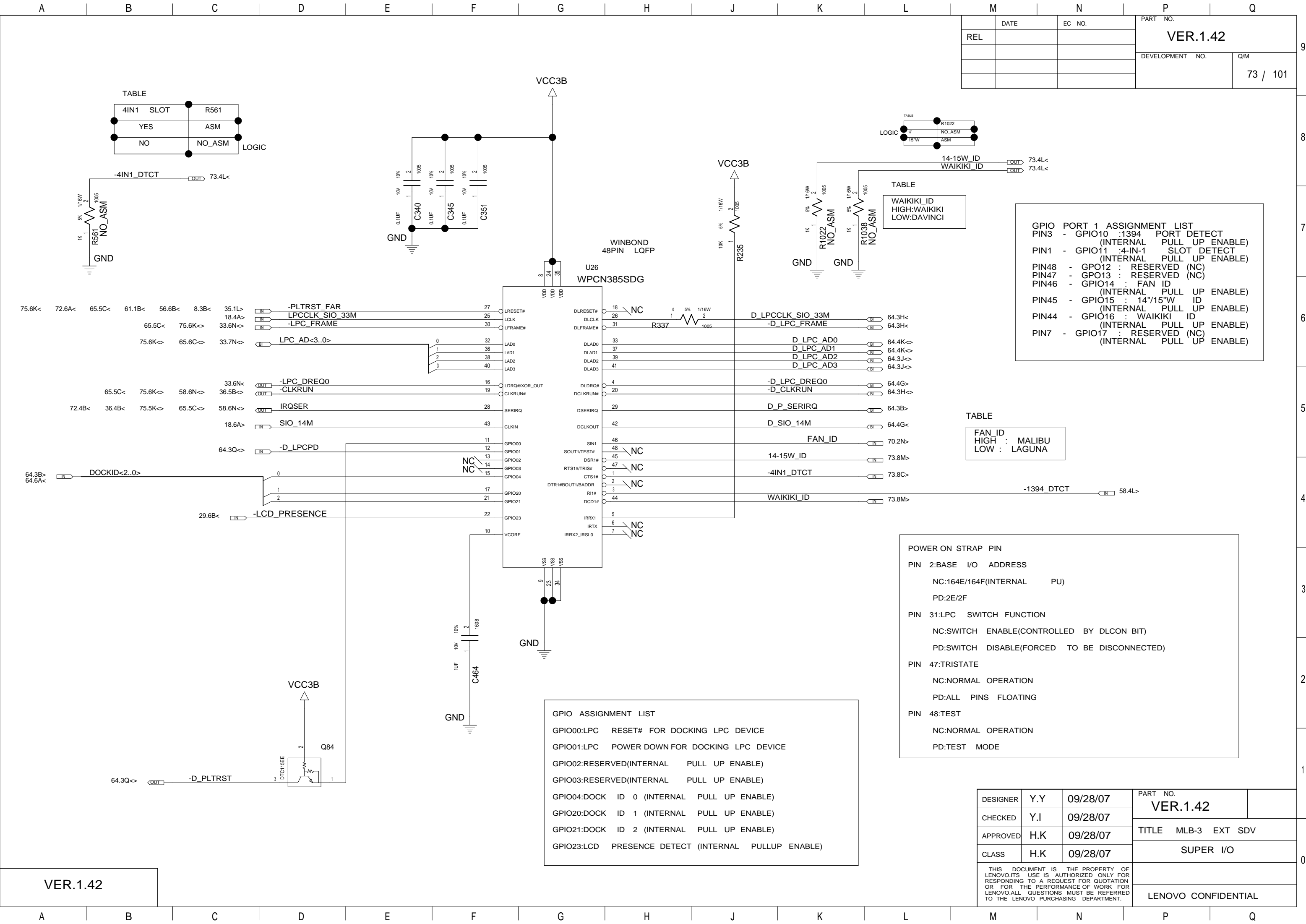


TABLE			
4IN1	SLOT	R561	
YES		ASM	
NO		NO_ASM	LOGIC

TABLE			
LOGIC	17	R1022	
	15"W	NO_ASM	
		ASM	

TABLE	
WAIKIKI_ID	HIGH:WAIKIKI LOW:DAVINCI

GPIO PORT 1 ASSIGNMENT LIST			
PIN3	- GPIO10 :1394	PORT DETECT	
	(INTERNAL	PULL UP	ENABLE)
PIN1	- GPIO11 :4-IN-1	SLOT DETECT	
	(INTERNAL	PULL UP	ENABLE)
PIN48	- GPO12 :	RESERVED (NC)	
PIN47	- GPO13 :	RESERVED (NC)	
PIN46	- GPIO14 :	FAN ID	
	(INTERNAL	PULL UP	ENABLE)
PIN45	- GPIO15 :	14"/15"W ID	
	(INTERNAL	PULL UP	ENABLE)
PIN44	- GPIO16 :	WAIKIKI ID	
	(INTERNAL	PULL UP	ENABLE)
PIN7	- GPIO17 :	RESERVED (NC)	
	(INTERNAL	PULL UP	ENABLE)

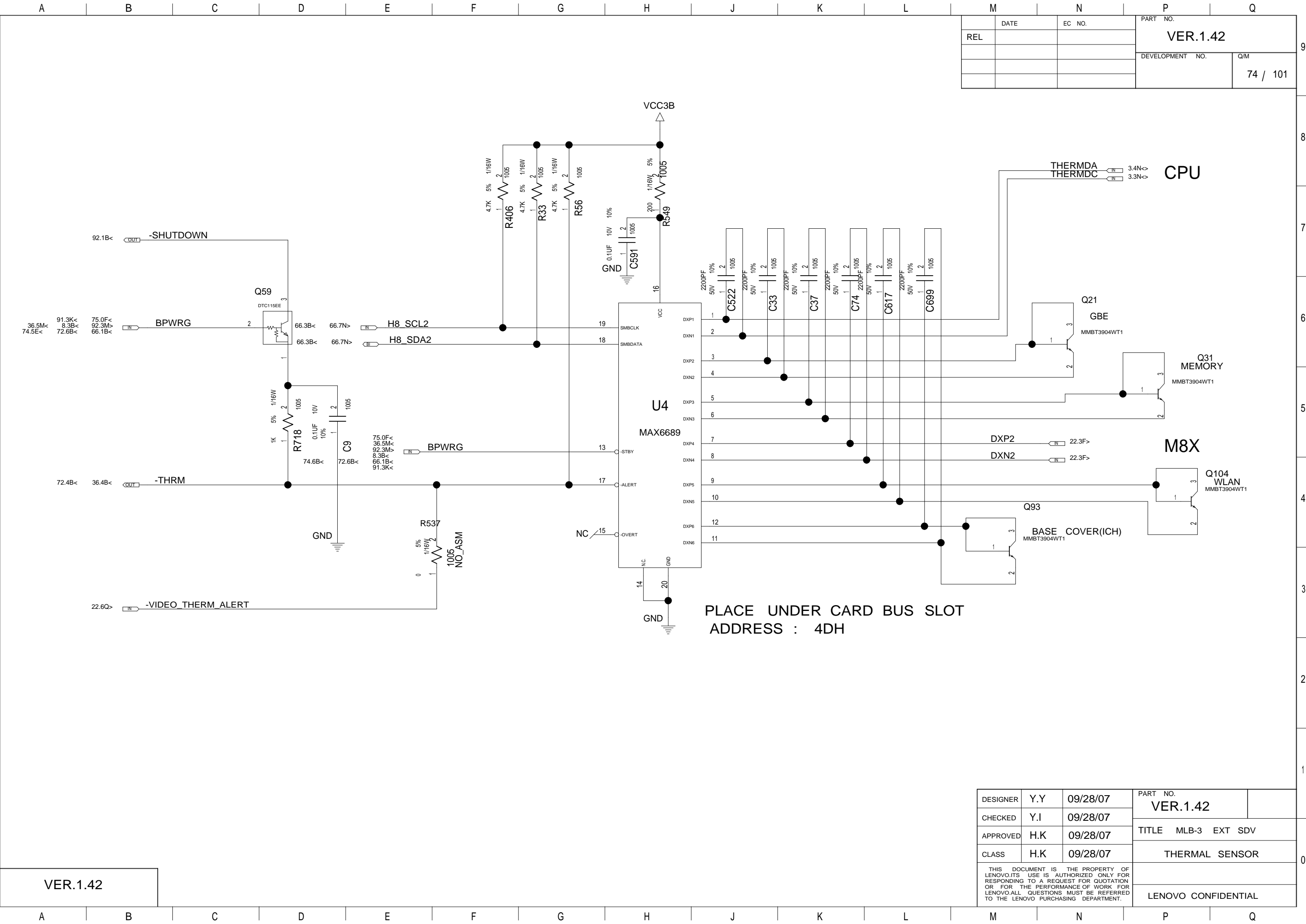
TABLE	
FAN_ID	HIGH : MALIBU LOW : LAGUNA

POWER ON STRAP PIN	
PIN 2:BASE	I/O ADDRESS
	NC:164E/164F(INTERNAL PU)
	PD:2E/2F
PIN 31:LPC	SWITCH FUNCTION
	NC:SWITCH ENABLE(CONTROLLED BY DLCON BIT)
	PD:SWITCH DISABLE(FORCED TO BE DISCONNECTED)
PIN 47:TRISTATE	
	NC:NORMAL OPERATION
	PD:ALL PINS FLOATING
PIN 48:TEST	
	NC:NORMAL OPERATION
	PD:TEST MODE

GPIO ASSIGNMENT LIST	
GPIO00:LPC	RESET# FOR DOCKING LPC DEVICE
GPIO01:LPC	POWER DOWN FOR DOCKING LPC DEVICE
GPIO02:RESERVED(INTERNAL	PULL UP ENABLE)
GPIO03:RESERVED(INTERNAL	PULL UP ENABLE)
GPIO04:DOCK	ID 0 (INTERNAL PULL UP ENABLE)
GPIO20:DOCK	ID 1 (INTERNAL PULL UP ENABLE)
GPIO21:DOCK	ID 2 (INTERNAL PULL UP ENABLE)
GPIO23:LCD	PRESENCE DETECT (INTERNAL PULLUP ENABLE)

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	SUPER I/O	
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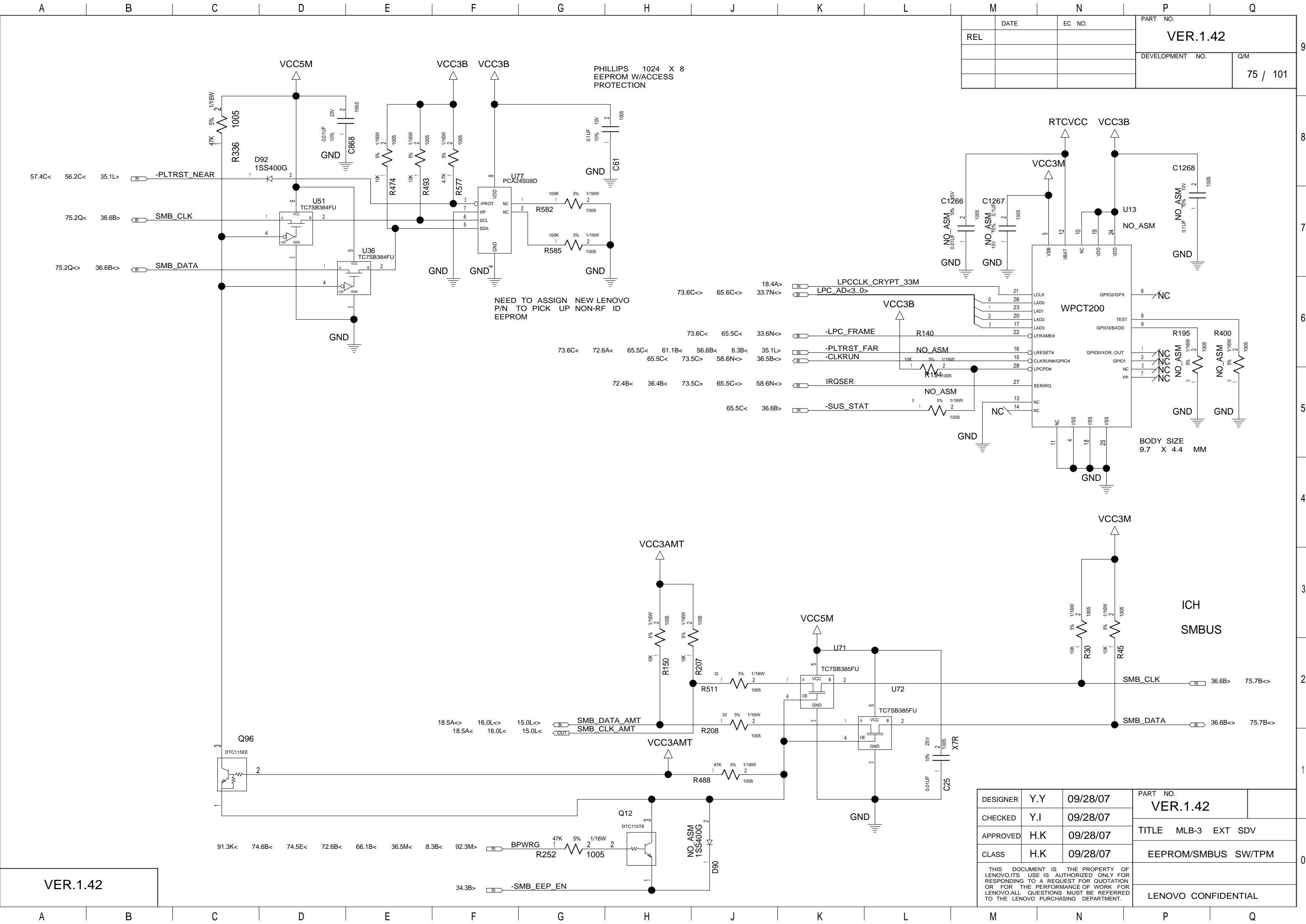
VER.1.42



		M		N		P		Q	
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REL						VER.1.42			
						DEVELOPMENT NO.		Q/M	
								74 / 101	

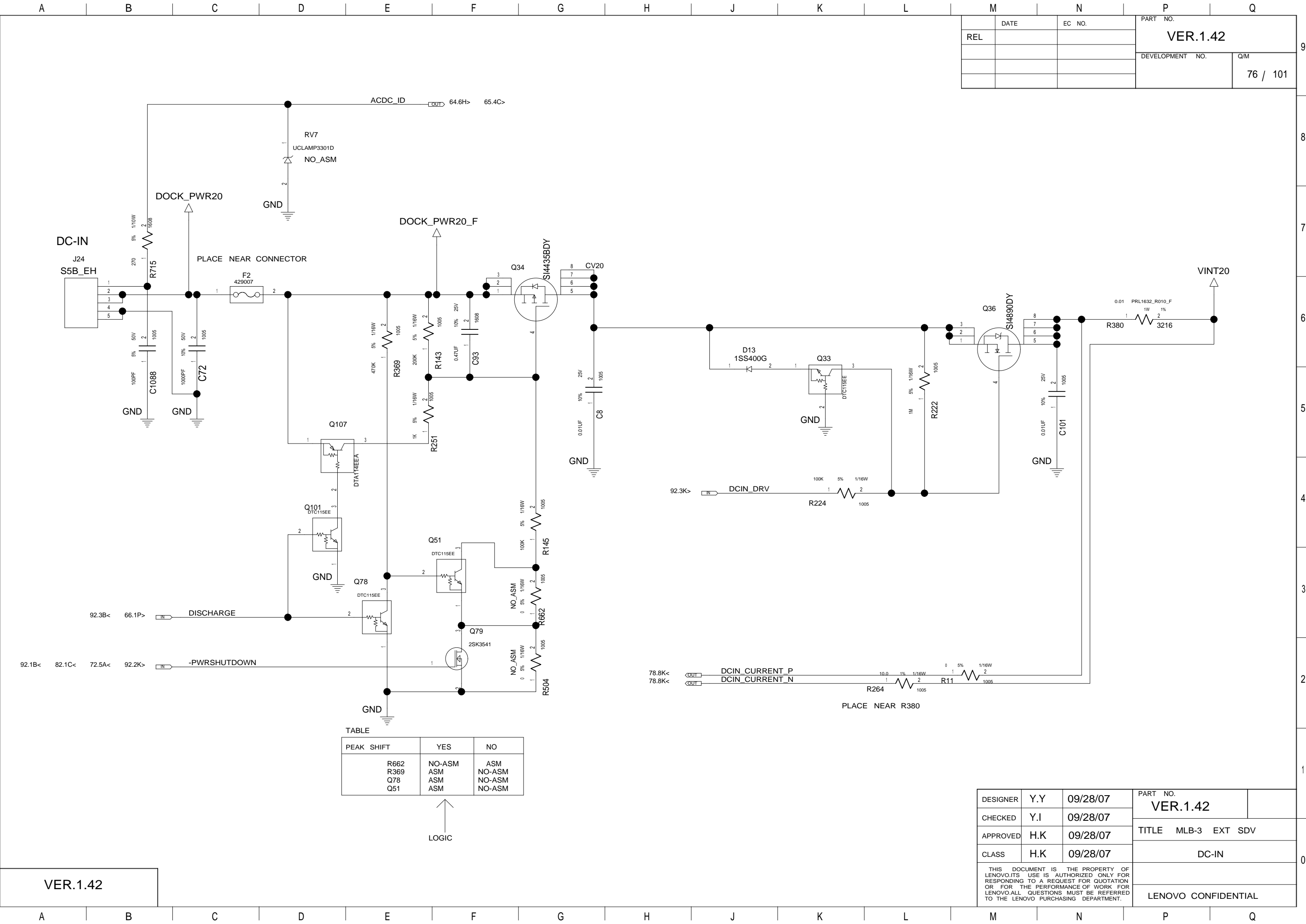
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DESIGNER	Y.Y	09/28/07	PART NO.		
CHECKED	Y.I	09/28/07	VER.1.42		
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV		
CLASS	H.K	09/28/07	THERMAL SENSOR		
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				75 / 101	

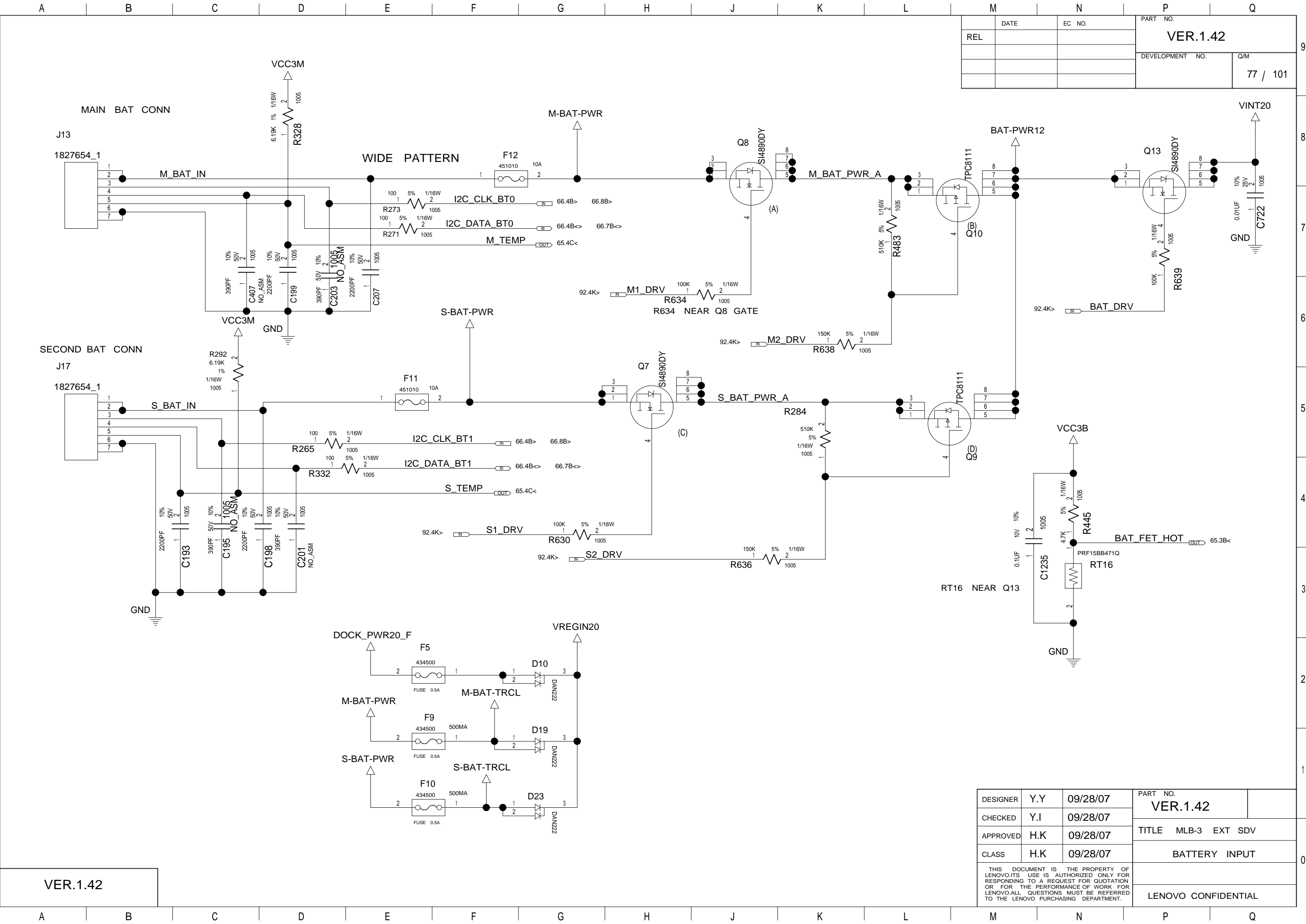
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CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	EEPROM/SMBUS SW/TPM	
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	DATE	EC NO.	PART NO.
REL			VER.1.42
			DEVELOPMENT NO.
			Q/M
			76 / 101

PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

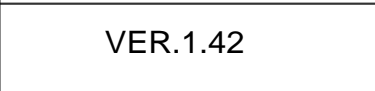
LOGIC

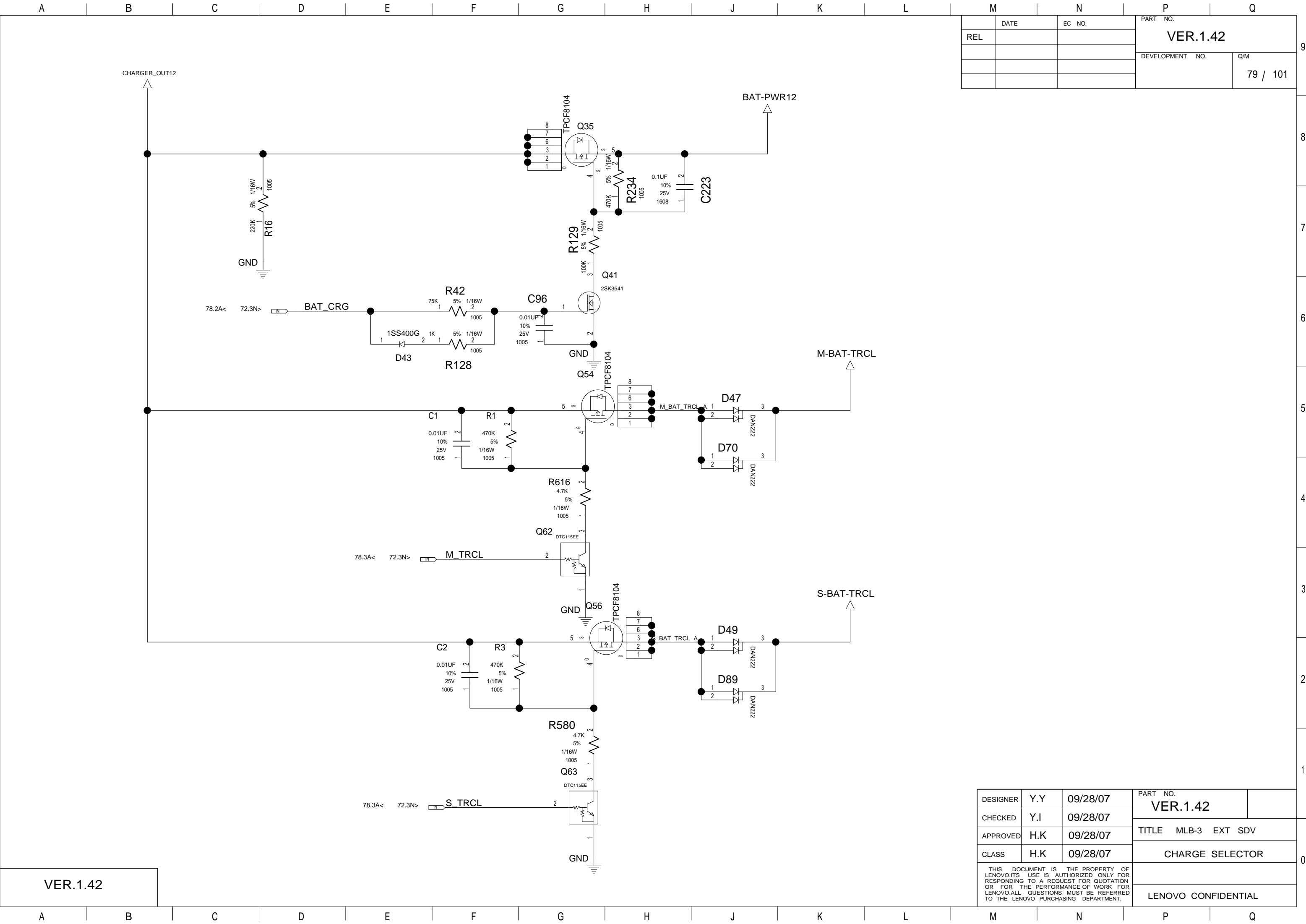


DATE		EC NO.	PART NO.	
REL			VER.1.42	
			DEVELOPMENT NO.	
			Q/M	
			77 / 101	

VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
CHECKED	Y.I	09/28/07	VER.1.42	
APPROVED	H.K	09/28/07	TITLE MLB-3 EXT SDV	
CLASS	H.K	09/28/07	BATTERY INPUT	
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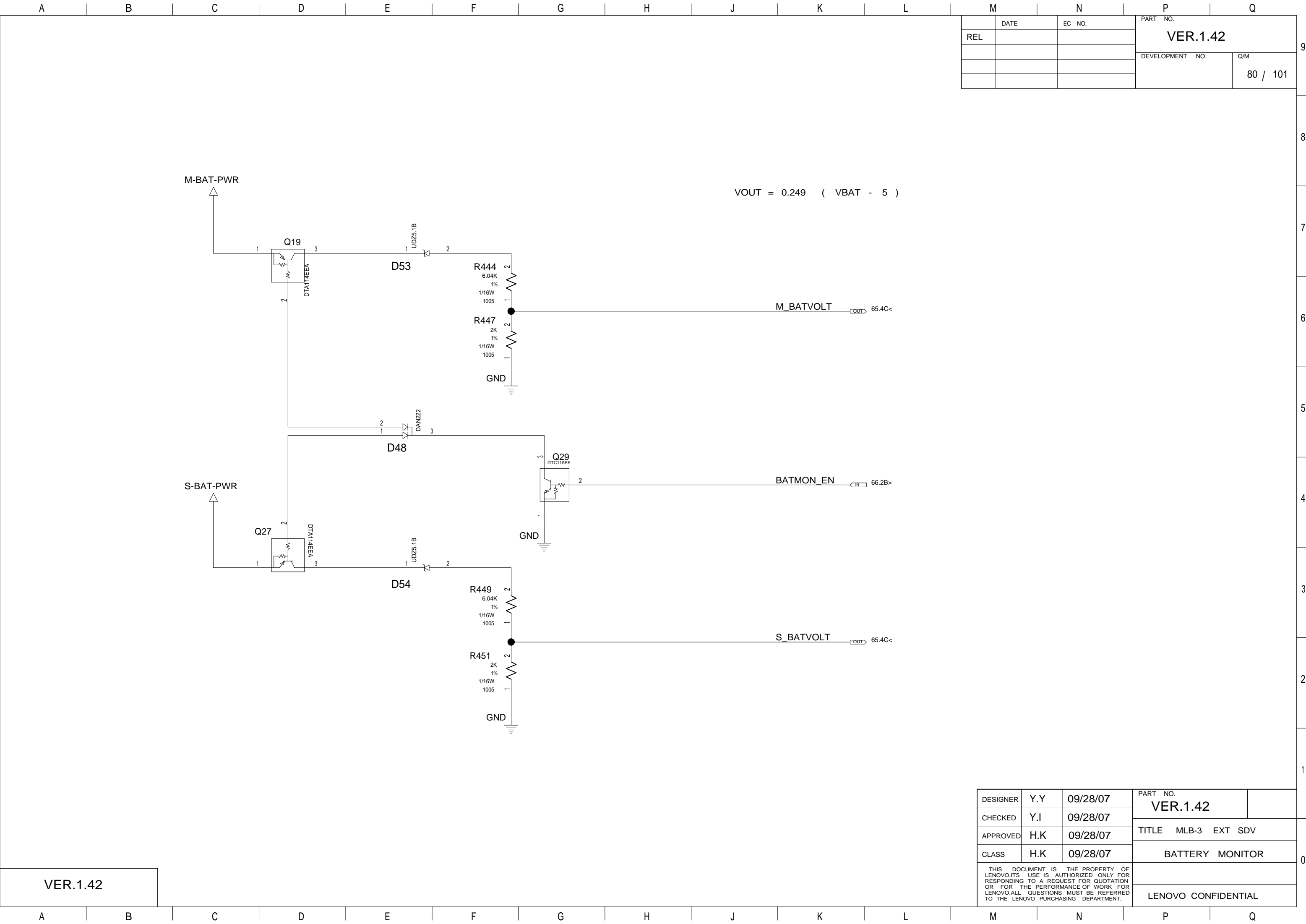




		DATE		EC NO.		PART NO.	
REL						VER.1.42	
						DEVELOPMENT NO.	
						Q/M	
						79 / 101	

VER.1.42

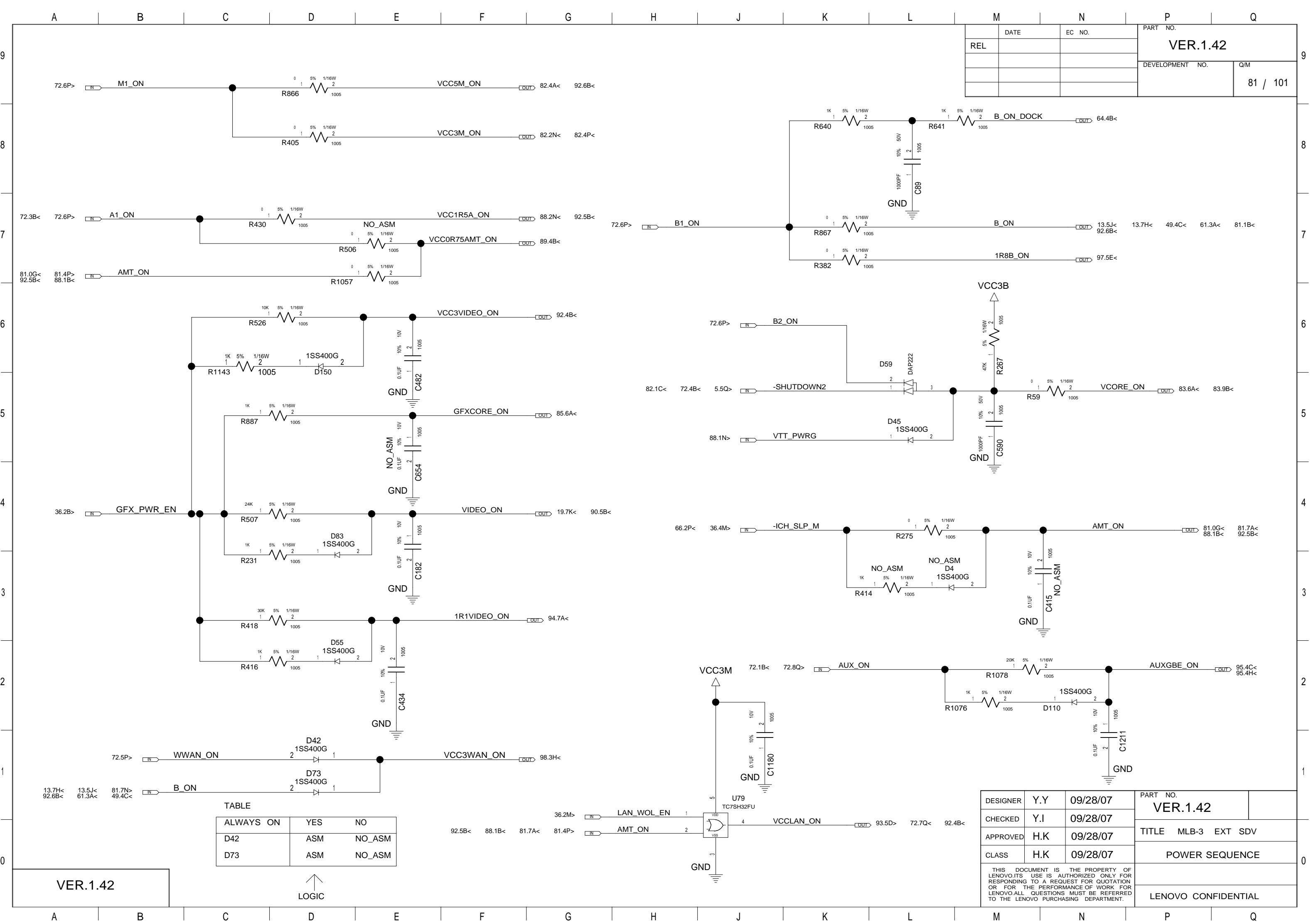
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CLASS	H.K	09/28/07	CHARGE SELECTOR		
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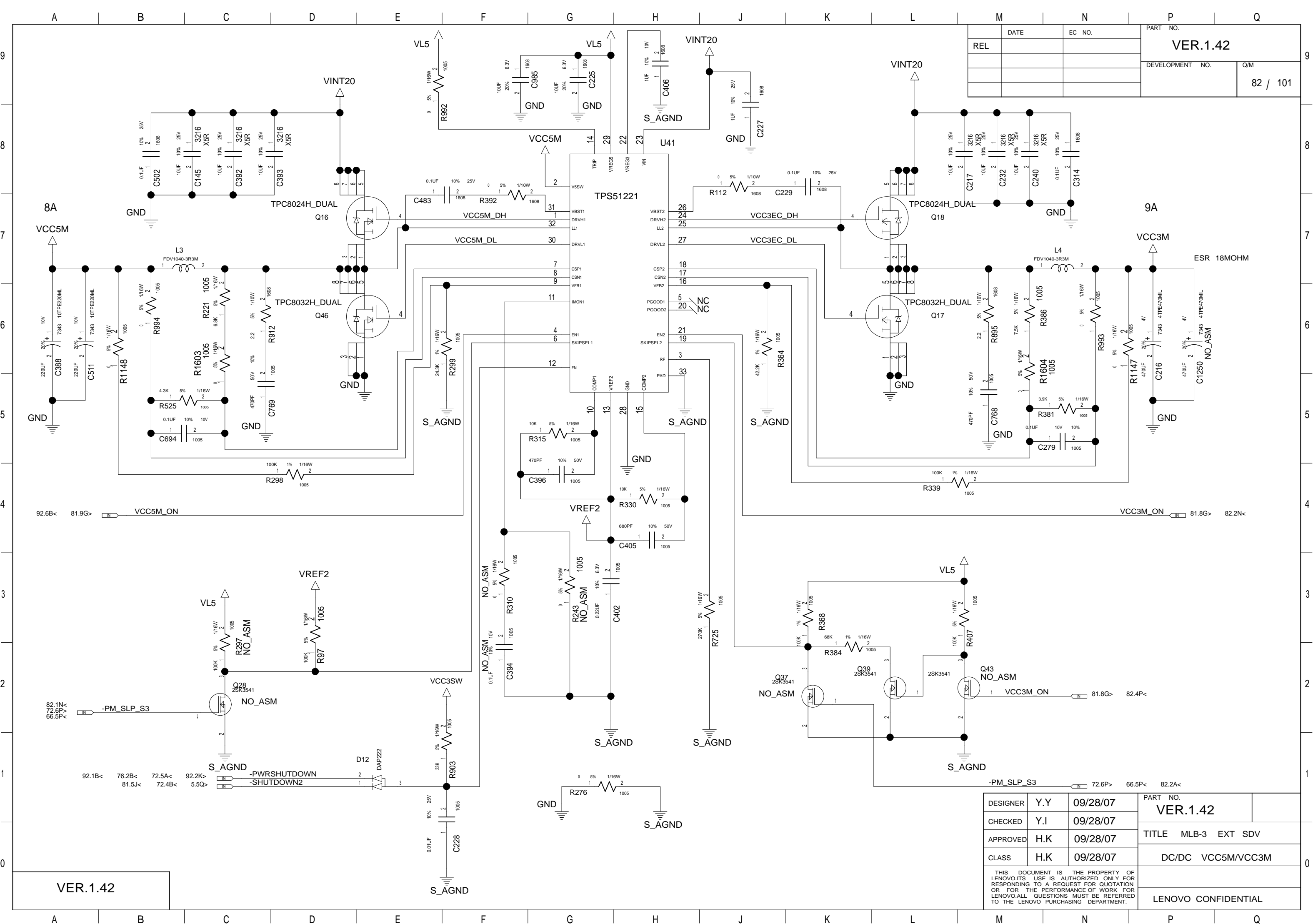


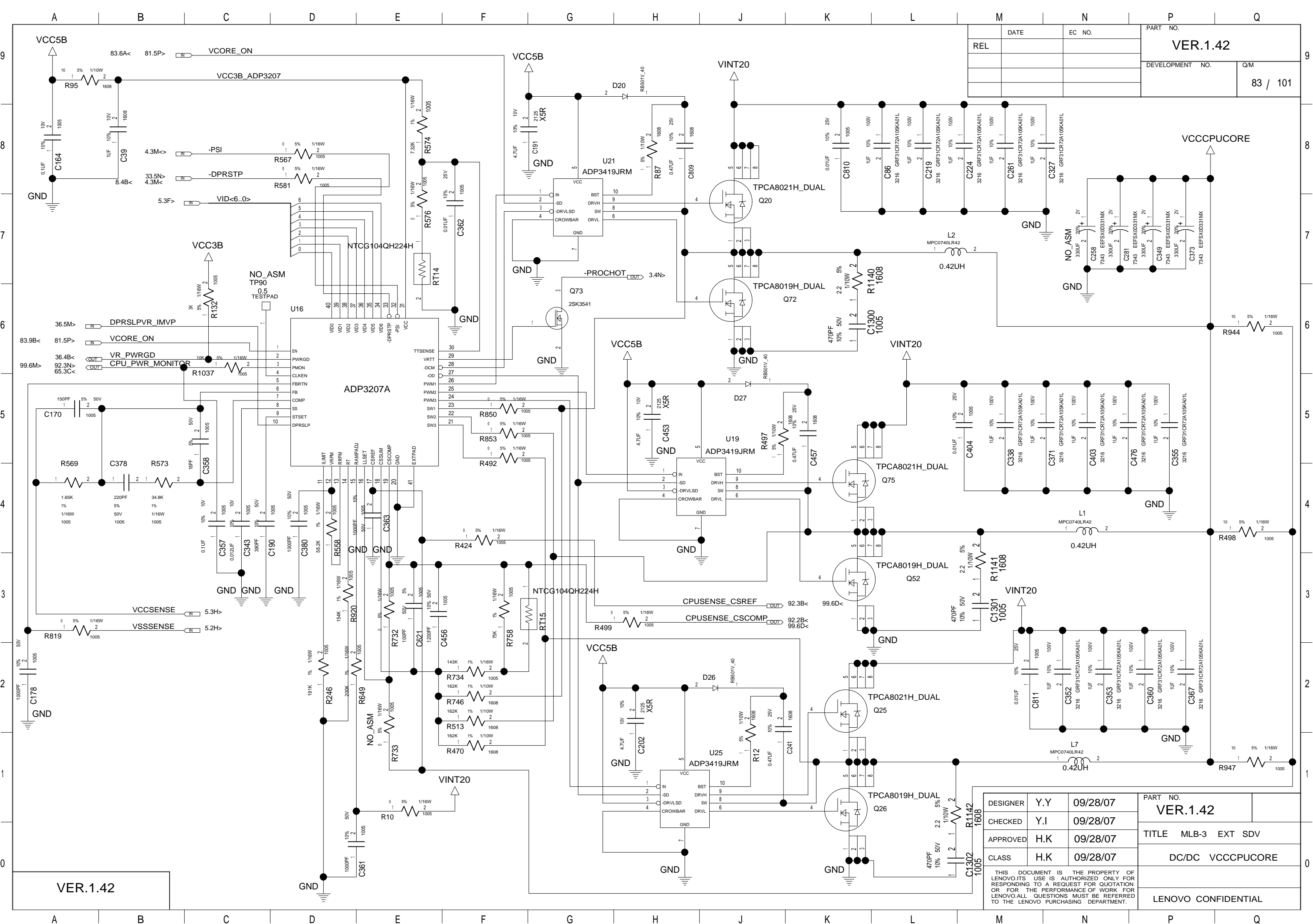
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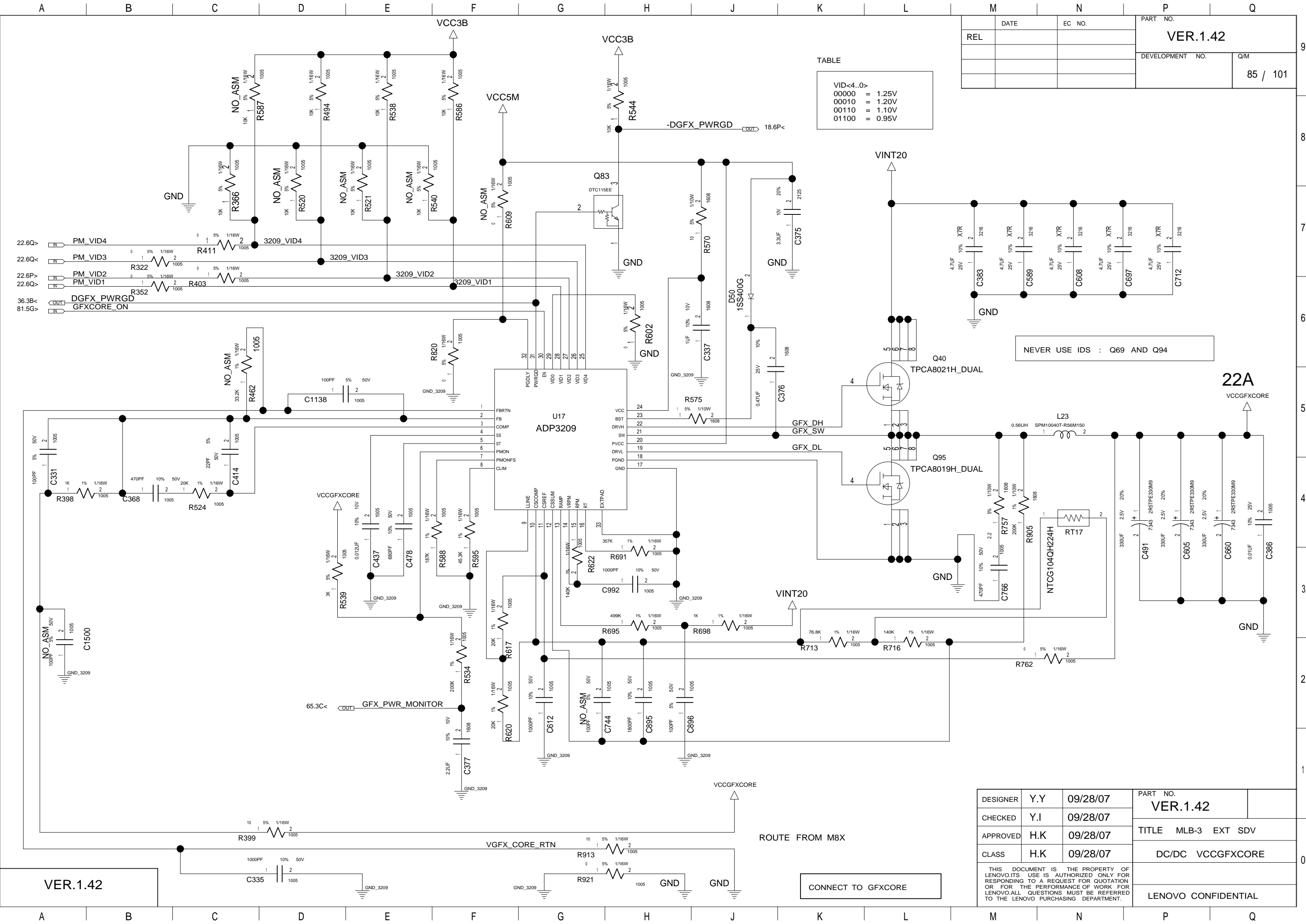
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CLASS	H.K	09/28/07	BATTERY MONITOR			
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
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				85 / 101	

TABLE	
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00010	= 1.20V
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NEVER USE IDS : Q69 AND Q94

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CLASS	H.K	09/28/07	DC/DC VCCGFXCORE	
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A		B		C		D		E		F		G		H		J		K		L		M		N		P		Q	

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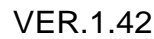
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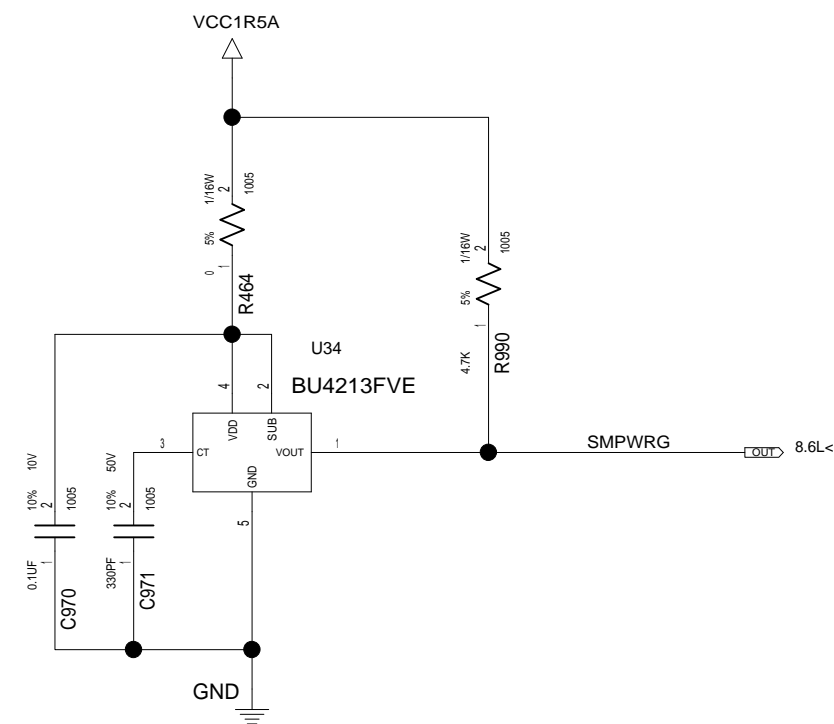
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VER.1.42

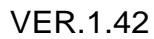
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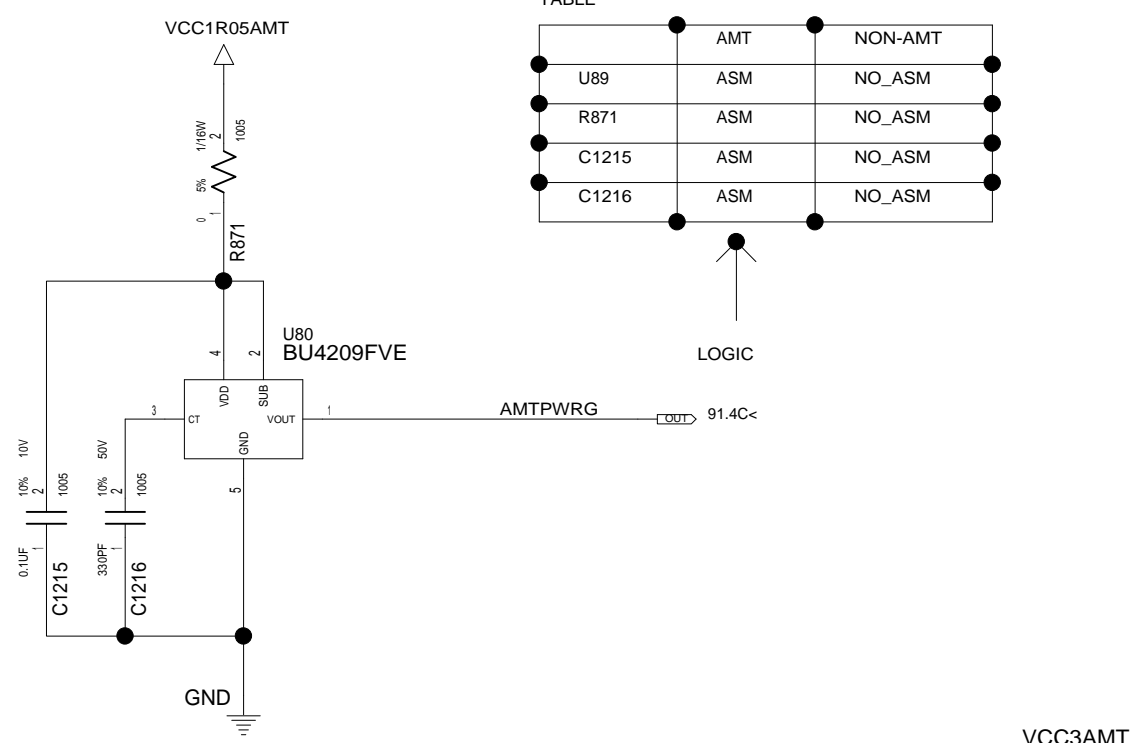
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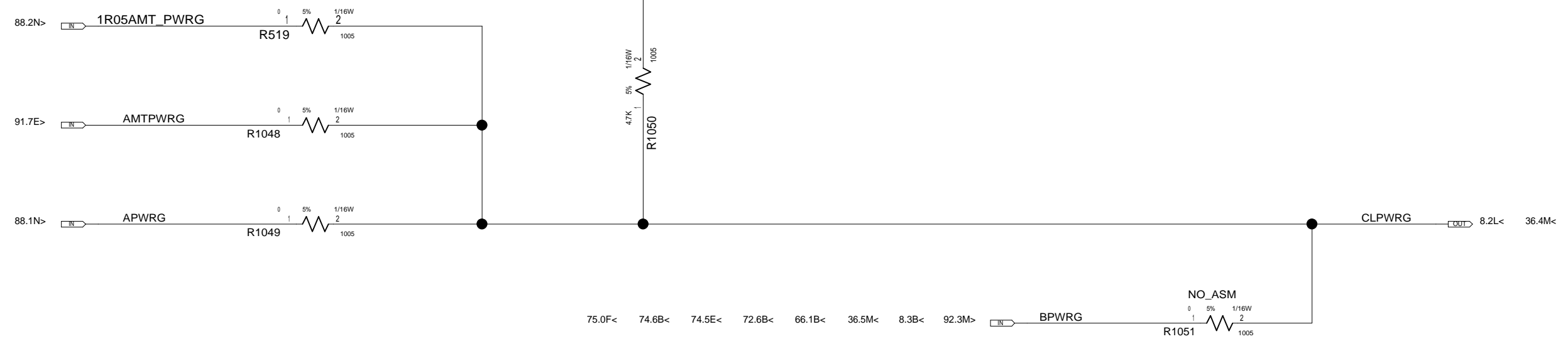


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REL			VER.1.42	
			DEVELOPMENT NO.	Q/M
				91 / 101



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LOGIC

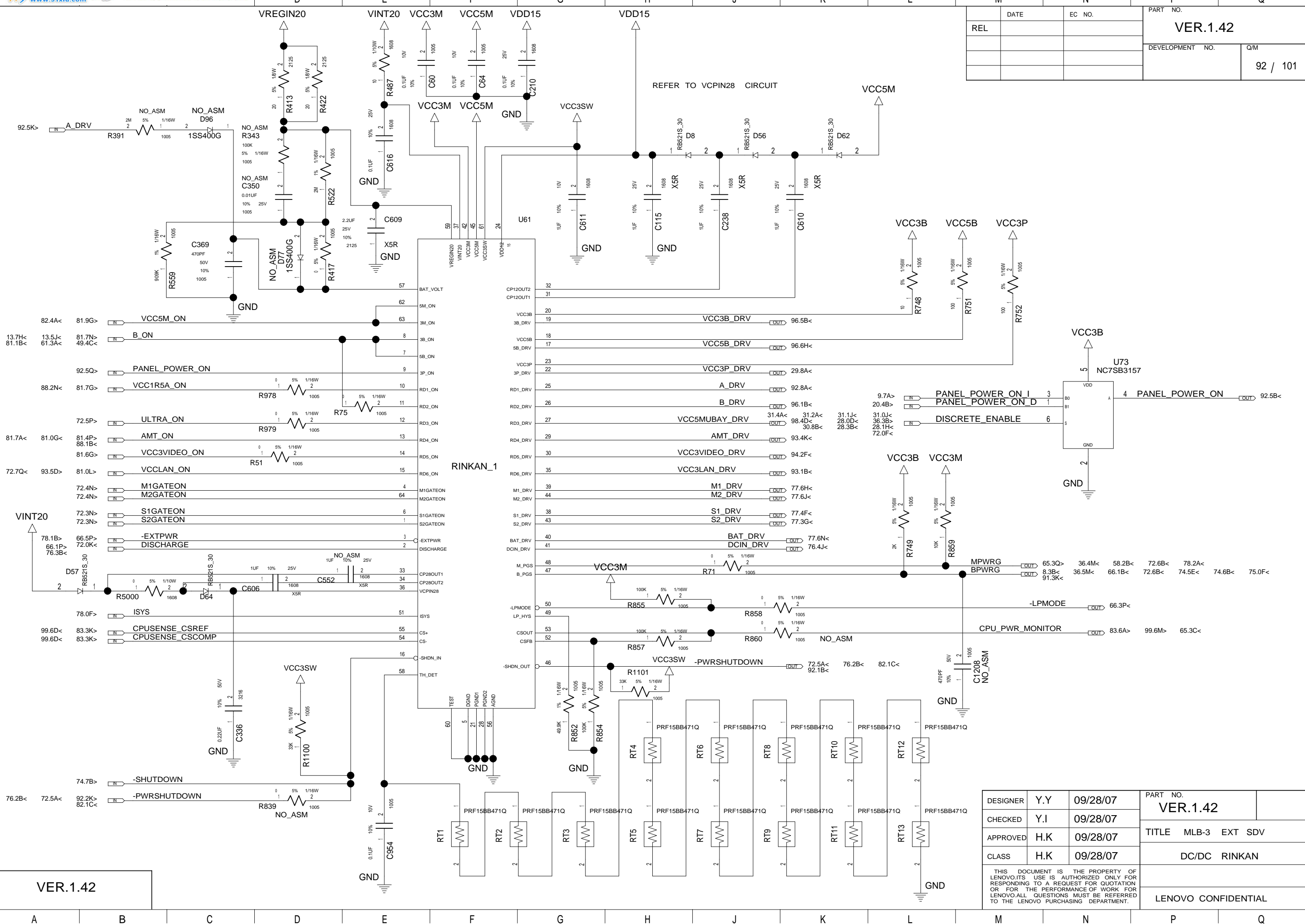


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OTHERS	ASM	NO_ASM

LOGIC

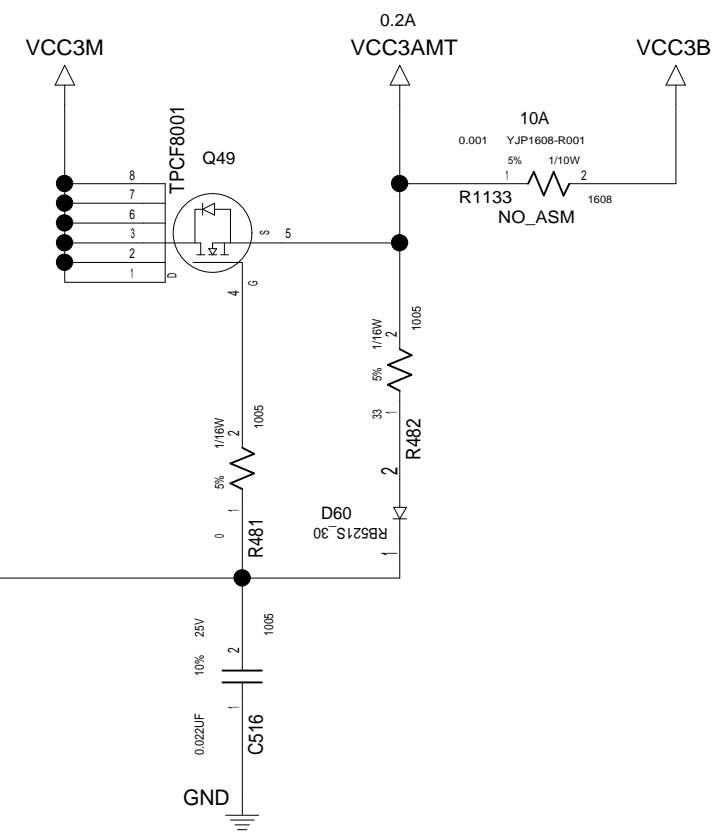
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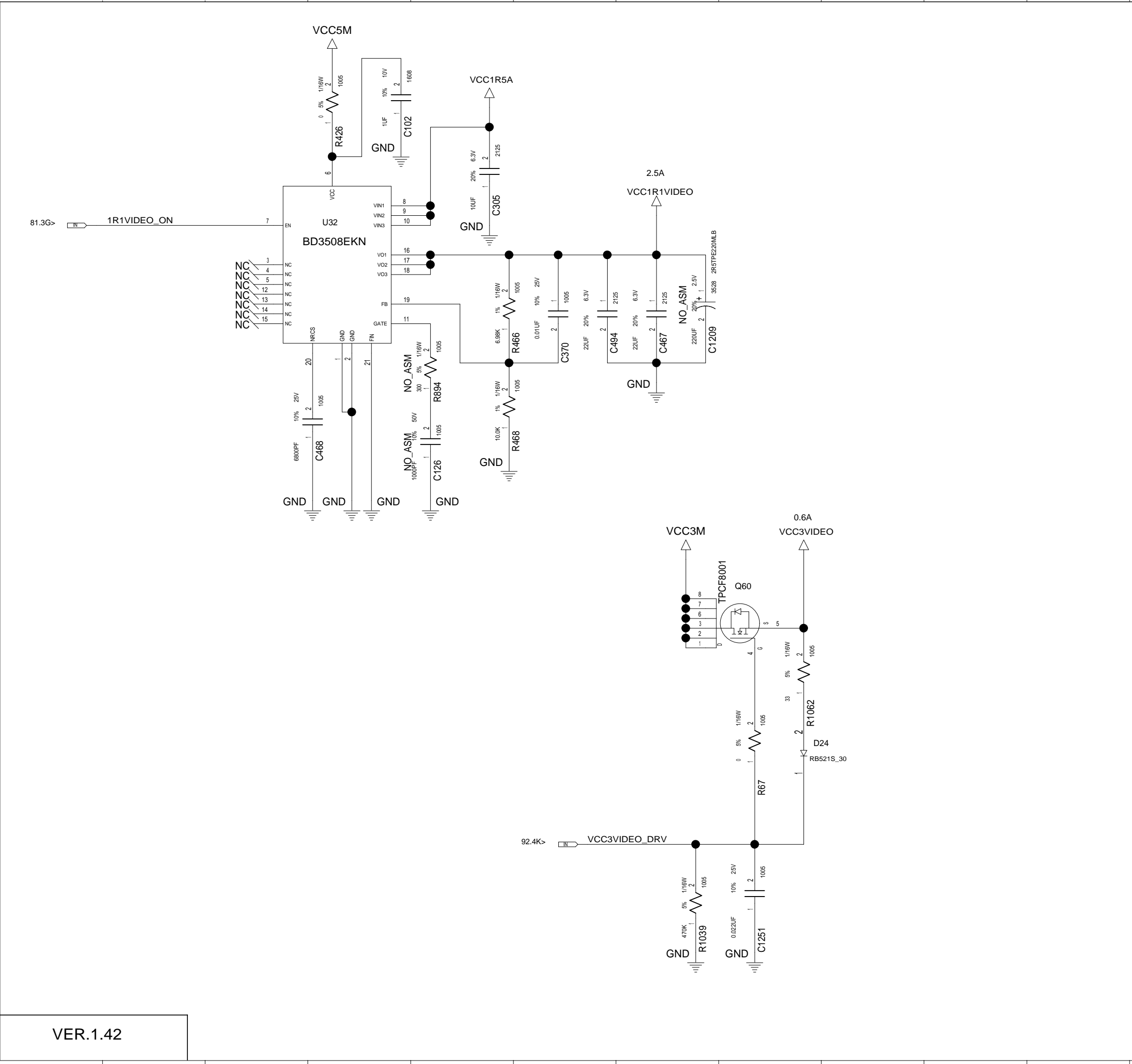




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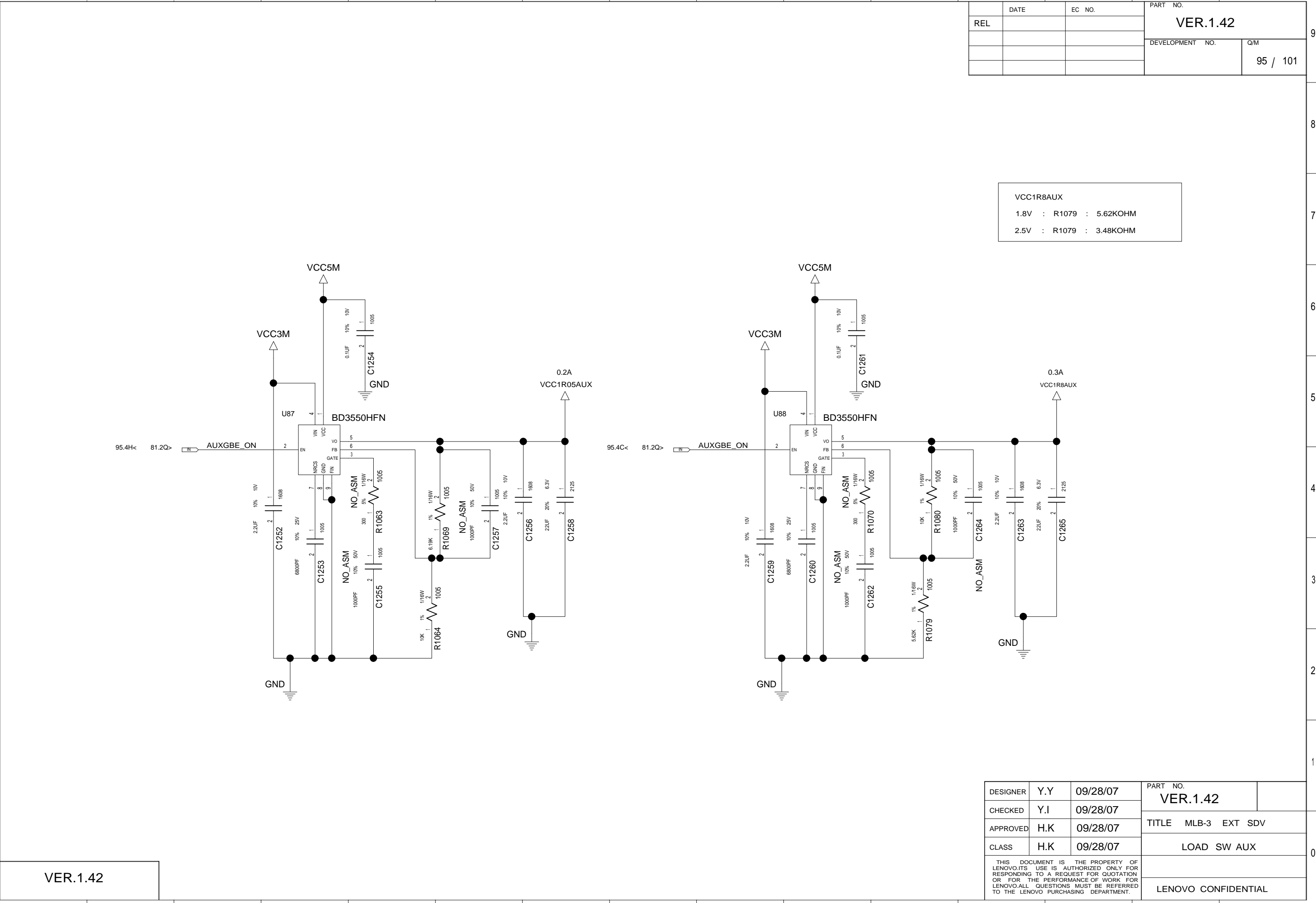
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DATE		EC NO.		PART NO.	
REL				VER.1.42	
				DEVELOPMENT NO.	
				Q/M	
				94 / 101	

VER.1.42

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CLASS	H.K	09/28/07	LOAD SW VIDEO	
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VER.1.42

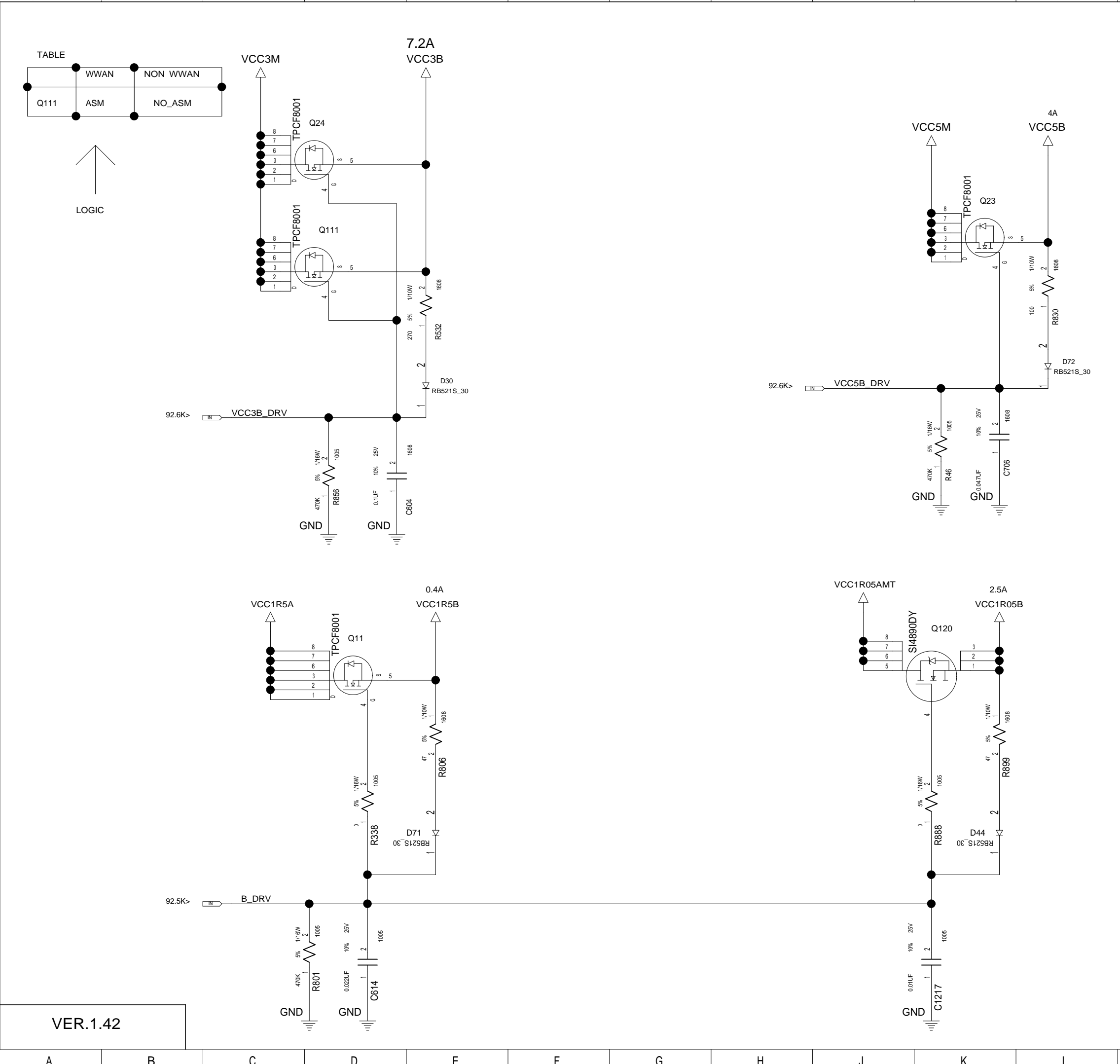
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VER.1.42

DEVELOPMENT NO.

Q/M

95 / 101

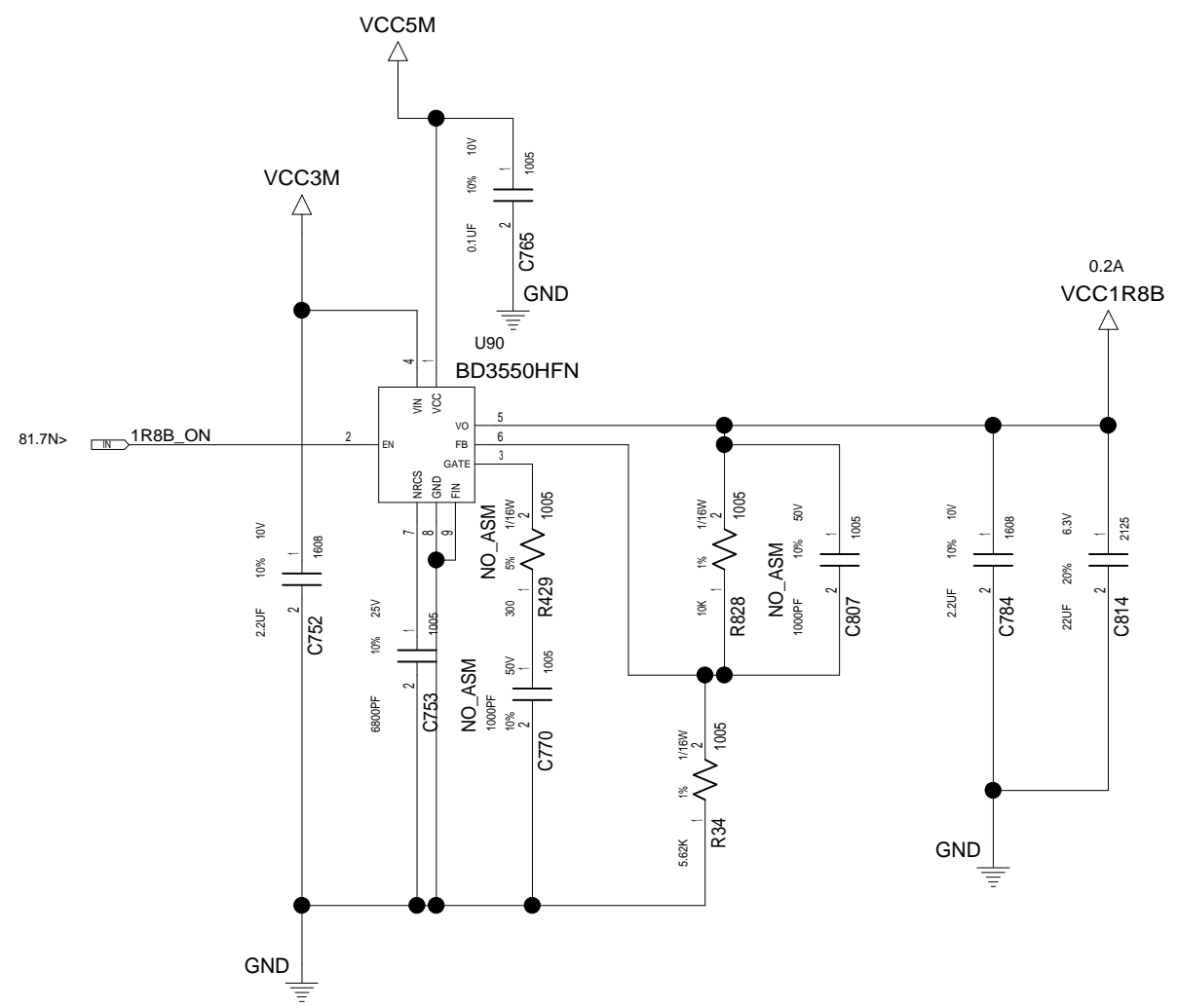


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REL			VER.1.42	
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VER.1.42

DESIGNER	Y.Y	09/28/07	PART NO.	
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CLASS	H.K	09/28/07	LOAD SW B	
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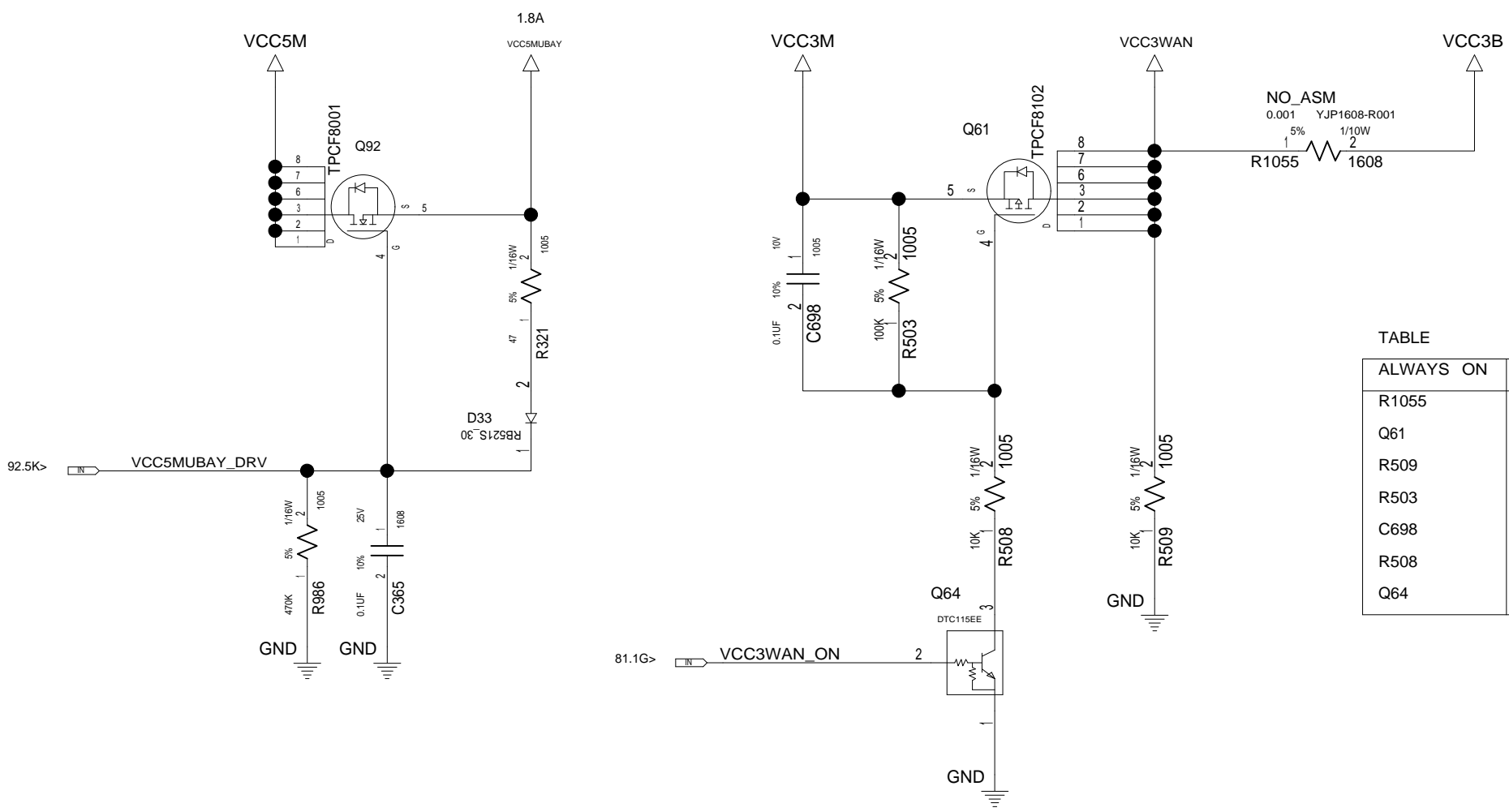
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VER.1.42

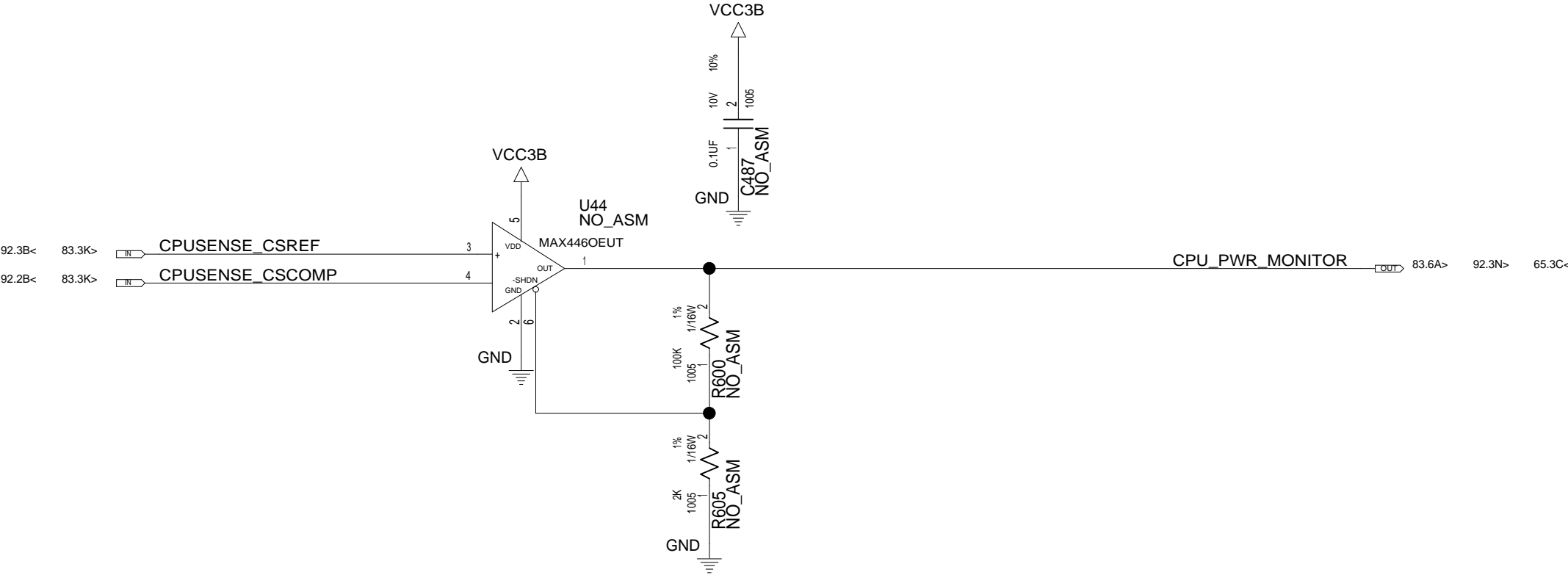
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CLASS	H.K	09/28/07	DC/DC VCC1R8B	
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REL			VER.1.42	
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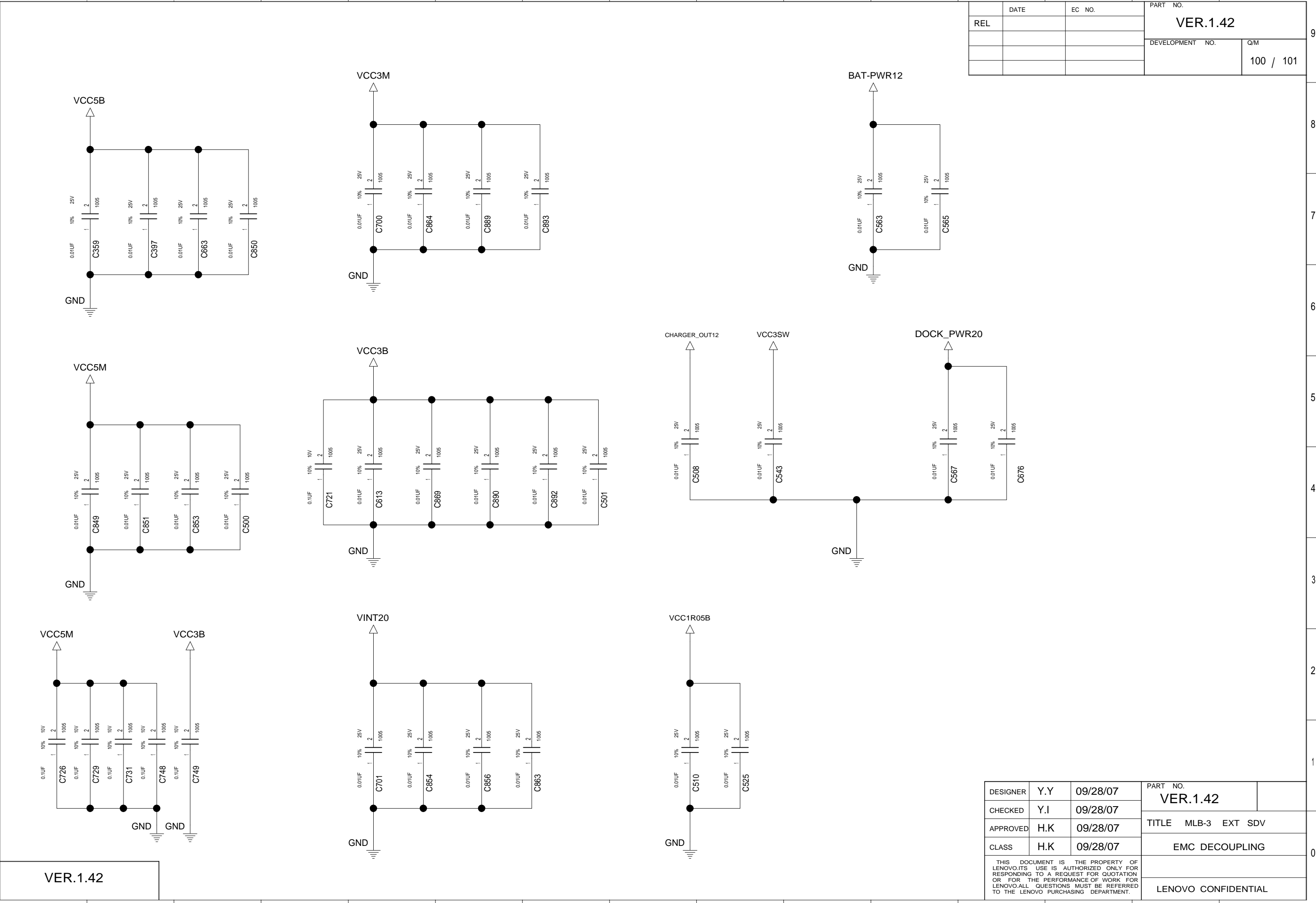


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REL				VER.1.42			
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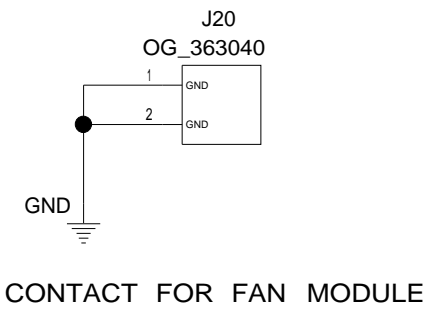
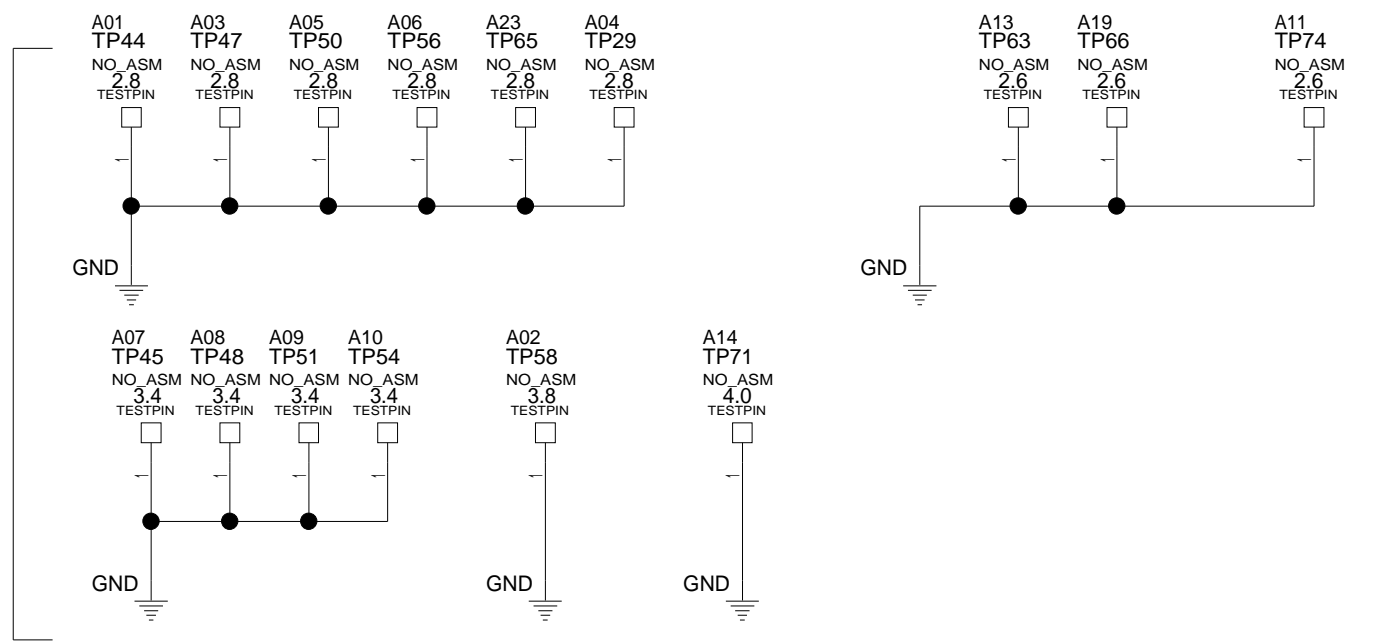
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				101 / 101

PTH FOR SCREW HOLE



CONTACT FOR FAN MODULE

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