

CLK-GEN
ICS954226

HOST 133/166MHz
PCIE 100MHz
VGA 96MHz
USB 48MHz
PCI 33MHz
REF 14MHz

Page 2

3V / 5V

+3VPCU
+3V_S5
+3VSUS
+5VSUS
+3V
+5V
+12V

Page 22

1.8V /
1.05V

+1.8VSUS
SMDDR_VTERM
+1.05V

Page 24

1.5V /
2.5V

+1.5V
+2.5V

Page 25

CPU
CORE

+VCC_CORE

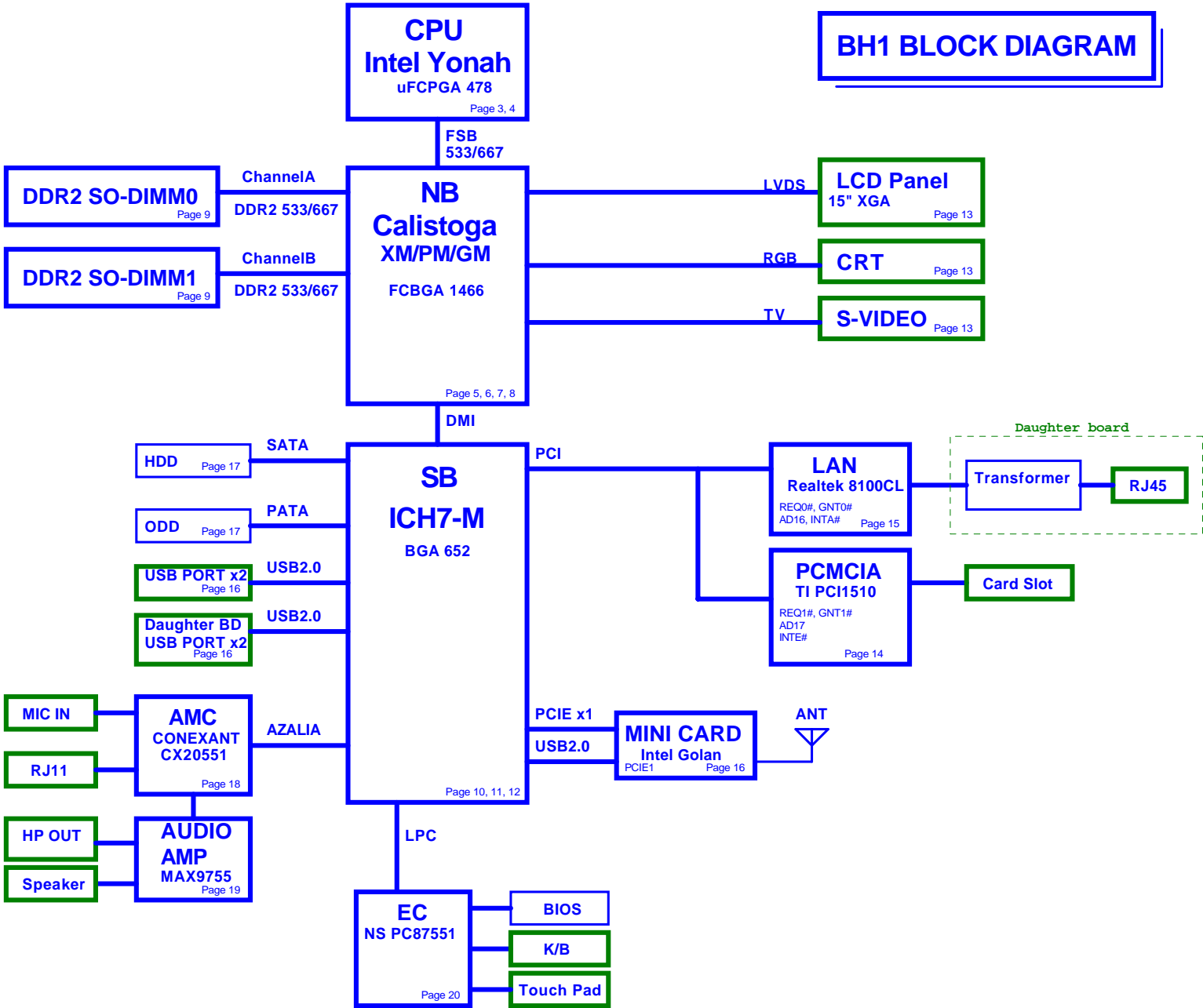
Page 23

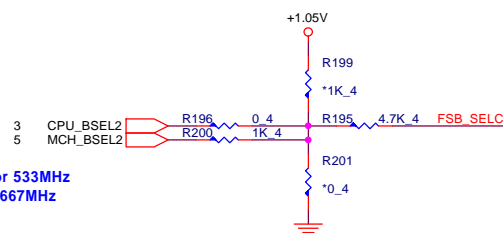
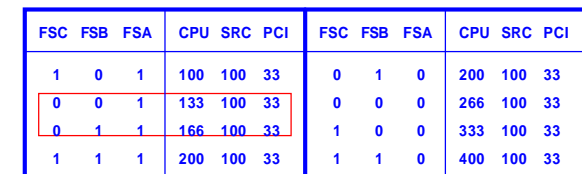
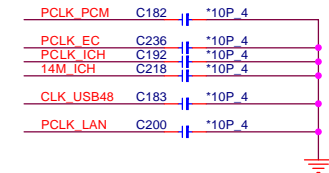
BATTERY
CHARGER

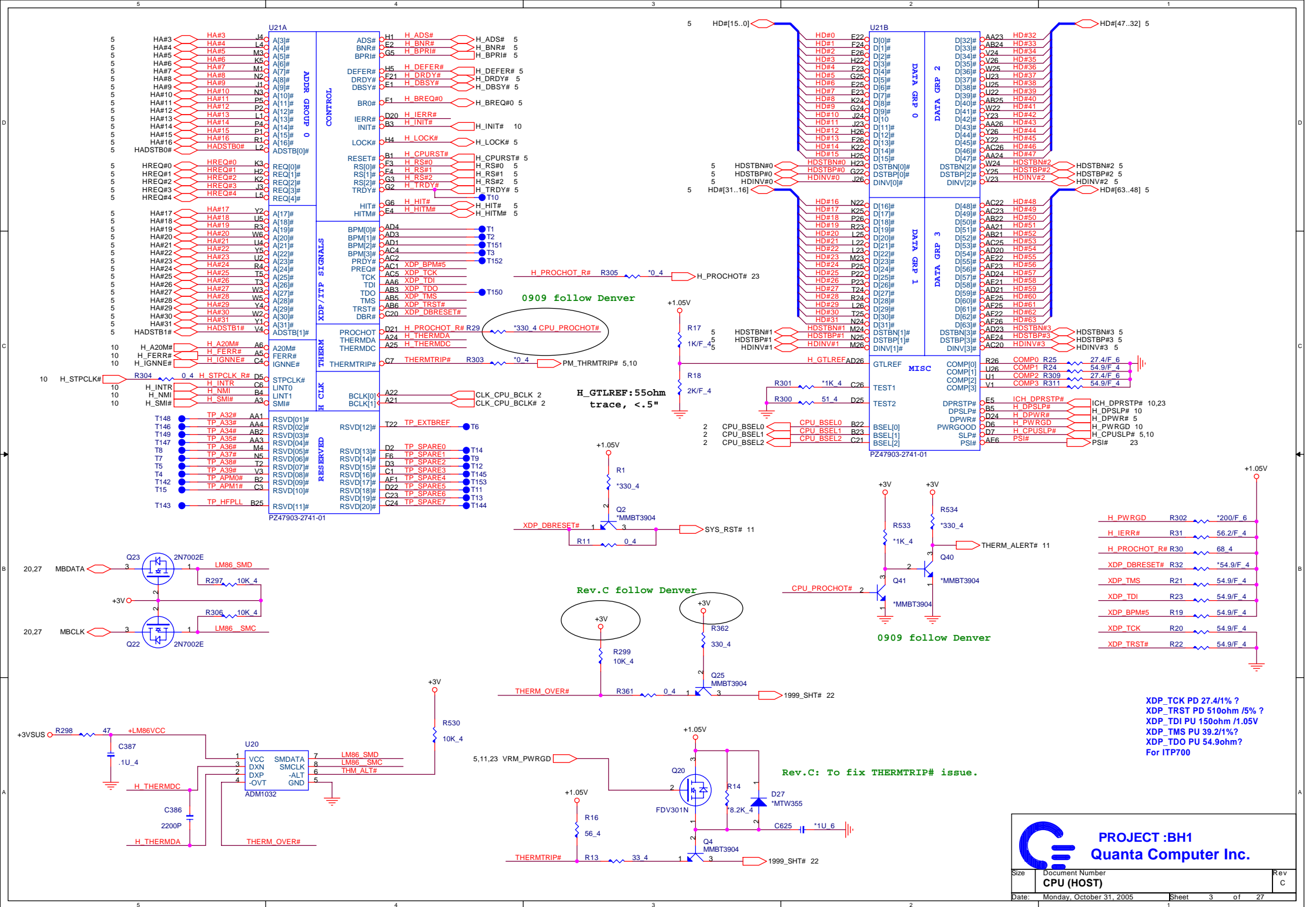
VIN

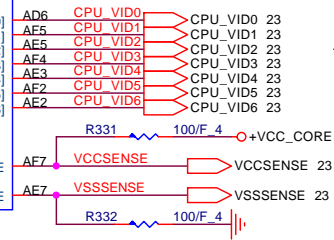
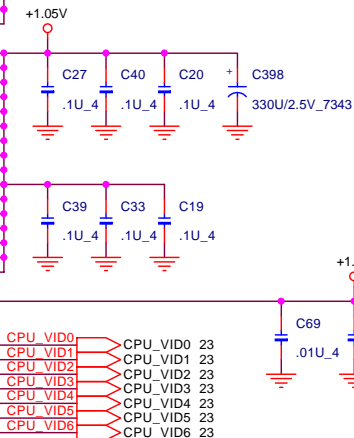
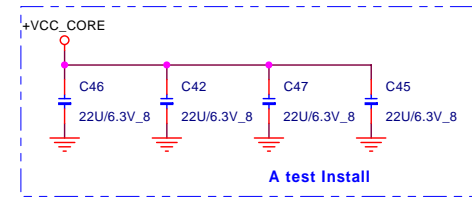
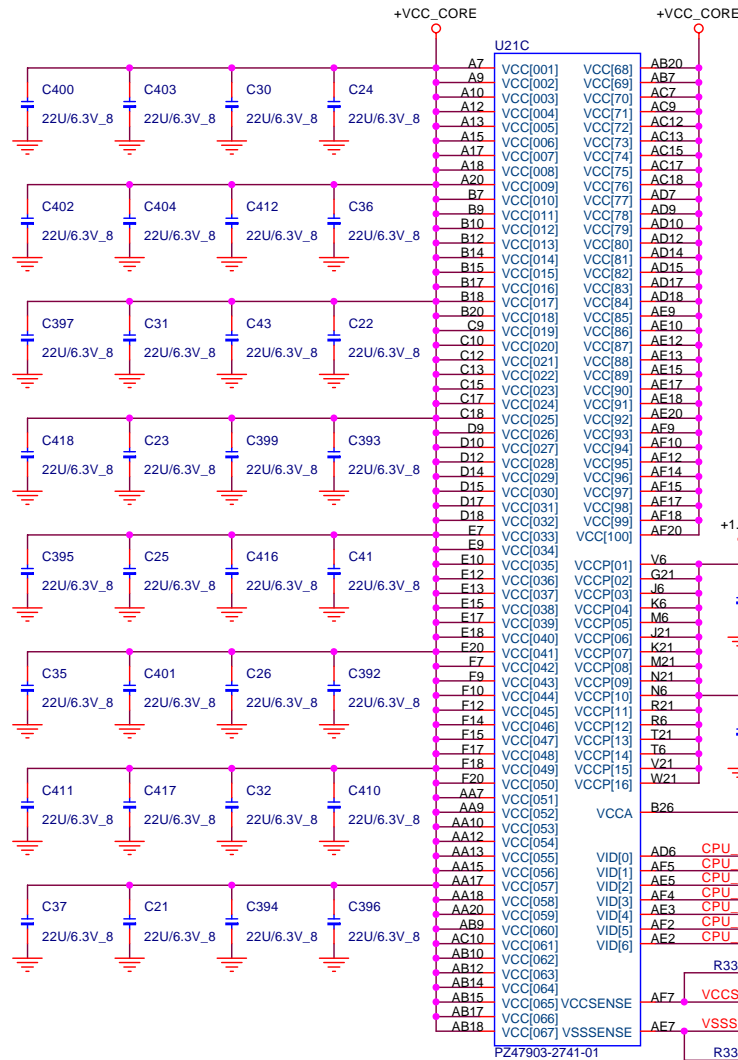
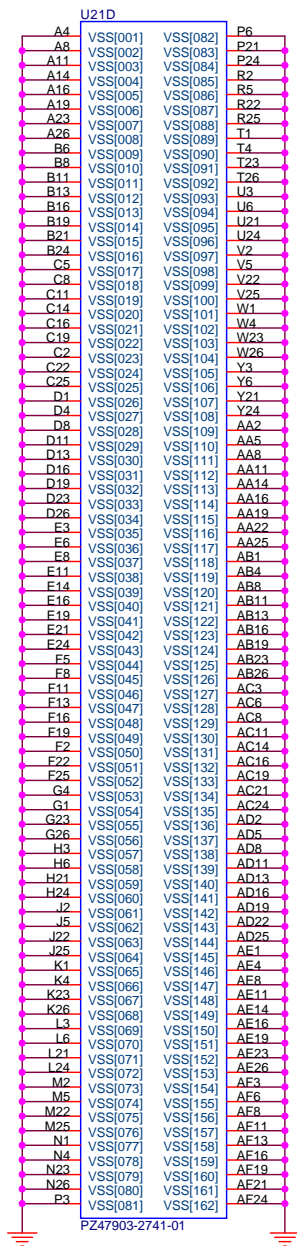
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Power State Table		
Power Name	Control Signal	Power State
+3VPCU +3V_S5 +3VSUS +3V	N/A S5_ON SUSON MAINON	ALWAYS S0 - S5 S0 - S3 S0
+5VSUS +5V	SUSON MAINON	S0 - S3 S0
+12V	MAINON	S0
+1.8VSUS +SMDDR_VTERM	SUSON MAINON	S0 - S3 S0
+1.05V	MAINON	S0
+1.5V	MAINON	S0
+2.5V	MAINON	S0
+VCC_CORE	VRON	S0



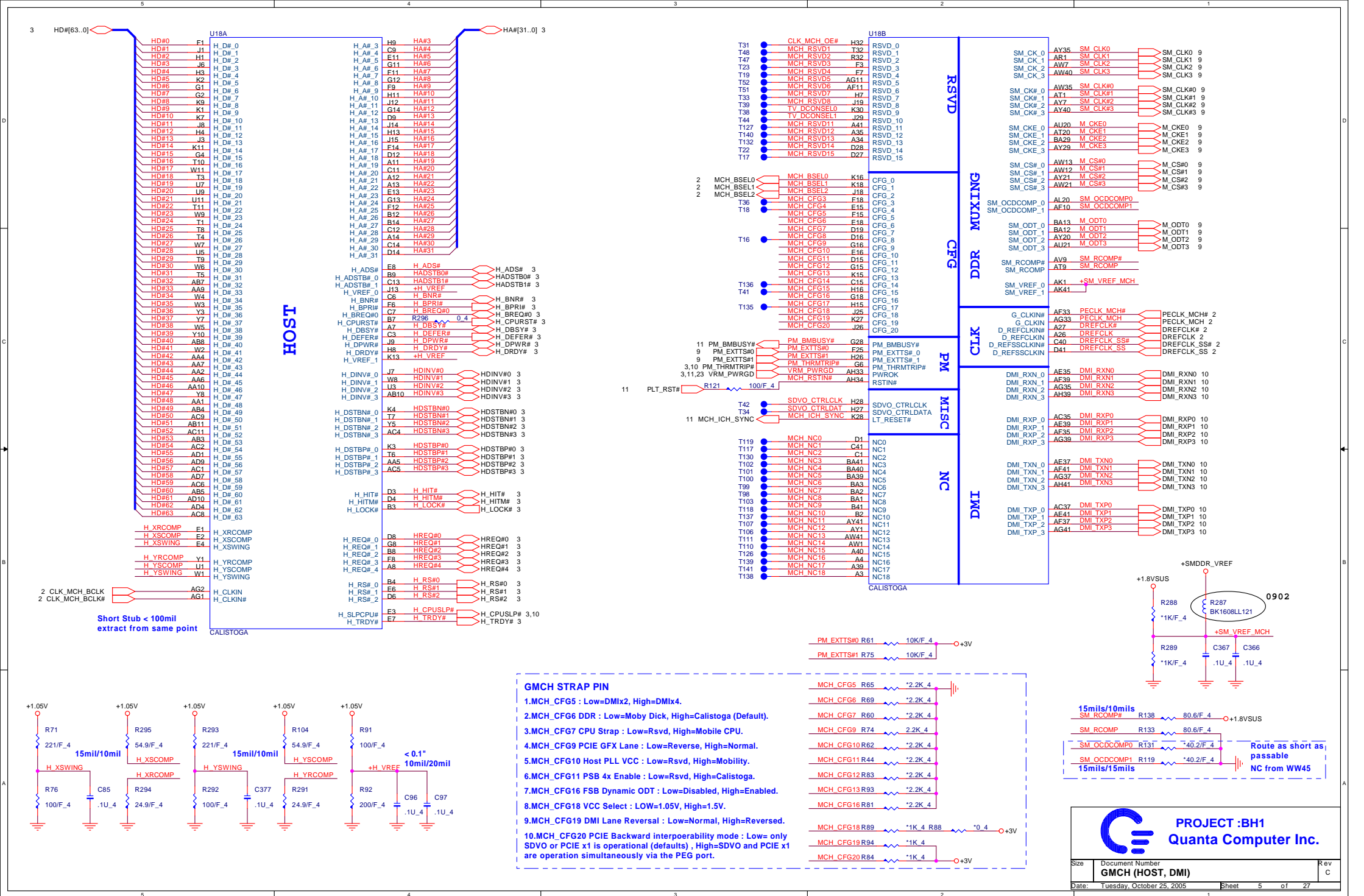




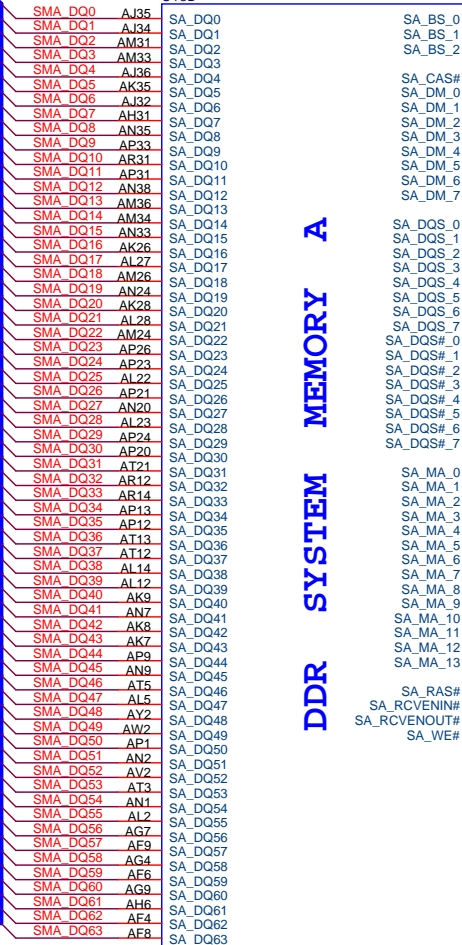


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	CPU (POWER)	C
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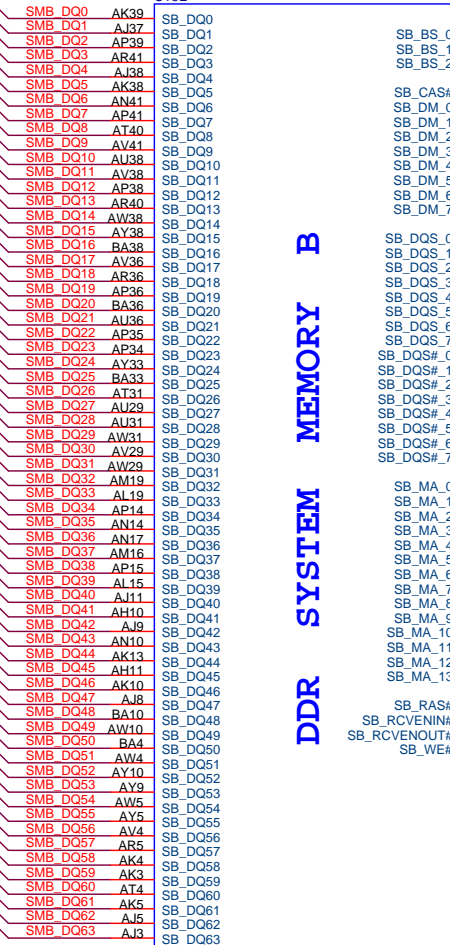


9 SMA_DQ[63..0]



CALISTOGA

9 SMB_DQ[63..0]

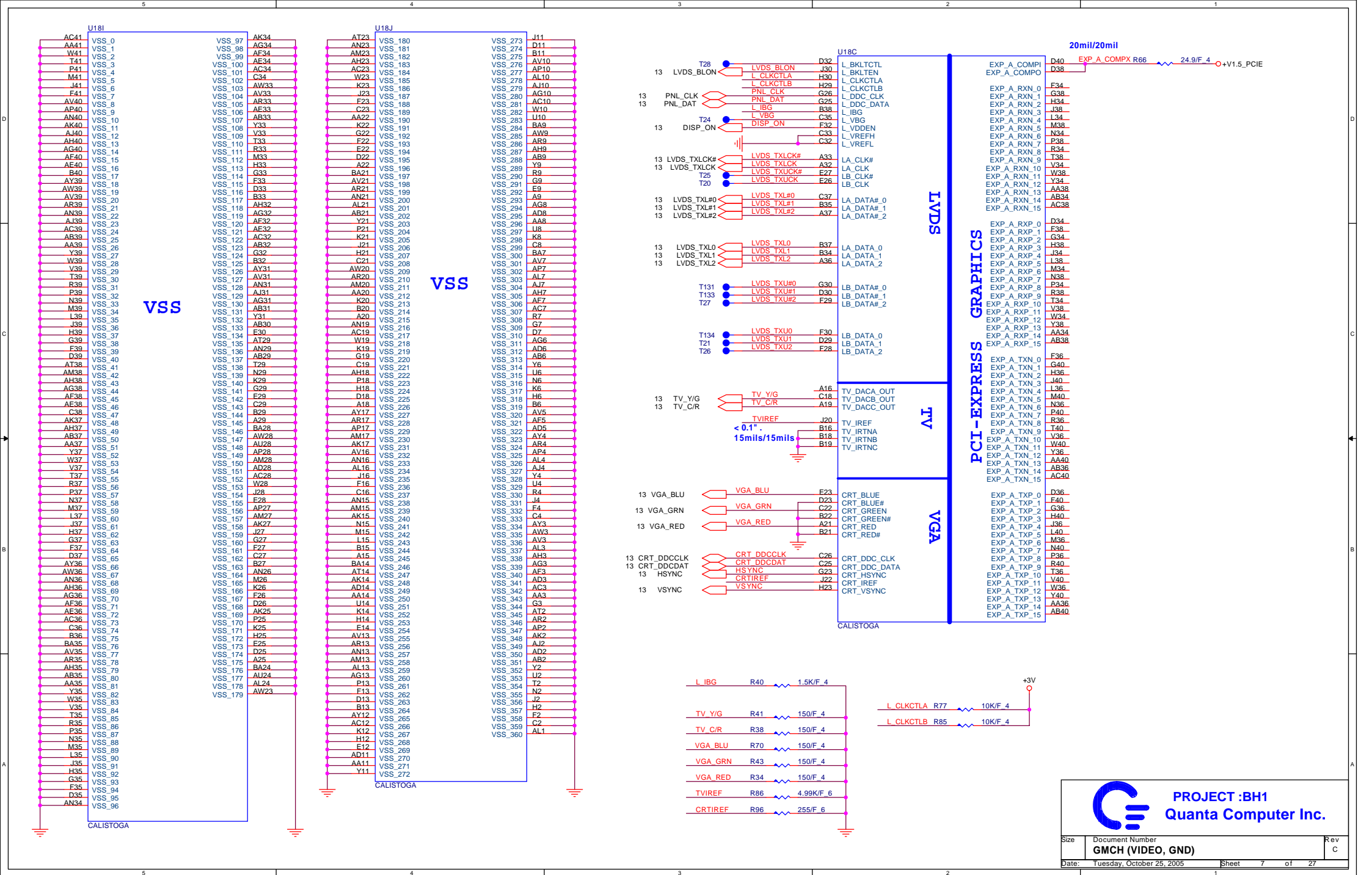


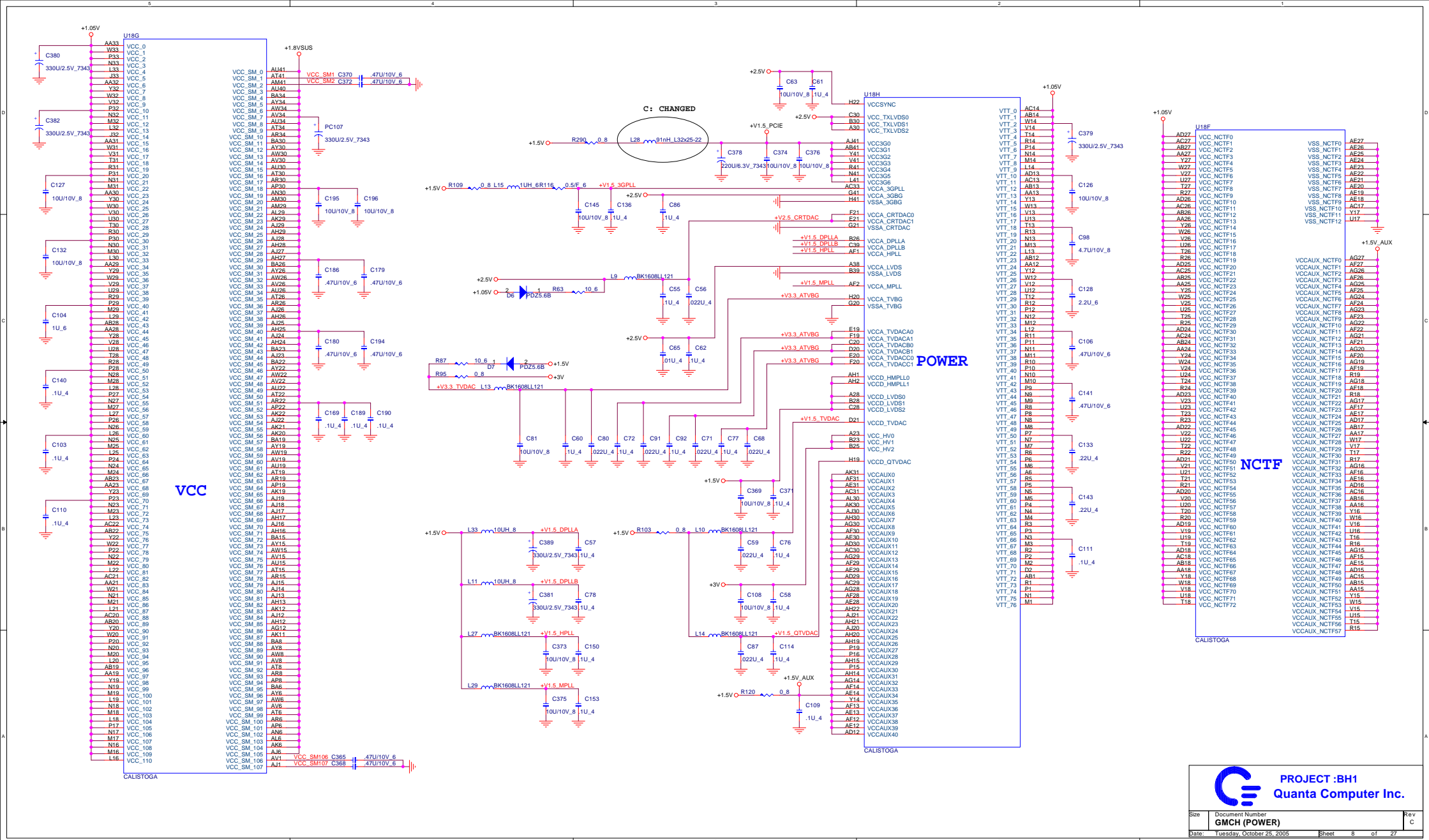
CALISTOGA

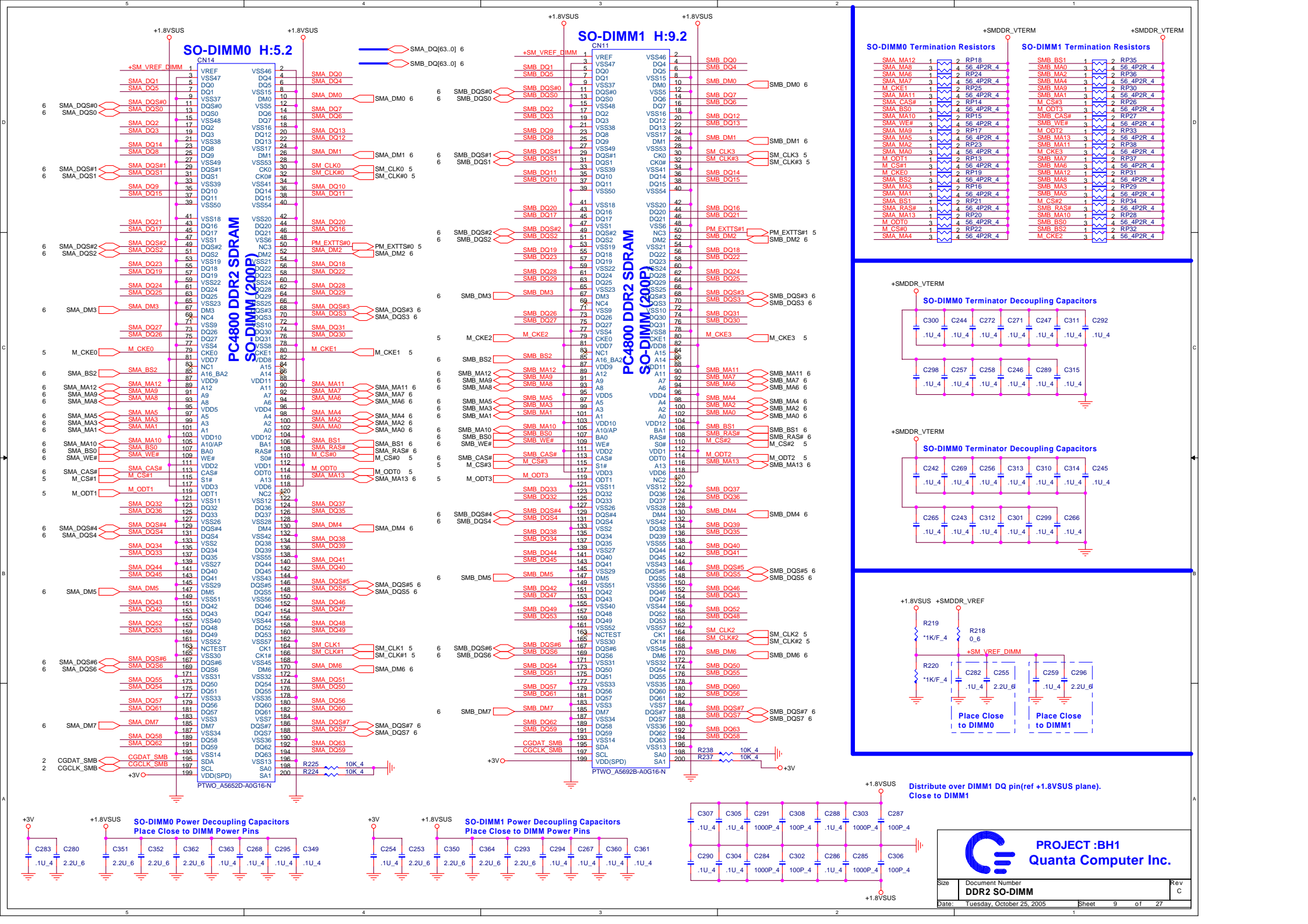


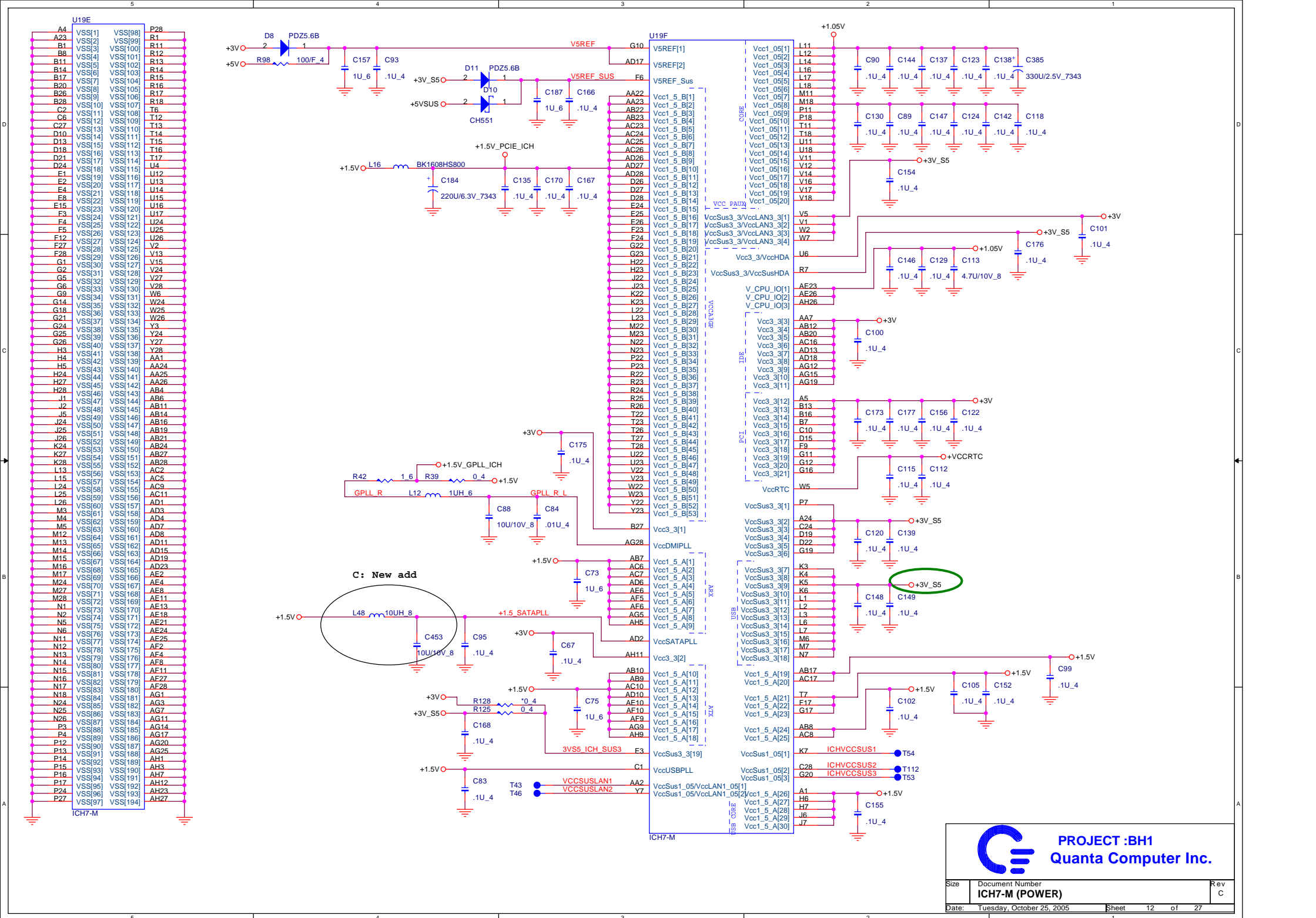
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	GMCH (MEMORY)	C
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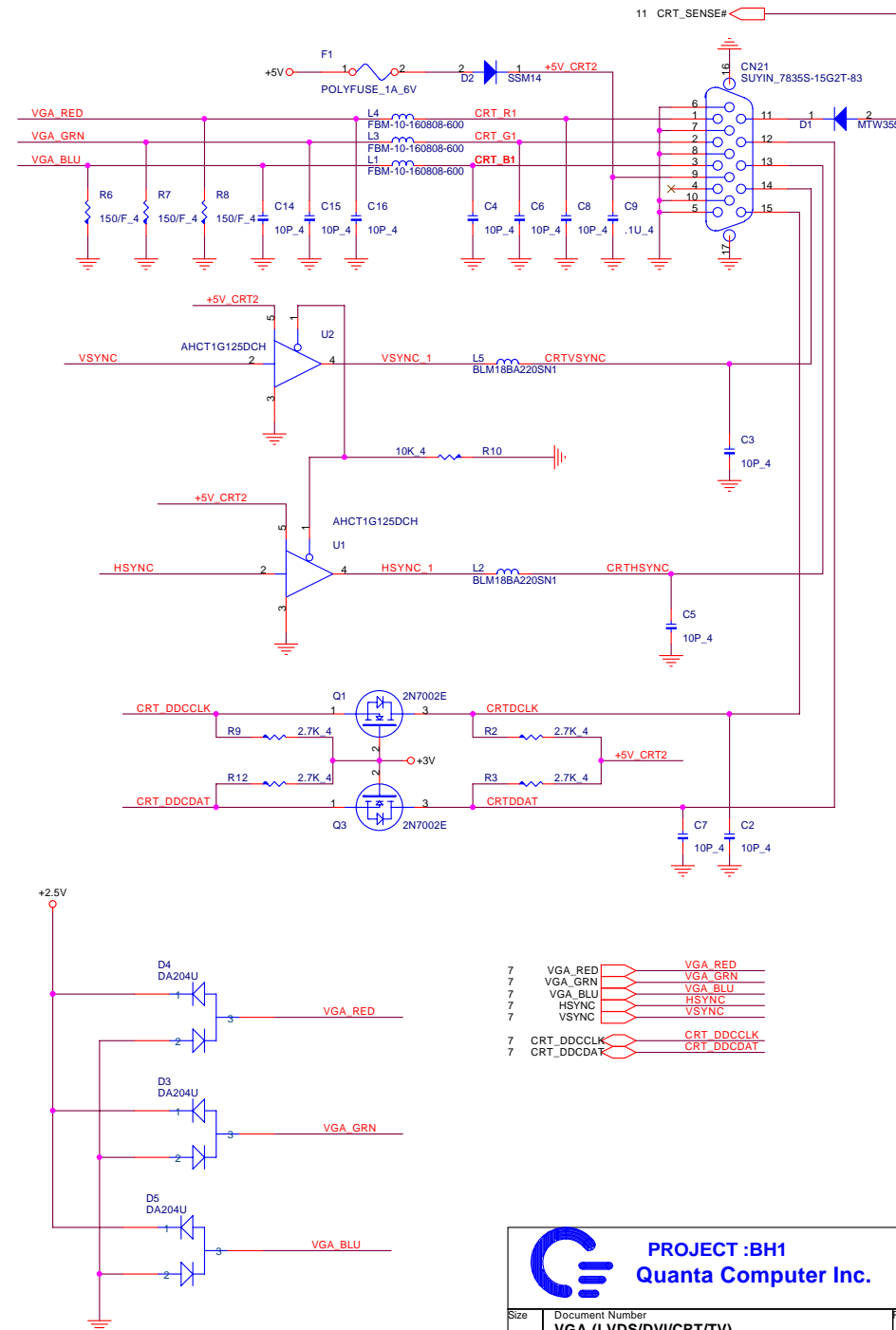
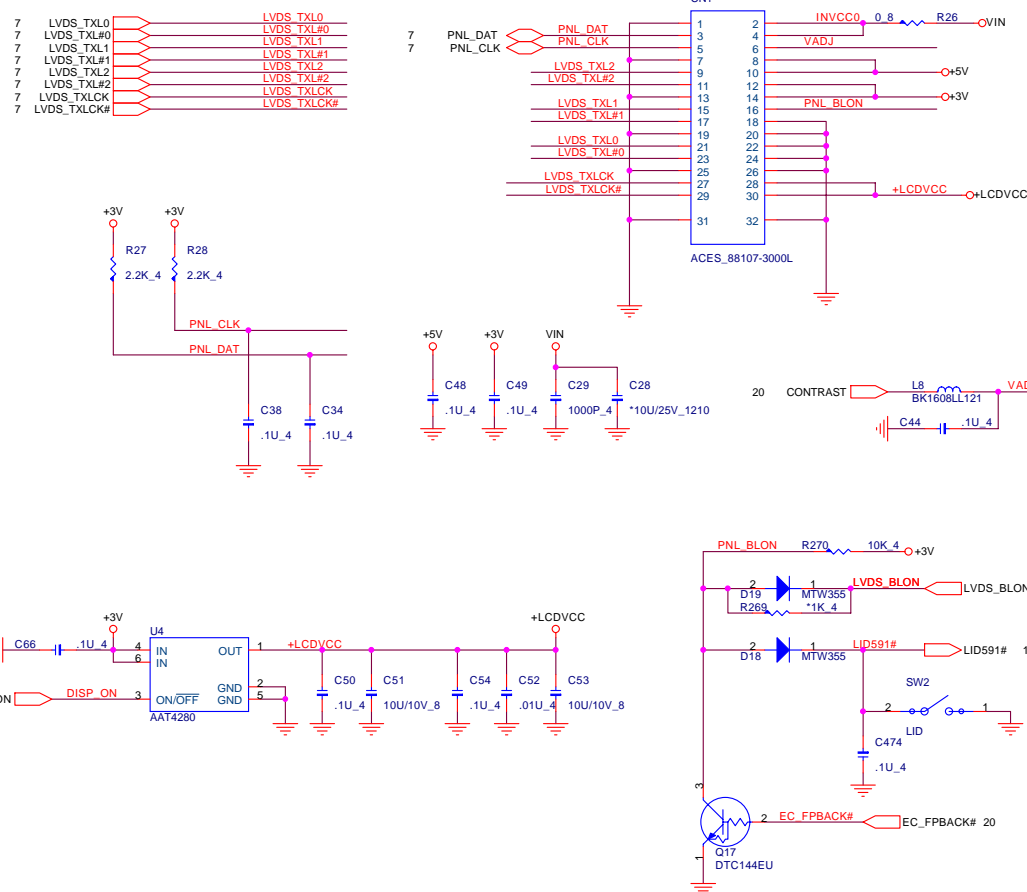
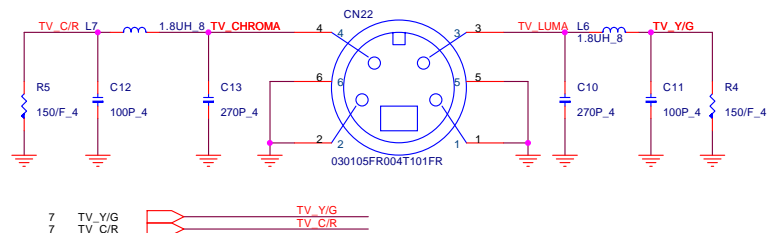




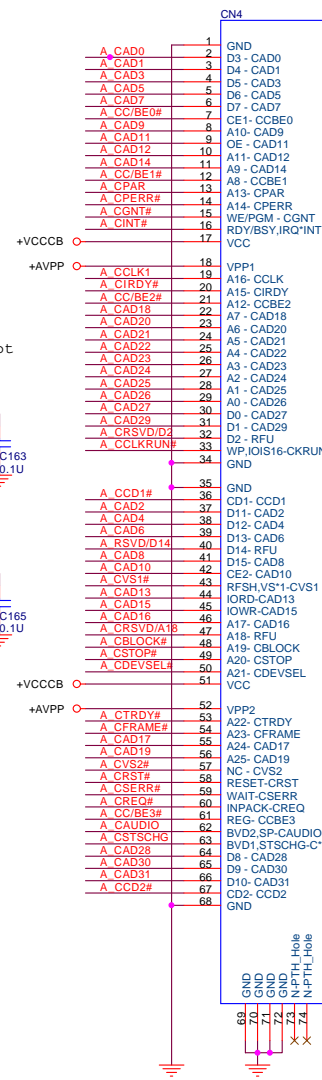
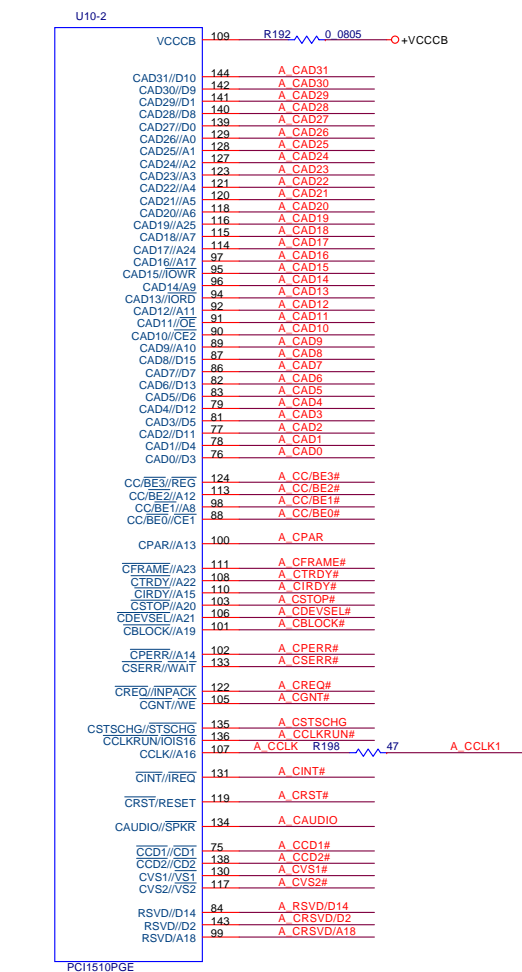
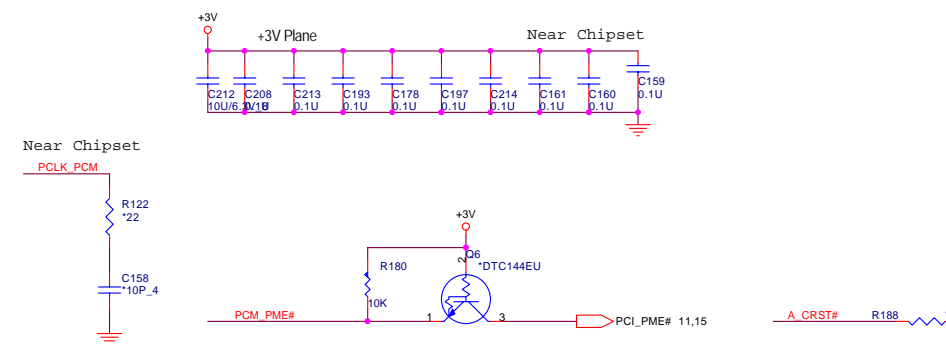
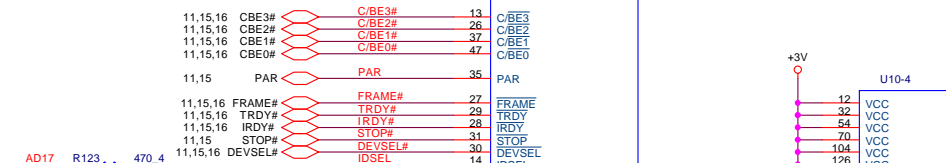
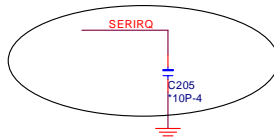




CRT



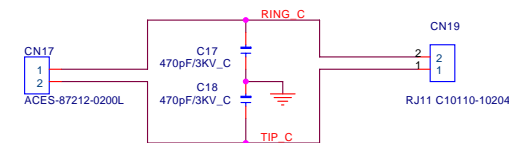
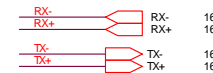
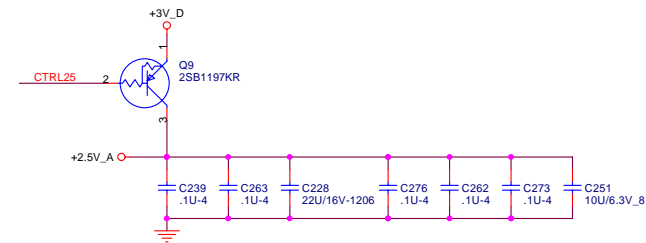
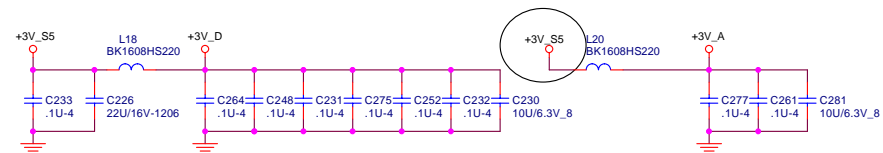
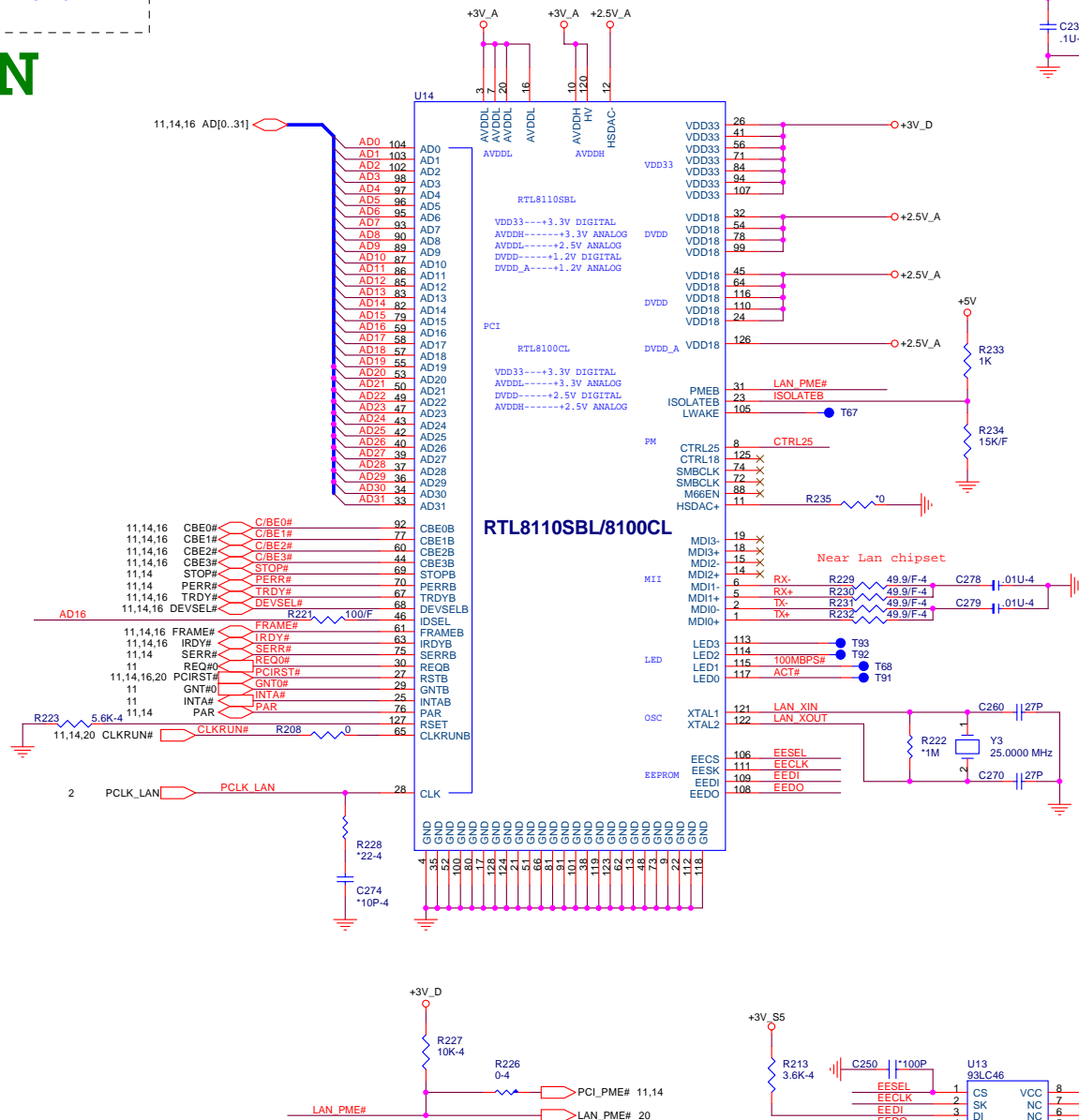
CBS



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ID Select : AD16
Interrupt Pin : INTA#
Request Indicate : REQ0#
Grant Indicate : GNT0#

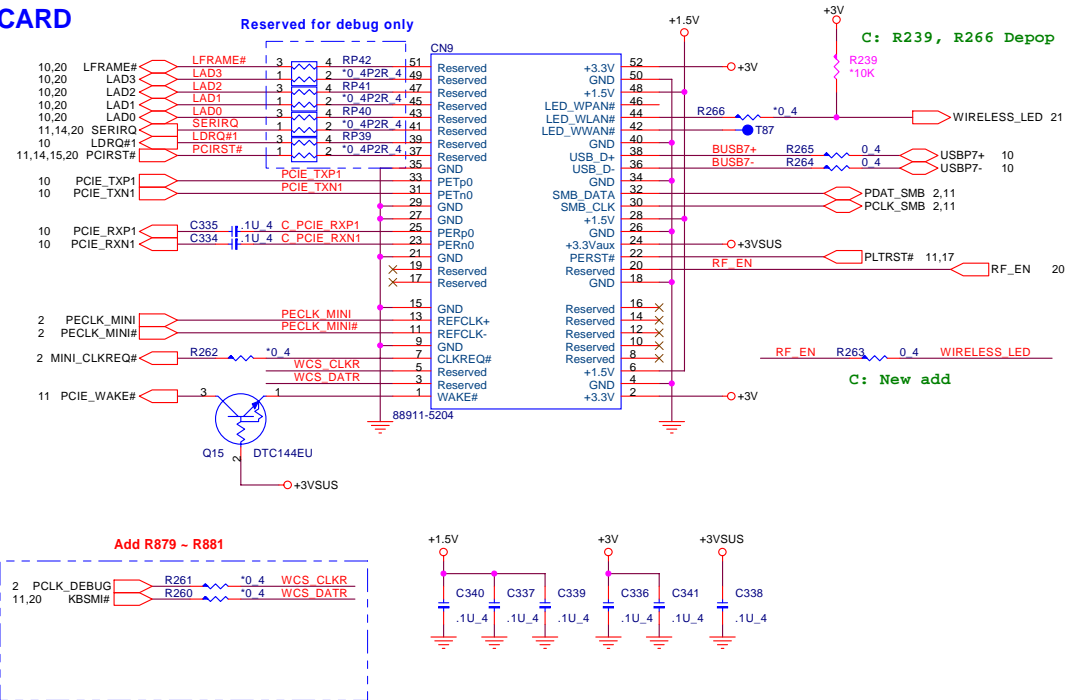
LAN



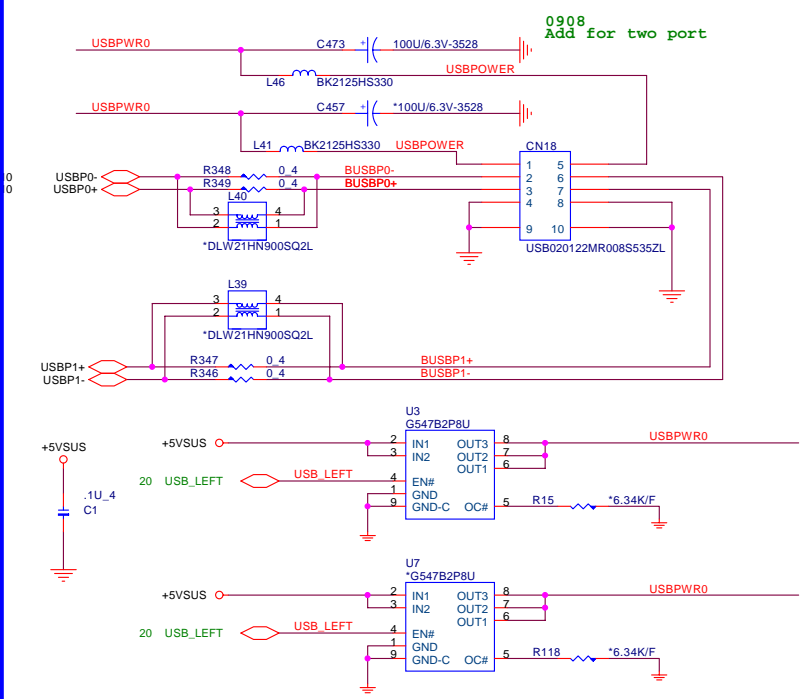
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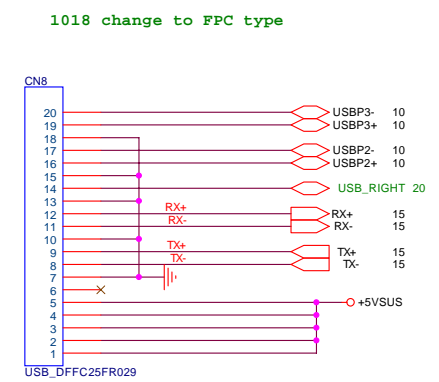
MINI CARD



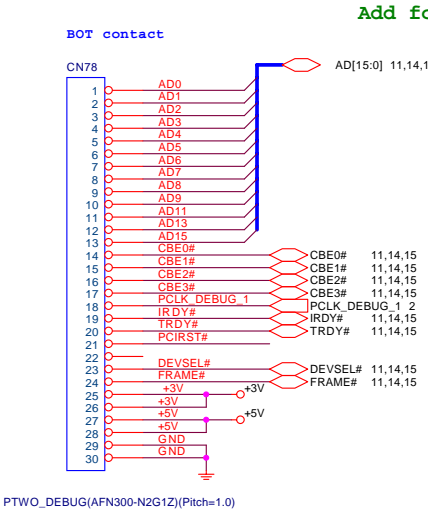
USB PORT x2



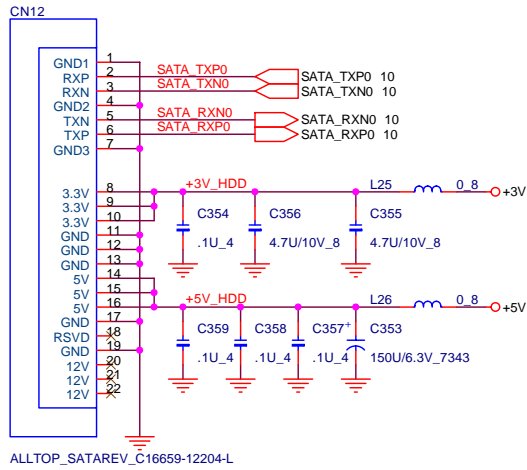
USB/LAN BOARD



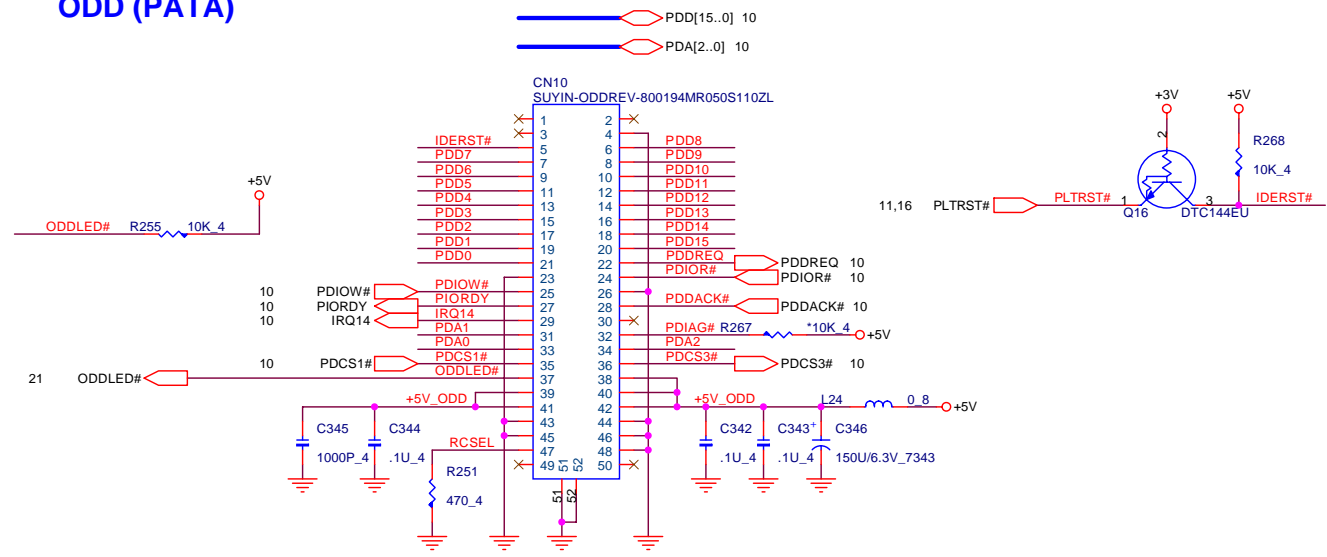
Debug card interface



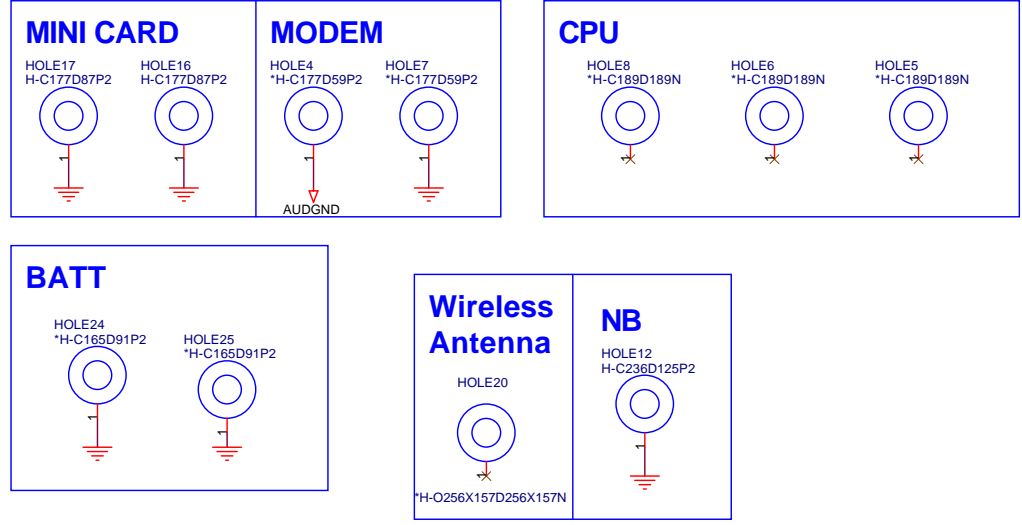
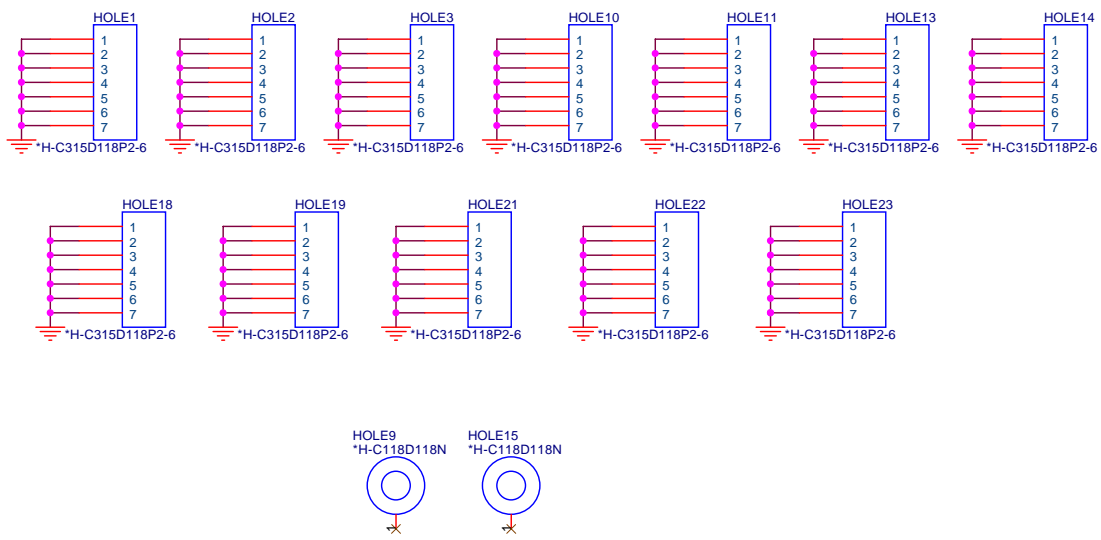
HDD (SATA)



ODD (PATA)



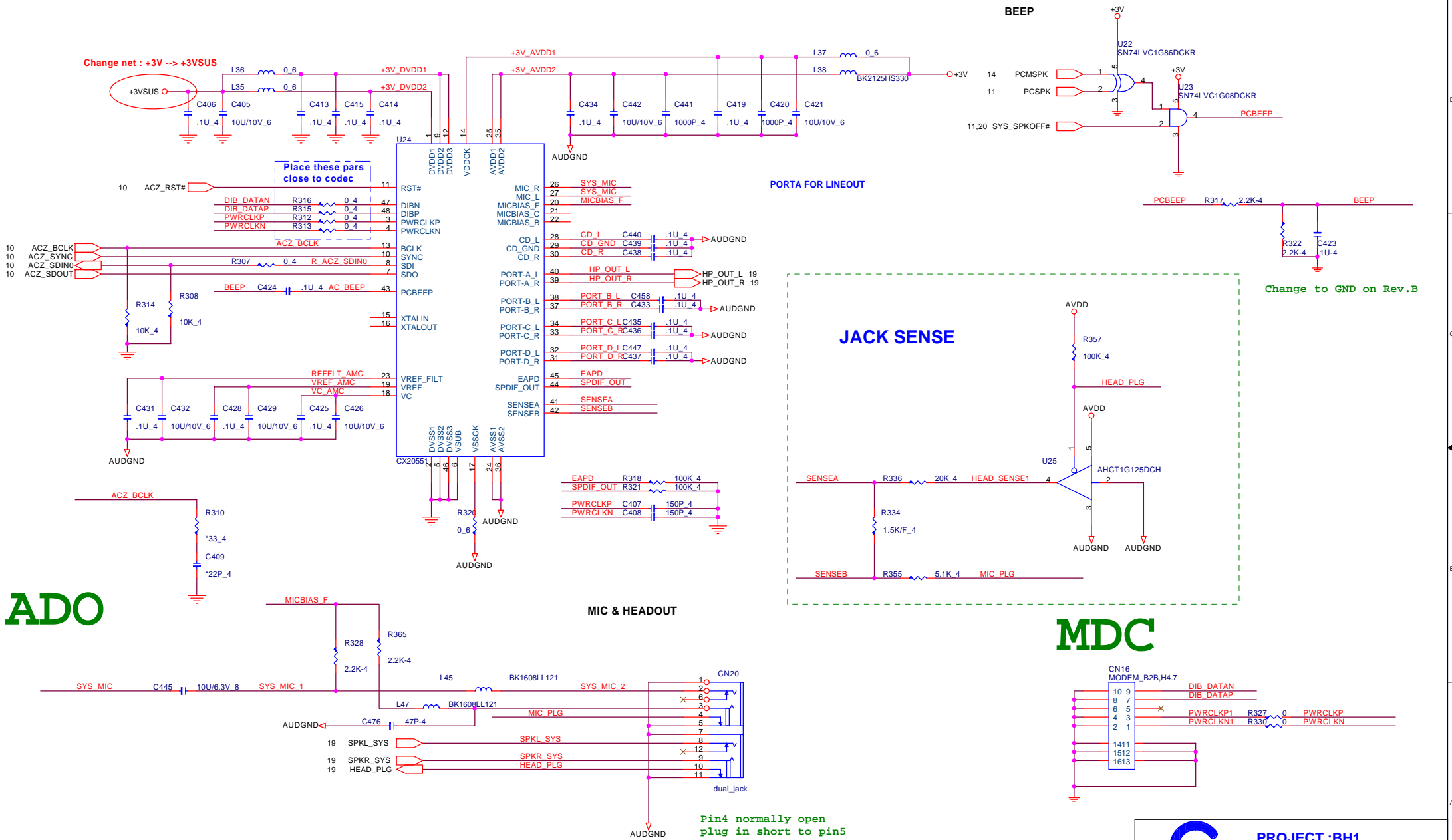
SCREW HOLE



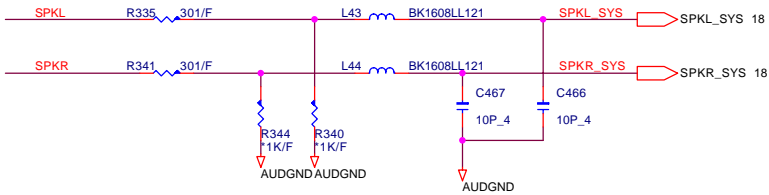
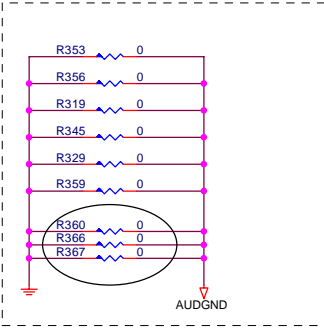
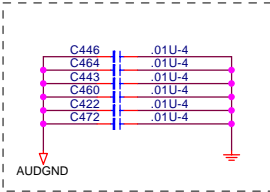
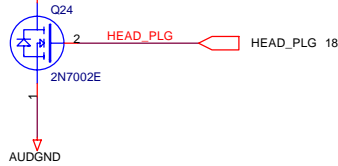
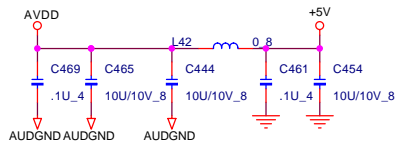
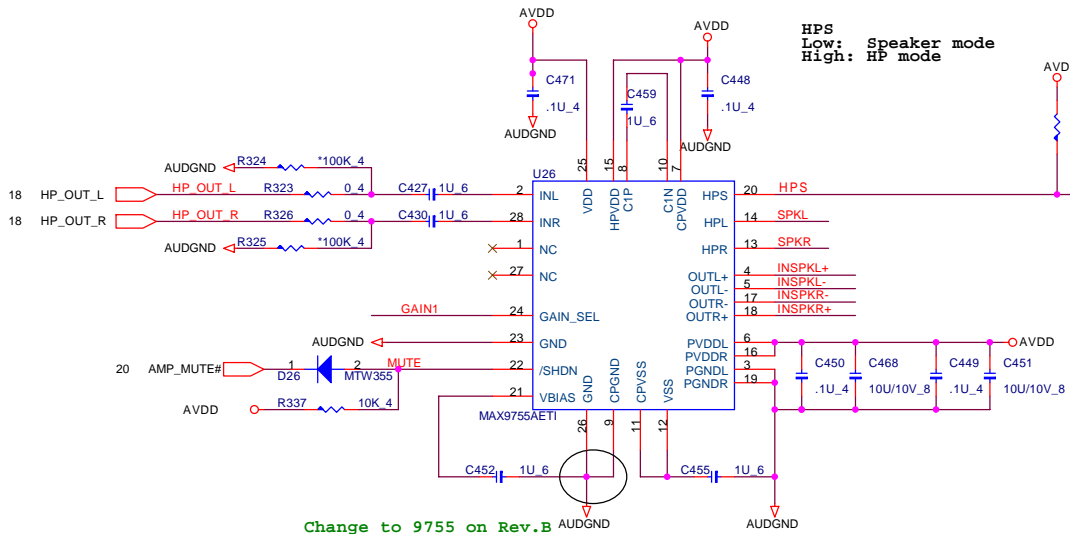
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	HDD/ODD/SCREW	C
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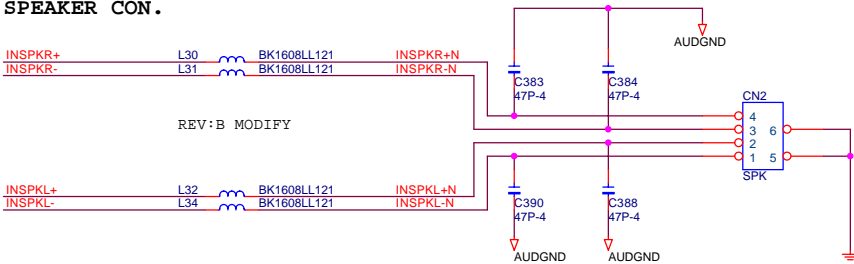
ADO



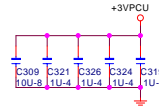
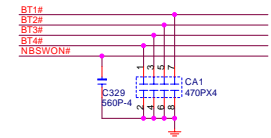
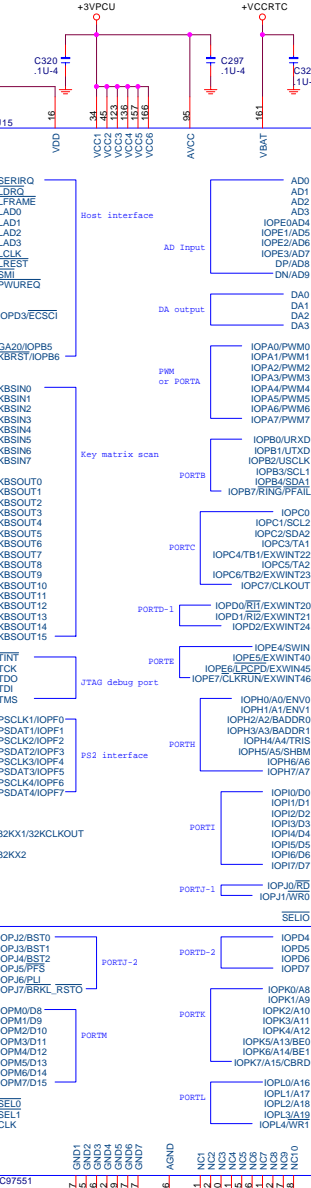
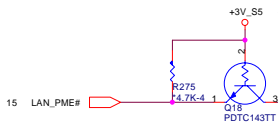
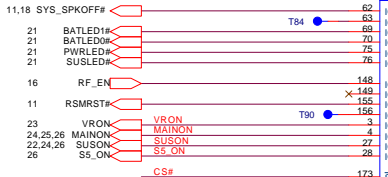
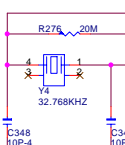
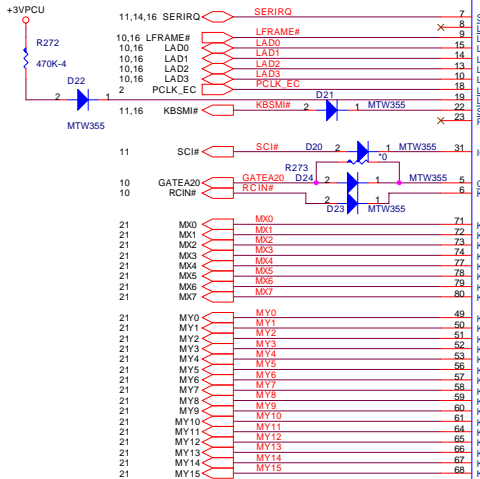
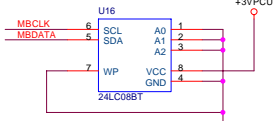
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



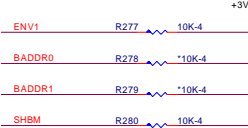
SPEAKER CON.



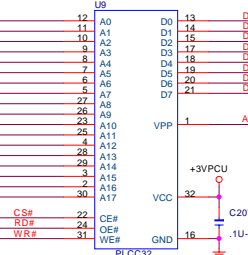
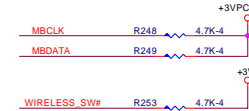
PCLK_EC R252 22-4 C327 10P-4



Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.



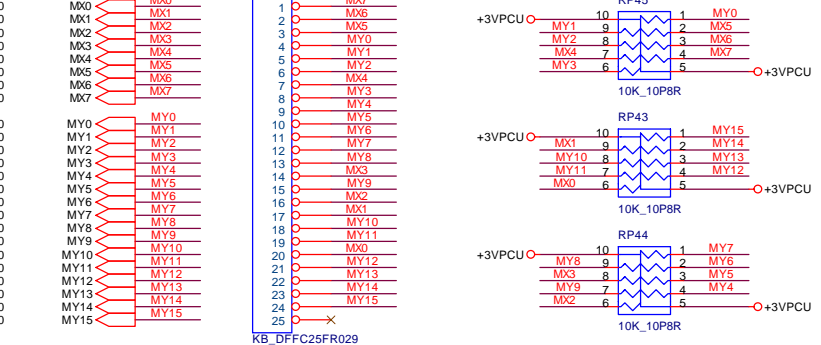
BADDR1-0	Index	I/O Address	Data
0 0	2E		2F
0 1	4E		4F
1 0	(HCF0BAH, HCF0BAL)	(HCF0BAH, HCF0BAL)+1	
1 1		Reserved	



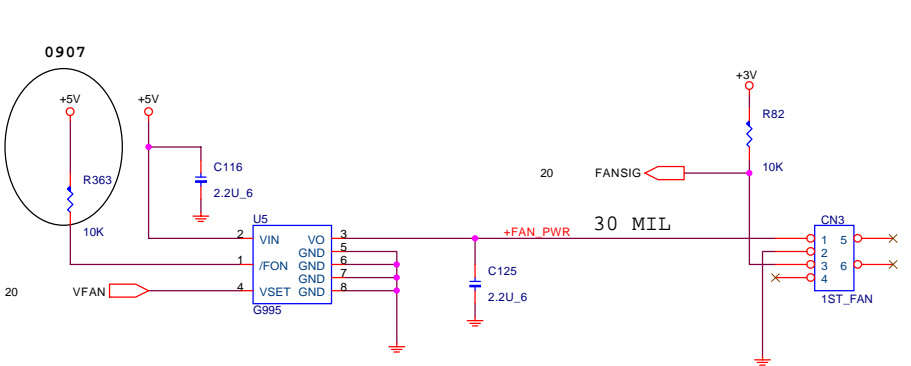
BIU configuration should match flash speed used

FOR 97551 ONLY

INT K/B

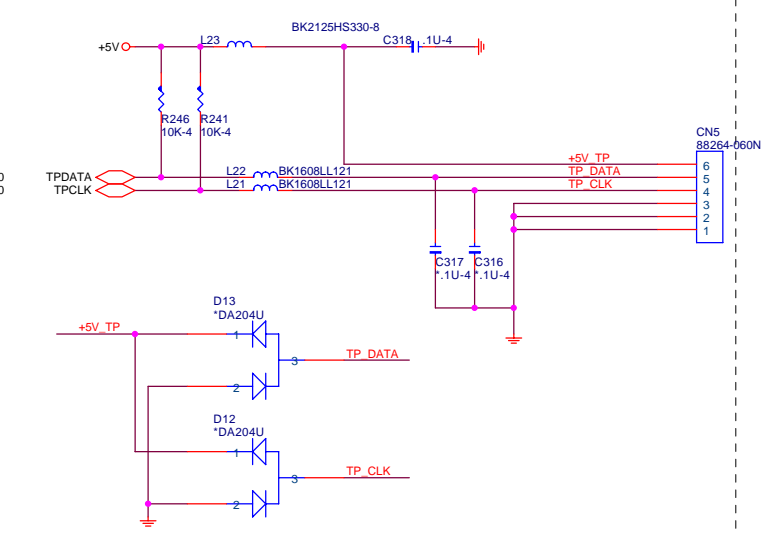


FAN CONTROL

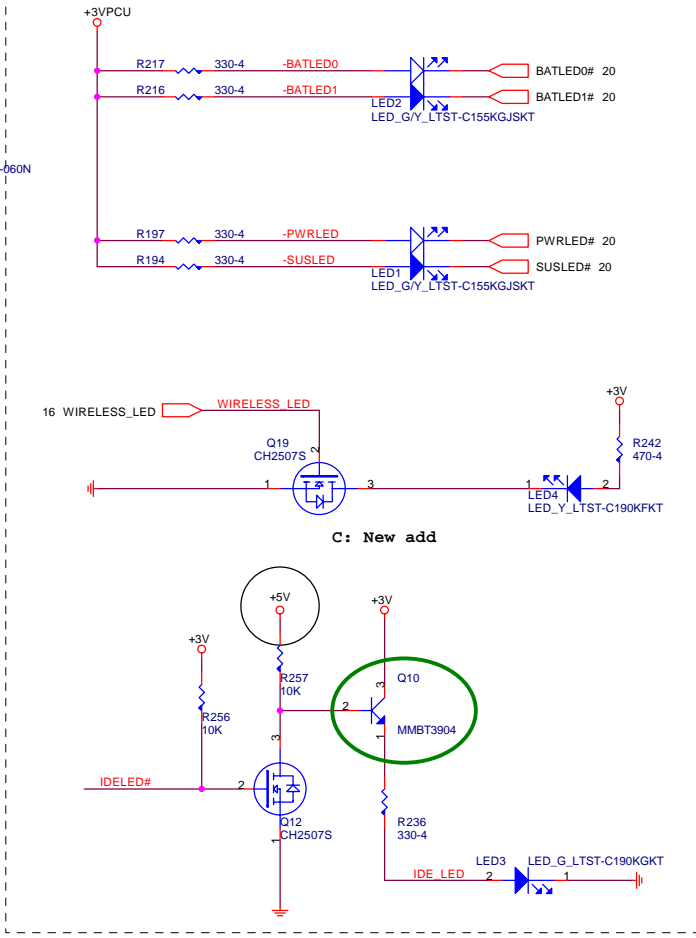


TP

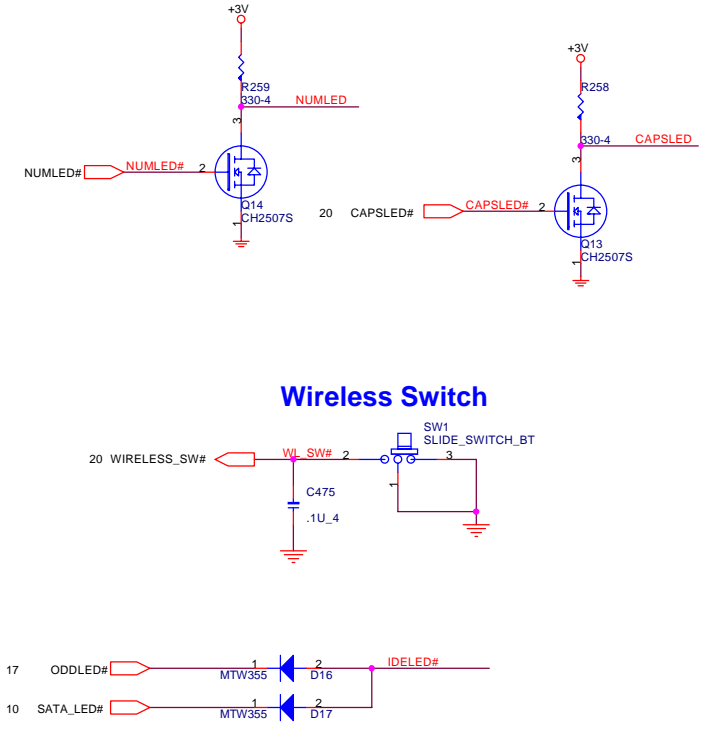
20 MIL



LED



Wireless Switch



LED BD

